

Compal confidential

Schematics Document

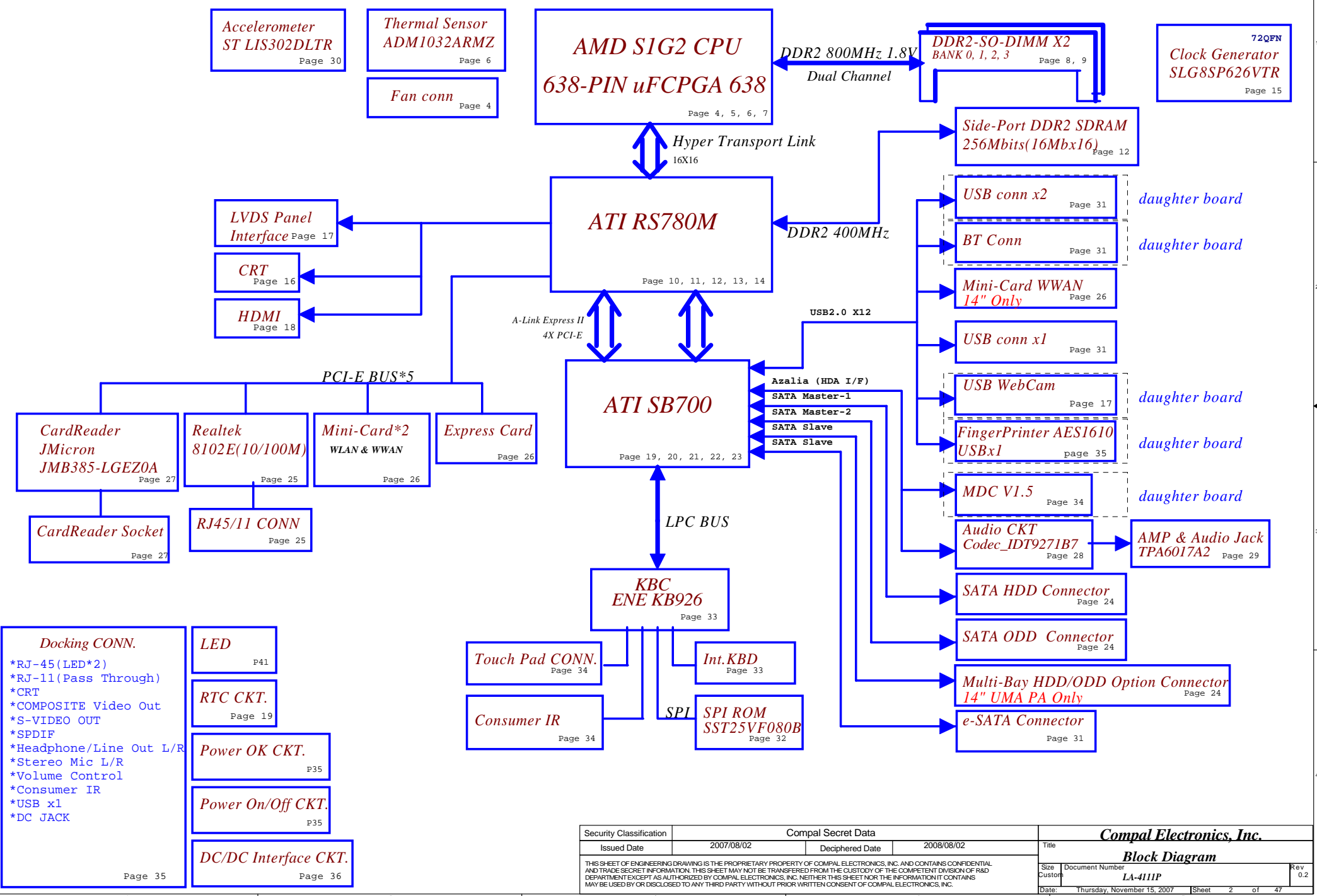
Mobile AMD S1G2 CPU with ATI
RS780M(NB) & SB700(SB) core logic

2007-11-14

REV:0.2

Security Classification	Compal Secret Data			Title Compal Electronics, Inc.		
Issued Date	2007/08/02	Deciphered Date	2008/08/02	Cover Sheet		
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Consumer AMD 14" UMA - Ripley (JBL20)



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				Block Diagram		
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Voltage Rails **O MEANS ON** **X MEANS OFF**


power plane	+B	+5VALW	+1.8V	+5VS +3VS +1.5VS +0.9V +VCCP +CPU_CORE
		+3VALW		+VGA_CORE +2.5VS +1.8VS +1.2VS +0.9VGA
State				
S0	O	O	O	O
S1	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

Symbol Note :

 : means Digital Ground

 : means Analog Ground

@ : means just reserve , no build
DEBUG@ : means just reserve for debug.

 Layout Notes

UMA@ : means for RS780M.
Please see VGA@ as no install. No support RX780M.

 11/14 update

 : Question Area Mark.(Wait check)

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

EC SM Bus1 address EC SM Bus2 address

Device	HEX	Address	Device	HEX	Address
Smart Battery	16H	0001 011X b	CPU	98H	1001 100X b
24C16	A0H	1010 000X b	AD1032-2 CPU	9AH	1001 101X b

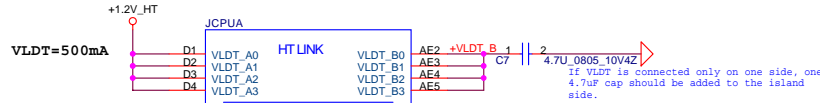
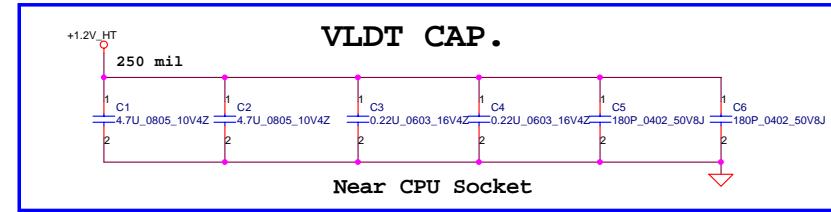
SMBUS Control Table

	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR CPU & ADM1032	SODIMM I / II	CLK CHIP	MINI CARD slot 2	LCD	HDMI	G-Sensor
SMB_EC_CK1 SMB_EC_DA1	KB926	X	V	V	X	X	X	X	X	X	X
SMB_EC_CK2 SMB_EC_DA2	KB926	X	X	X	V	X	X	X	X	X	X
I2C_CLK I2C_DATA	RS780M	X	X	X	X	X	X	X	V	X	X
DDC_CLK0 DDC_DATA0	RS780M	X	X	X	X	X	X	X	X	V	X
DDC_CLK1 DDC_DATA1	RS780M	X	X	X	X	X	X	X	X	X	X
SCL0 SDA0	SB700	X	X	X	X	V	V	X	X	X	X
SCL1 SDA1	SB700	X	X	X	X	X	X	V	X	X	X
SCL2 SDA2	SB700	X	X	X	X	X	X	X	X	X	V
SCL3 SDA3	SB700	X	X	X	X	X	X	X	X	X	X

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<10> H_CADIP[0..15] → H_CADIP[0..15]
 <10> H_CADIN[0..15] → H_CADIN[0..15]

H_CADOP[0..15] → H_CADOP[0..15] <10>
 H_CADON[0..15] → H_CADON[0..15] <10>



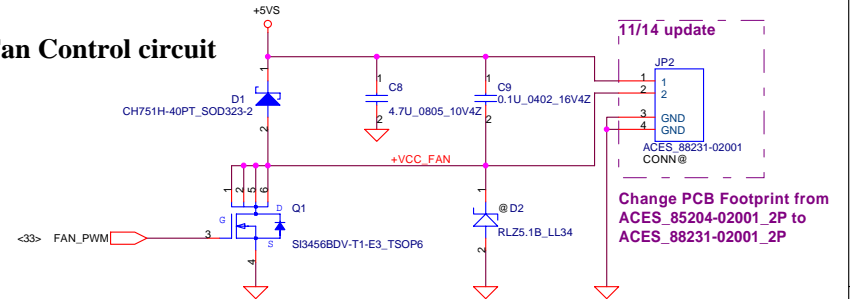
H_CADIP0	E3	L0_CADIN_H0	L0_CADOUT_H0	AD1	H_CADOP0
H_CADIN0	E2	L0_CADIN_L0	L0_CADOUT_L0	AC1	H_CADON0
H_CADIP1	E1	L0_CADIN_H1	L0_CADOUT_H1	AC2	H_CADOP1
H_CADIN1	F1	L0_CADIN_L1	L0_CADOUT_L1	AC3	H_CADON1
H_CADIP2	G3	L0_CADIN_H2	L0_CADOUT_H2	AB1	H_CADOP2
H_CADIN2	G2	L0_CADIN_L2	L0_CADOUT_L2	AA1	H_CADON2
H_CADIP3	G1	L0_CADIN_H3	L0_CADOUT_H3	AA2	H_CADOP3
H_CADIN3	H1	L0_CADIN_L3	L0_CADOUT_L3	AA3	H_CADON3
H_CADIP4	J1	L0_CADIN_H4	L0_CADOUT_H4	W2	H_CADOP4
H_CADIN4	I3	L0_CADIN_L4	L0_CADOUT_L4	W3	H_CADON4
H_CADIP5	K1	L0_CADIN_H5	L0_CADOUT_H5	Y1	H_CADOP5
H_CADIN5	L2	L0_CADIN_L5	L0_CADOUT_L5	U1	H_CADON5
H_CADIP6	L1	L0_CADIN_H6	L0_CADOUT_H6	U2	H_CADOP6
H_CADIN6	L3	L0_CADIN_L6	L0_CADOUT_L6	U3	H_CADON6
H_CADIP7	M1	L0_CADIN_H7	L0_CADOUT_H7	T1	H_CADOP7
H_CADIN7	N2	L0_CADIN_L7	L0_CADOUT_L7	R1	H_CADON7
H_CADIP8	E5	L0_CADIN_H8	L0_CADOUT_H8	AD4	H_CADOP8
H_CADIN8	F5	L0_CADIN_L8	L0_CADOUT_L8	AD3	H_CADON8
H_CADIP9	F3	L0_CADIN_H9	L0_CADOUT_H9	AD5	H_CADOP9
H_CADIN9	F4	L0_CADIN_L9	L0_CADOUT_L9	AC5	H_CADON9
H_CADIP10	G5	L0_CADIN_H10	L0_CADOUT_H10	AB4	H_CADOP10
H_CADIN10	H5	L0_CADIN_L10	L0_CADOUT_L10	AB3	H_CADON10
H_CADIP11	H3	L0_CADIN_H11	L0_CADOUT_H11	AB5	H_CADOP11
H_CADIN11	H4	L0_CADIN_L11	L0_CADOUT_L11	AA5	H_CADON11
H_CADIP12	K3	L0_CADIN_H12	L0_CADOUT_H12	Y5	H_CADOP12
H_CADIN12	K4	L0_CADIN_L12	L0_CADOUT_L12	W5	H_CADON12
H_CADIP13	L5	L0_CADIN_H13	L0_CADOUT_H13	V4	H_CADOP13
H_CADIN13	M5	L0_CADIN_L13	L0_CADOUT_L13	V3	H_CADON13
H_CADIP14	M3	L0_CADIN_H14	L0_CADOUT_H14	V5	H_CADOP14
H_CADIN14	M4	L0_CADIN_L14	L0_CADOUT_L14	U5	H_CADON14
H_CADIP15	N5	L0_CADIN_H15	L0_CADOUT_H15	T4	H_CADOP15
H_CADIN15	P5	L0_CADIN_L15	L0_CADOUT_L15	T3	H_CADON15

<10> H_CLKIP0 → J3 L0_CLKIN_H0 L0_CLKOUT_H0 Y1 H_CLKOP0 <10>
 <10> H_CLKIN0 → J2 L0_CLKIN_L0 L0_CLKOUT_L0 W4 H_CLKON0 <10>
 <10> H_CLKIP1 → J5 L0_CLKIN_H1 L0_CLKOUT_H1 Y4 H_CLKOP1 <10>
 <10> H_CLKIN1 → K5 L0_CLKIN_L1 L0_CLKOUT_L1 Y3 H_CLKON1 <10>

<10> H_CTLIP0 → N1 L0_CTLIN_H0 L0_CTLOUT_H0 R2 H_CTLOP0 <10>
 <10> H_CTLIN0 → P1 L0_CTLIN_L0 L0_CTLOUT_L0 T5 H_CTLON0 <10>
 <10> H_CTLIP1 → P3 L0_CTLIN_H1 L0_CTLOUT_H1 T5 H_CTLOP1 <10>
 <10> H_CTLIN1 → P4 L0_CTLIN_L1 L0_CTLOUT_L1 R5 H_CTLON1 <10>

FOX_PZ6382A-284S-41F_GRIFFIN
 CONN@
 Athlon 64 S1
 Processor Socket
 9/20 SP07000DM00/SP07000RQ00

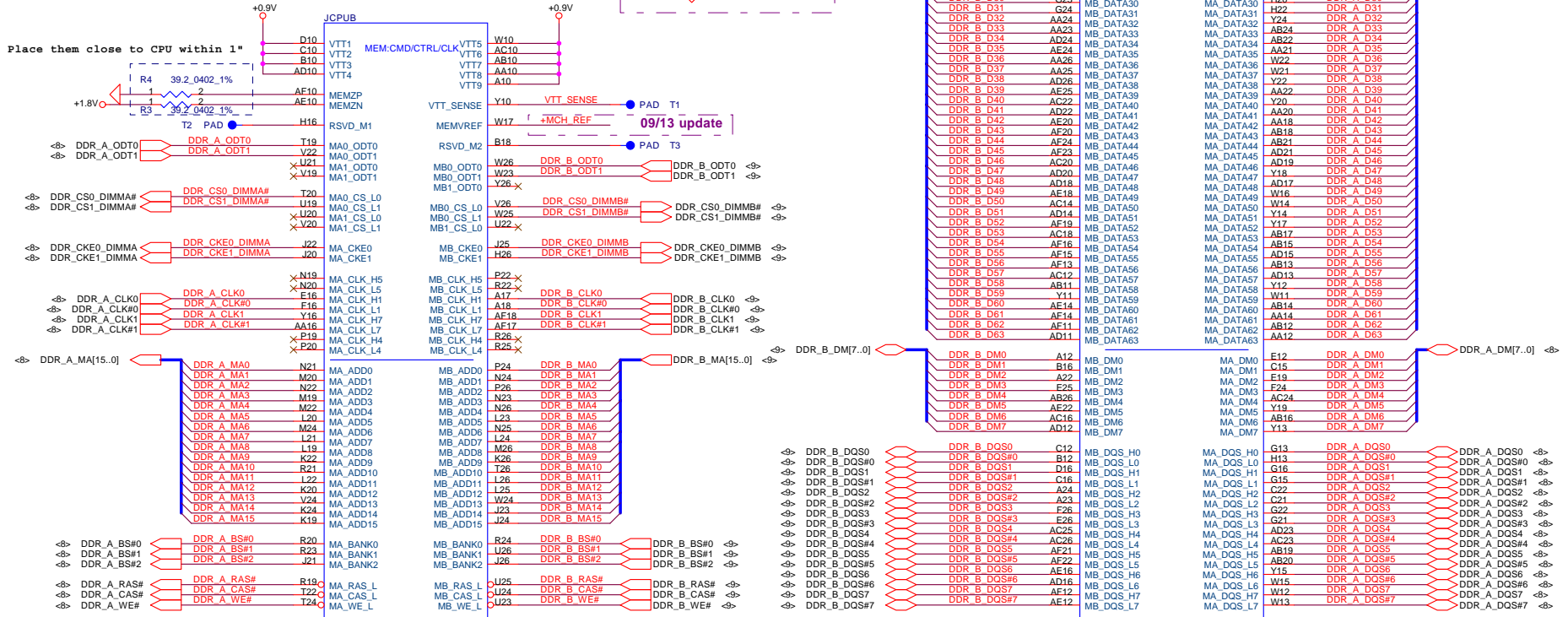
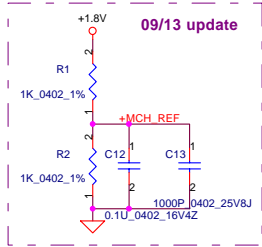
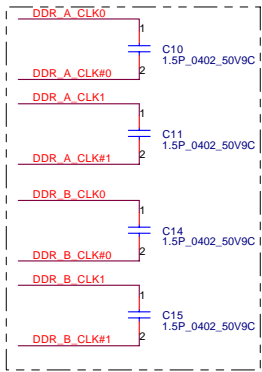
PWM Fan Control circuit



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Processor DDR2 Memory Interface

PLACE CLOSE TO PROCESSOR WITHIN 1.5 INCH

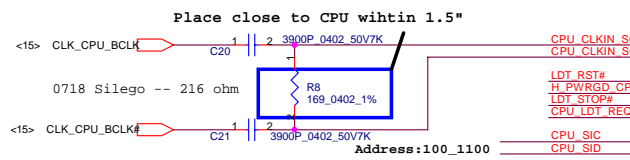
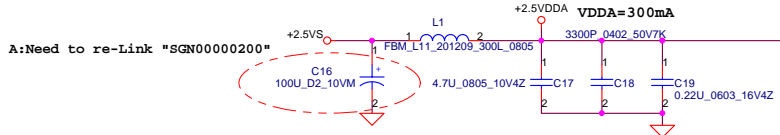


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Athlon 64 S1
Processor
Socket
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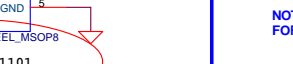
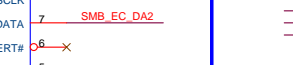
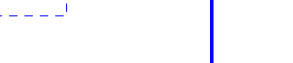
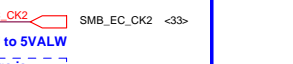
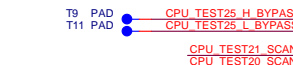
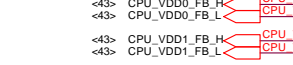
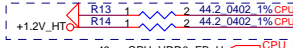
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Athlon 64 S1
Processor Socket
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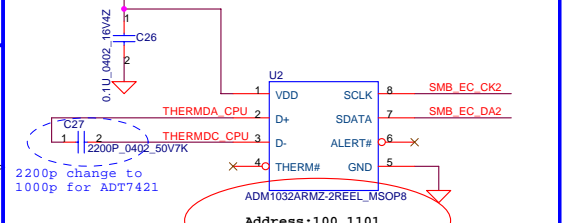
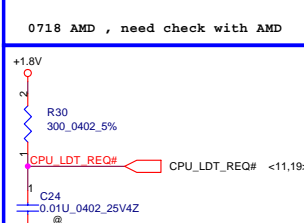
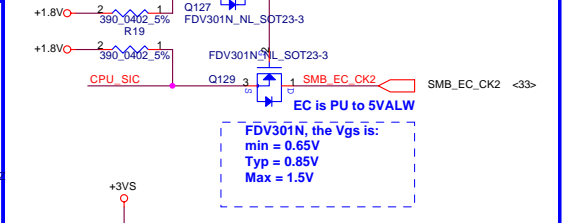
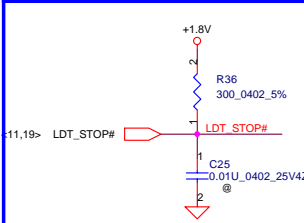
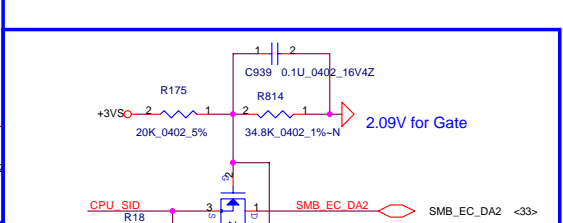
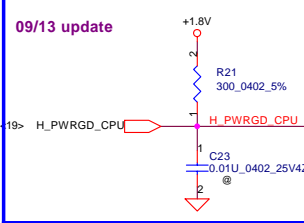
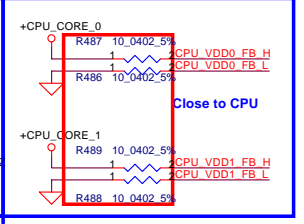
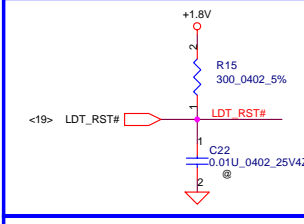
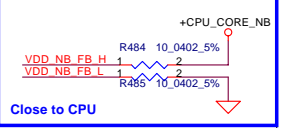
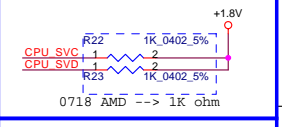
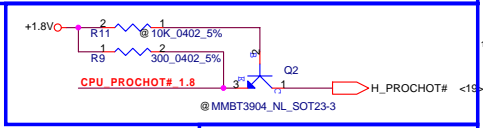
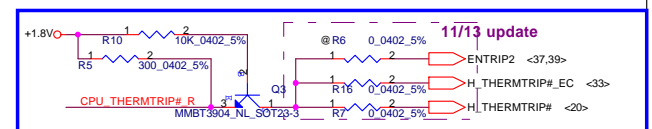


Address:100_1100

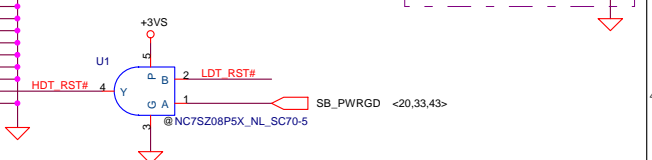
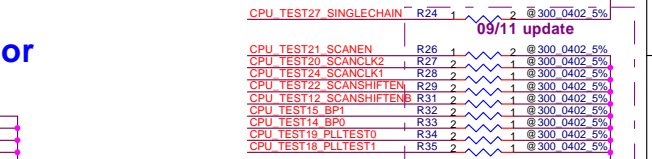
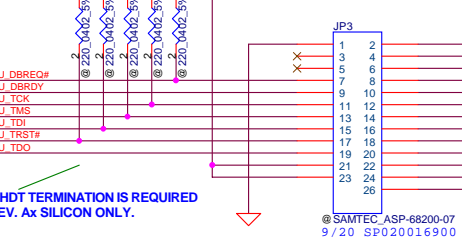


JCPUD		KEY1		KEY2	
VDDA1	VDDA2	M11	W18	X	X
CLKIN_H	CLKIN_L	A6	CPU_SVC	CPU_SVC	<43>
RESET_L	PWR0K	A4	CPU_SVD	CPU_SVD	<43>
LDT_STOP_L	LDTSTOP_L	AF6	CPU_THERMTRIP#_R	CPU_THERMTRIP#_R	<37,39>
LDTREQ_L	LDTREQ_L	AC7	CPU_PROCHOT#_1.8	CPU_PROCHOT#_1.8	<33>
SIC	SID	AA8	CPU_MEMHOT#_1.8V	CPU_MEMHOT#_1.8V	<20>
ALERT_L	SIC	W7	THERMDC_CPU	THERMDC_CPU	<33>
HT_REF0	HT_REF0	W8	THERMDA_CPU	THERMDA_CPU	<33>
VDD0_FB_H	VDD0_FB_L	Y9	PAD	T43	
VDD1_FB_H	VDD1_FB_L	H6	VDD_NB_FB_FT	VDD_NB_FB_H	<43>
VDDIO_FB_H	VDDIO_FB_L	G6	VDD_NB_FB_L	VDD_NB_FB_L	<43>
DBRDY	TMS	E10	CPU_DBREQ#	CPU_DBREQ#	
TCK	TRST_L	AE9	CPU_TDO	CPU_TDO	
TDI	TEST28_H	J7	ICPU_TEST28_H_FL_LCHZR_P	ICPU_TEST28_H_FL_LCHZR_P	route as differential as short as possible testpoint under package
TEST23	TEST28_L	H8	ICPU_TEST28_L_FL_LCHZR_N	ICPU_TEST28_L_FL_LCHZR_N	
TEST18	TEST19	D7	CPU_TEST17_BP3	CPU_TEST17_BP3	
TEST17	TEST16	E7	CPU_TEST16_BP2	CPU_TEST16_BP2	
TEST15	TEST14	F7	CPU_TEST15_BP1	CPU_TEST15_BP1	
TEST25_H	TEST25_L	C7	CPU_TEST14_BP0	CPU_TEST14_BP0	
TEST21	TEST20	C3	CPU_TEST29_H_FBCKOUT_P	CPU_TEST29_H_FBCKOUT_P	
TEST20	TEST10	K8	CPU_TEST29_L_FBCKOUT_N	CPU_TEST29_L_FBCKOUT_N	
TEST24	TEST8	C4			
TEST22	TEST12	C8			
TEST12	TEST29_H	CA			
TEST27	TEST29_L	CA			
TEST6	RSVD10	H18			
	RSVD9	H19			
	RSVD8	AAZ			
	RSVD7	D5			
	RSVD6	C5			

FOX_PZ6382A-284S-41F_GRIFFIN CONN@

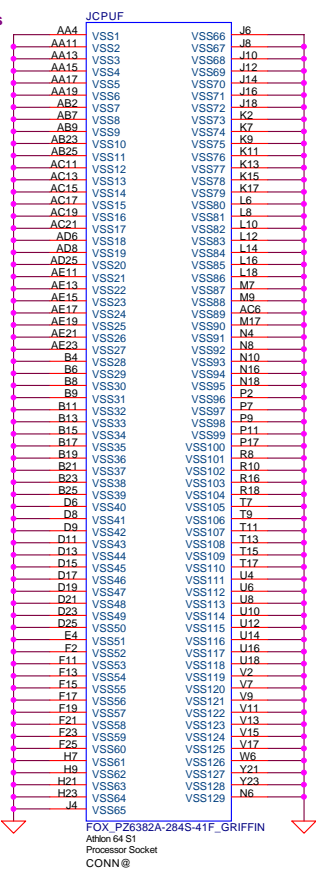
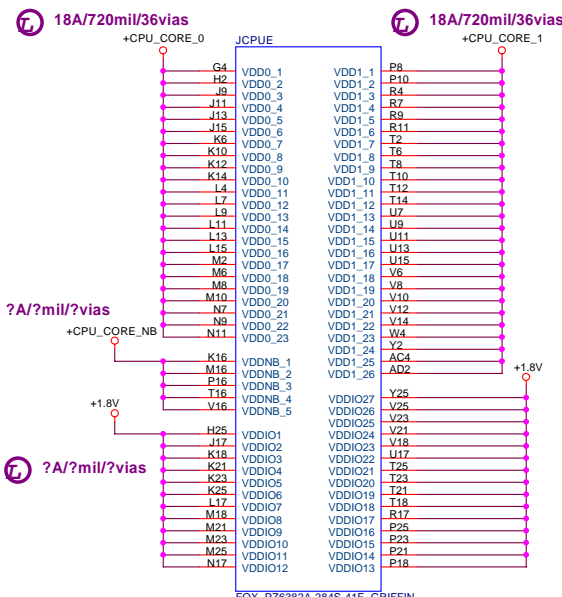
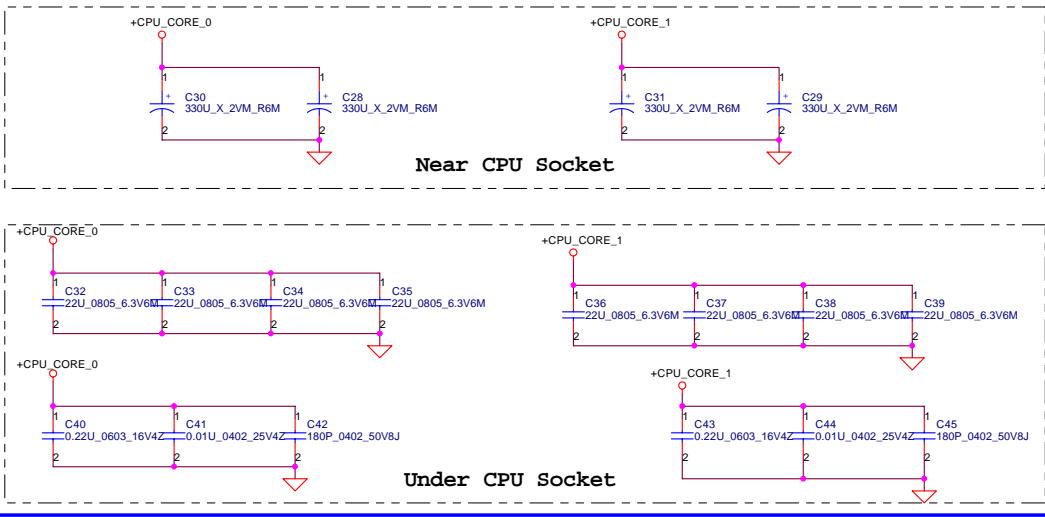


HDT Connector



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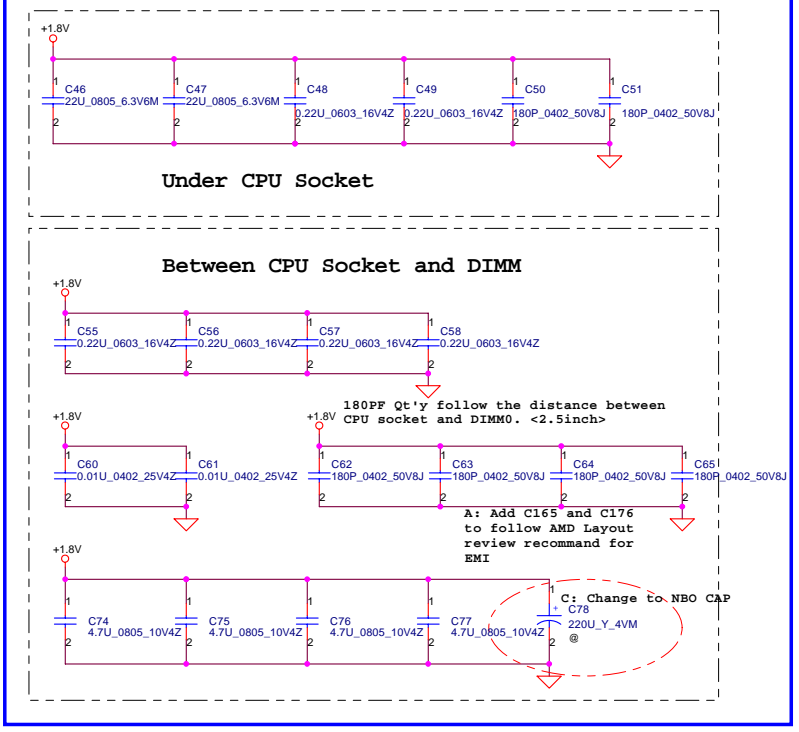
VDD(+CPU_CORE) decoupling.



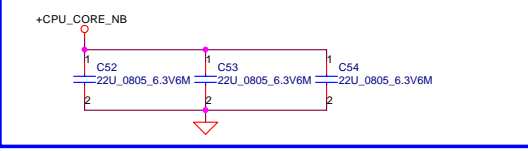
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Athlon 64 S1
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FOX_P26382A-284S-41F_GRIFFIN
Athlon 64 S1
Processor Socket
CONN@

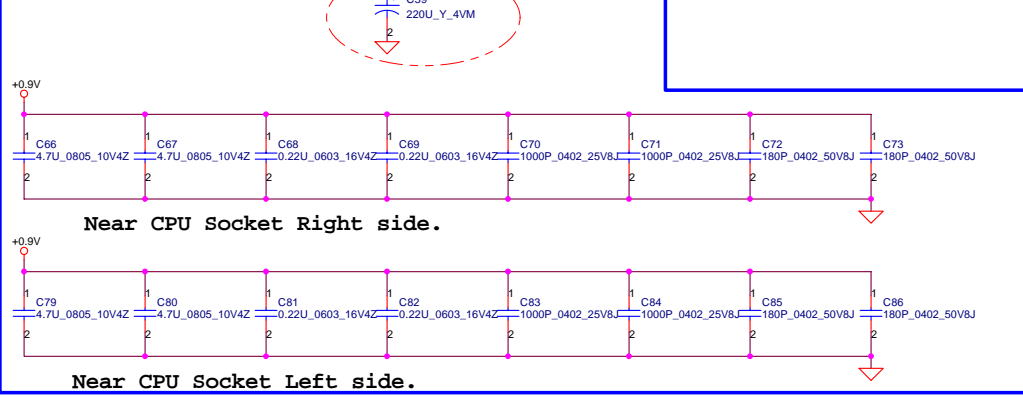
VDDIO decoupling.



+CPU_CORE_NB decoupling.



VTT decoupling.

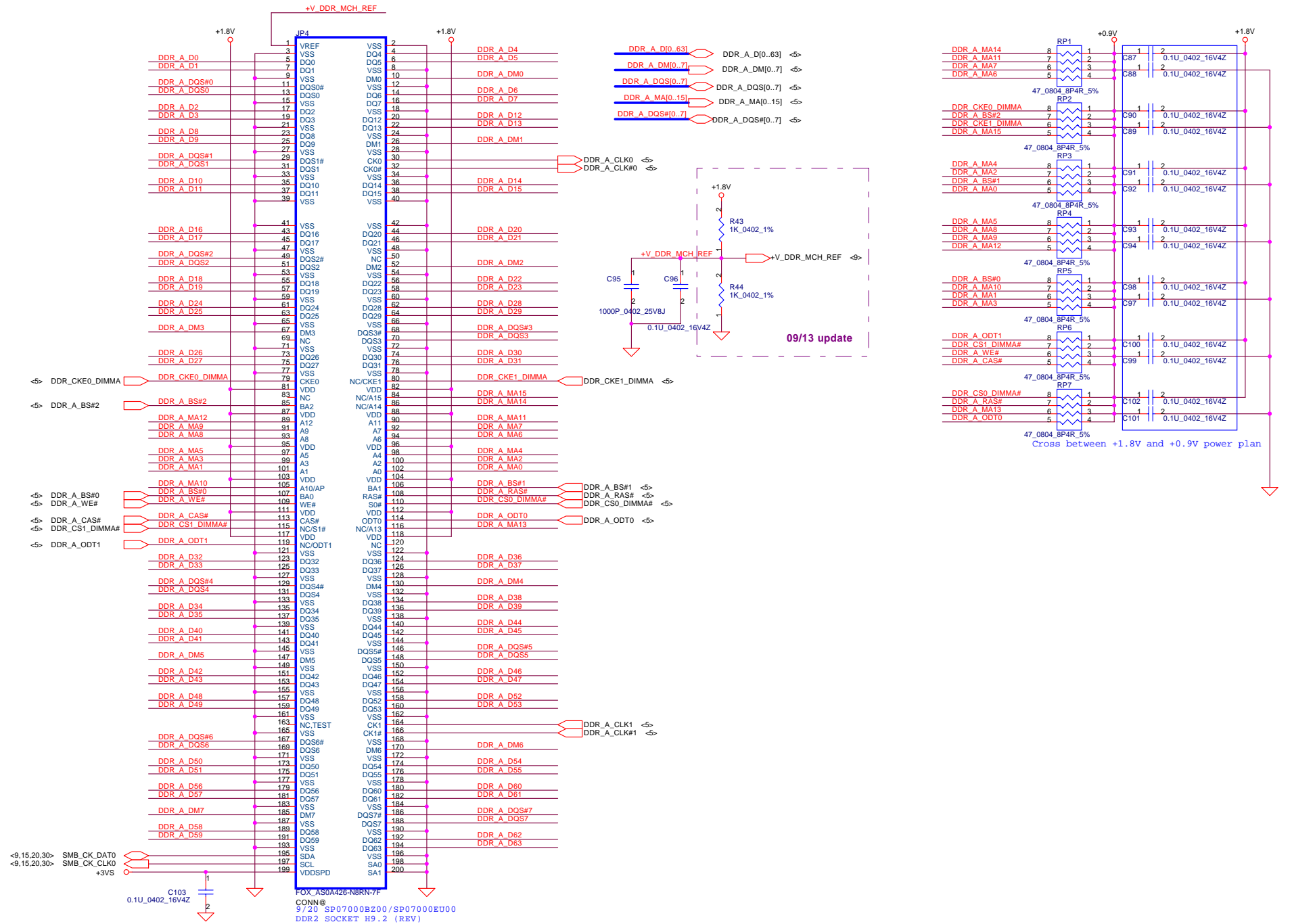


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Issued Date	2007/08/02	Deciphered Date
		2008/08/02

Title		Compal Electronics, Inc.	
		AMD CPU SIG2 PWR & GND	
Size	Document Number	Rev	
Custom	LA-411IP	0.2	
Date:	Thursday, November 15, 2007	Sheet	of 47

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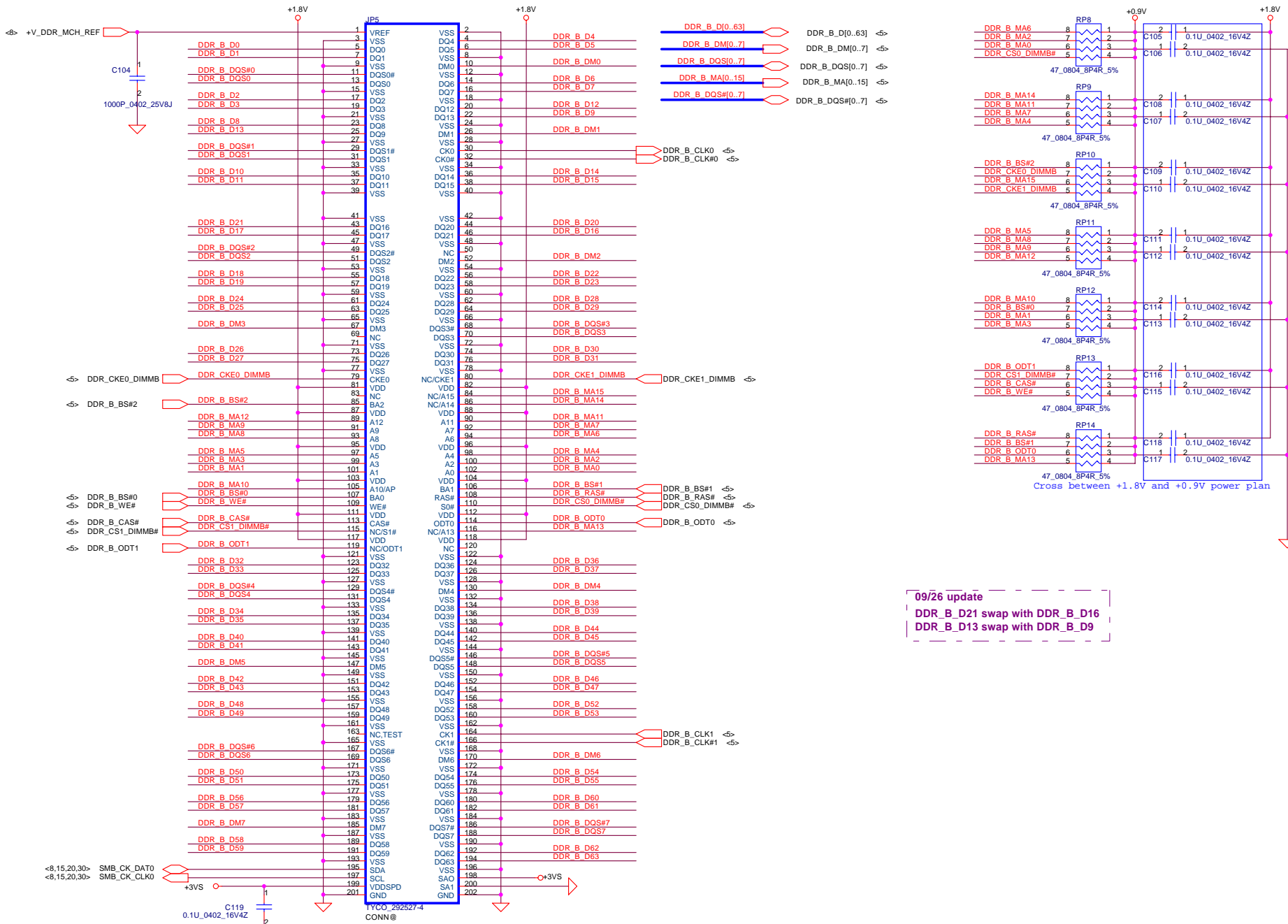
hexairnt@mail.com



<9,15,20,30> SMB_CK_DAT0
 <9,15,20,30> SMB_CK_CLK0
 +3VS
 C103
 0.1U_0402_16V4Z
 FOX ASS0426-N8RN-7F
 CONN@
 9/20 SP07000B200/SP07000EU00
 DDR2 SOCKET H9.2 (REV)

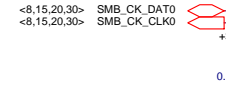
Security Classification		Compal Secret Data	
Issued Date	2007/08/02	Deciphered Date	2008/08/02
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Compal Electronics, Inc.			
DDR2 SO-DIMM 0			
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09/26 update
 DDR_B_D21 swap with DDR_B_D16
 DDR_B_D13 swap with DDR_B_D9

Cross between +1.8V and +0.9V power plan

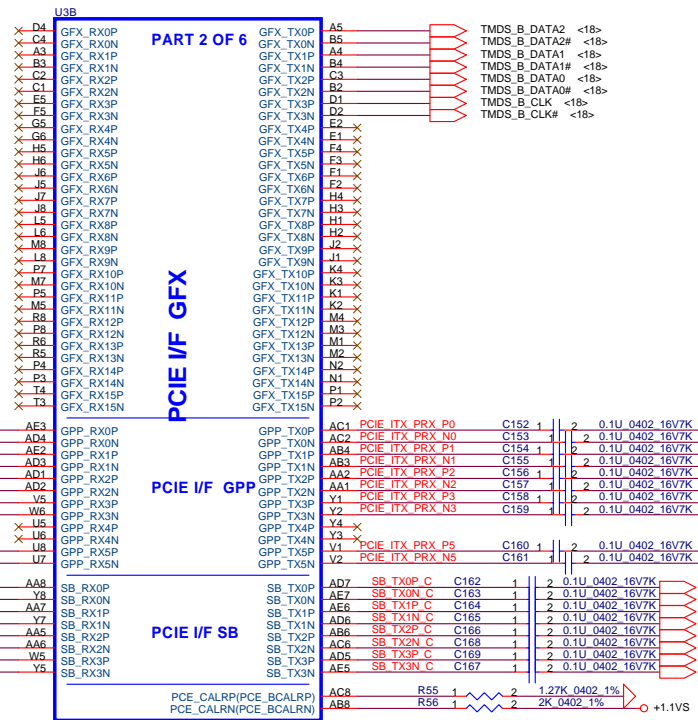


9/20 SP07000ET00/SP07000GN00

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		2008/08/02
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			DDR II SO-DIMM I	
Size	Document Number		Rev	
Custom	LA-411IP		0.2	
Date:	Thursday, November 15, 2007	Sheet	of	47

mailto:henrain@hotmail.com

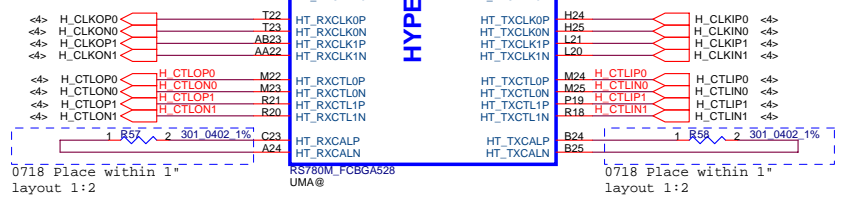


RS780M Display Port Support (muxed on GFX)

DP0	GFX_TX0, TX1, TX2 and TX3 AUX0 and HPD0
DP1	GFX_TX4, TX5, TX6 and TX7 AUX1 and HPD1

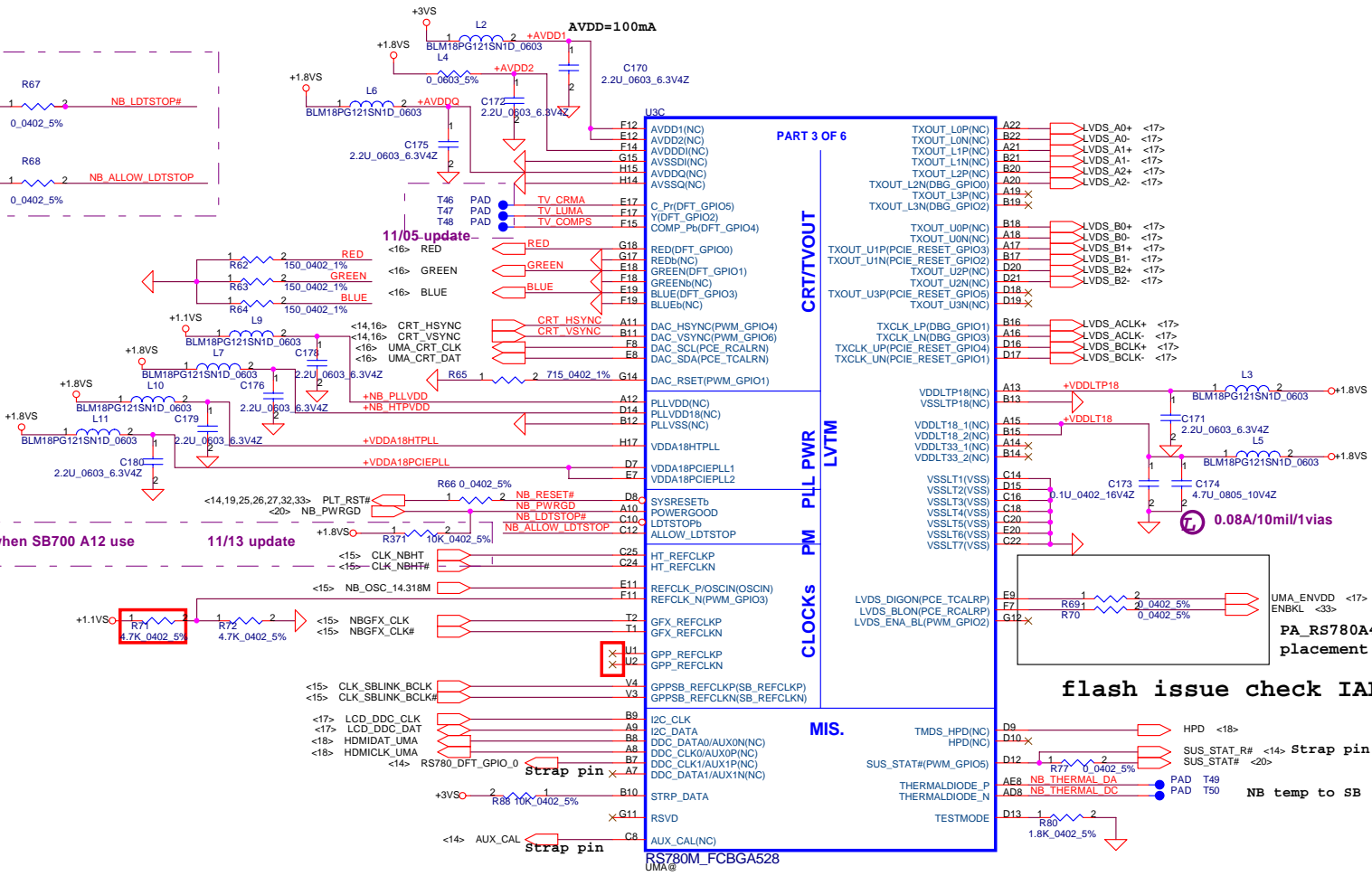
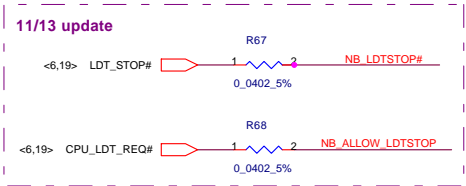
9/20 SA00001ZG00(A11) S IC 216-0674001-00/RS780M FCBGA528 0FH

New Card
CardReader
WLAN
LAN10/100
TV Tuner



NEED CHECK R68 & R69 WITH AMD

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				RS780-HT/PCIE		
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Date:	Thursday, November 15, 2007	Sheet	10	of	47	



PART 3 OF 6

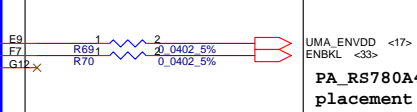
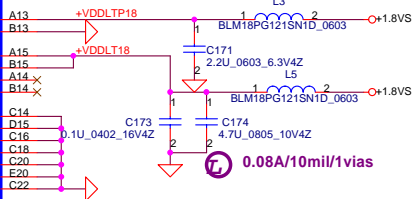
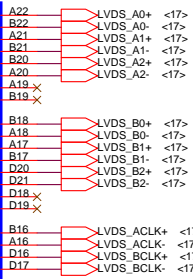
CRIT/VOUT

LVTM

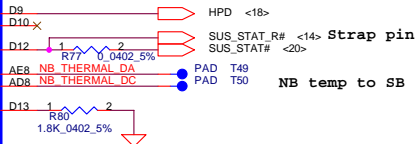
PM PLL PWR

CLOCKS

MIS.



flash issue check IALAA



Install when SB700 A12 use

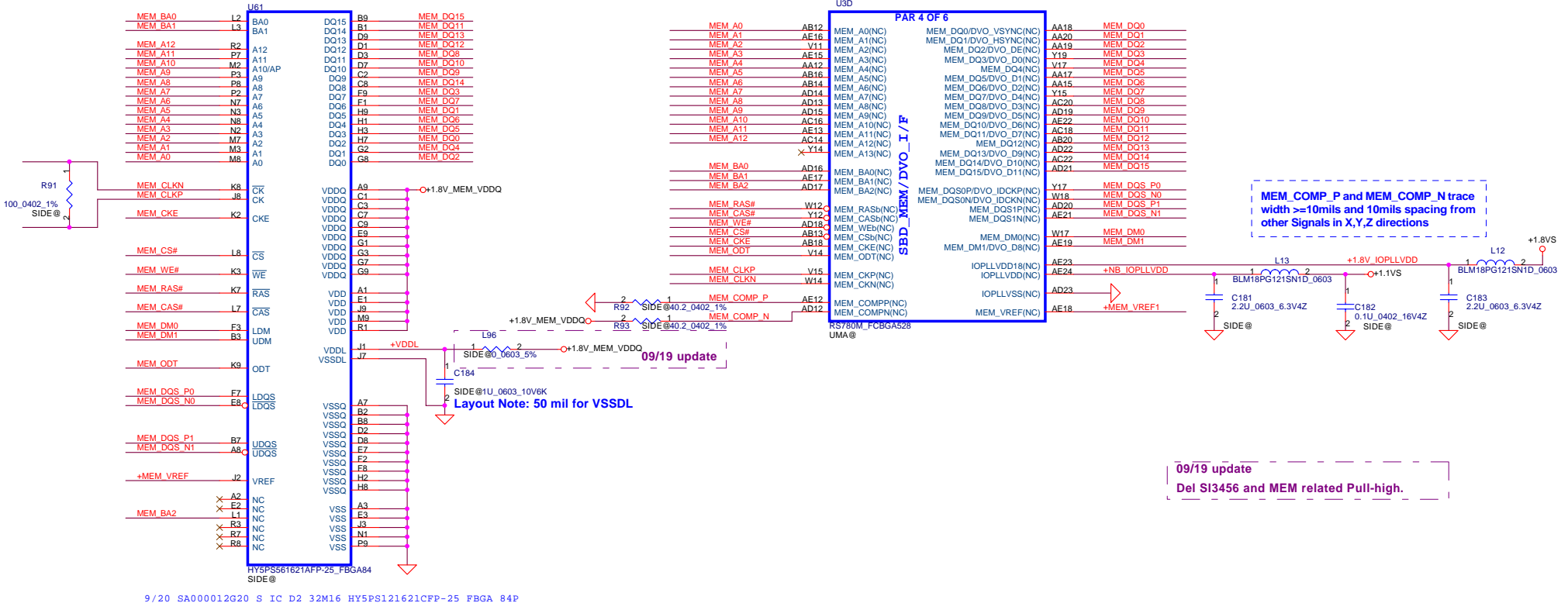
11/13 update

UMA_ENVDD <17>
ENBK <33>
PA_RS780A4
placement close to NB ball

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		2008/08/02
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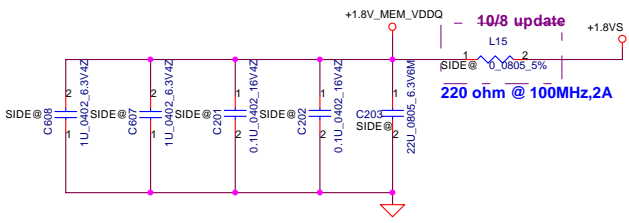
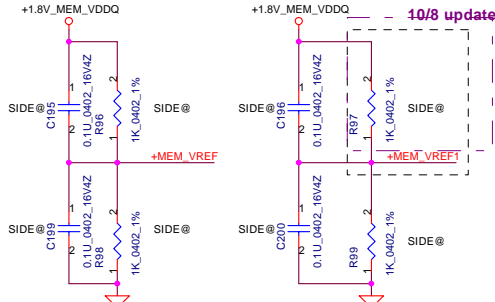
Title			
Compal Electronics, Inc.			
RS780 VEDIO/CLK GEN			
Size	Document Number		Rev
Custom	LA-411P		0.2
Date:	Thursday, November 15, 2007	Sheet	of 47

mailto:netaint@notmail.com

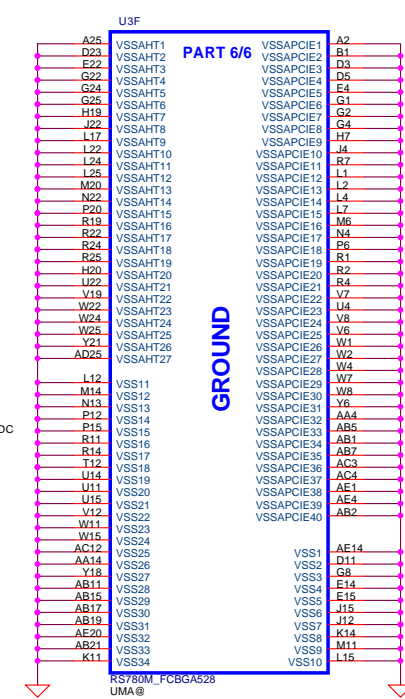
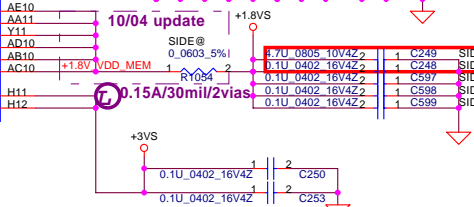
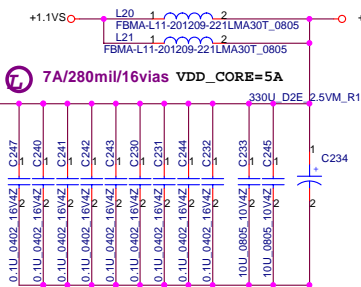
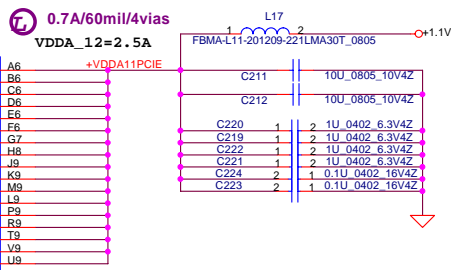
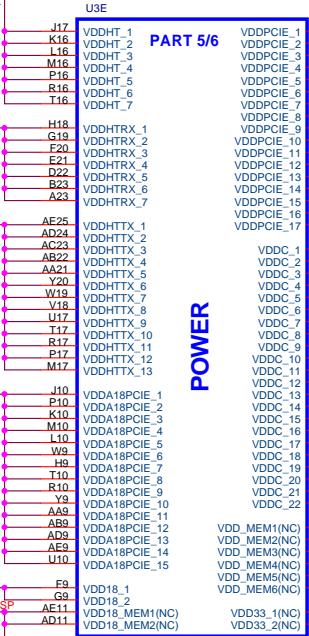
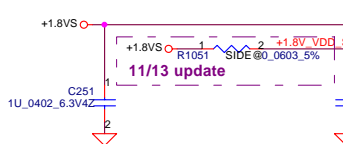
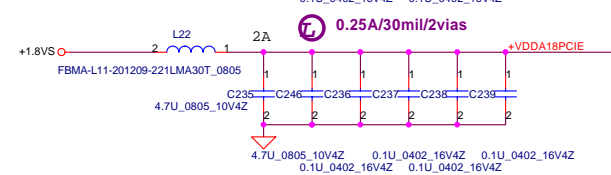
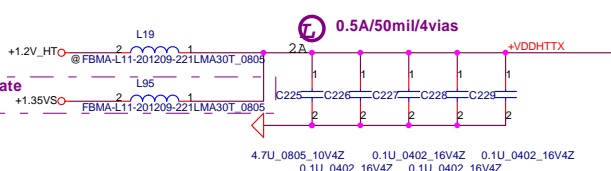
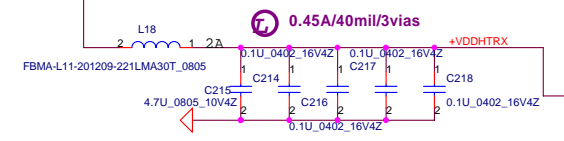
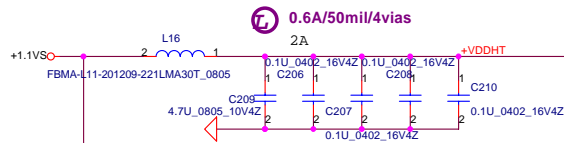


9/20 SA000012G20 S IC D2 32M16 HY5PS121621CFP-25 FBGA 84P

Side Port disable, VREF need connect to +1.8VS for DDR2



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				RS780 Side-Port DDR2 SDRAM	
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		Custom	LA-4111P	0.2	
Date:	Thursday, November 15, 2007	Sheet	12	of	47



09/19 update

11/13 update

10/8 update

Just for RS780M A11 version boot issue

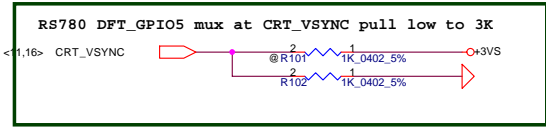
09/19 update

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		2008/08/02

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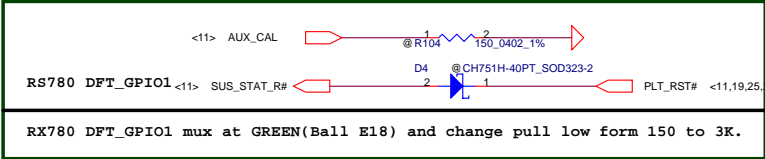
Title		
Compal Electronics, Inc.		
RS780 PWR/GND		
Size	Document Number	Rev
Custom	LA-411P	0.2
Date:	Thursday, November 15, 2007	Sheet of 47

mailto:henzai@hotmail.com



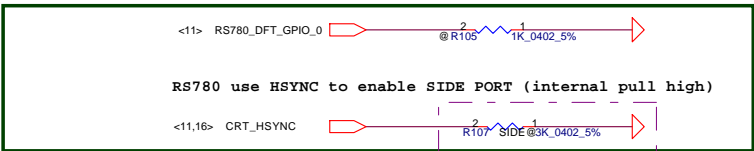
DFT_GPIO5:STRAP_DEBUG_BUS_GPIO_ENABLEb

Enables the Test Debug Bus using GPIO.
 1 : Disable (RS740) Enable (RX780, RS780)
 0 : Enable (Rs740) Disable (RX780, RS780)
 PIN: RS740-->RS780_AUX_CAL; RX780-->NB_TV_C; RS780--> VSYNC#



DFT_GPIO1: LOAD_EEPROM_STRAPS

Selects Loading of STRAPS from EPROM
 1 : Bypass the loading of EEPROM straps and use Hardware Default Values
 0 : I2C Master can load strap values from EEPROM if connected, or use default values if not connected
 RS740/RX780: DFT_GPIO1 RS780:SUS_STAT

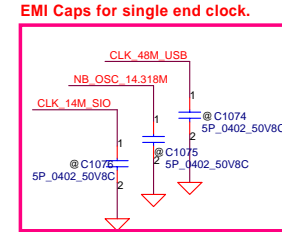
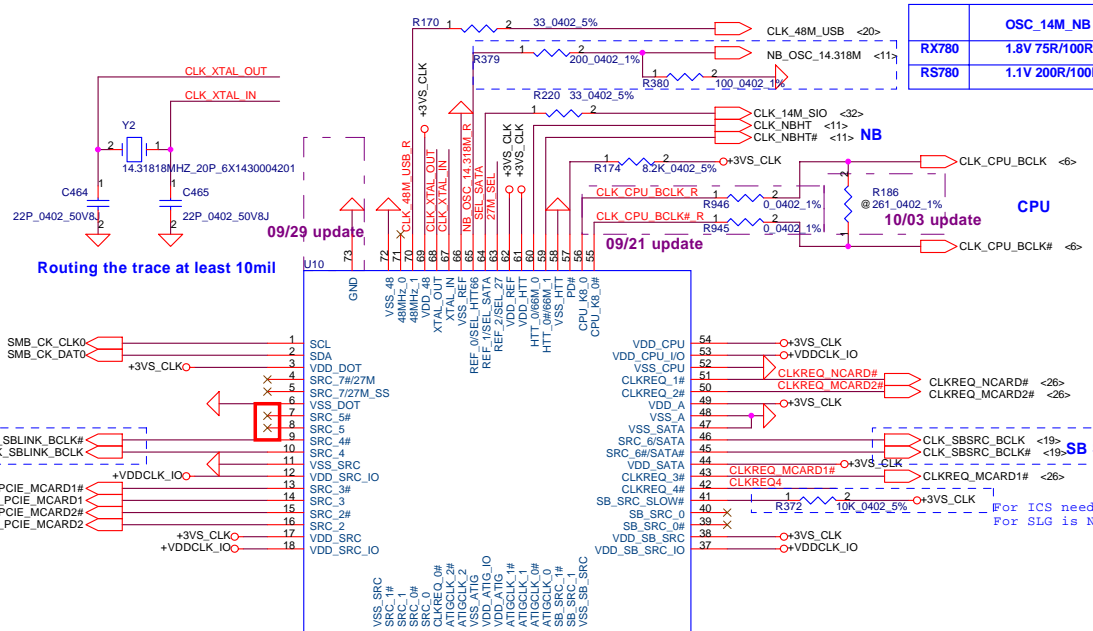
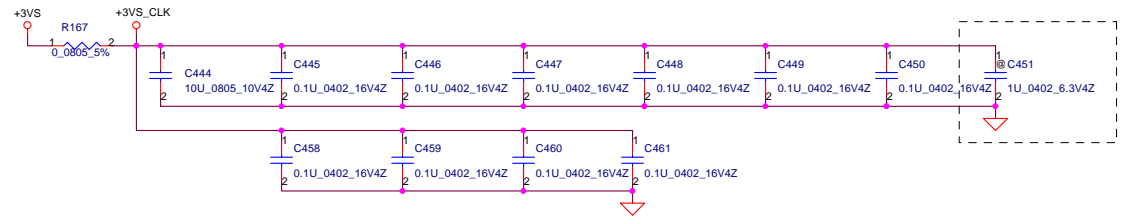
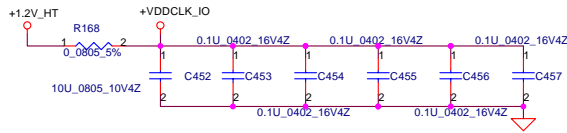


DFT_GPIO0: STRAP_DEBUG_BUS_PCIE_ENABLEb

RX780: Enables the Test Debug Bus using PCIE bus
 1 : Disable (Can still be enabled using nbcfg register access)
 0 : Enable

RS740/RS780: Enables Side port memory (RS780 use HSYNC#)
 1. Disable (RS740/RS780)
 0 : Enable (RS740/RS780)

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Issued Date	2007/08/02	Deciphered Date	2008/08/02	RS780 STRAPS	
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SEL_SATA	1	configure as SATA output
	0	configure as normal SRC(SRC_6) output
	*	default

27M_SEL	1	configure as 27M and 27M_SS output
	0	configure as SRC_7 output
	*	default

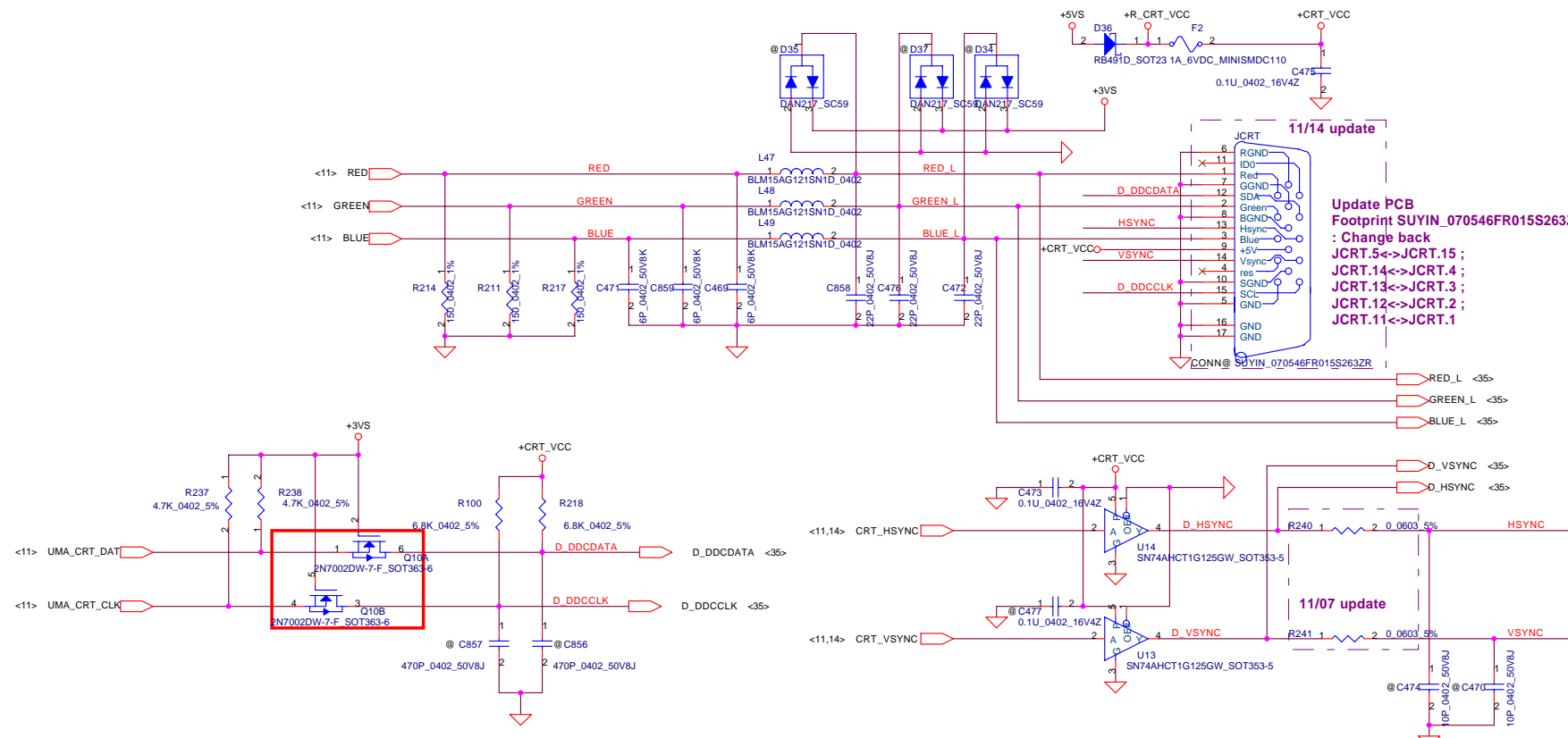
Use voltage divider resistor R379 & R380 to pull low

NB_OSC_14.318M	1	configure as single-ended 66MHz output
	0	configure as differential 100MHz output
	*	default

CLKREQ_NCARD#	1	8.2K_0402_5%	+3VS_CLK
CLKREQ_MCARD2#	1	8.2K_0402_5%	
CLKREQ_MCARD1#	1	8.2K_0402_5%	
CLKREQ_LAN#	1	8.2K_0402_5%	
CLKREQ_Q4	1	8.2K_0402_5%	

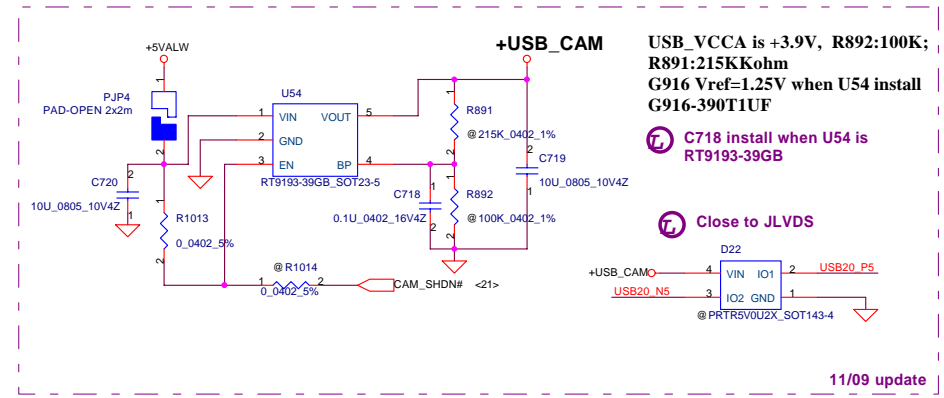
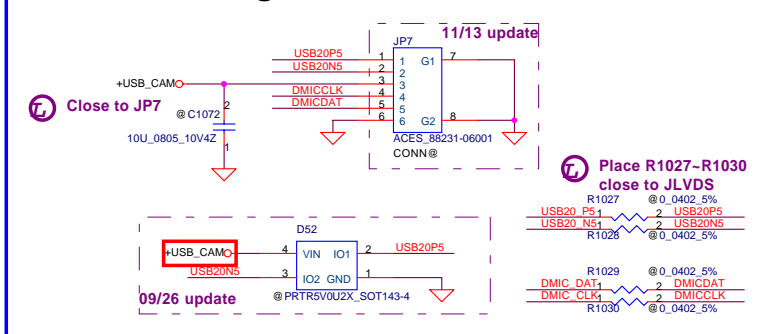
NB CLOCKS	RX780	RS780
HT_REFLCLKP	100M DIFF	100M DIFF
HT_REFLCKN	100M DIFF	100M DIFF
REFCLK_P		vref
REFCLK_N	14M SE (1.8V)	14M SE (1.1V)
GFX_REFLCLK	100M DIFF	100M DIFF(IN/OUT)

CRT CONNECTOR



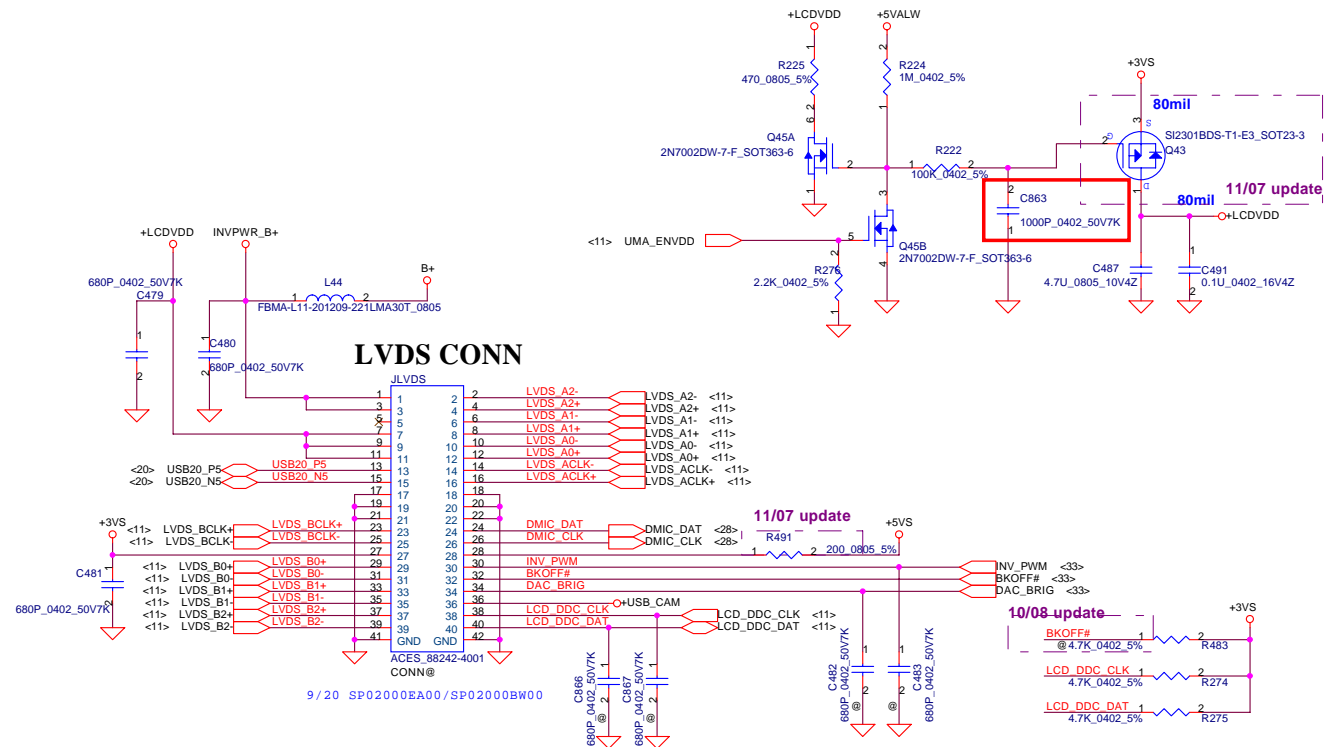
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				Date:	Thursday, November 15, 2007	Sheet 16 of 47

WebCam+Digital Mic Reserve

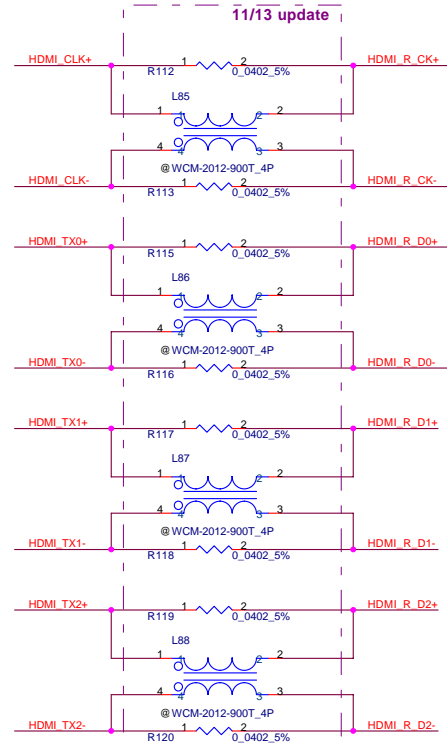
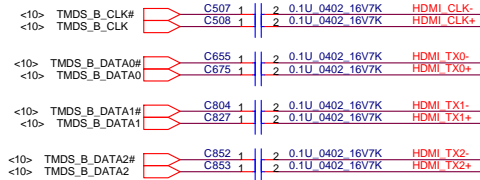
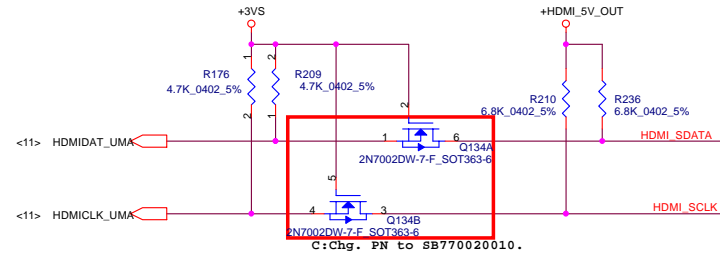
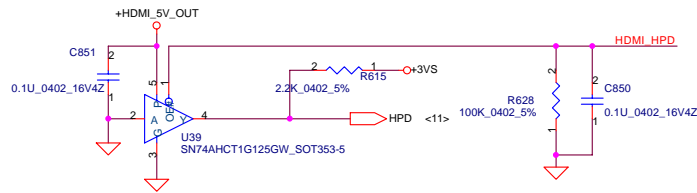


LVDS A2-	C1056	1	2	@10P_0402_50V8J	LVDS A2+
LVDS A1-	C1057	1	2	@10P_0402_50V8J	LVDS A1+
LVDS A0-	C1058	1	2	@10P_0402_50V8J	LVDS A0+
LVDS ACLK-	C1059	1	2	@10P_0402_50V8J	LVDS ACLK+
LVDS B2-	C1060	1	2	@10P_0402_50V8J	LVDS B2+
LVDS B1-	C1061	1	2	@10P_0402_50V8J	LVDS B1+
LVDS B0-	C1062	1	2	@10P_0402_50V8J	LVDS B0+
LVDS BCLK-	C1063	1	2	@10P_0402_50V8J	LVDS BCLK+

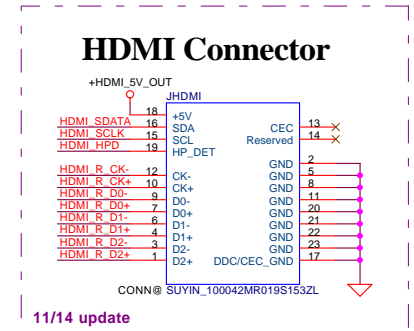
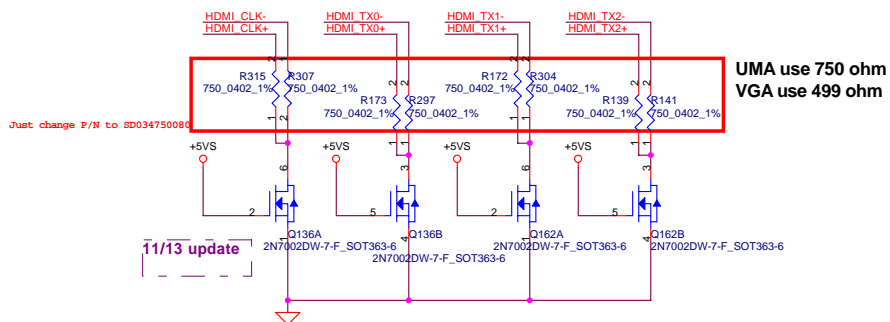
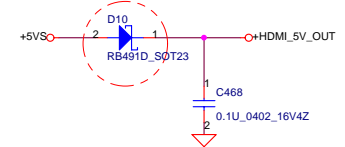
09/19 update



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<p>Compal Electronics, Inc.</p> <p>LCD CONN. / WebCam</p>		<p>Document Number</p> <p>LA-411P</p>	<p>Rev</p> <p>0.2</p>
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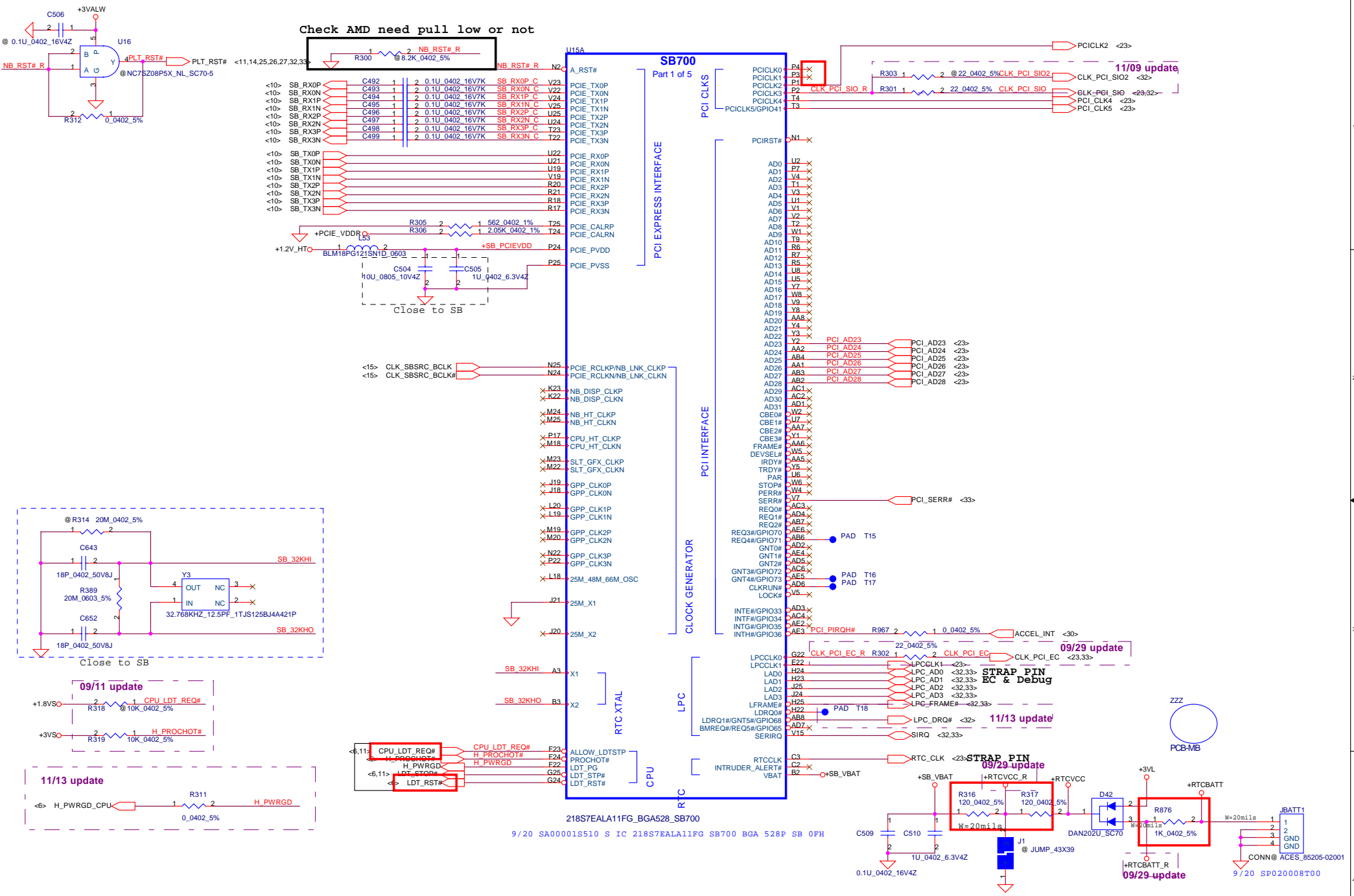


MP:Update D10 to meet HDMI.



Change PCB Footprint from SW_WCM2012F2S_4P to KING_WCM-2012-900T_4P

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Size	Document Number	Date:		Sheet	Rev
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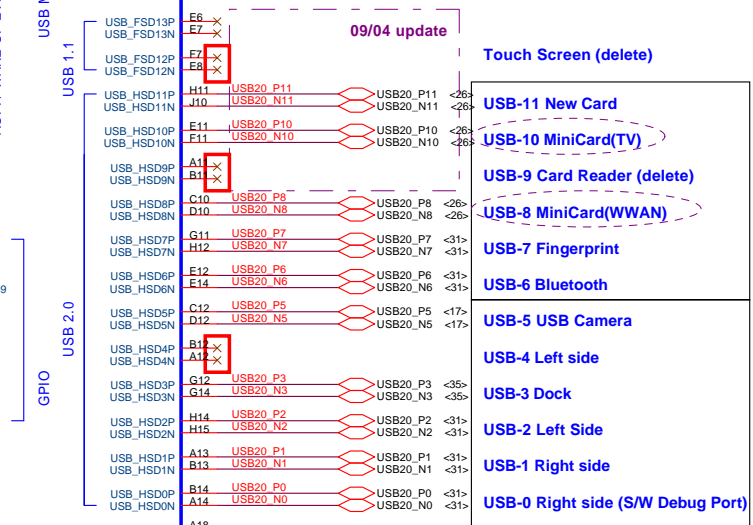
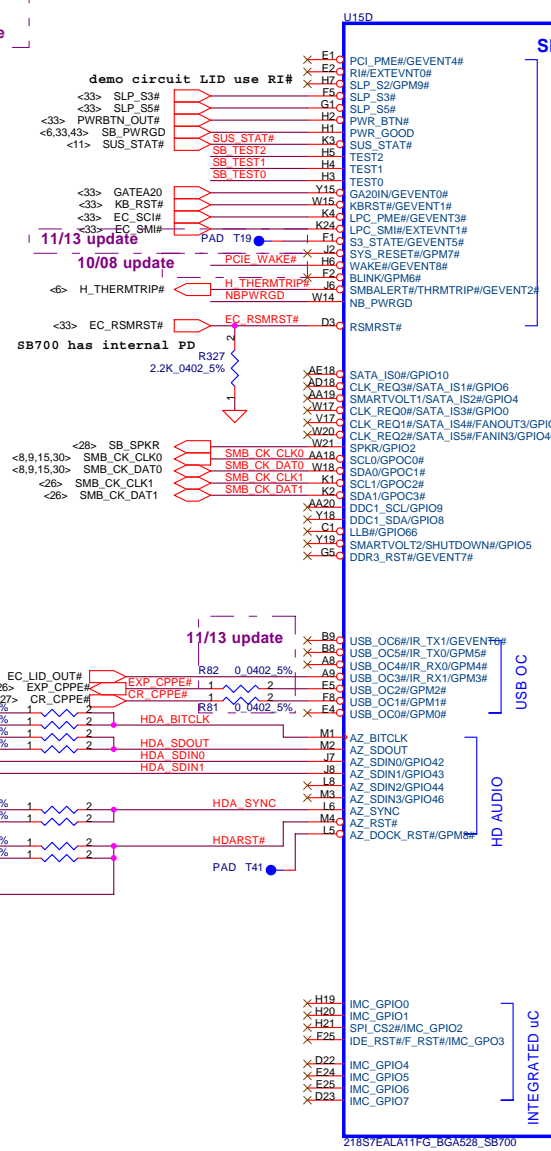
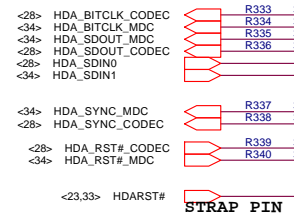
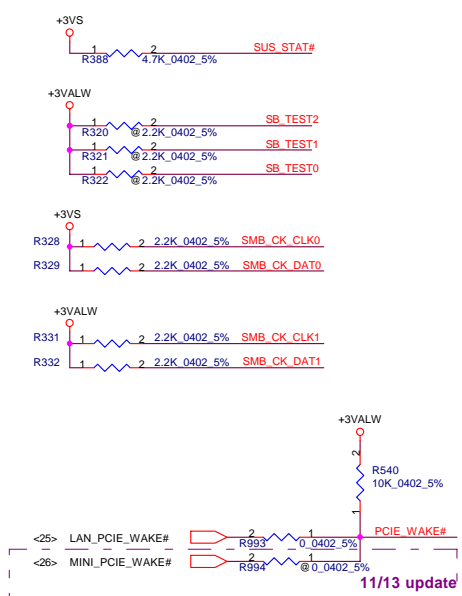
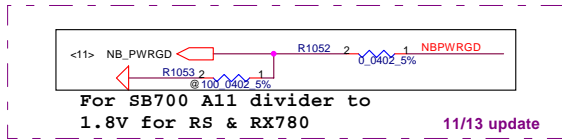


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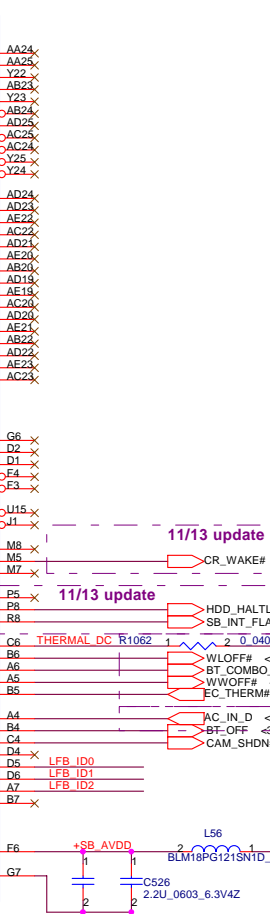
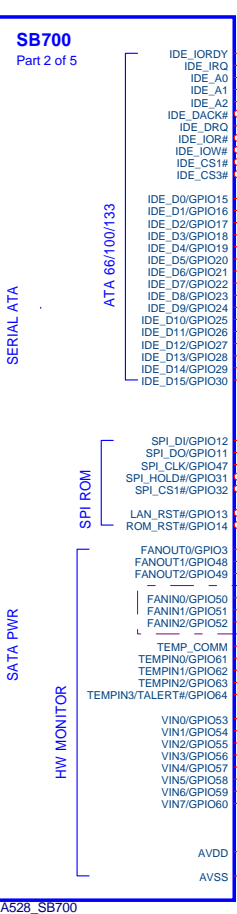
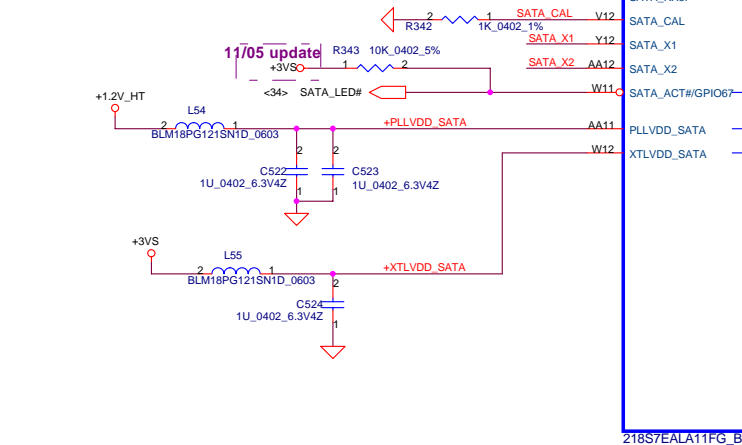
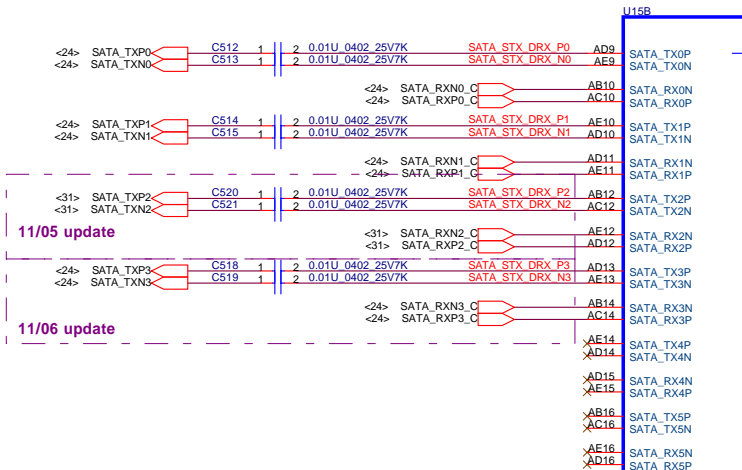
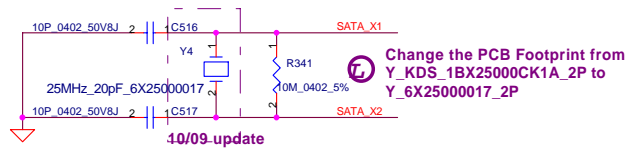
Title		
Compal Electronics, Inc.		
SB700-PCI/PCI/ACPI/LPC/RTC		
Size	Document Number	Rev
Custom	LA-411P	0.2
Date:	Thursday, November 15, 2007	Sheet of 47

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- Touch Screen (delete)
- USB-11 New Card
- USB-10 MiniCard(TV)
- USB-9 Card Reader (delete)
- USB-8 MiniCard(WWAN)
- USB-7 Fingerprint
- USB-6 Bluetooth
- USB-5 USB Camera
- USB-4 Left side
- USB-3 Dock
- USB-2 Left Side
- USB-1 Right side
- USB-0 Right side (S/W Debug Port)

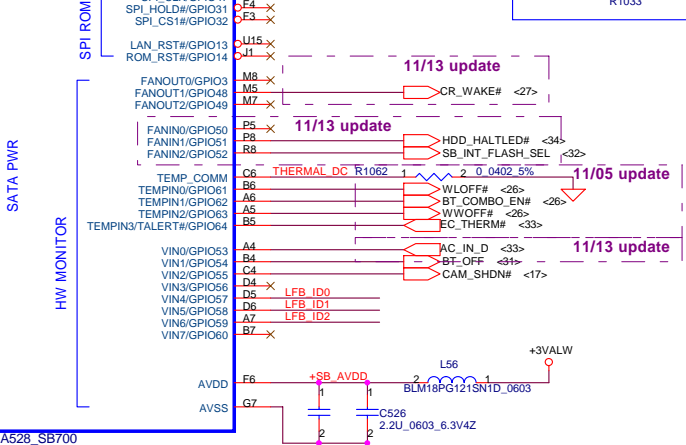
Security Classification	Compal Secret Data		Title	
Issued Date	2007/08/02	Deciphered Date	2008/08/02	Compal Electronics, Inc.
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Date:	Thursday, November 15, 2007	Sheet	20 of 47	



Local Frame Buffer Strapping List
Copy from Becks.

	LFB_ID2	LFB_ID1	LFB_ID0
Hynix	0	0	0
Qimonda	0	0	1
Samsung	0	1	0

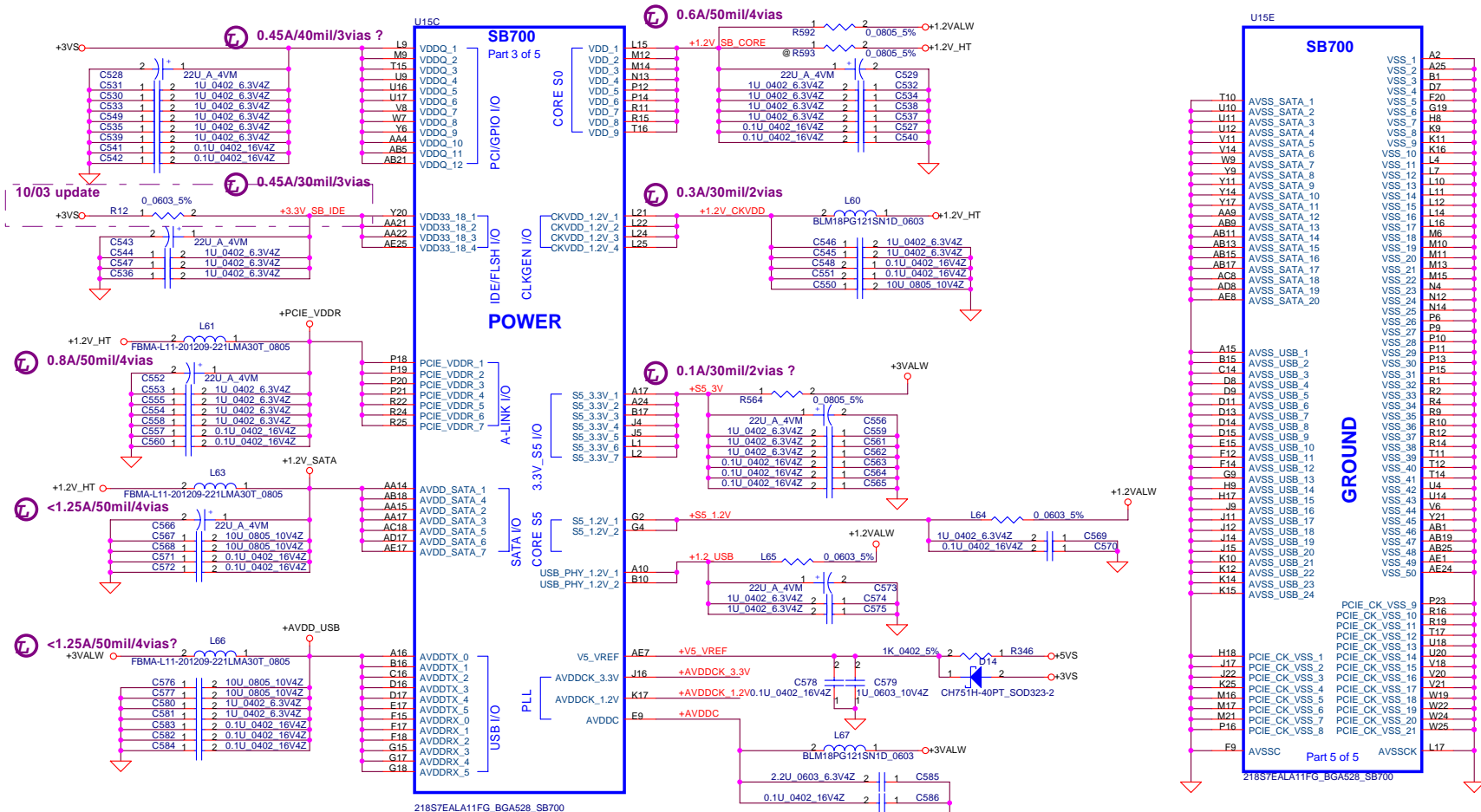
LFB_ID0 to LFB_ID2 got internal PU 10K to S5.



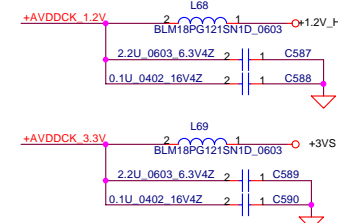
Security Classification	Compal Secret Data	
Issued Date	2007/08/02	Deciphered Date 2008/08/02
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Size			SB700 SATA/IDE/SPI	
Custom	Document Number	LA-411P	Rev	0.2
Date:	Thursday, November 15, 2007	Sheet	of	47

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218S7EAL11FG_BGA528_SB700



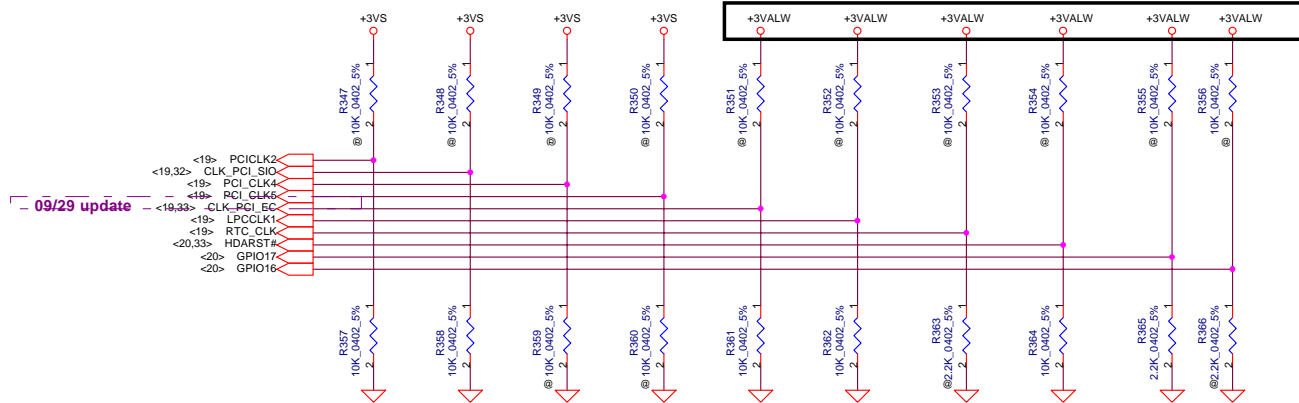
Security Classification	Compal Secret Data	
Issued Date	2007/08/02	Deciphered Date
		2008/08/02
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Title			
Compal Electronics, Inc.			
SB700 PWR/GND			
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REQUIRED STRAPS

NOTE: SB700 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK

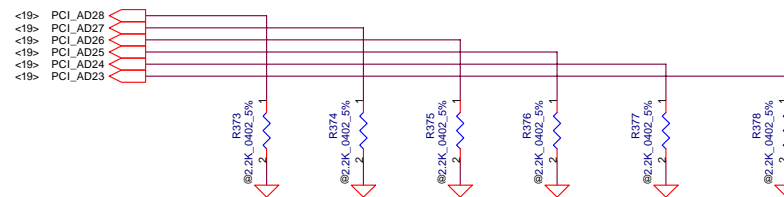
	PCI_CLK2	PCI_CLK3	PCI_CLK4	PCI_CLK5	AZ_RST_CD#	LPC_CLK1	RTC_CLK	LPC_CLK0	GP17	GP16
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	ENABLE PCI MEM BOOT	CLKGEN ENABLED	INTERNAL RTC DEFAULT	EC ENABLED	Internal pull up H,H = Reserved H,L = SPI ROM	
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			DISABLE PCI MEM BOOT DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	EC DISABLED DEFAULT	L,H = LPC ROM (Default) L,L = FWH ROM	



DEBUG STRAPS

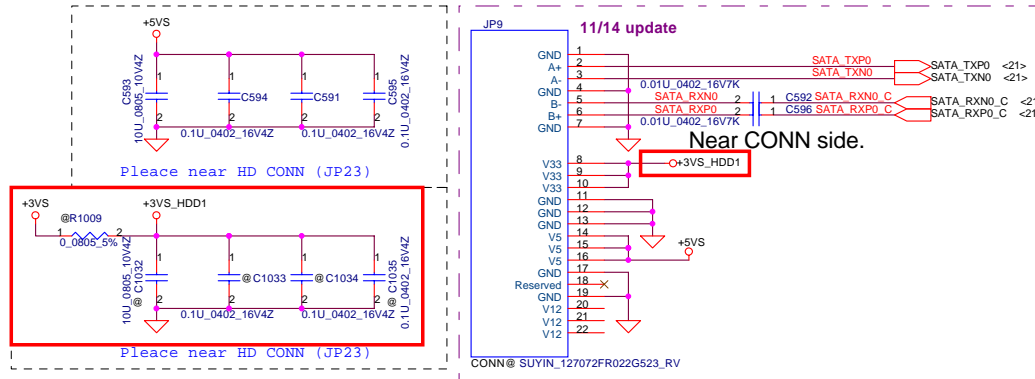
SB700 HAS 15K INTERNAL PU FOR PCI_AD[28:23]

	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	RESERVED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	

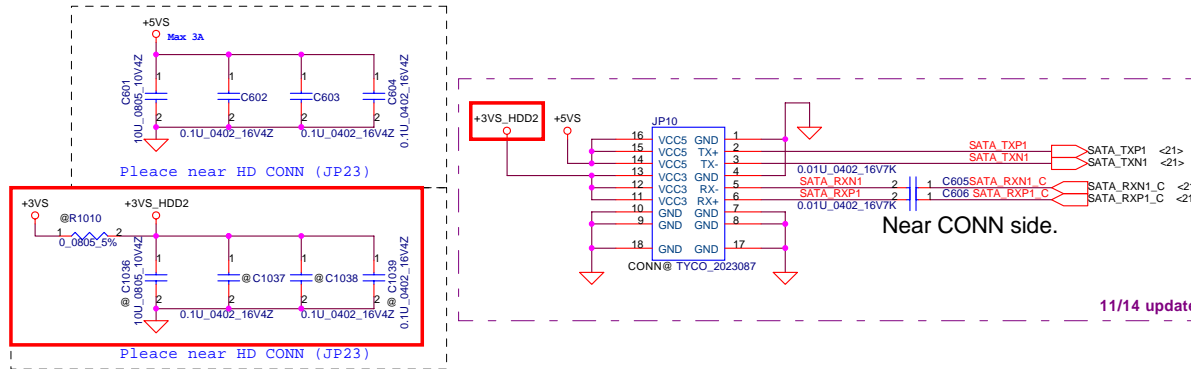


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				Custom	LA-4111P
Date:	Thursday, November 15, 2007	Sheet	of	47	

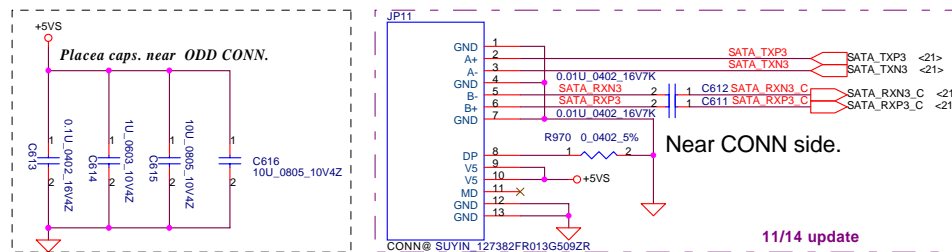
HDD Connector



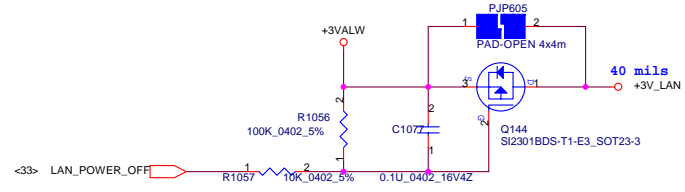
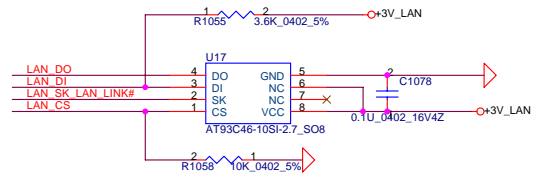
Multi-Bay Connector-option



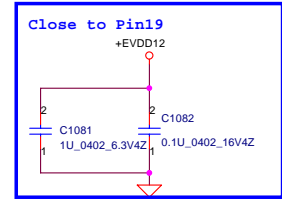
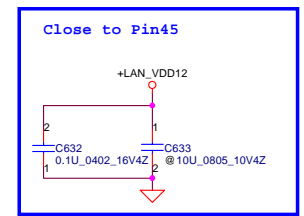
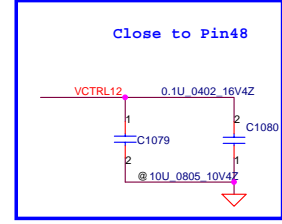
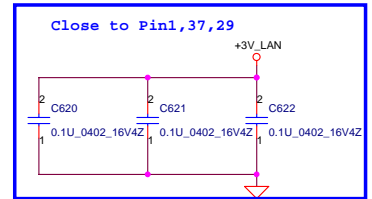
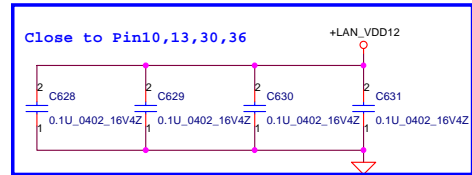
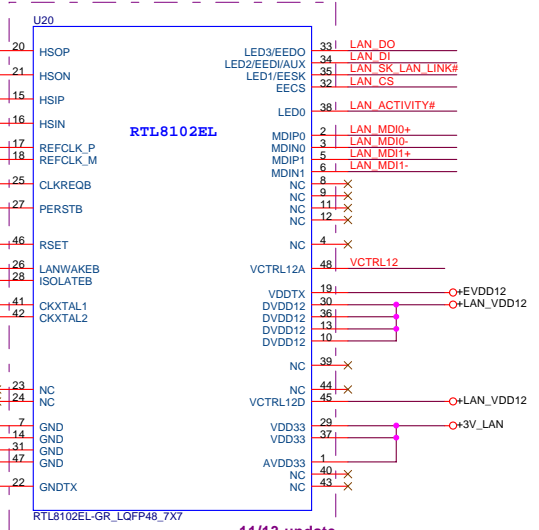
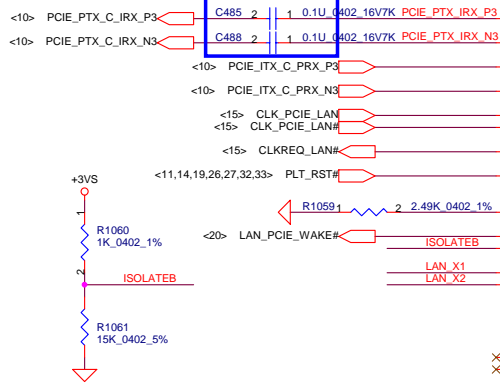
CD-ROM Connector



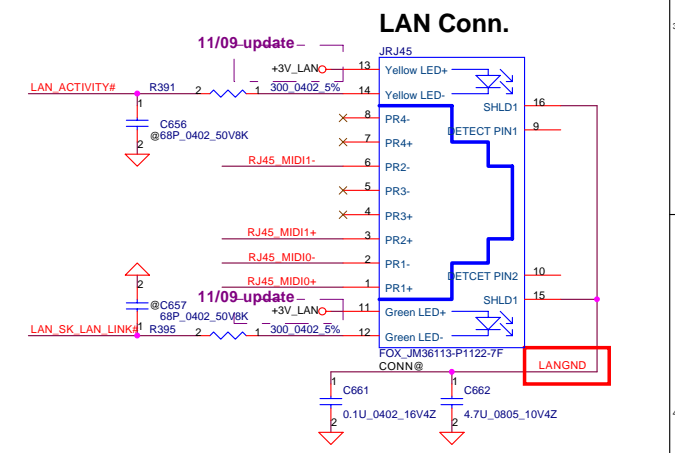
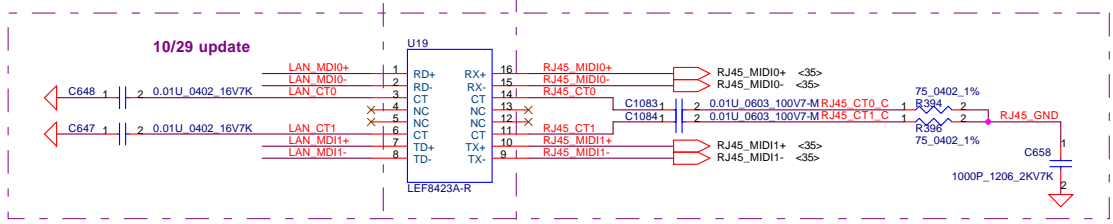
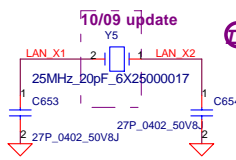
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Place Close to Chip



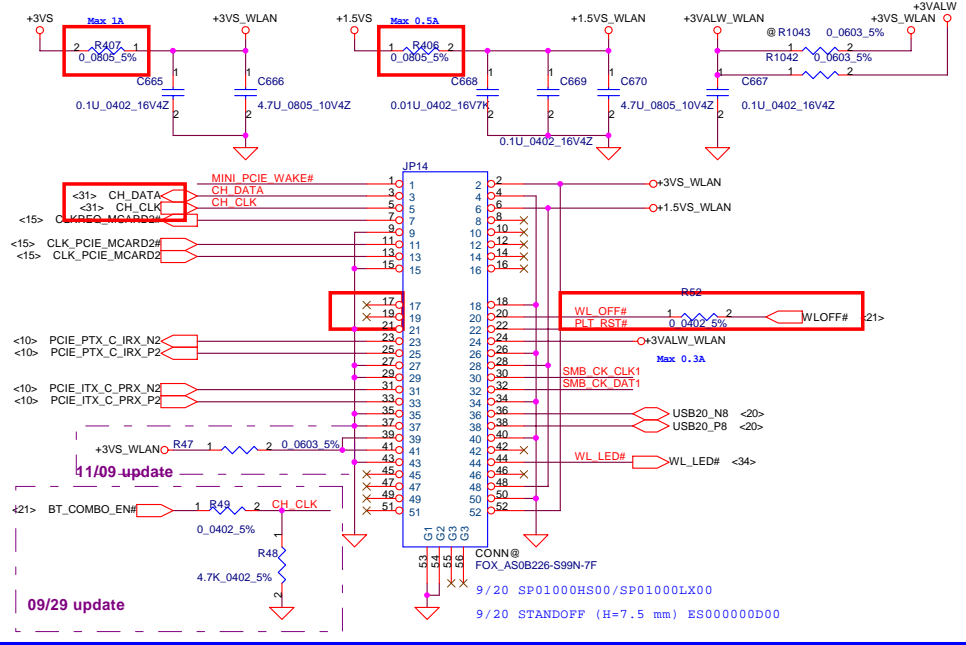
Change the PCB Footprint from Y_KDS_1BX25000CK1A_2P to Y_6X25000017_2P



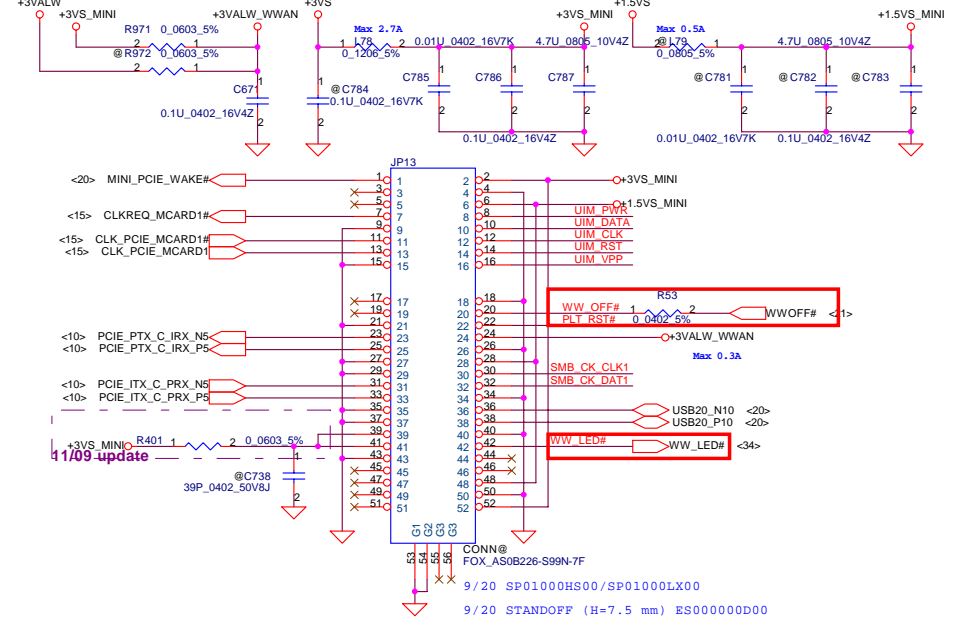
Security Classification	Compal Secret Data	
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Title		Rev	
RTL8111C/8102E 10/100/1000 LAN		0.2	
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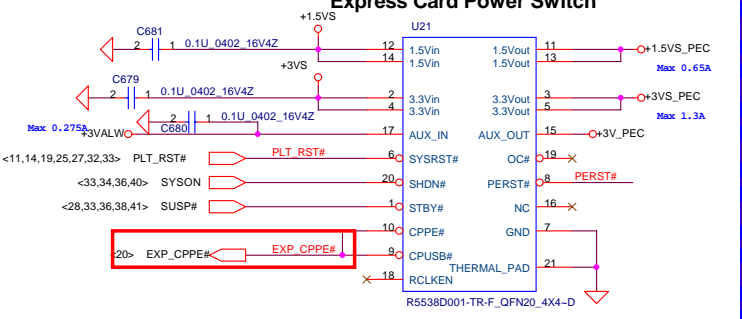
Mini Card Slot 1---WLAN



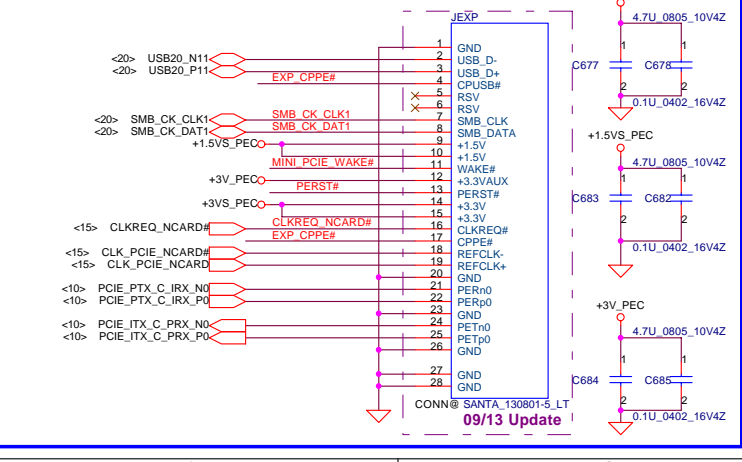
Mini Card Slot 2---TV tuner / WWAN / Robson



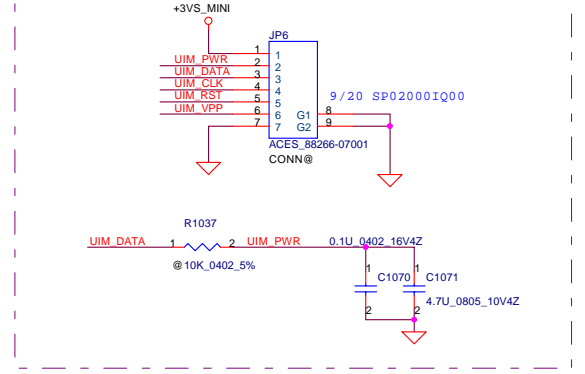
New Card



USE TI TPS2231MRGPR



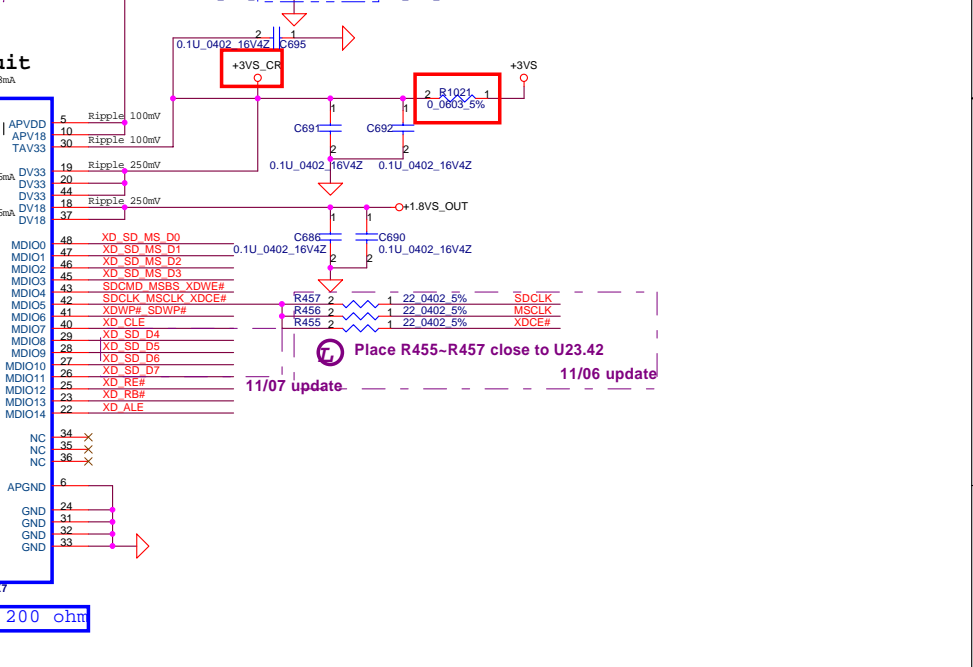
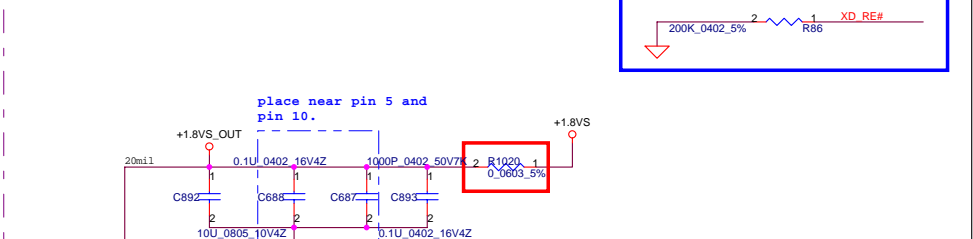
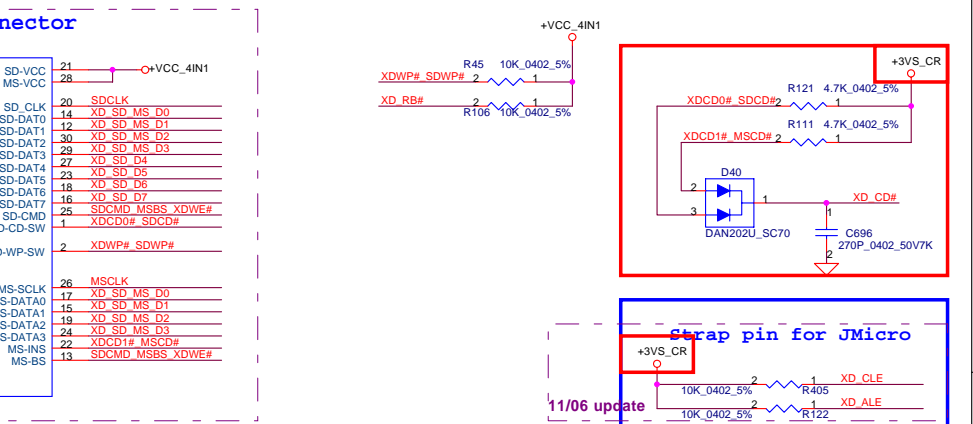
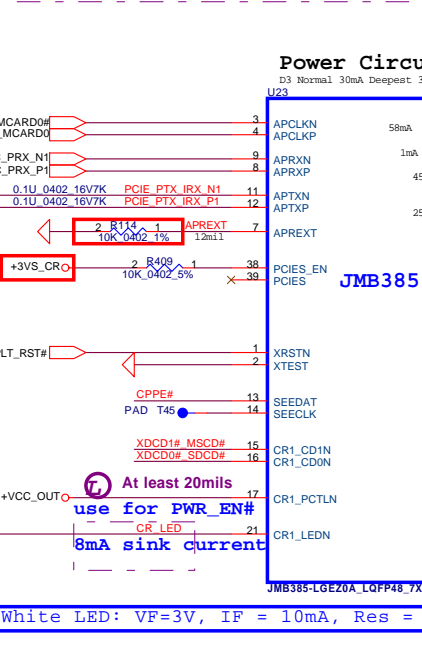
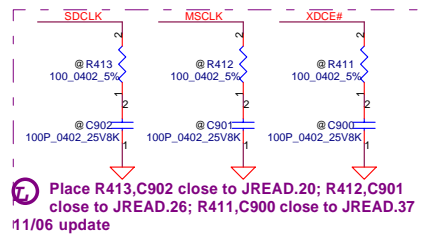
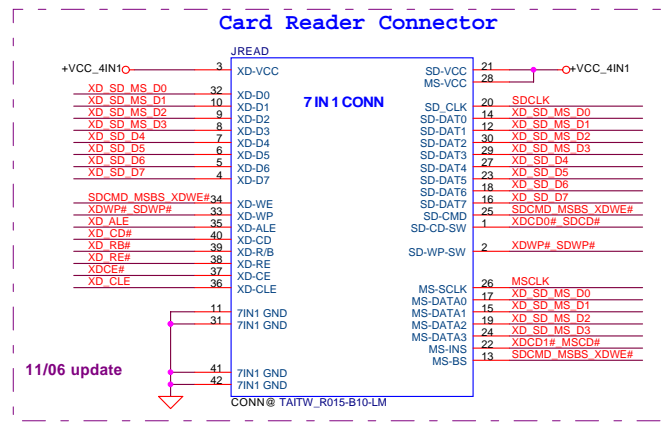
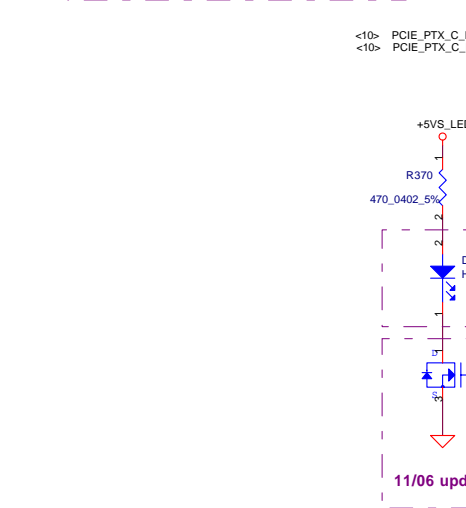
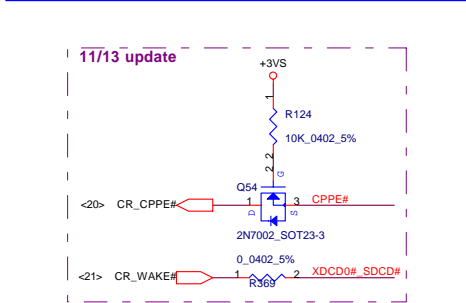
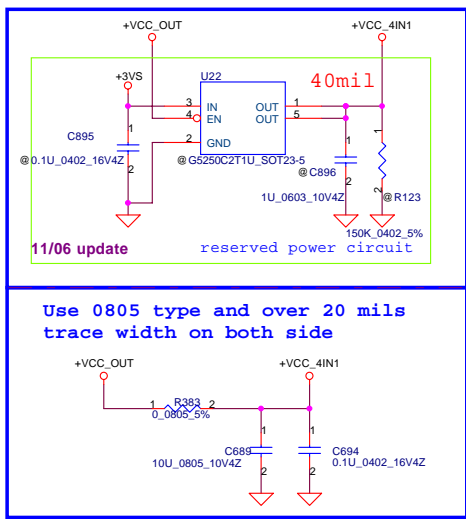
11/09 update



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		2008/08/02

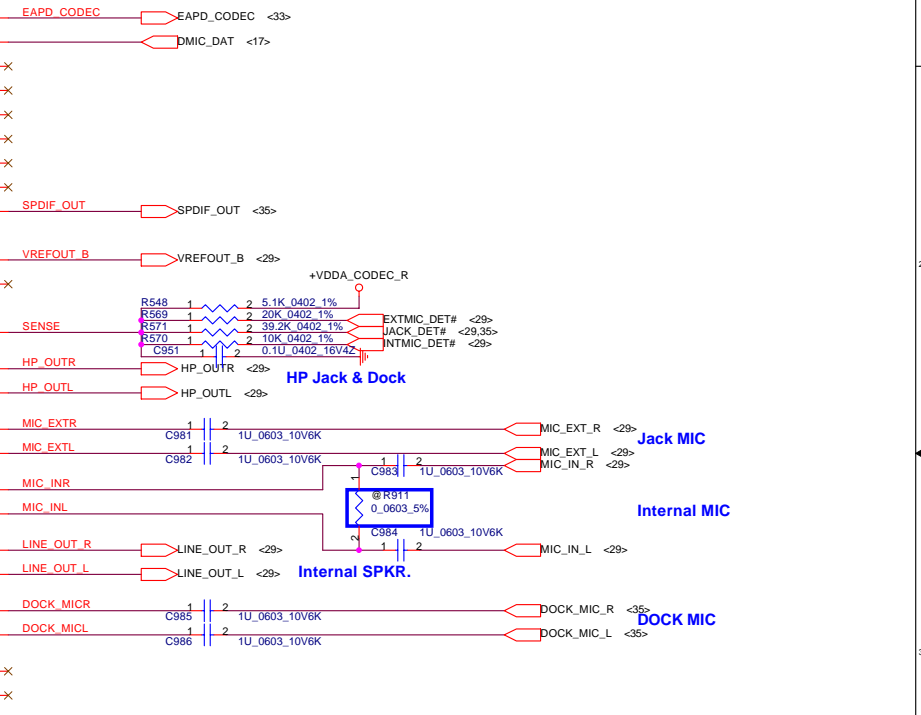
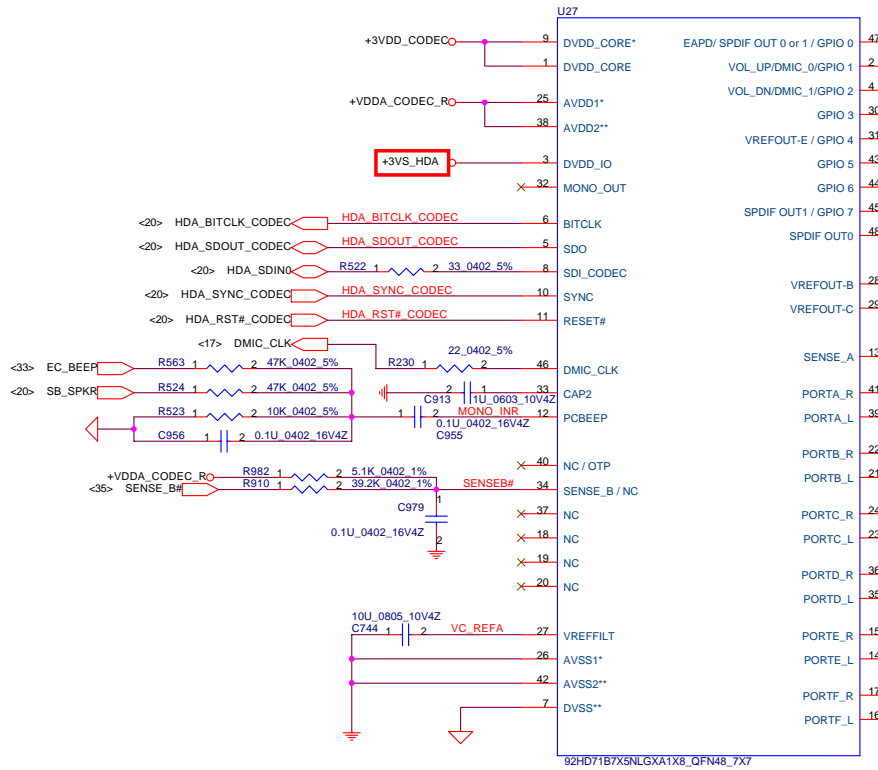
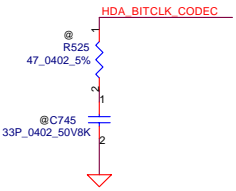
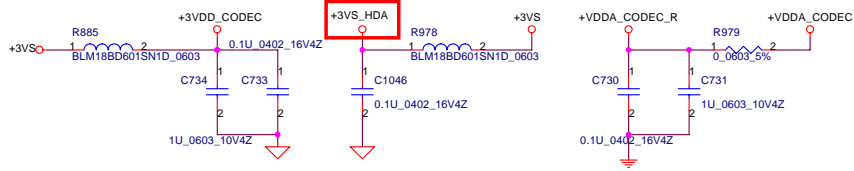
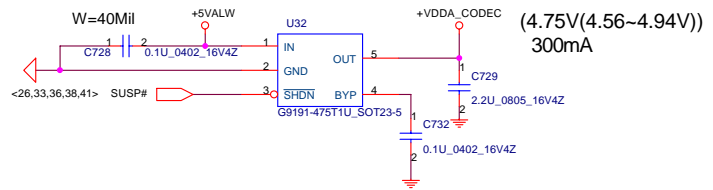
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WLAN/TV tuner/Express Card		
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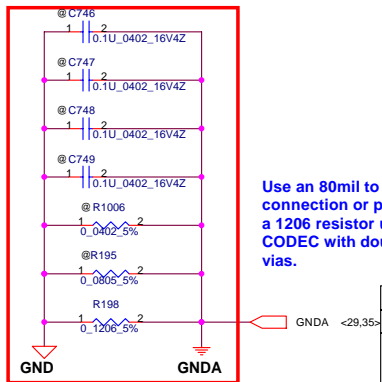


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			Custom	LA-4111P	0.2
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CODEC POWER

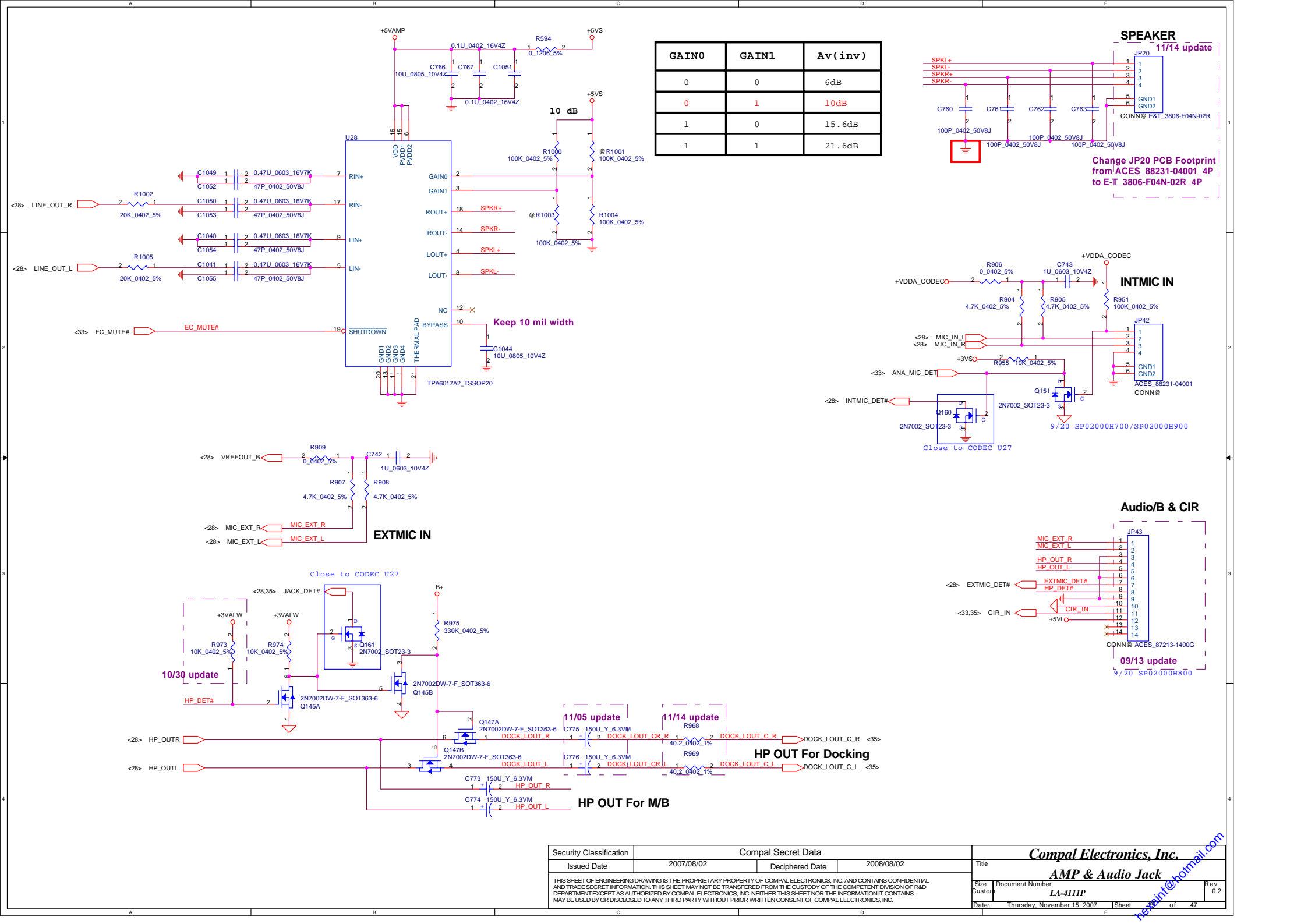


SENSE A		SENSE B	
Port	Resistor	Port	Resistor
A	39.2K	E	39.2K
B	20K	F	20K
C	10K	G	10K
D	5.11K	H	5.11K

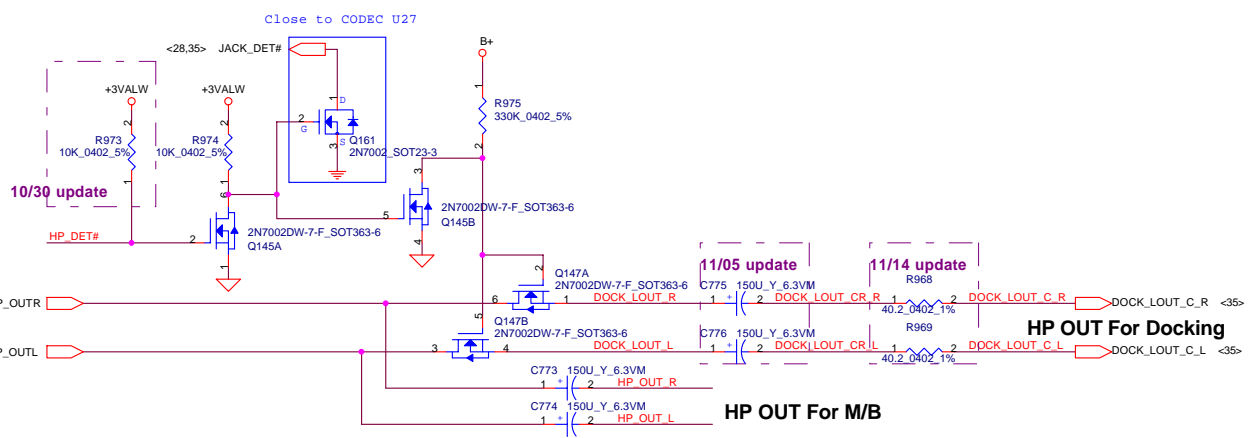
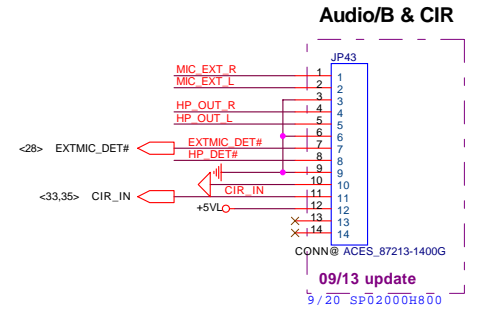
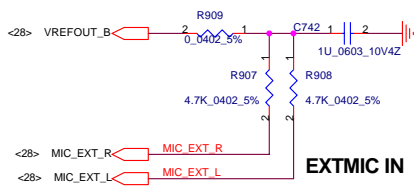
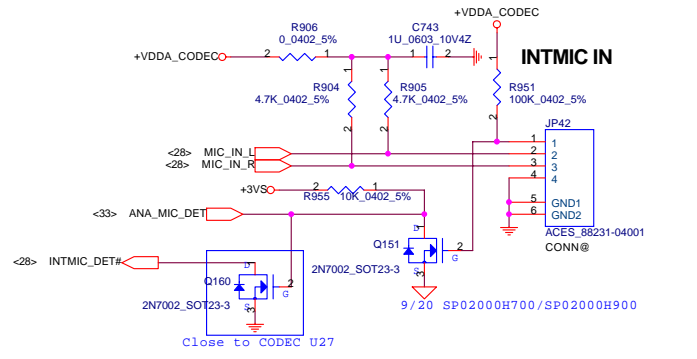
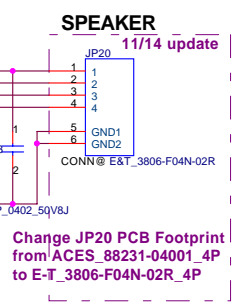


Use an 80mil to connection or place a 1206 resistor under CODEC with double vias.

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Issued Date	2007/08/02	Deciphered Date	2008/08/02	Audio Codec-IDT9271B7	
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GAIN0	GAIN1	Av (inv)
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB



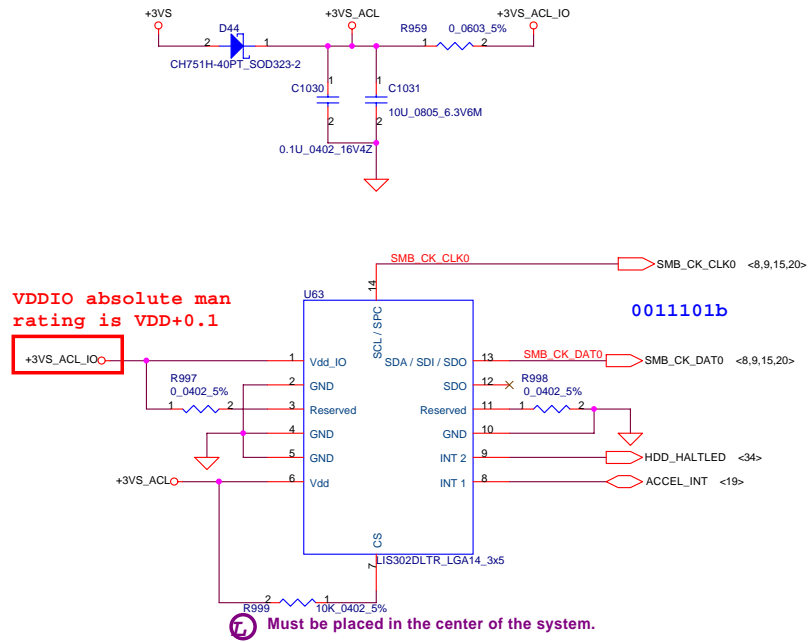
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Issued Date	2007/08/02	Deciphered Date
		2008/08/02

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Title		
Compal Electronics, Inc.		
AMP & Audio Jack		
Size	Document Number	Rev
Custom	LA-411P	0.2
Date:	Thursday, November 15, 2007	Sheet of 47

helenai@hotmail.com

ACCELEROMETER



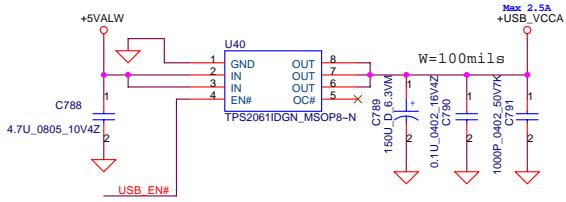
Security Classification	Compal Secret Data	
Issued Date	2007/08/02	Deciphered Date
		2008/08/02

Title		Compal Electronics, Inc.
		Accelerometer

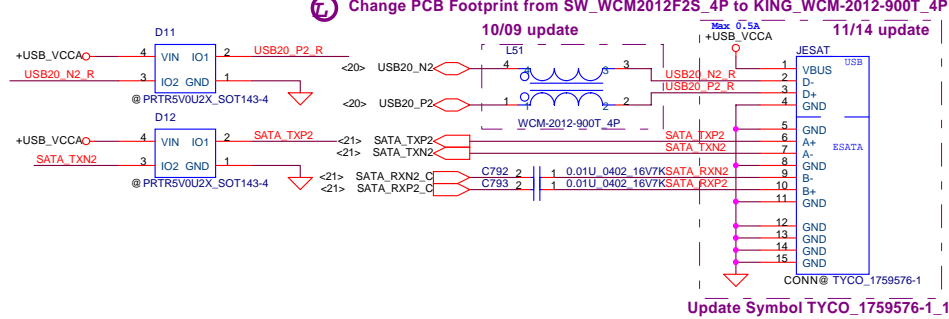
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Size	Document Number	Rev
Custom	LA-4111P	0.2
Date:	Thursday, November 15, 2007	Sheet 30 of 47

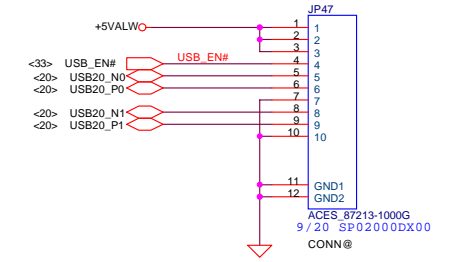
Left side USB CONNECTOR



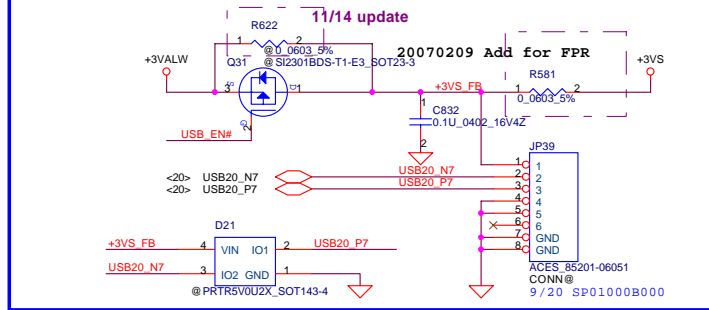
Left side eSATA5/USB2 combination Connector



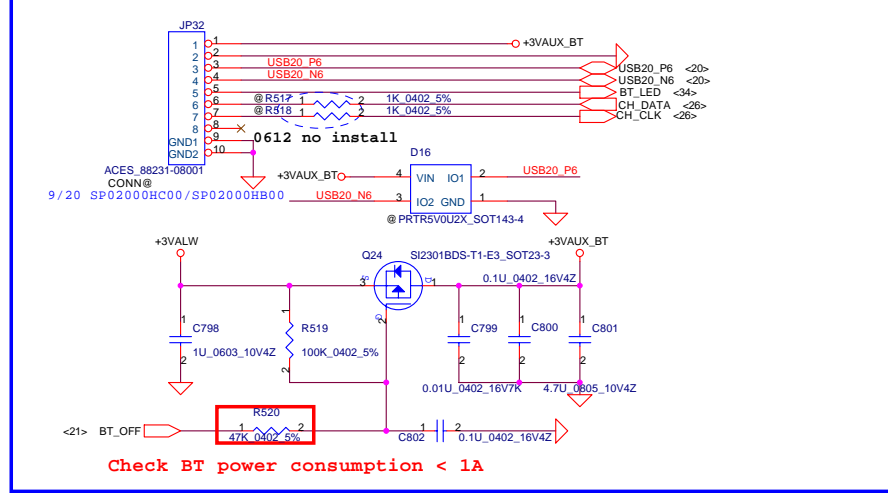
Right side USB 0&1 Board Conn



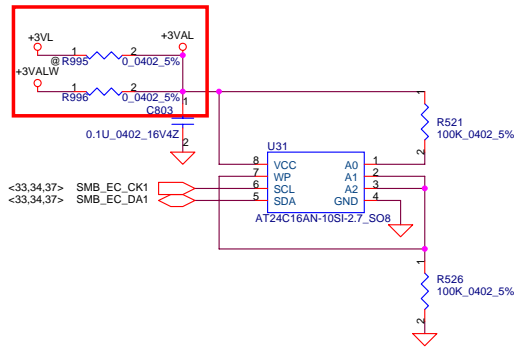
Finger printer



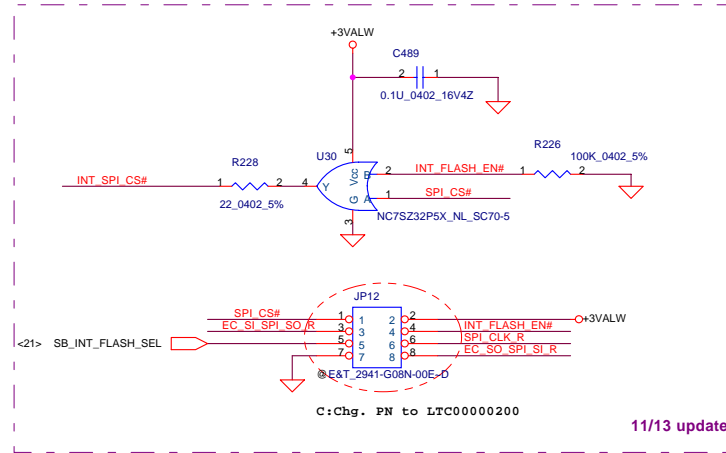
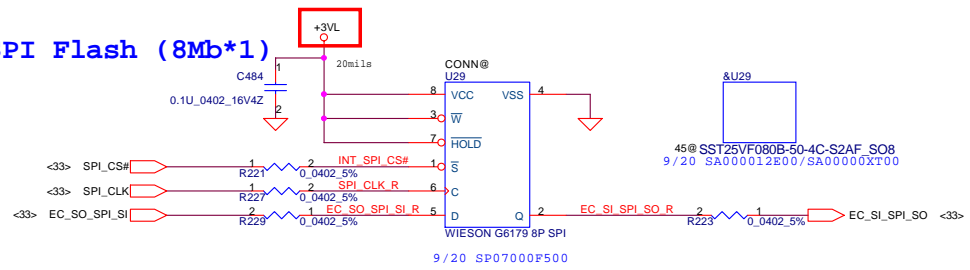
BT Connector



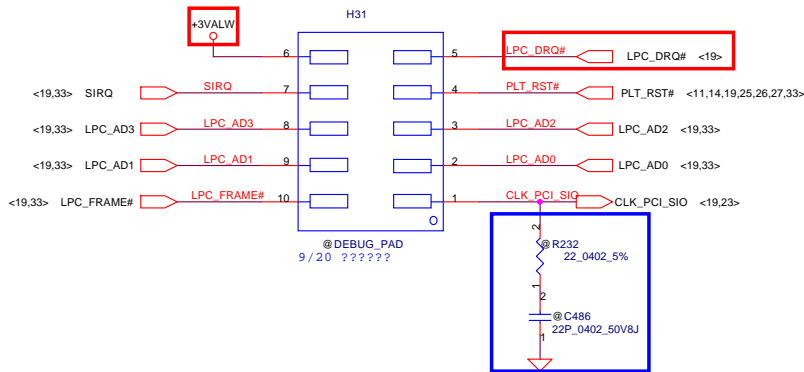
Security Classification	Compal Secret Data		Title	Compal Electronics, Inc.	
Issued Date	2007/08/02	Deciphered Date	2008/08/02	USB, BT, eSATA, FPR	
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			Custom	LA-4111P	0.2
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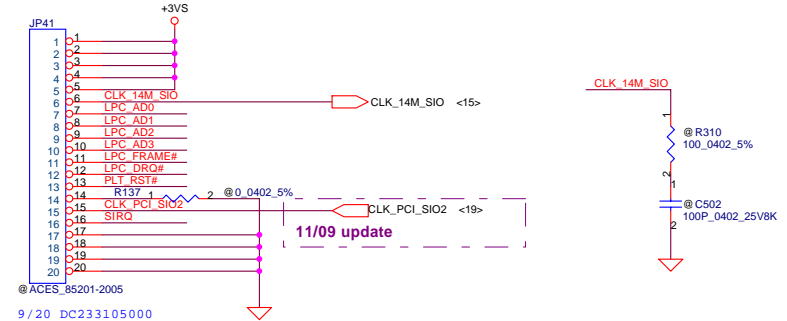
SPI Flash (8Mb*1)



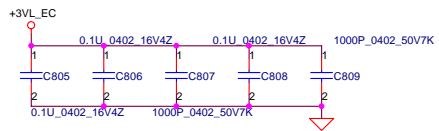
LPC Debug Port



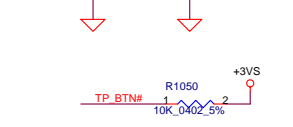
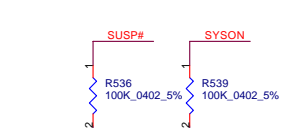
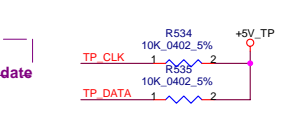
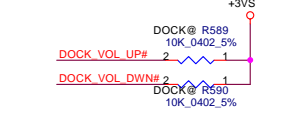
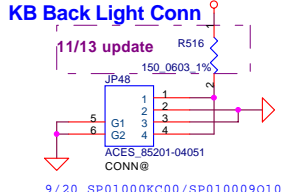
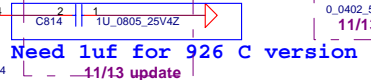
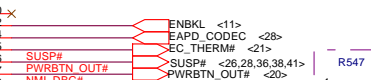
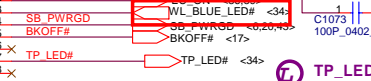
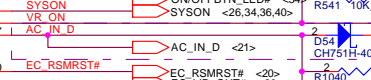
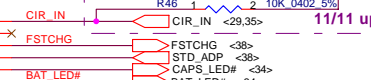
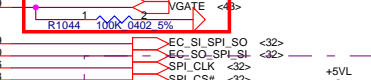
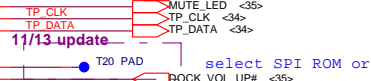
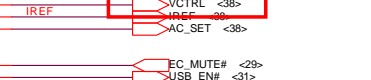
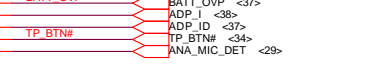
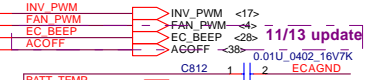
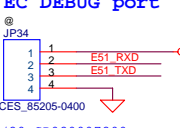
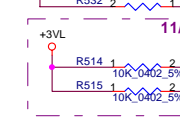
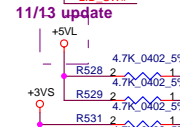
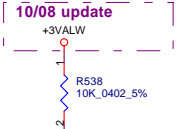
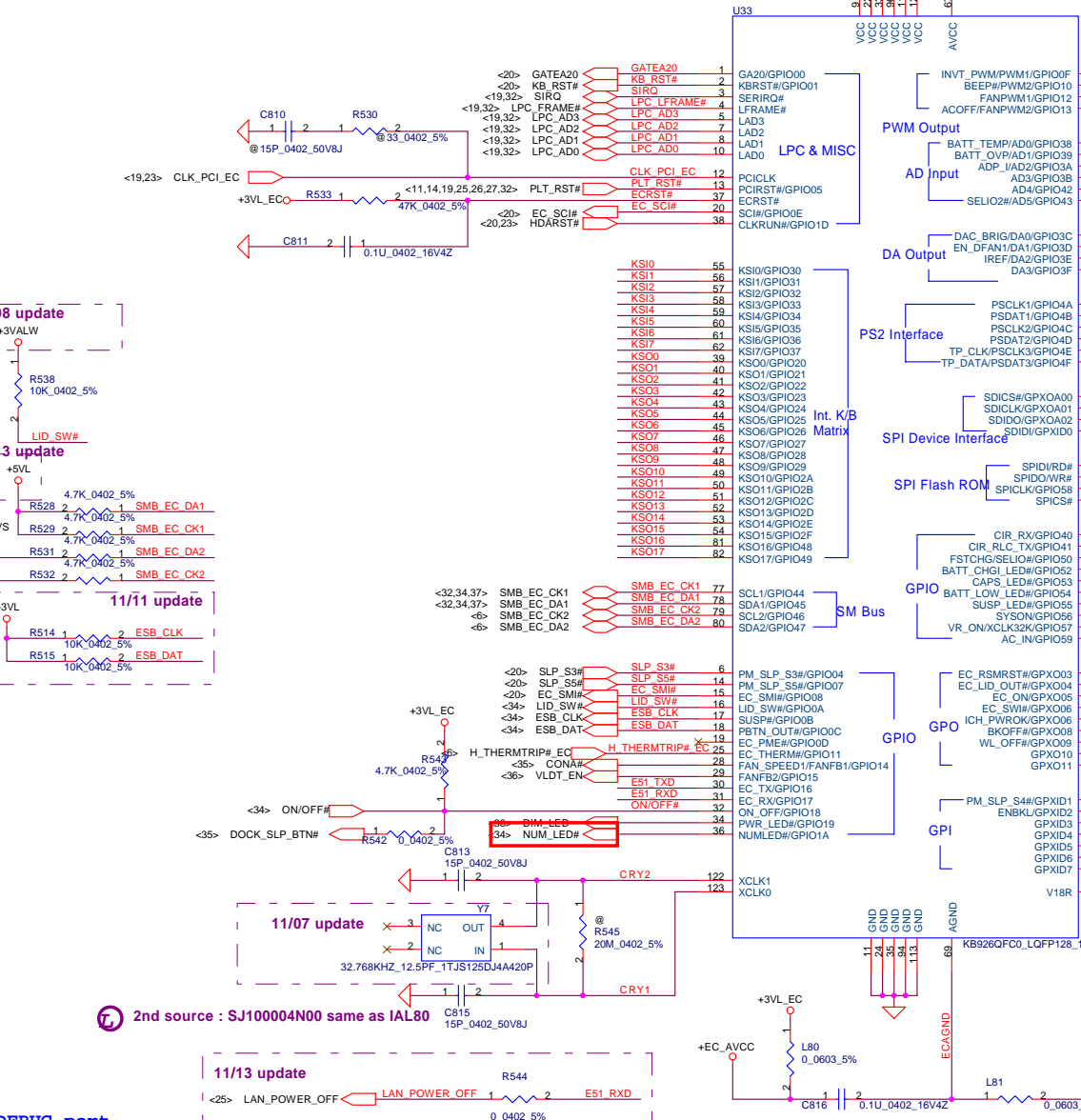
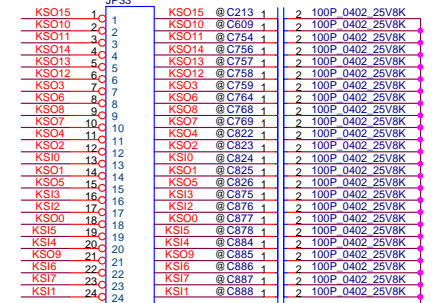
LPC Debug Port



Security Classification		Compal Secret Data		Title	
Issued Date	2007/08/02	Deciphered Date	2008/08/02	BIOS ROM/Debug Tool	
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Keyboard Connector For EMI



2nd source : SJ100004N00 same as IAL80

LAN_POWER_OFF

Need luf for 926 C version

TP_LED#=L, T/P disable
TP_LED#=float (GPO), T/P enable

Security Classification	Compal Secret Data	
Issued Date	2007/08/02	Deciphered Date
		2008/08/02

Title		Compal Electronics, Inc.	
		EC KB926/KB conn	
Size	Document Number	LA-411P	Rev
Custom			0.2
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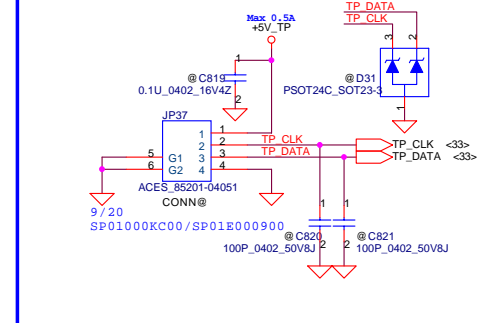
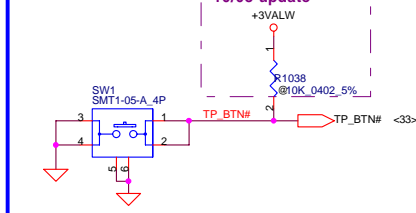
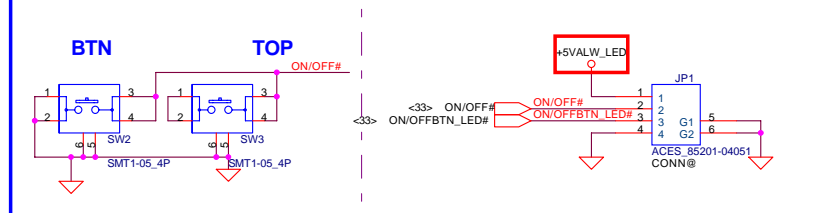
for debug only

ON/OFF Button Connector

TP ON/OFF

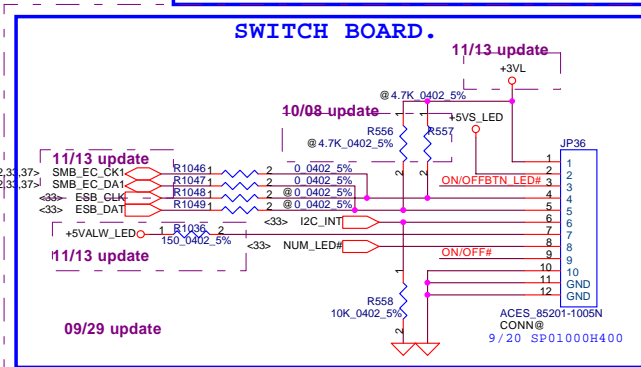
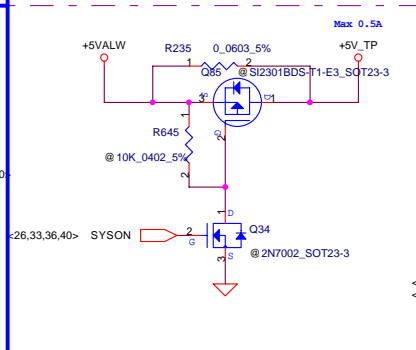
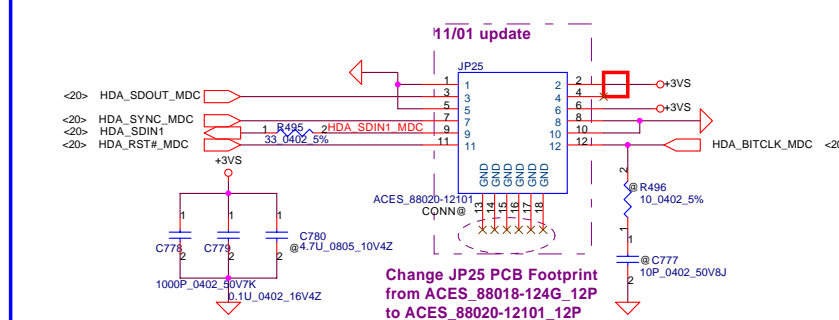
10/08 update

M/B TO TP/B



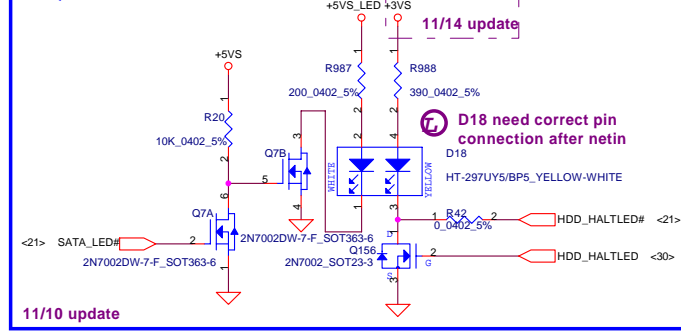
MDC 1.5 Conn.

9/20 SP01000J100
9/20 STANDOFF (H= 5.0 mm) ES000000800



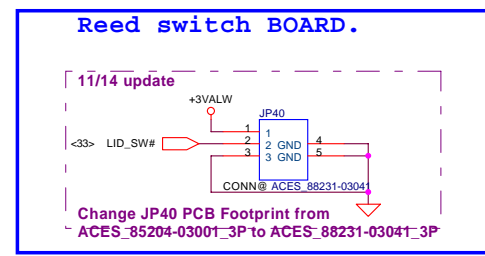
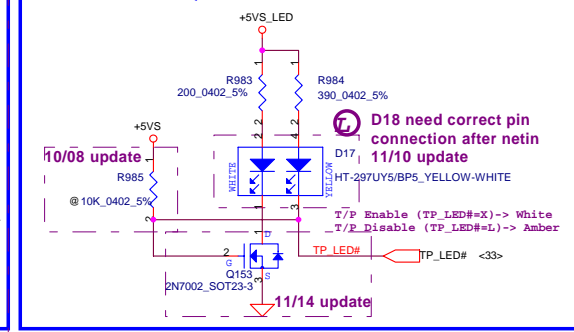
HDD/G-Sensor LED

11/14 update



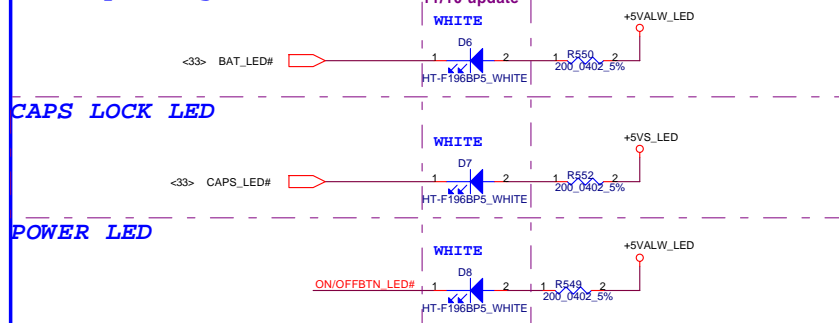
TouchPAD ON/OFF LED

11/10 update

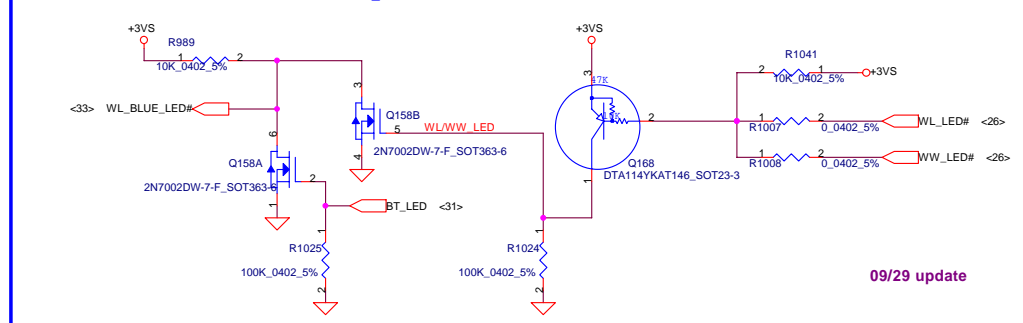


Battery Charge LED

11/10 update

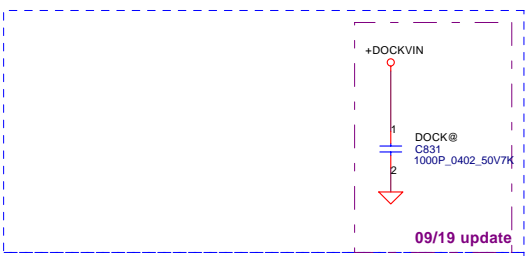


WLAN and BT LED inform pin to KBC



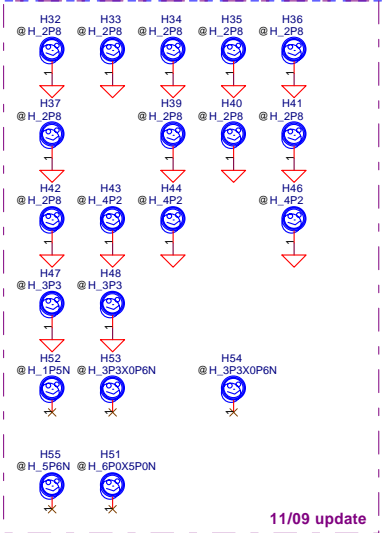
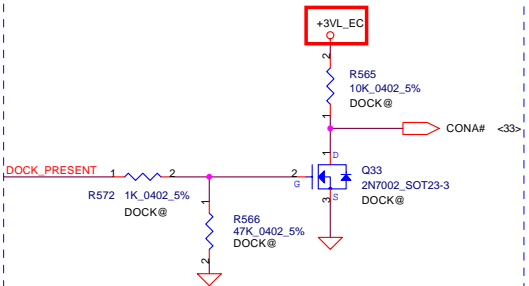
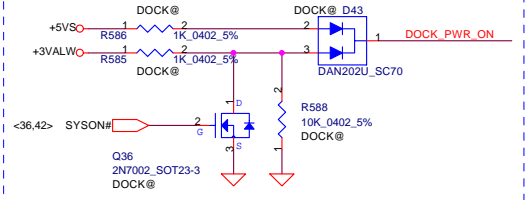
White LED: VF=3V, IF = 10mA, Res = 200 ohm
Amber LED: VF=1.8V, IF = 8mA, Res = 390 ohm

Security Classification	Compal Secret Data	Title	
Issued Date	2007/08/02	Compal Electronics, Inc.	
Deciphered Date	2008/08/02	TP,MDC,ON/OFF,S/W,LED,Reed	
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		Custom	LA-411P
		Date:	Thursday, November 15, 2007
		Sheet	34 of 47



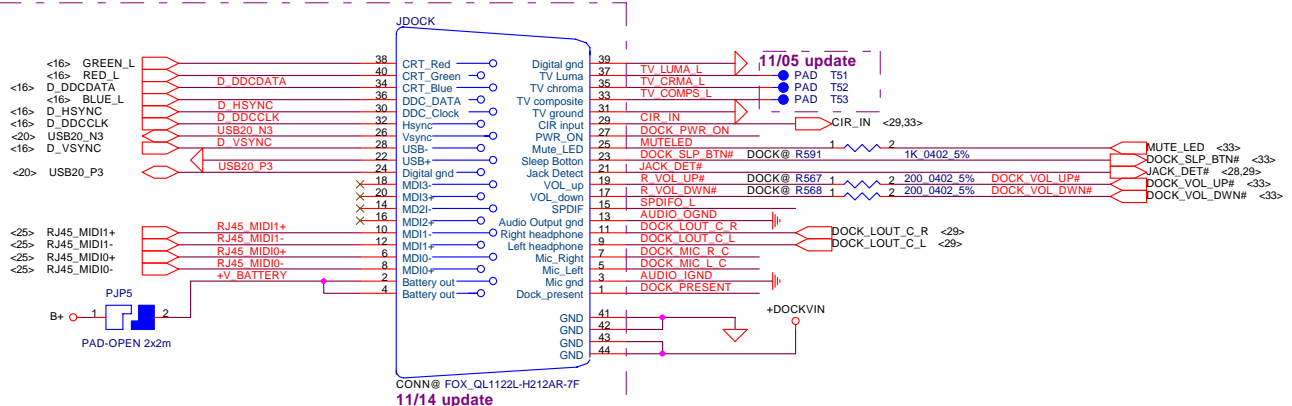
09/19 update

DOCK_PWR_ON Spec
 0V = Notebook S4/S5, Dock off
 2.5V = Notebook S3, Dock on
 4V = Notebook S0, Dock on

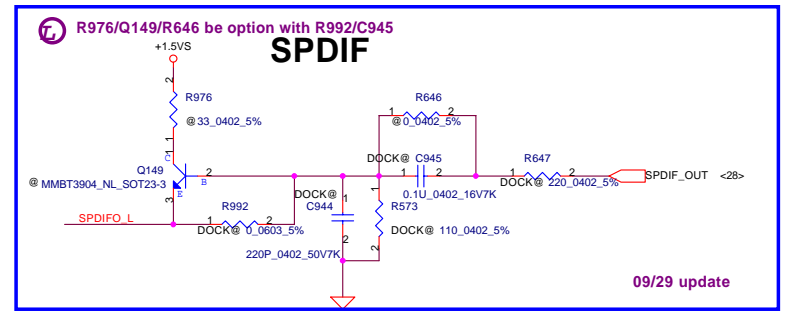
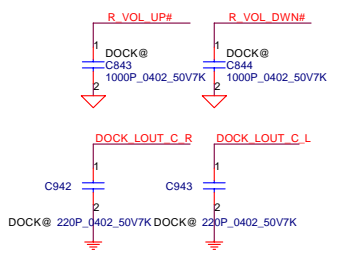


11/09 update

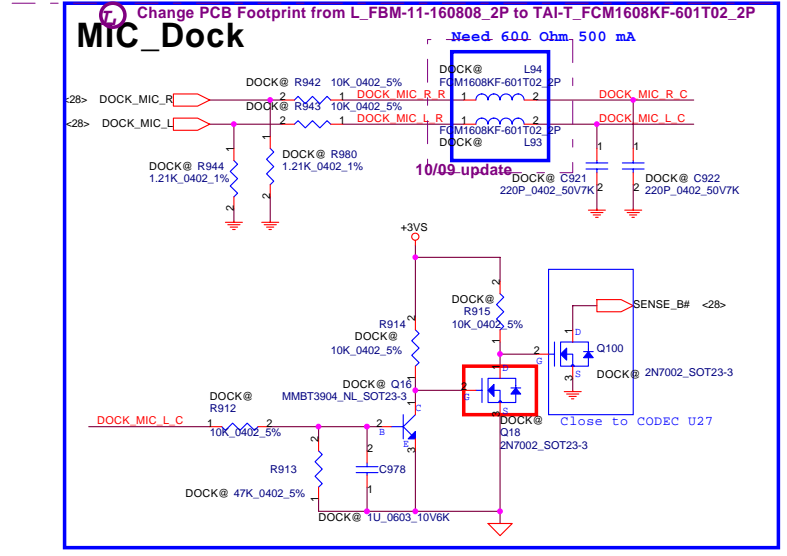
Atlas/ Saturn Dock



CONN@ FOX_QL1122L-H212AR-7F
 11/14 update
 Update symbol finish and swap back
 JDOCK Pin38/40, 34/36, 30/32, 26/28, 22/24, 10/12, 6/8



09/29 update



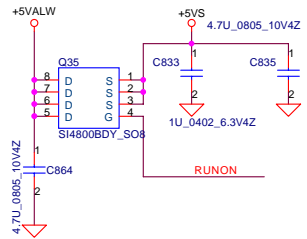
Change PCB Footprint from L_FBM-11-160808_2P to TAI-T_FCM1608KF-601T02_2P
 Need 600 Ohm 500 mA

Security Classification	Compal Secret Data	
Issued Date	2007/08/02	Deciphered Date
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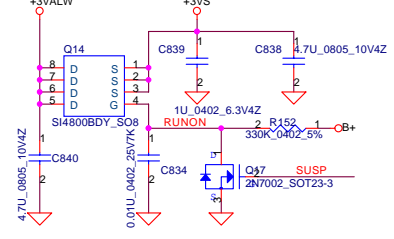
Title		Compal Electronics, Inc.	
		DOCK CONN	
Size	Document Number	Date:	Thursday, November 15, 2007
Custom	LA-411P	Sheet	of 47
Rev	0.2		

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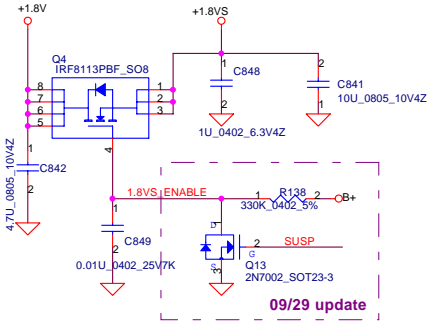
+5VALW TO +5VS



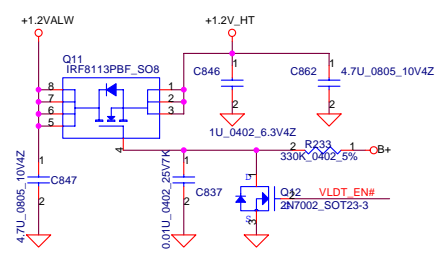
+3VALW TO +3VS



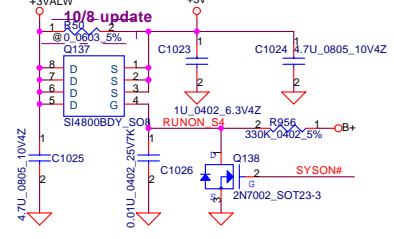
+1.8V TO +1.8VS



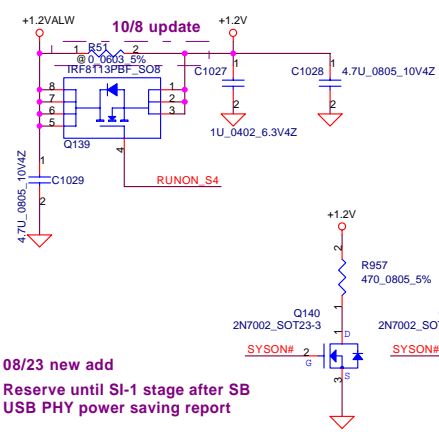
+1.2VALW TO +1.2V_HT



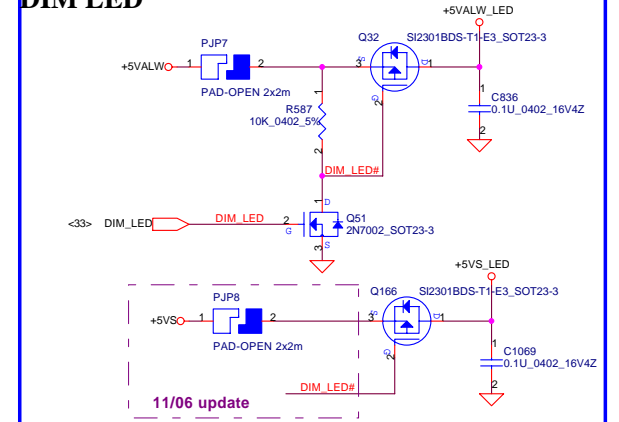
+3VALW TO +3V



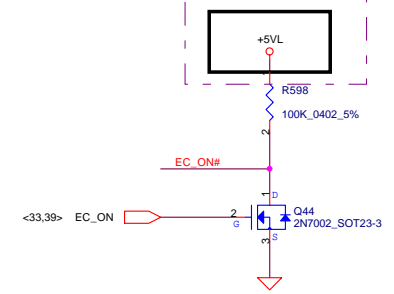
+1.2VALW TO +1.2V



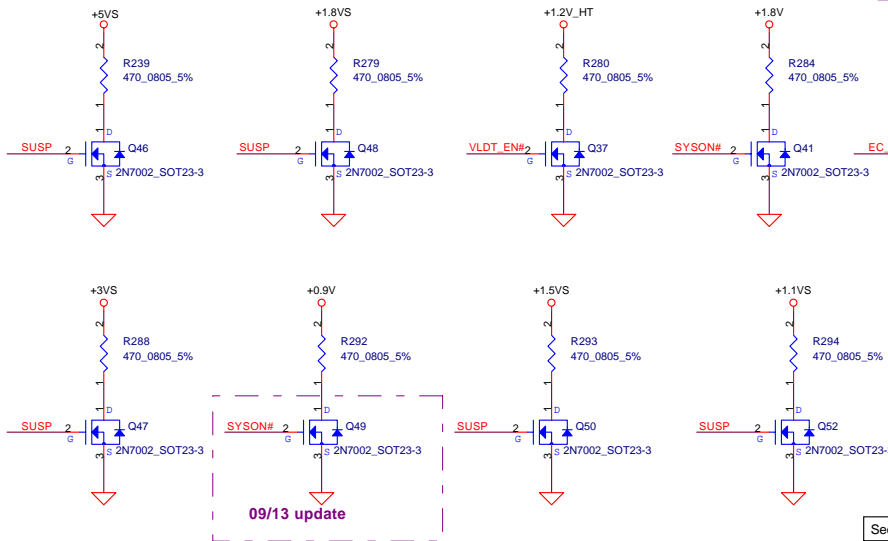
DIM LED



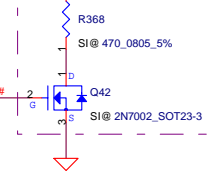
09/13 update



Discharge circuit



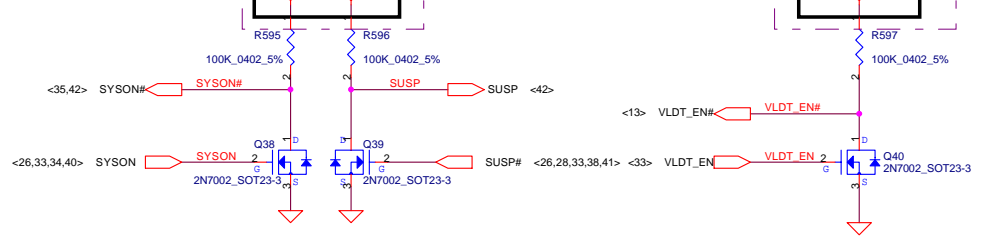
09/29 update (Wait EC Code fix)



09/13 update (Del +V_DDR_MCH_REFP)



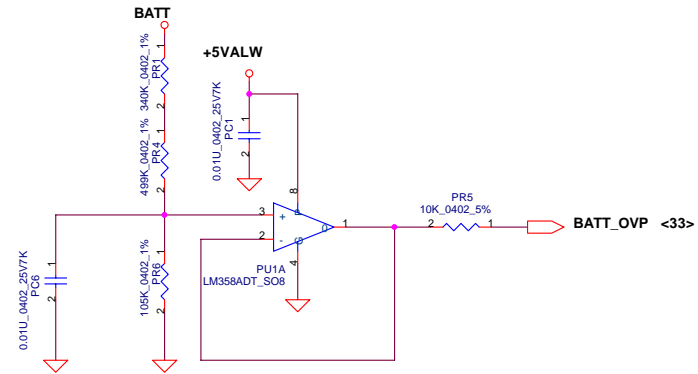
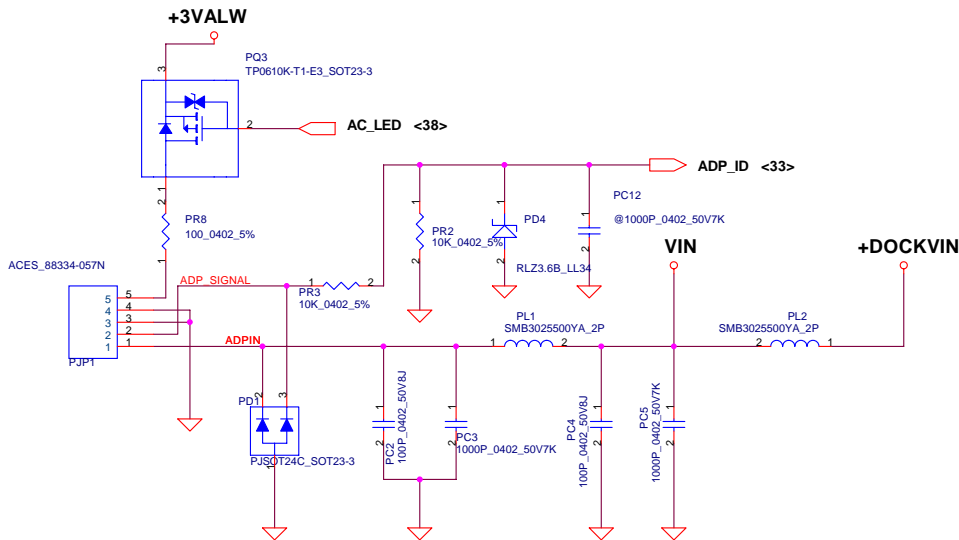
Change to +3VL (same as EC) to avoid leakage



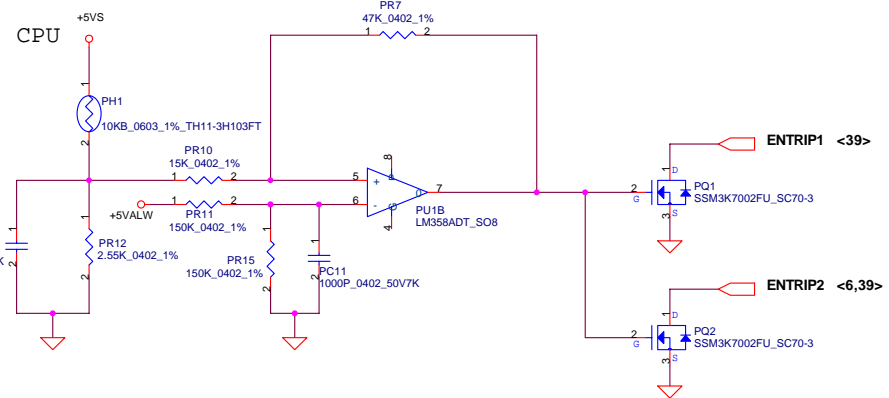
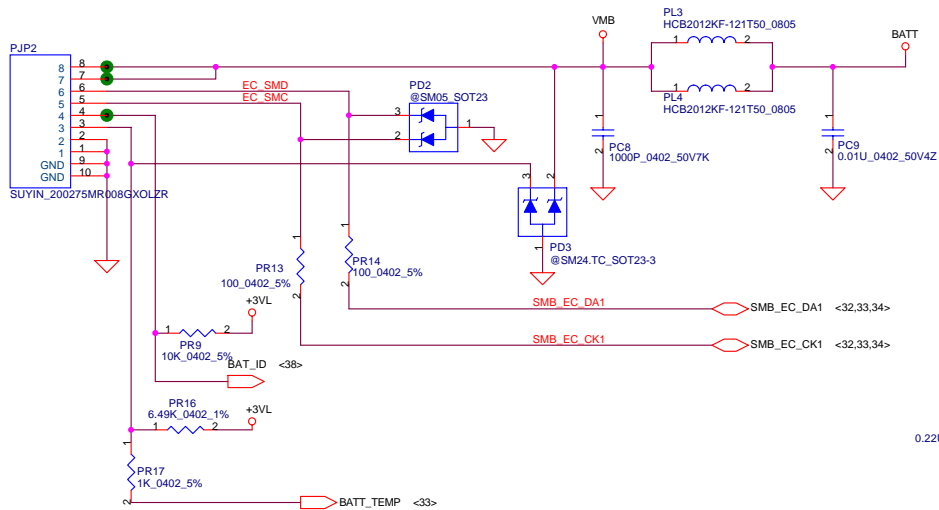
Security Classification	Compal Secret Data			Title		
Issued Date	2007/08/02	Deciphered Date	2008/08/02	Compal Electronics, Inc.		
				DC/DC Circuits		
				Size	Document Number	Rev
				Custom	LA-411P	0.2
				Date:	Thursday, November 15, 2007	Sheet 36 of 47

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BATT1
45@CR2032 RTC BATTERY



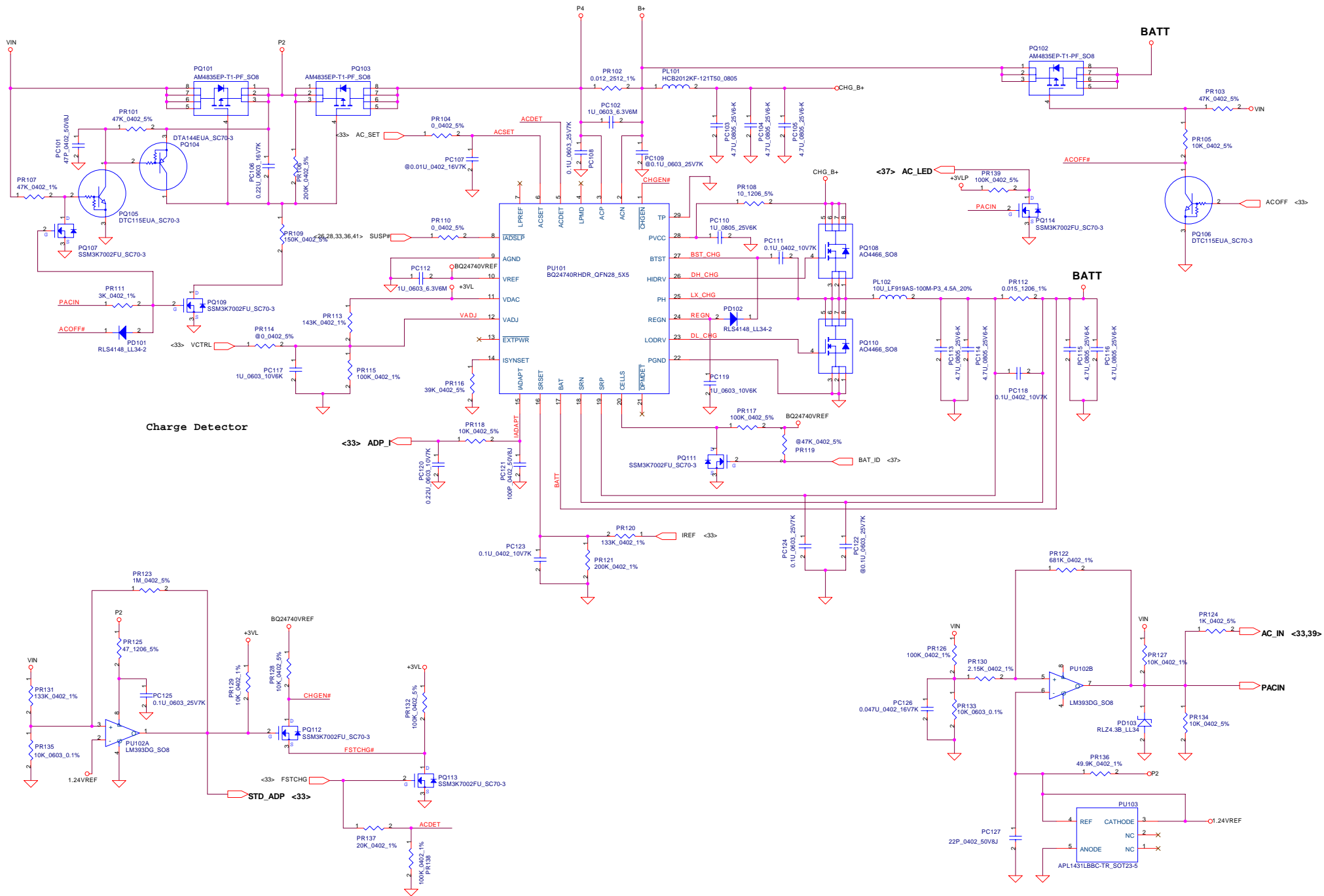
PH1 under CPU bottom side :
CPU thermal protection at 90 +-3 degree C
Recovery at 47 +-3 degree C



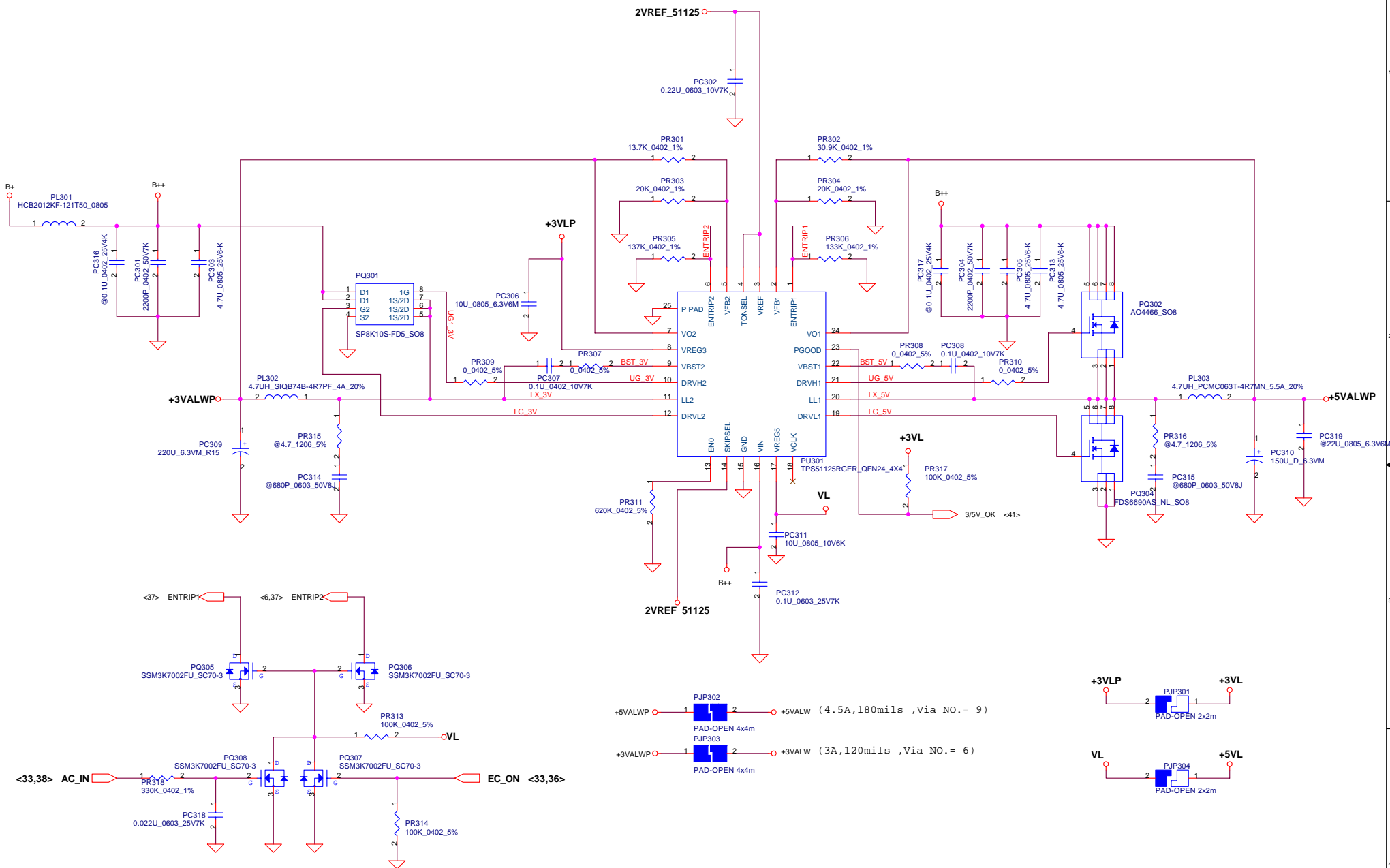
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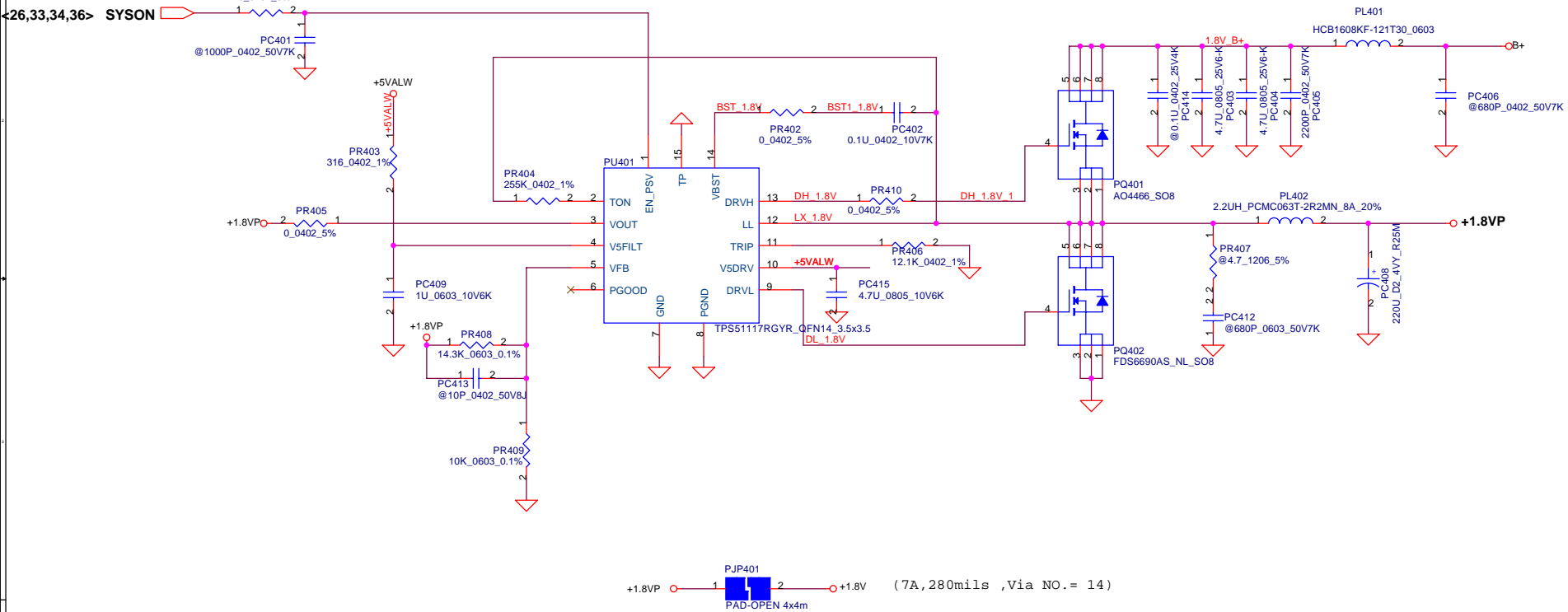
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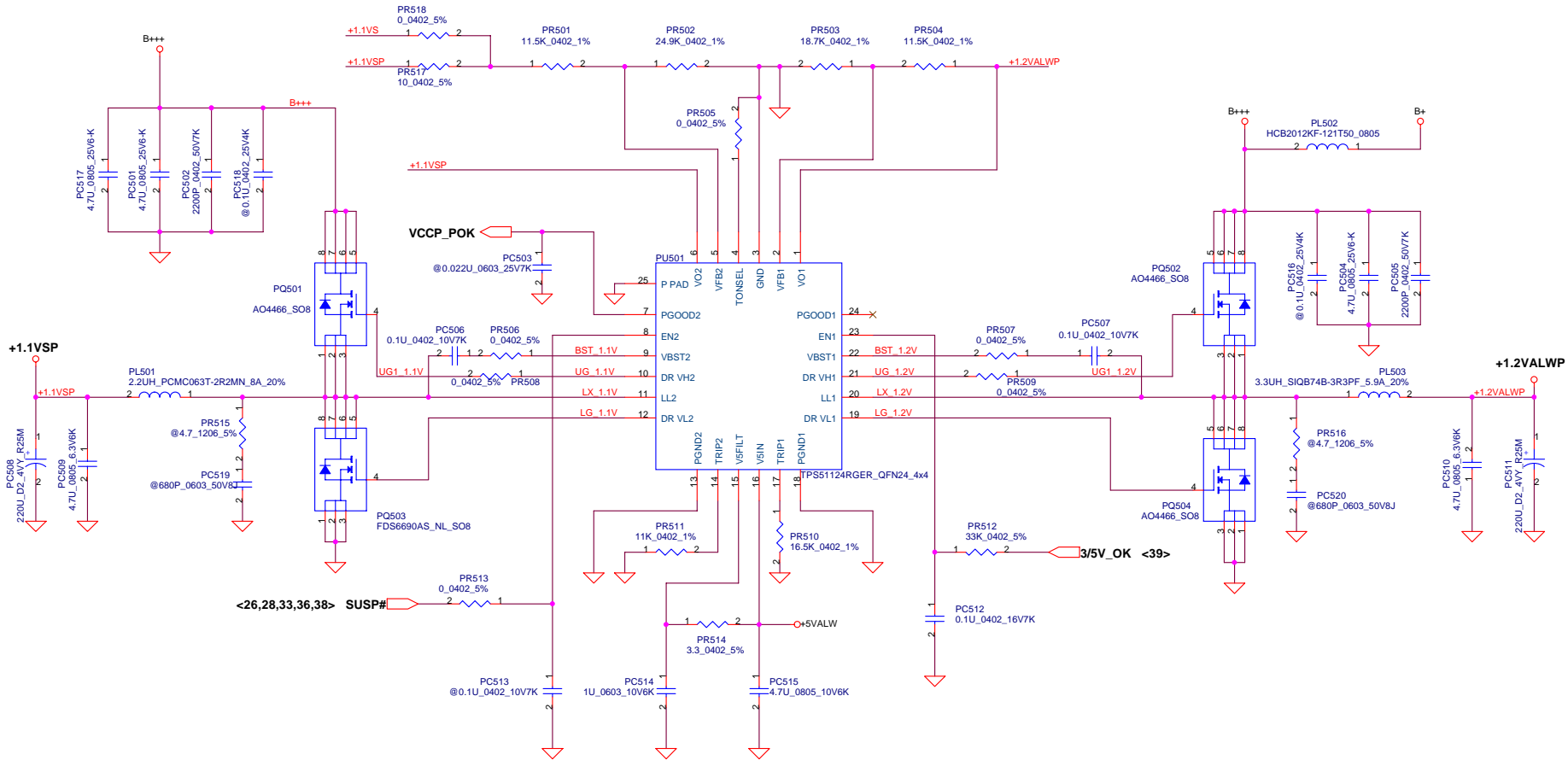
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			3.3VALWP/5VALWP		
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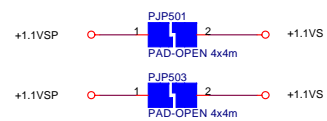
mailto:henry@hotmail.com



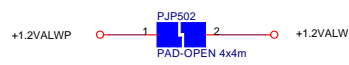
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(6A,240mils ,Via NO.=12)

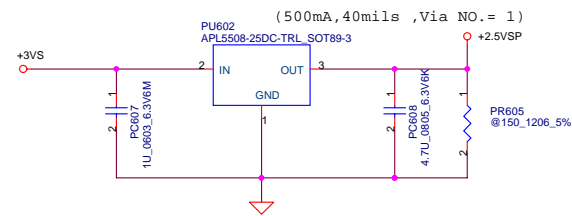
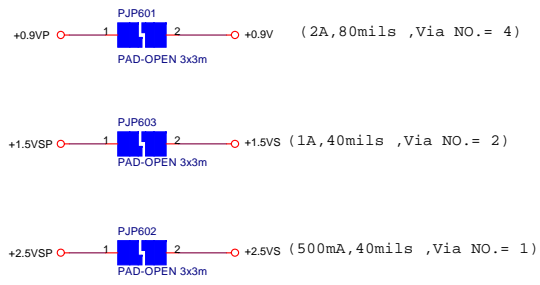
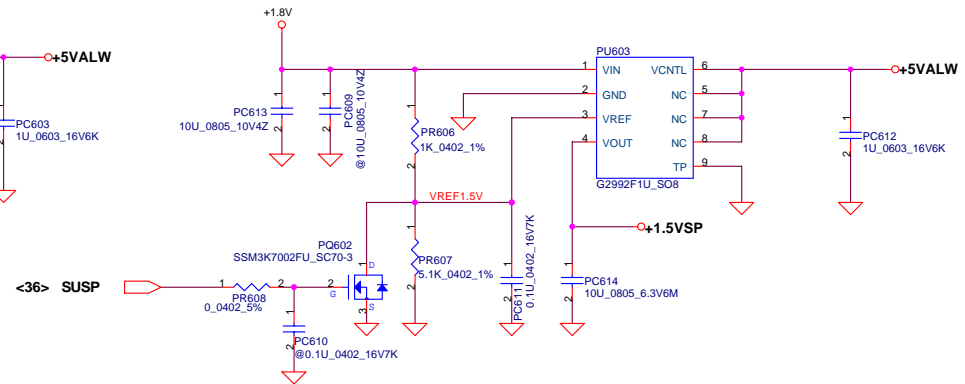
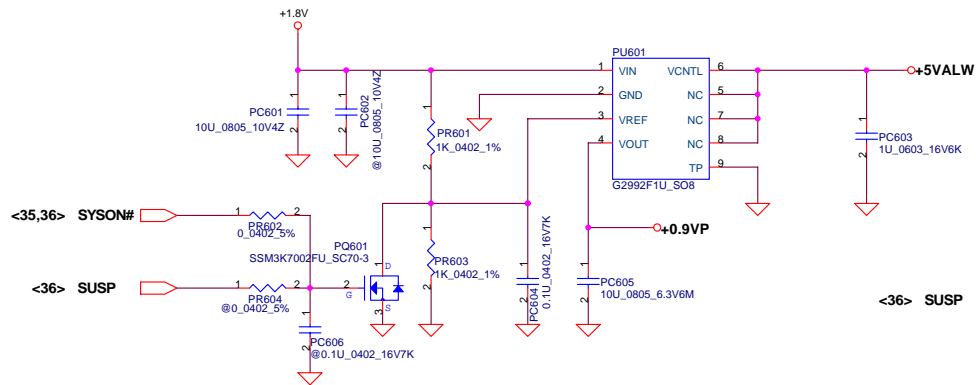


(4A,160mils ,Via NO.=8)

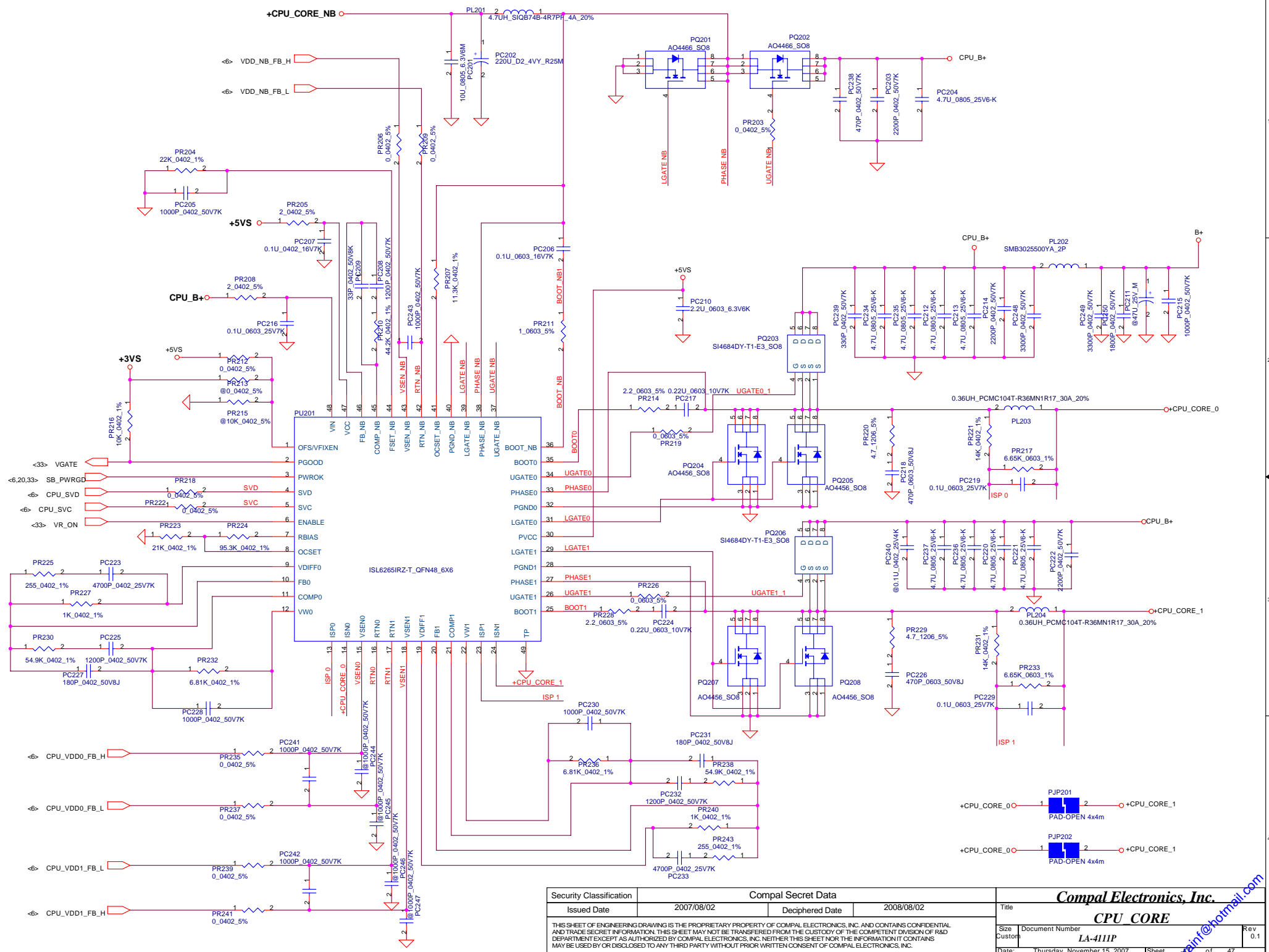


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			CPU CORE	
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Version Change List (P. I. R. List) for Power Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	37	DC Connector /CPU_OTP	9/29	Compal	for Layout	PL3 change the value from SMB3025500YA_2P to HCB2012KF-121T50_0805 and add PL4 the same of the value.	
2	41	1.1VSP/1.2VALWP	9/29	Compal	HW request	PC508 and PC511 change the value from 220U_6.3VM_R15 to 220U_D24VY_R25M	
3	41	1.1VSP/1.2VALWP	9/29	Compal	HW request	Add PJP503	
4	43	CPU_CORE	9/29	Compal	HW request	PC202 change the value from 220U_6.3VM_R15 to 220U_D24VY_R25M	
5	43	CPU_CORE	9/29	Compal	TI FAE suggested that after he review the layout.	Add PC241, PC242, PC243, and the value are 1000P_0402_50V7K. Reserve PC244, PC245, PC246, PC247, and the value are 1000P_0402_50V7K.	
6	43	CPU_CORE	9/29	Compal	TI FAE suggested that after he review the layout.	Add PJP201, PJP202	
7	38	Charger	9/29	Compal	the footprint is wrong	Change the footprint of PR102	
8	37	DC Connector /CPU_OTP	10/08	Compal	for Layout	These two choke are parallel ,it's not series.	
9	38	Charger	10/08	Compal	the footprint is wrong	Change the footprint of PR102	
10	40	1.8VP	10/08	Compal	PWR request	Delete PC410 and PC411	
11	41	1.1VSP/1.2VALWP	10/08	Compal	PWR request	Add PR517, PR518	
12	37	DC Connector /CPU_OTP	11/01	Compal	PWR request	Add PD4, PC12	
13	37	3.3VALWP/5VALWP	11/01	Compal	for Layout	change PQ301, Cencel PQ303	
14	43	CPU_CORE	11/02	Compal	EMI request	Add PC248, PC249, PC250	
15	37	3.3VALWP/5VALWP	11/12	Compal	for Layout	Change PC310, add PC319	

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Version Change List (P. I. R. List) for HW Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	25	LAN	10/29	HW	Change LAN Chip U20 from Marvell 88E8042 to Realtek RTL8102EL	Update the LAN Design page and support circuit	0.2
2	25	LAN	10/29	HPQ	Add POE(Power Over Ethernet) design	Update the LAN Design page and support circuit	0.2
3	16	CRT	10/29	HW	CRT can not display	Change the CRT Conn. signals connection first. Wait correct symbol for fix	0.2
4	29	Audio	10/30	HW	Speaker no sound	Add R973(10K_0402) to +3VALW on HP_DET#	0.2
5	4	FAN	11/01	HW	FAN Conn. not correct part	Change JP2 PCB Footprint from ACES_85204-02001_2P to ACES_88231-02001_2P	0.2
6	29	Speaker	11/01	HW	Speaker Conn. not correct part	Change JP20 PCB Footprint from ACES_85204-04001_4P to ACES_88231-04001_4P	0.2
7	34	MDC	11/01	HW	MDC Conn. not correct part	Change JP20 PCB Footprint from ACES_88018-124G_12P to ACES_88020-12101_12P	0.2
8	11,35	TV_OUT	11/05	HW	TV-OUT Function no support	Del R59,R60,R61,R115,R116,R117 and TV-OUT related design.	0.2
9	11,21	NB/SB Thermal	11/05	HW	NB Thermal Function no support (locate too far)	Cancel NB_THERMAL_DA/DC connection between NB and SB,del C500	0.2
10	21,31	SB SATA	11/05	HW	SB SATA Port 5 change to Port 2 for ATI Common Design	Change SB SATA port 5 to port 2	0.2
11	21	SB SATA	11/05	HW	SB SATA_ACT# Pull High become +3VS	Change R343.1 power rail from +5VS to +3VS. Install R343.	0.2
12	21	SB GPIO	11/05	HW	Change SB GPIO refer to JBK00 for common	1. Connect U15.C6 to GND by 0_0402. 2. Change WLOFF# from GPIO50 to GPIO61. 3. Change BT_COMBO_EN# from GPIO51 to GPIO62. 4. Change WWOFF# from GPIO52 to GPIO63.	0.2
13	31	SB SATA	11/05	HW	Vertical L51 1<-->4 , 2<-->3 for layout routing	Vertical L51 1<-->4 , 2<-->3 for layout routing	0.2
14	29	Audio HP OUT	11/05	HW	Add 150UF Caps for each DOCK_LOUT_R/L	Add 150UF Caps for each DOCK_LOUT_R/L	0.2
15	25	LAN Transfemor	11/05	HW	Correct U19 LAN Transfemor pin definition	Correct U19 LAN Transfemor pin definition	0.2
16	21,24	SB SATA	11/06	HW	SB SATA Port 4 change to Port 3 for ATI Open Issue	Change SB SATA port 4 to port 3	0.2
17	36	DIM LED	11/06	HW	Reduce DIM LED unnecessary design	Del R1026 and Q167, add Net "DIM_LED#" for connect. Change location from PJP604 to PJP8.	0.2
18	27	CardReader	11/06	HW	Change CardReader Socket for M/E new part and Chip for JMicron new version	Change JREAD to TAITW_R015-B10-LM. Reserve R413,C902 close to JREAD.20; R412,C901 close to JREAD.26; R411,C900 close to JREAD.37. Change R457 close to U23.42 Add R455,R456 close to U23.42 Del Q169,R1051. Change net CR_LED# become CR_LED connect U23.21 and Q53.2 Add R454 pull down to GND Change R405,R122 from 200K to 10K pull-high Remove C895,U22	0.2

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Version Change List (P. I. R. List) for HW Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
19	16	CRT	11/07	HW	Normalize CRT design for common	Change L83,L84 (10_0402) become R241,R240 (0_0603)	0.2
20	17	LCD	11/07	HW	Normalize LCD design for common	Change R491 from 200_0402 to 200_0805	0.2
21	18	LCD	11/07	CIC	CIC feedback RMA concern for common	Change Q43 from AOS3413 to SI2301	0.2
22	33	KBC	11/07	HW	Normalize KB926 Crystal part for common	Change Y7 from 9H03200413 small size become 1TJS125DJ4A420P normal size.	0.2
23	17	WebCam	11/09	HW	Change U54 WebCam power design and related	Change U54 from G916-390T1UF to RT9193-39GB. Remove R891,R892 if no use G916-390T1UF. Add C718 close to U54.4 for RT9193-39GB. Remove R1027-R1030 for JP7 no install. Change JP7 from 8pin to 6pin	0.2
24	18	HDMI	11/09	HW	Reduce HDMI Design	Remove R490(100K_0402)	0.2
25	19,32	SB-CLK-Debug	11/09	HW	Debug Card no function issue	Del R1031,add R303 close to R301 and U15.P2 Connect for CLK_PCI_SIO2 to JP41.15	0.2
26	25	LAN	11/09	HW	RJ45 LED Power correct back	Change JRJ45.13 and JRJ45.11 from +3V_LAN_LED to +3V_LAN	0.2
27	18	HDMI	11/09	HW	Reduce HDMI Design	Remove R490(100K_0402)	0.2
28	6	CPU	11/09	HW	Add H_THERMTRIP# one more way	Add R16 close to Q3.1 for H_THERMTRIP#	0.2
29	33	KBC	11/09	HW	Update KBC Pin Definition for common	Add H_THERMTRIP# to U33.25	0.2
30	35	Holes	11/09	ME	Update for M/E Drawing	Del H49 H50 H38 H45 for M/E drawing change	0.2
31	26	Mini-Card	11/09	HW	Reduce Mini-Card design, change SIM Card design	Replace D17 and D47 become R52 and R53 Del R400 and R46, Change JP6 pin definition for common	0.2
32	33	KBC	11/09	HW	Reserve 0_0603 for KB Back Light	Add R516 (0_0603) between JP48.1/4 and +5VS_LED	0.2
33	27	CardReader	11/10	HW	Correct CardReader LED part	Change D5 from SC500004E00(AQUA_WHITE) to SC500004W00(WHITE)	0.2
34	34	LED Function	11/10	HW	Correct LED function for common	Change LED from D50,D30,D27 SC500004E00 (AQUA_WHITE) to D6,D7,D8 SC500004W00(WHITE) Change LED from D45,D46 SC500004B00 (AQUA_WHITE/AMBER) to D17,D18 SC500005M00 (YELLOW/WHITE) Add Q7,R20 and R42 close to D18	0.2
35	21	SB-GPIO	11/10	HW	Add one more way for GSENSOR LED# Inform pin	Add HDD_HALTLED# connect from U15.P8	0.2
36	33	KBC-GPIO	11/11	HW	Add CIR_IN PH to +5VL Add ESB_CLK/DAT PH to +3VL	Add R46 10K_0402 PH to +5VL close to U33 Add R514,R515 10K_0402 PH to +3VL close to U33	0.2

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Version Change List (P. I. R. List) for HW Circuit

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37	6,31	CPU,FPR	11/13	HW	Reduce S3 power consumption	Change R15.2,R21.2,R36.2,R30.2 connection from +1.8V to +1.8VS; Remove R622, install R581	0.2
38	11	NB	11/13	HW	Reduce the level shift design for Chip A12.	Del Q6,R87; Q5,R84 and replace by 0ohm (add R67,R68) connect directly. Install R371 (10K ohm)	0.2
39	17	WebCam	11/13	HW	Update the WebCam+Digital Mic reserver conn.	Change JP7 from SP02000HC00(8pin)-->SP02000L00(6pin)	0.2
40	6,33	CPU,KBC	11/13	HW	Update THERMTRIP# design to EC	Change R16.2 connection from THERMTRIP# to THERMTRIP#_EC for separate	0.2
41	18	HDMI	11/13	HW	Remove EMI solution become reserve for verify	Add R112,R113,R115-R120 close to each L85-L88 for co-lay	0.2
42	19,32	SB,BIOS	11/13	HW	Reduce SB related design for Chip A12 and others	Del Q155,R986, and add R311 close to U15. Del R1011 become T18, Cancel R1012 and connect to H31 and JP41 directly	0.2
43	21,32	SB,BIOS	11/13	HW	BIOS Debug Tool reserve	Add SB_INT_FLASH_SEL and related (JP12,U30,R228,R226,C489 close to U29)	0.2
44	25	LAN	11/13	HW	Update LAN Chip Symbol link to CIS server	Update LAN Chip U20 Symbol link to CIS server	0.2
45	13	NB	11/13	HW	Add 0ohm_0603 to separate VDD18_MEM	Add R1051(0_0603) between +1.8VS & +1.8V_VDD_SP	0.2
46	18	HDMI	11/13	HW	Reduce HDMI related design for common	Del R490 (100K_0402)	0.2
47	20	SB	11/13	HW	Reduce SB related design for common and A12 chip	Remove R994 (0_0402) Change U15.F1 connection become test point Remove R1053, change R1052 become 0_0402	0.2
48	20,21,27	SB,Cardreader	11/13	HW	Reserve Cardreader D3E function (CR_WAKE# & CR_CPPE#)	Add R81 close to U15;Q54,R124 close to U23 for connect U15.F8 to U23.13 ;Add R369 close to U23 for connect U15.M5 to U23.16	0.2
49	21,33	SB,KBC	11/13	HW	Reduce SB related design for common	Del D51 and R1034, Change the net AC_IN become AC_IN_D	0.2
50	28,33	Codec,KBC	11/13	HPQ	EC_BEEP function for KBC add	Add R563 close to C955; Add R544 close to U33.31	0.2
51	33	KBC	11/13	HW	Reduce S5 Power Consumption	Change R1040.1 connection from +3VL_EC to +3VALW Del R546 PH to +3VL_EC, Del D26 replace by add R547 close to U33 for short	0.2
52	33	KBC	11/13	HW	Reduce KBC Design for common and Ver:C0 Chip Change from SA00001J530 to SA00001J540	Del R537 become Test Point, change R516 become 150_0603 Remove R1044, change R1040 from 10K to 100K Change R528.2 , R529.2 connection from +5VALW to +5VL Install C814 (1U_0805)	0.2
53	34	Switch Design	11/13	HW	Update CSD function board design for common	Change JP36.1 connection become +3VL;Change R1046.1 and R1047.1 connection become SMB_EC_CK1/DA1 Change JP36.7 connection from GND to +5VALW_LED by Change Q153 from 2N7002DW to 2N7002	0.2
54	34	LED	11/14	HW	Correct T/P On/Off LED design define Correct G-Sensor LED design define	Change R988.1 connection from +5VS_LED to +3VS	0.2
55	29	Audio-Dock	11/14	HPQ	For GS mark requirement	Add R968,R969 close to C775/C776.	0.2
56	29	Holes	11/14	ME	Update Holes to meet M/E Drawing	Add back H52 become H_1P5N; Del CF4	0.2
57	4,24	Multi-Bay	11/14	ME	Update Symbol to meet M/E Drawing	Update JP2,JP9,JP10,JP11,JP20,JP40,JHDMI,JESAT,JCRT, JDOCK Symbol	0.2

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