

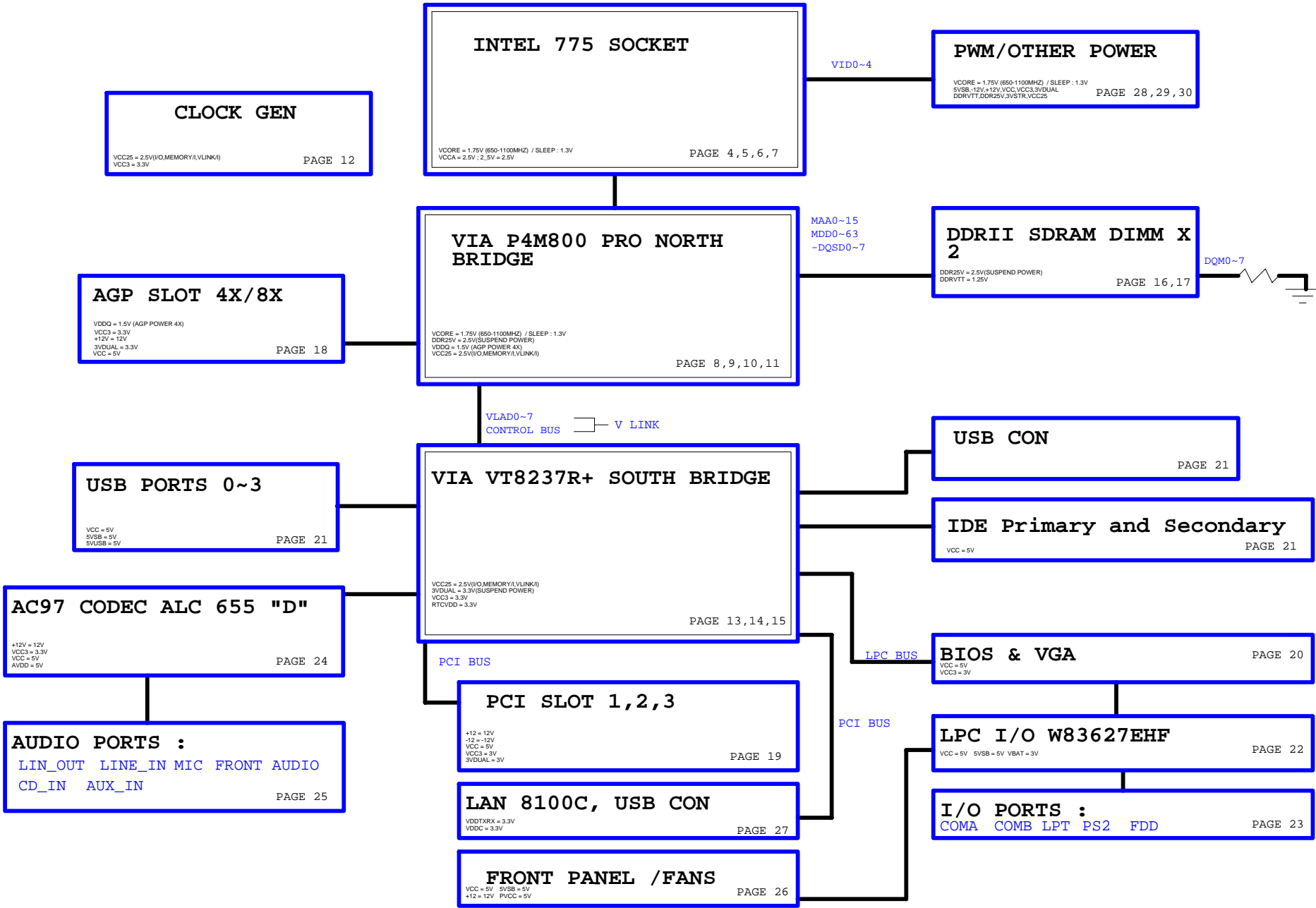
Model Name: GA-8VM800PMD-775

Revision :1.0

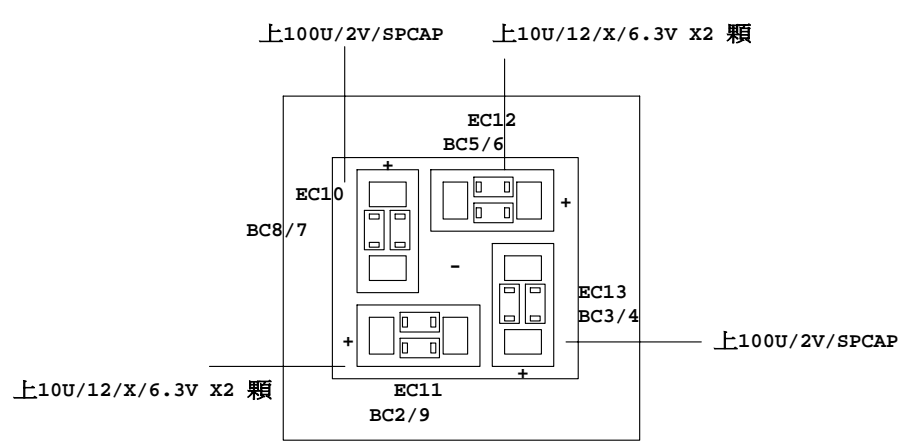
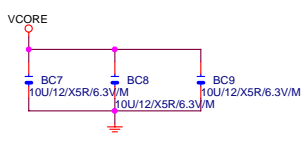
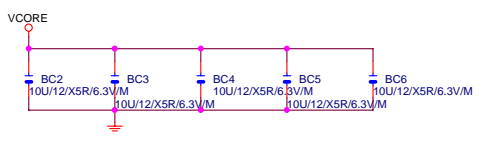
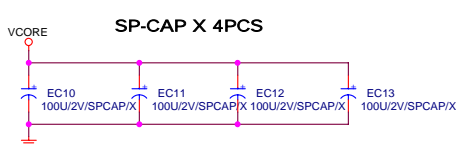
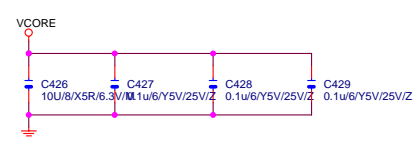
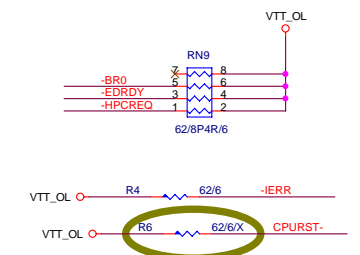
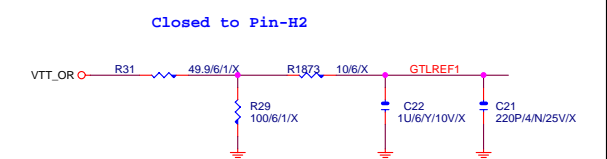
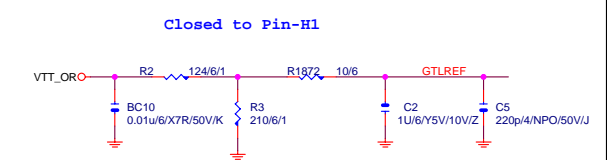
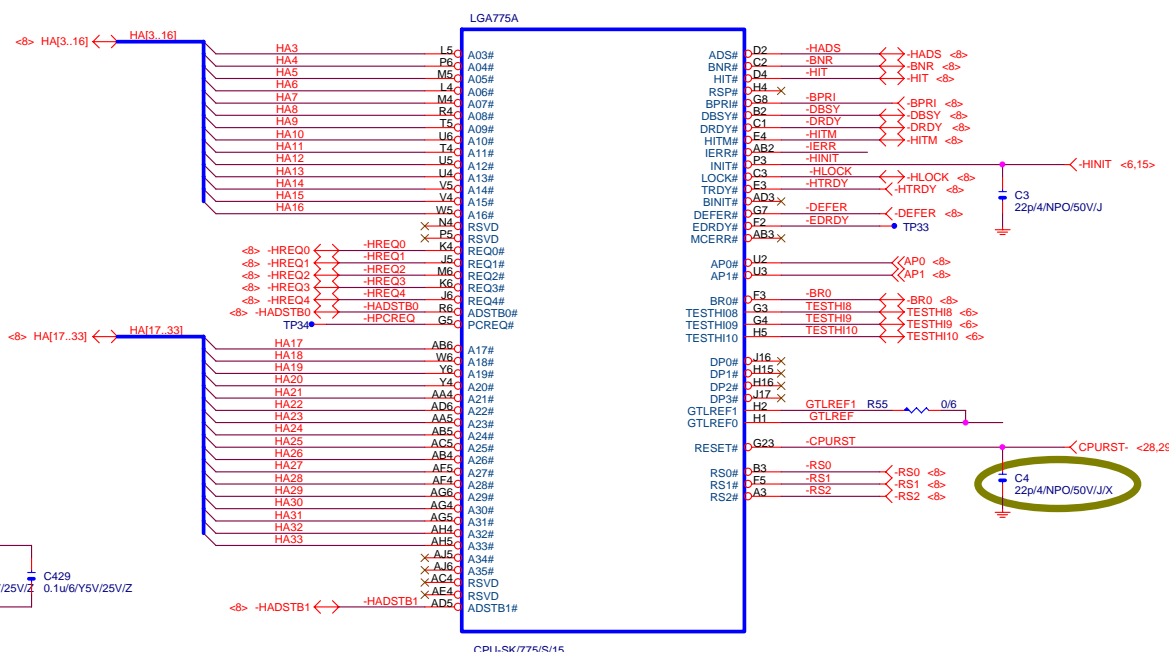
SHEET	TITLE
1	BOM & PCB MODIFY HISTORY
2	COVER SHEET
3	BLOCK DIAGRAM
4,5,6,7	INTEL P4_LGA775 CPU
8,9,10,11	P4M800 PRO(NORTH BRIDGE)
12	CLOCK GENERATOR (ICS950952)
13,14,15	VT8237R+/CD (SOUTH BRIDGE)
16,17	DDR2 SDRAM DIMMS 1,2 DDR TERMINATION
18	AGP SLOT
19	PCI SLOT 1,2,3
20	BIOS , VGA Connect
21	IDE,USB
22	LPC I/O_W83627EHF
23	COM,PRT,FDD,KB/MS,IR
24	AC 97 CODEC
25	AUDIO JACK
26	PANEL,FANS
27	LAN 8100C & USB CONNECTOR
28	DDR POWER
29	ATX CONN,3VDUAL,VDDQ DC POWER
30	VCORE PHASE PWM FAIRCHILD 5018B

GIGABYTE		
COVER SHEET		
Title	Document Number	Rev
Size	GA-8VM800PMD-775	1.0
Custom		
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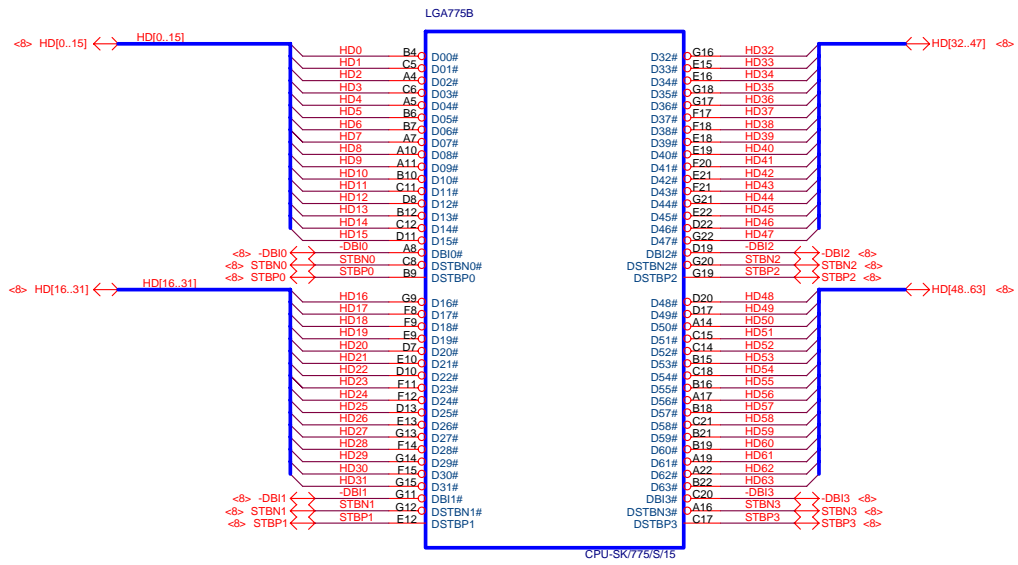
BLOCK DIAGRAM



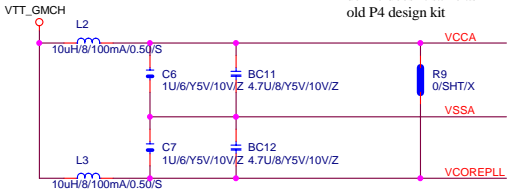
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Title	BLOCK DIAGRAM		
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Title P4_LGA775-A			
Size	Document Number	GA-8VM800PMD-775	
Custom	Date:	Wednesday, November 30, 2005	Rev 1.0
		Sheet 4	of 30

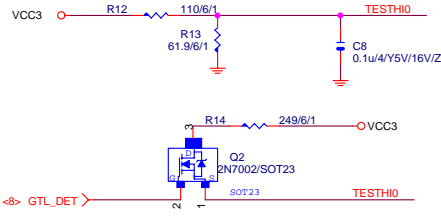


Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit

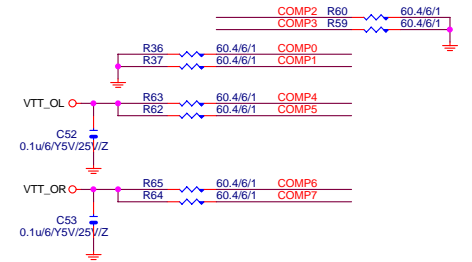


As close as possible to
CPU socket

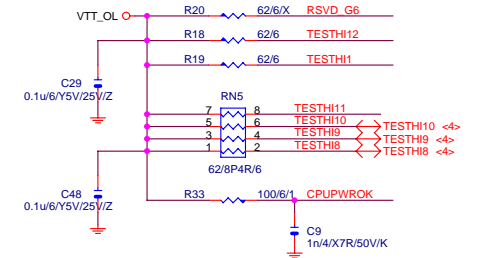
Trace width doesn't
less than 12 Mil



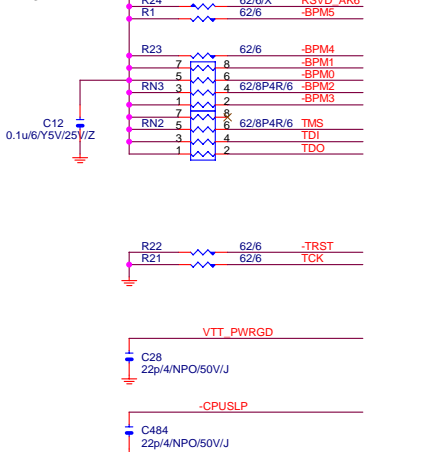
Place outside of CPU socket



Locate at ICH6 Side

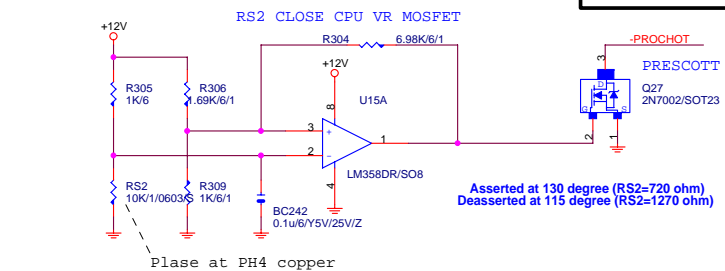
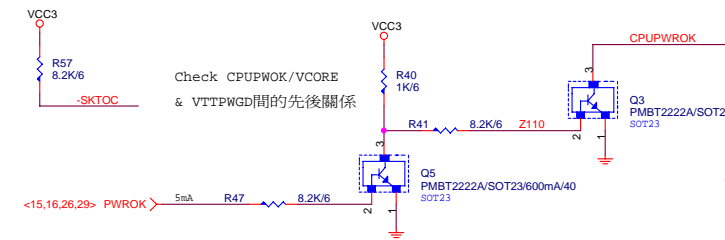
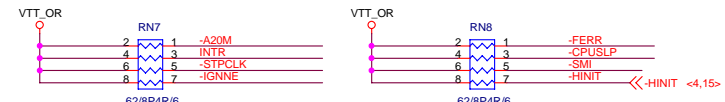


Remove
C11



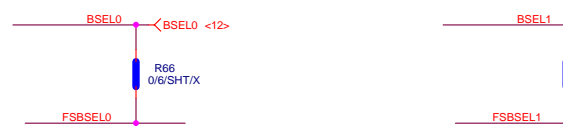
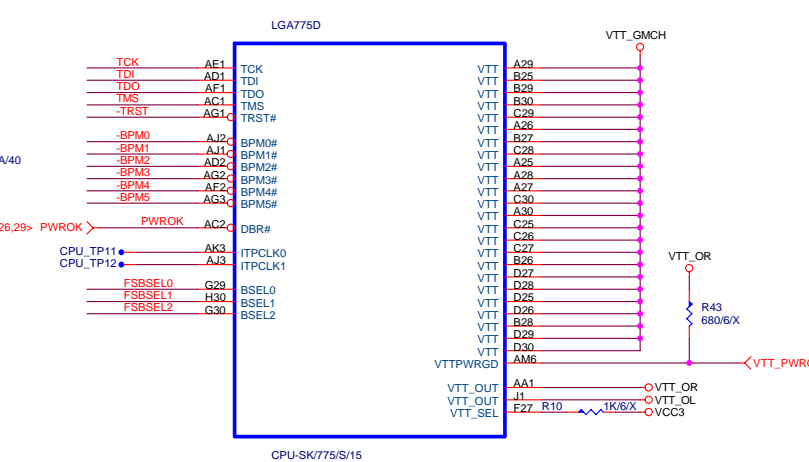
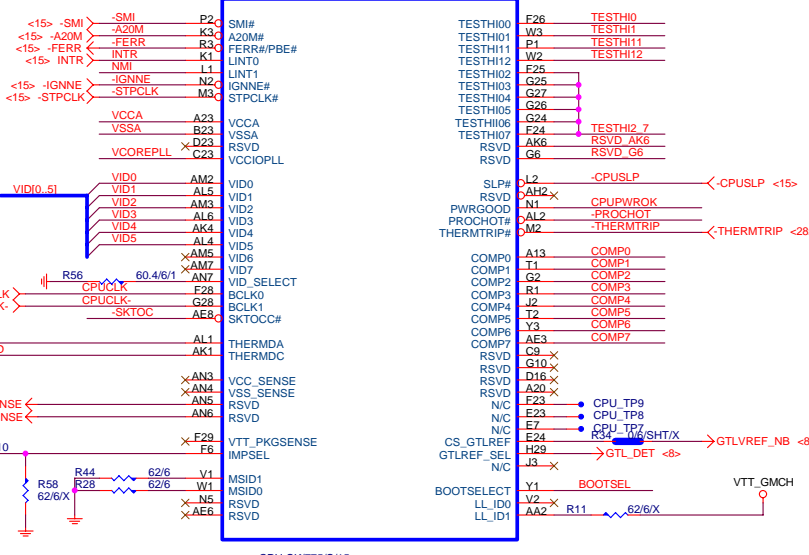
CPU

NA	FSB	FSA	Clock
FSBSEL3	FSBSEL1	FSBSEL0	Clock
1	0	1	100MHz
0	0	1	1.33MHz
0	1	1	1.66MHz
0	1	0	2.00MHz
0	0	0	2.66MHz



PROCESSOR HOT

Asserted at 130 degree (RS2=720 ohm)
Deasserted at 115 degree (RS2=1270 ohm)

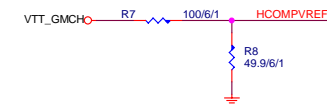
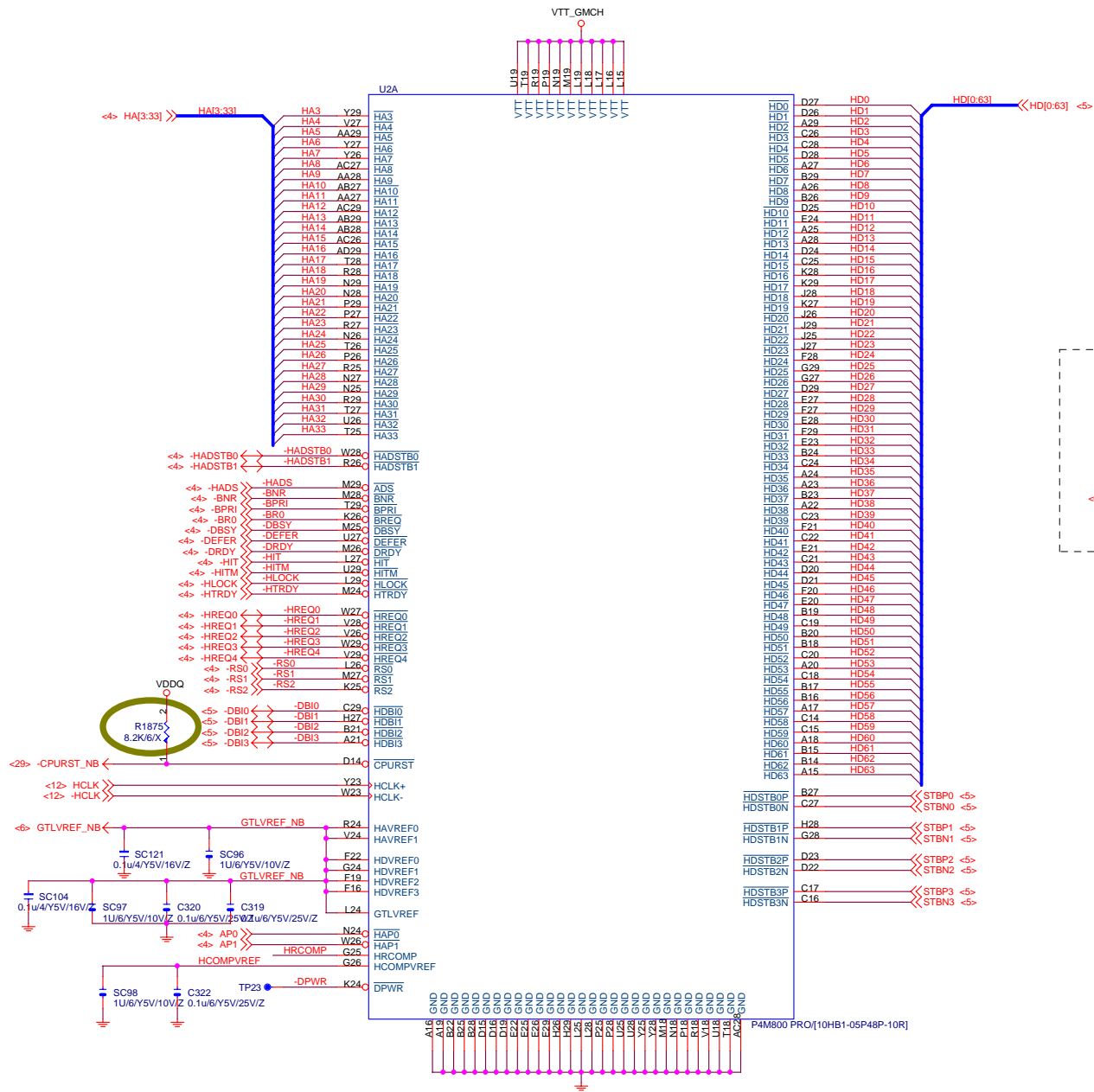


GIGABYTE

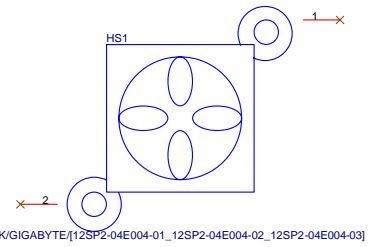
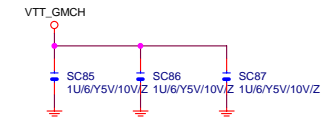
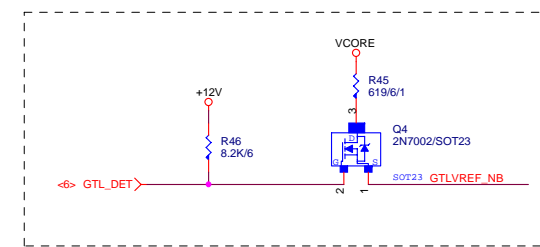
Title: P4_LGA775-C

Document Number: GA-8VM800PMD-775 Rev 1.0

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Please Close to P4M800



NB Heatsink

P4M800 PRO[10HB1-05P48P-10R]

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NORTH P4M800		
Title	Document Number	Rev
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Date:	Wednesday, November 30, 2005	Sheet 8 of 30

MD0
MD1
MD2
MD3
MD4
MD5
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MD7
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MD59
MD60
MD61
MD62
MD63

CKE0
CKE1
CKE2
CKE3

MAA0
MAA1
MAA2
MAA3
MAA4
MAA5
MAA6
MAA7
MAA8
MAA9
MAA10
MAA11
MAA12
MAA13

MAA14
MAA15

DMCOMP

MEMDET

-DQS0
-DQS1
-DQS2
-DQS3
-DQS4
-DQS5
-DQS6
-DQS7

DCLKI
MCLKO
MCLKO

MVREF_NB

MEMDET

DMCOMP

SC110
0.1u/4Y5V/16V/Z

MCLKO
MCLKO
DCLKI

-CS0
-CS1
-CS2
-CS3

MAA0
MAA1
MAA2
MAA3

MAA4
MAA5
MAA6
MAA7

MAA8
MAA9
MAA10
MAA11

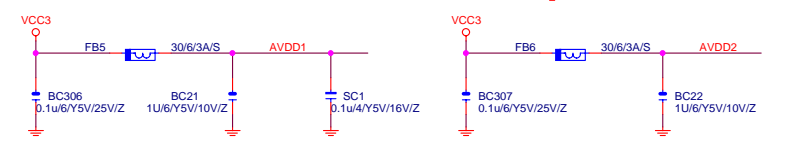
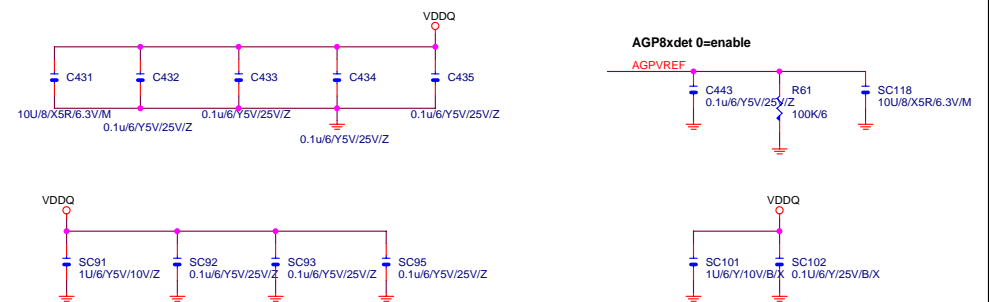
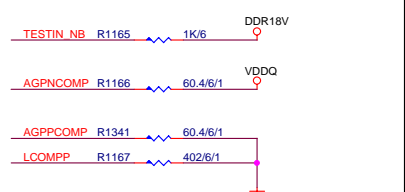
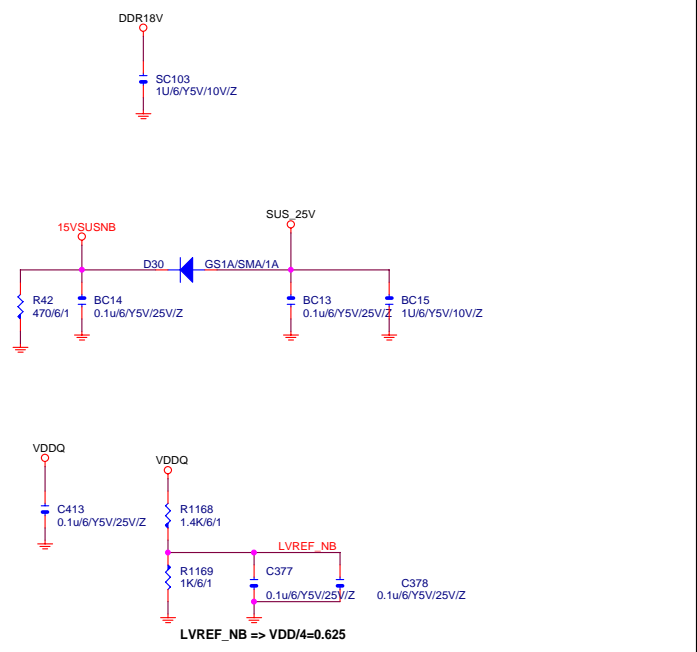
MAA12
MAA13
MAA14
MAA15

-SRASA
-SCASA
-SWEA

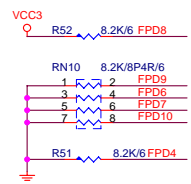
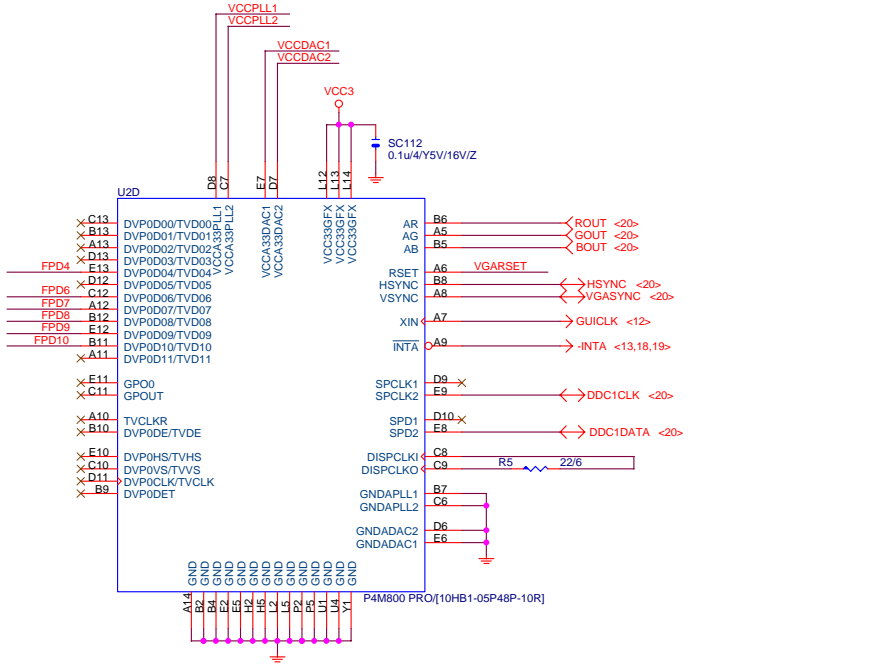
GIGABYTE

NORTH P4M800

Title	NORTH P4M800		Rev
Size	Document Number	GA-8VM800PMD-775	1.0
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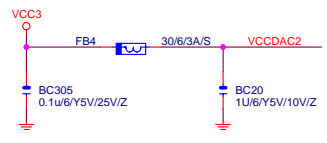
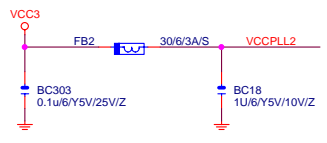
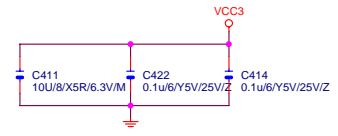
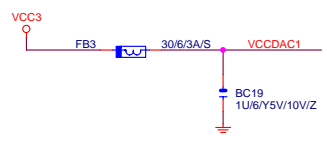
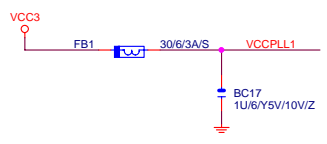
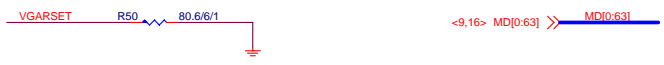


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Title	Document Number	Rev
Size	GA-8VM800PMD-775	1.0
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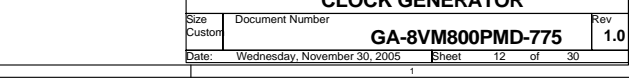
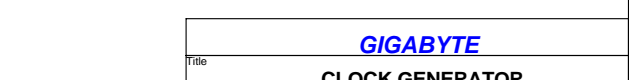
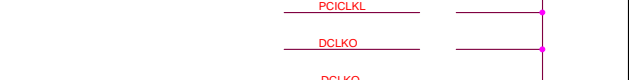
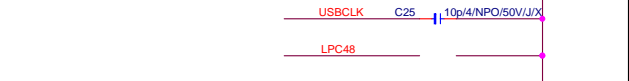
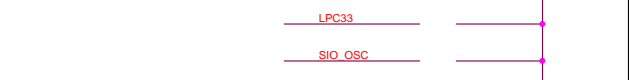
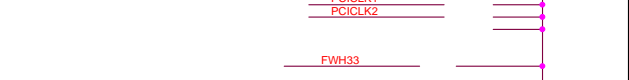
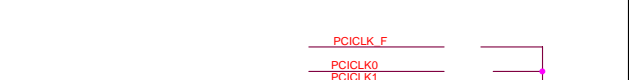
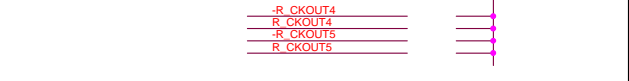
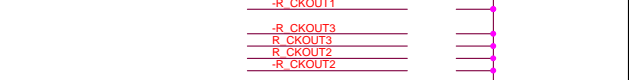
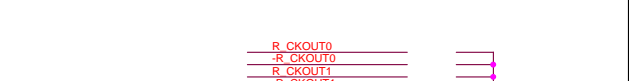
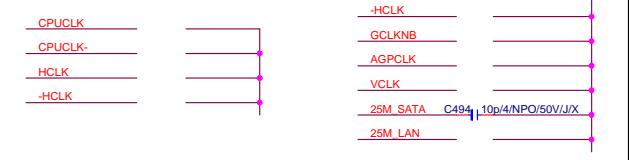
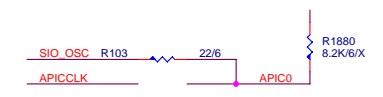
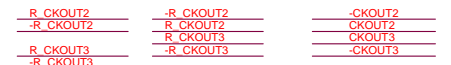
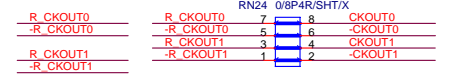
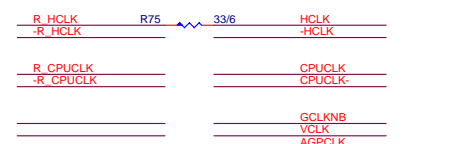
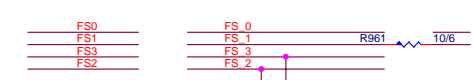
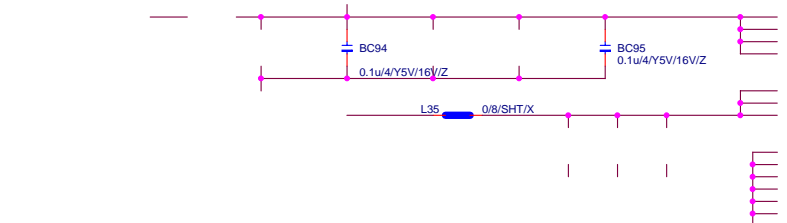
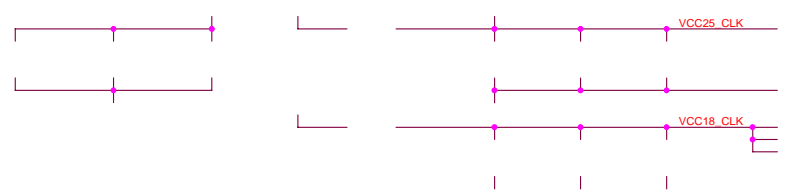


GFX power up strapping setting:

- TVD4/DVP0D4 =>AGP Port Muxing**
0: Two 12-bit DVI interface
1: One 24-bit Panel Interface
- TVD5/DVP0D5 =>Dedicated DVI Port Configuration**
0: TMDS
1: TV Encoder
- TVD6/DVP0D6 =>Dedicated DVI Port Selection**
0: Disable
1: Enable
- TVD8/DVP0D8 =>External AGP Function Enable**
0: External
1: Internal
- TVD9/DVP0D9 =>PCI Signal Test Output Enable**
0: Disable
1: Enable
- TVD10/DVP0D10 =>CPUCLK/MCK Clock Select**
0: From NB
1: From External
- TVD7/DVP0D7 =>GFX Clock Select(VCK/LCCK/ECK)**
0: Refer Internal PLL
1: From External

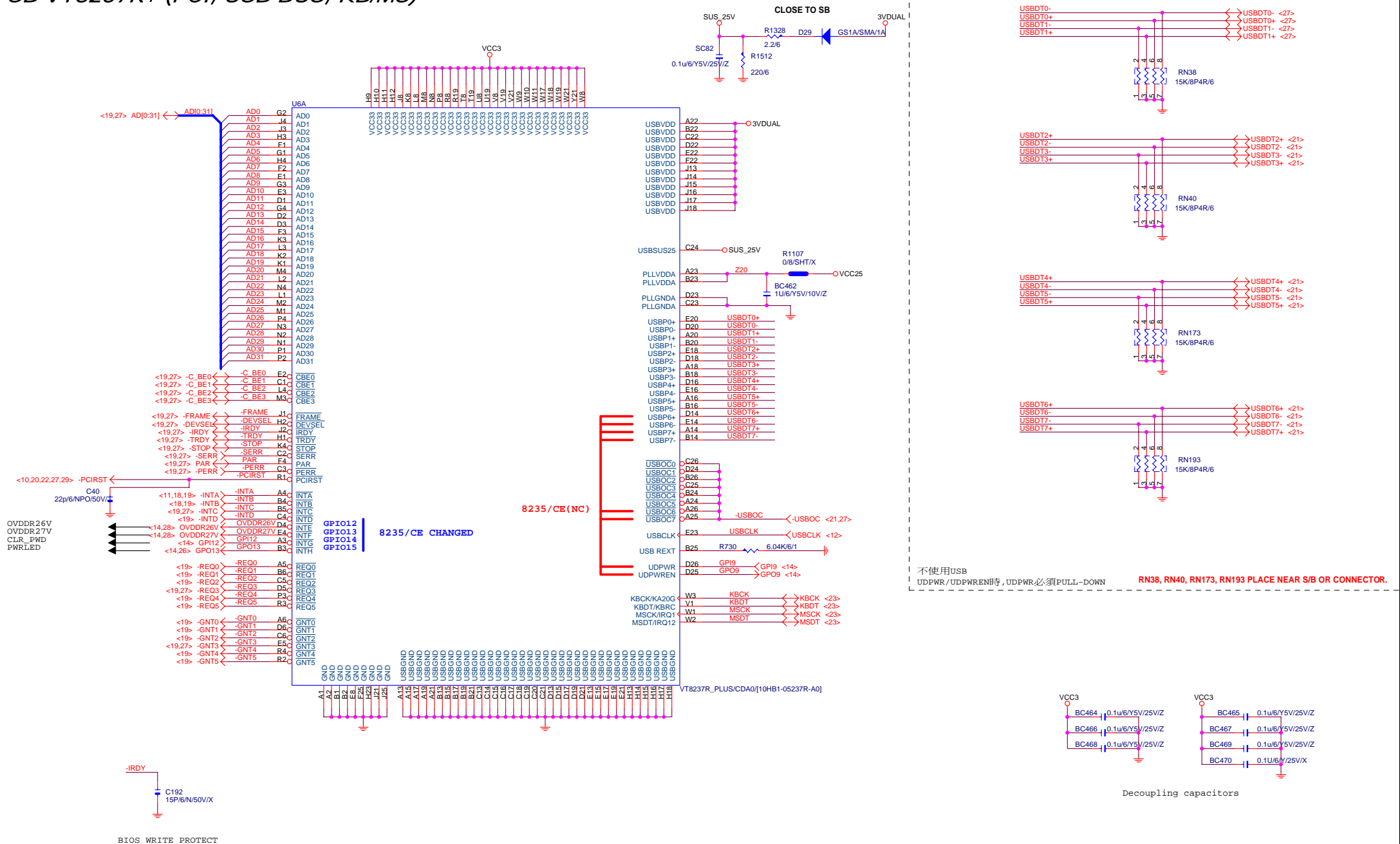


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GIGABYTE		
CLOCK GENERATOR		
Title		
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SB VT8237R+ (PCI, USB BUS, KB/MS)



不使用USB
UDPPWR/UDPWREN時, UDPPWR必須PULL-DOWN **RN38, RN40, RN173, RN193 PLACE NEAR S/B OR CONNECTOR.**

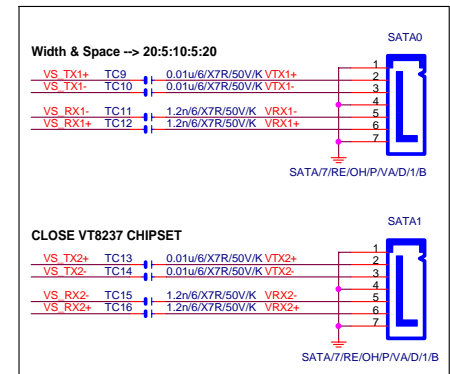
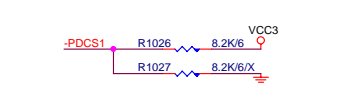
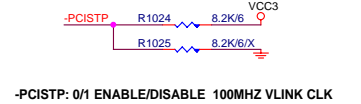
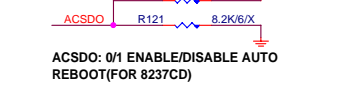
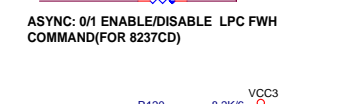
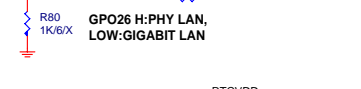
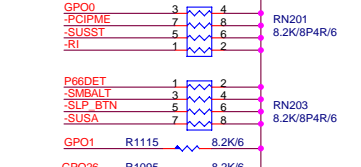
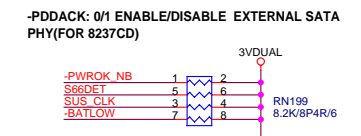
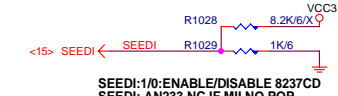
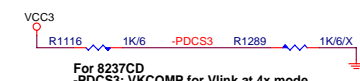
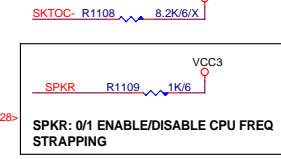
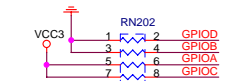
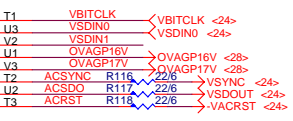
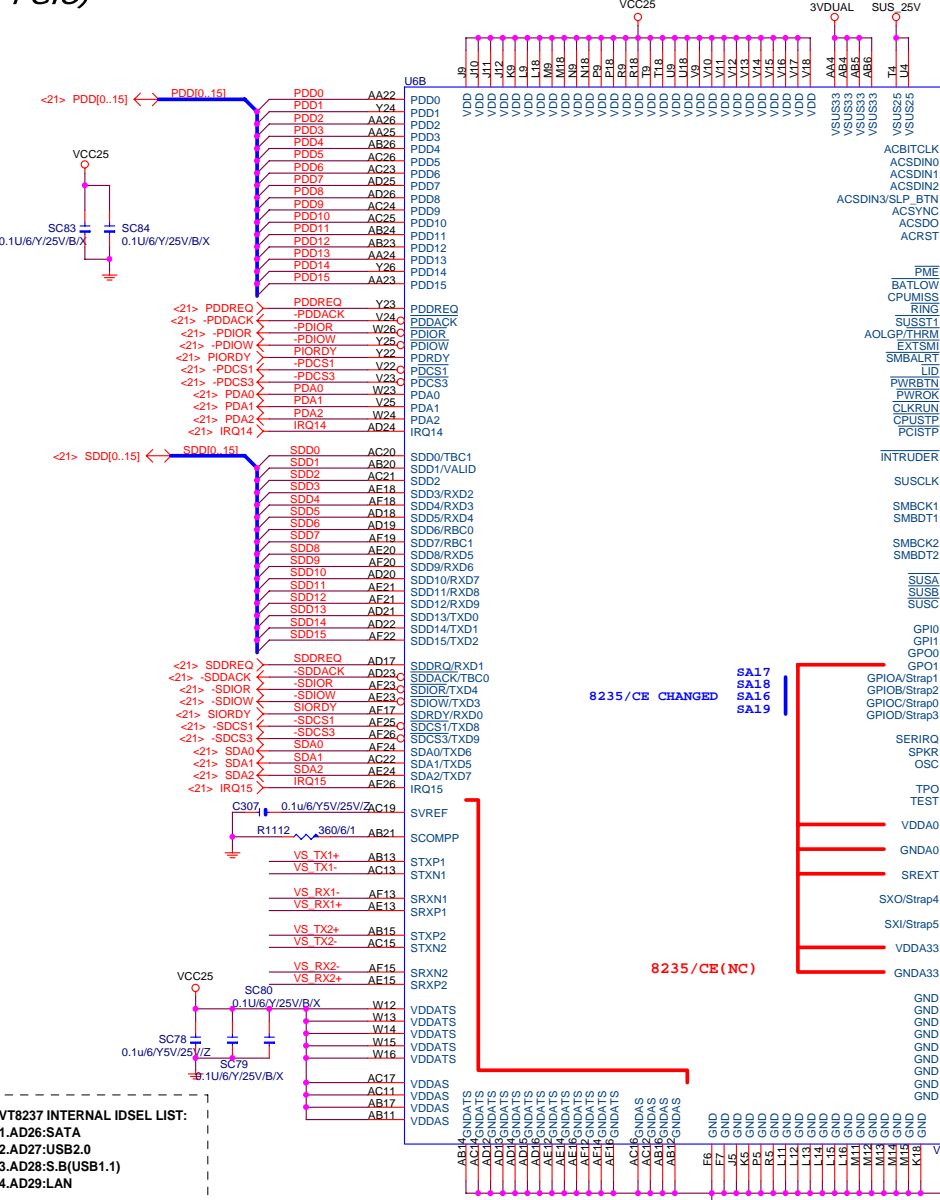
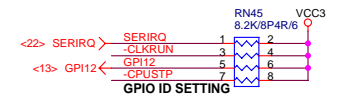
GIGABYTE		
SB VT8237CD (PCI, USB BUS, KB/MS)		
Title	Rev	
Size	Document Number	1.0
Custom	GA-8VM800PMD-775	
Date:	Wednesday, November 30, 2005	Sheet 13 of 30

SB VT8237R+ (SATA,IDE, AC97, POWER MANAGEMENT, PGIO)

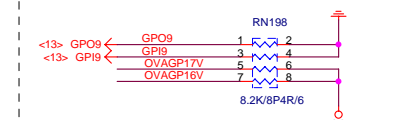
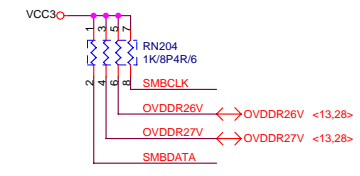
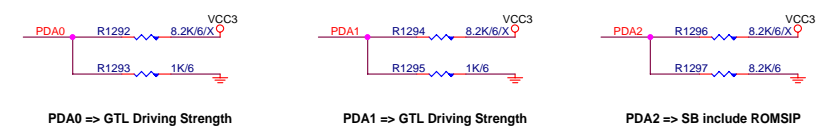
P/SDA1	P/SDA0	GTL Driving Strength
0	0	WEAK
0	1	MIDDLE WEAK
1	0	STRONG
1	1	STRONGEST

GPIOD = GTL Pull
 0 --> Enable
 1 --> Disable

GPIOB
 0 --> IOQ=8
 1 --> IOQ=1

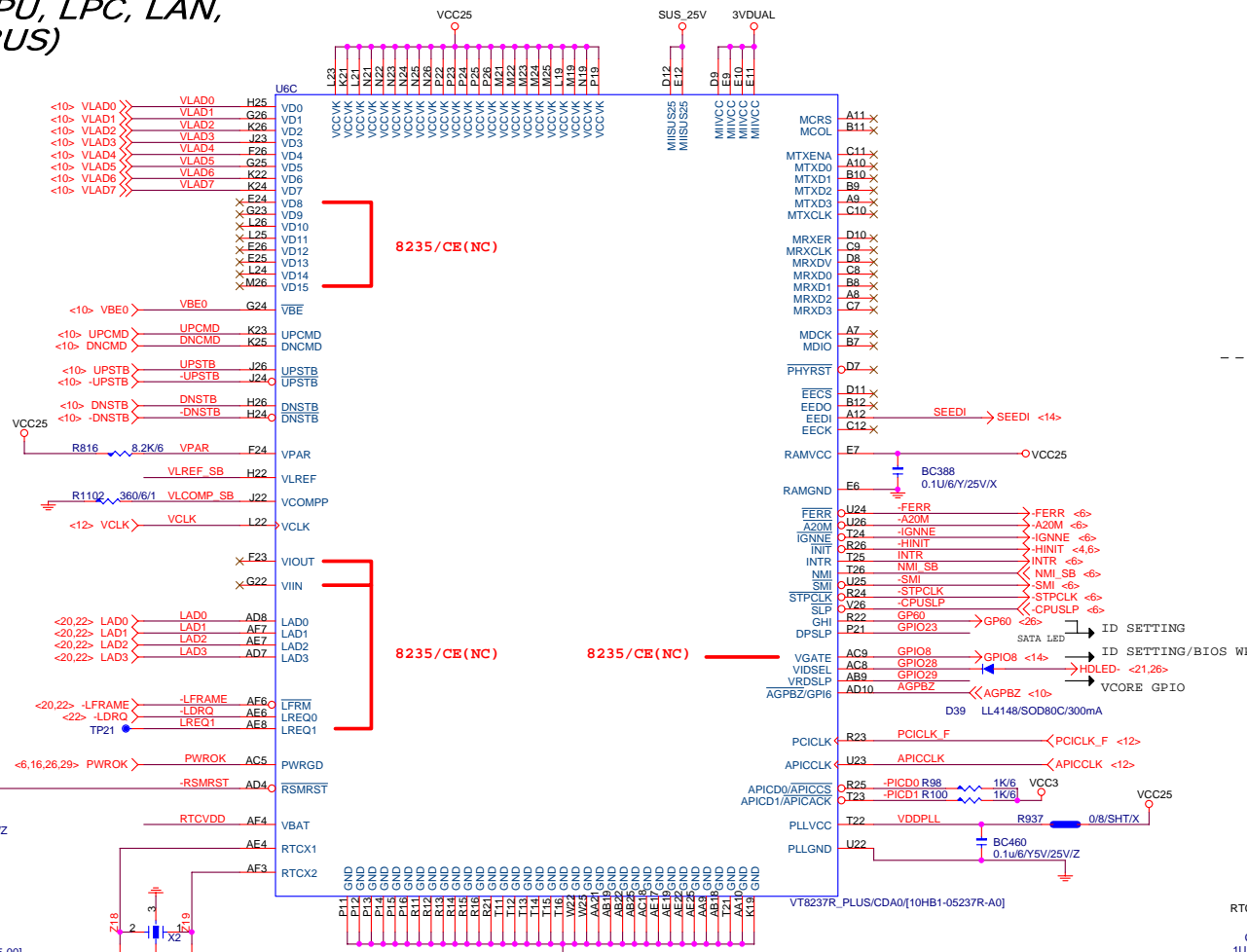


FOR 8237CD
 H: 66/AUTOMODE (GPIOA,C,H) L: 100/133/166/200

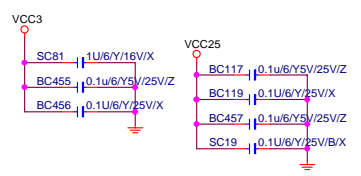


GIGABYTE			
Title	SB VT8237CD (SATA,IDE, AC97, POWER , GPIO)		
Size	Document Number	GA-8VM800PMD-775	Rev 1.0
Custom			
Date:	Wednesday, November 30, 2005	Sheet 14	of 30

SB VT8237(Vlink, CPU, LPC, LAN, LAN EEPROM, I2C BUS)

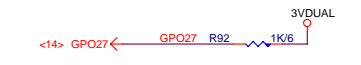


Decoupling capacitors

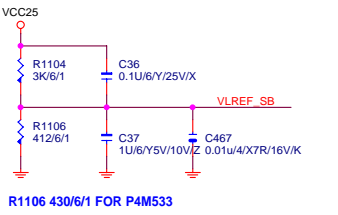
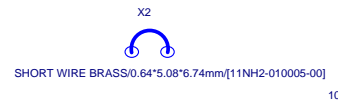
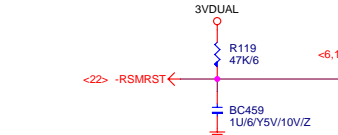
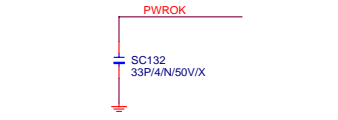
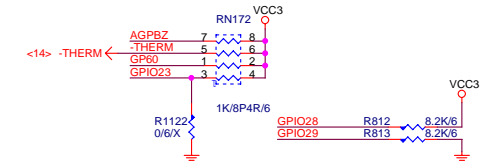
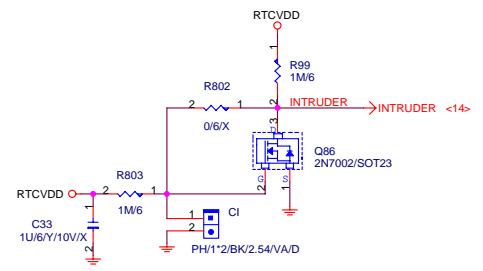


Internal MAC P.U

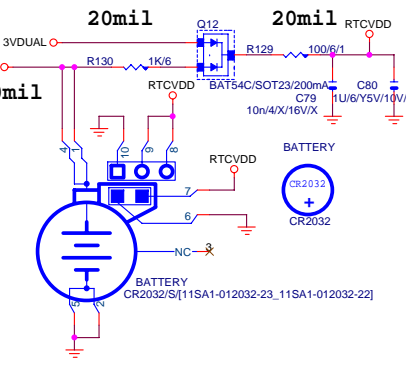
AN233 REMOVE Sep.8



Case Open Circuits



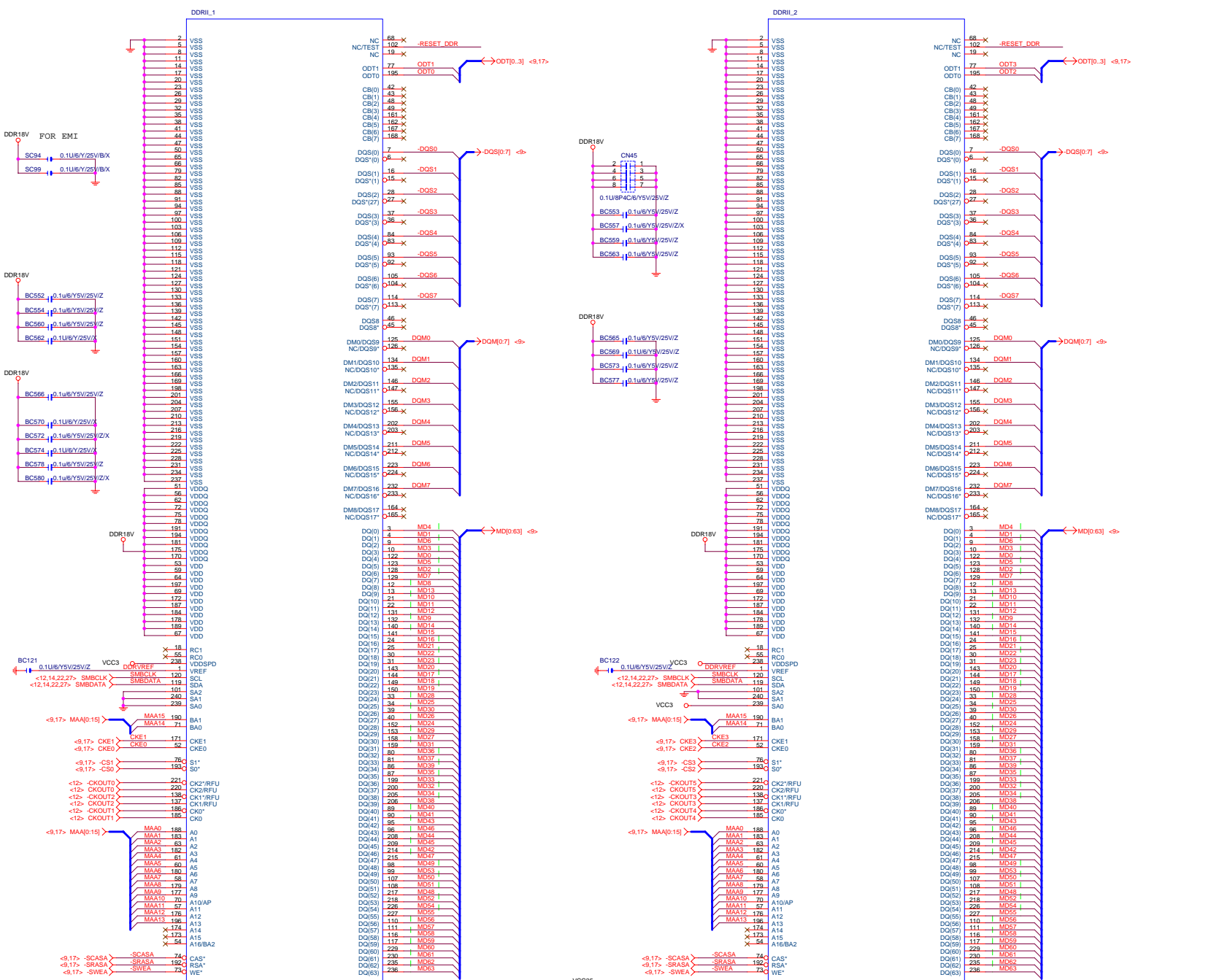
R1106 430/6/1 FOR P4M533



CLR_CMOS	CLEAR COMS JUMPER
SHORT	CLEAR CMOS
OPEN	NORMAL (Default)

GIGABYTE

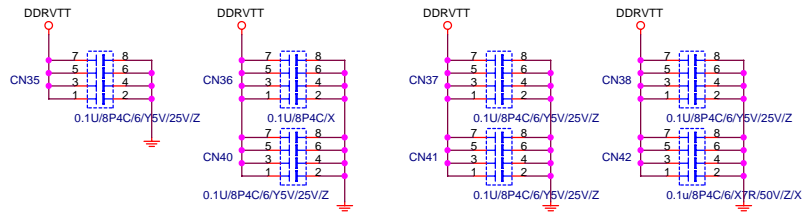
Title			SB VT8237CD(Vlink, CPU, LPC, LAN, I2C)		
Size	Document Number				Rev
Custom	GA-8VM800PMD-775				1.0
Date:	Wednesday, November 30, 2005	Sheet	15	of	30



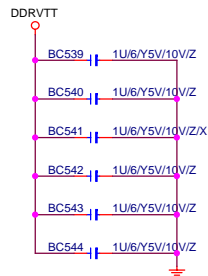
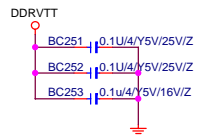
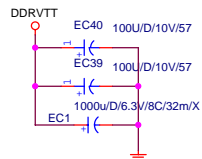
For Register DDR Support



GIGABYTE		
DDRII UNBUFFERED 1.2		
Title	Document Number	
Size	GA-8VM800PMD-775	
Customer	Rev 1.0	
Date	Wednesday, November 30, 2005	Sheet 16 of 30

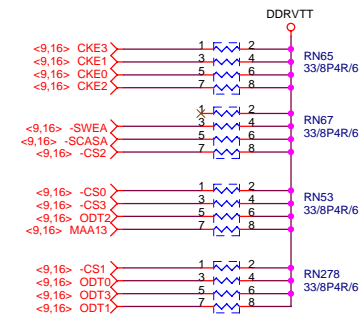
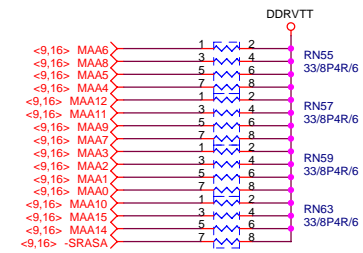
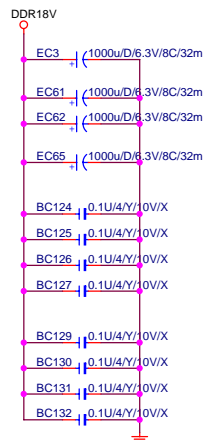


DDRVTT Decouple

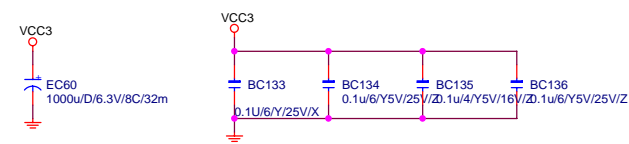
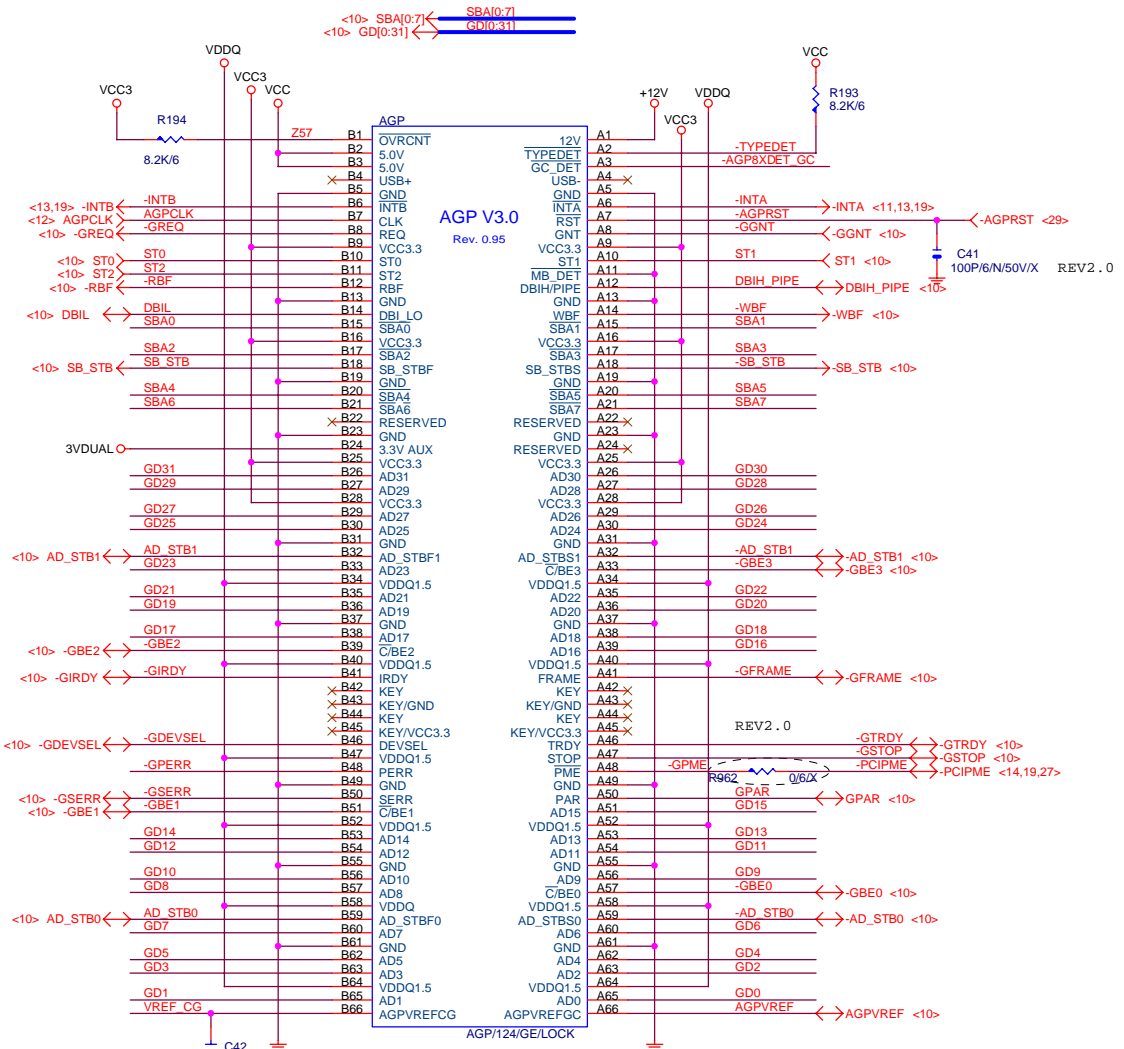


NOTE: Place these decoupling capacitors close to VTT_MEM termination resistors. (one decoupling capacitor for each two R-packs)

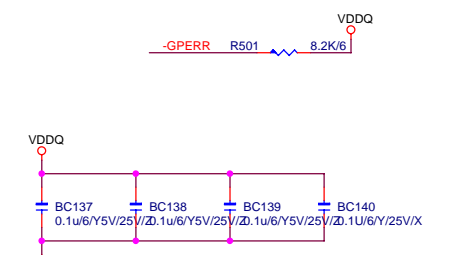
DDR18V Decouple



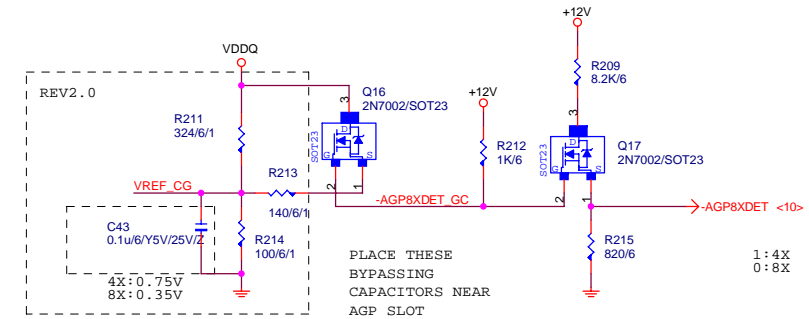
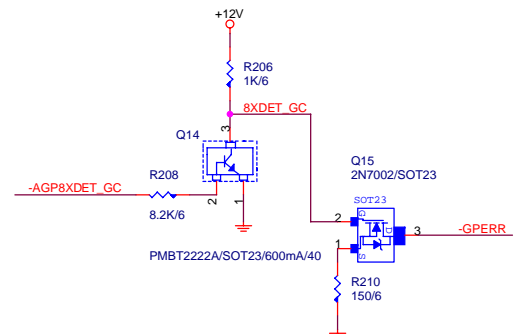
GIGABYTE		
Title DDRII Terminator		
Size Custom	Document Number GA-8VM800PMD-775	Rev 1.0
Date:	Wednesday, November 30, 2005	Sheet 17 of 30



Place 1 at each pair of 3.3V pins
Decoupling capacitors
(Place near AGP slot)



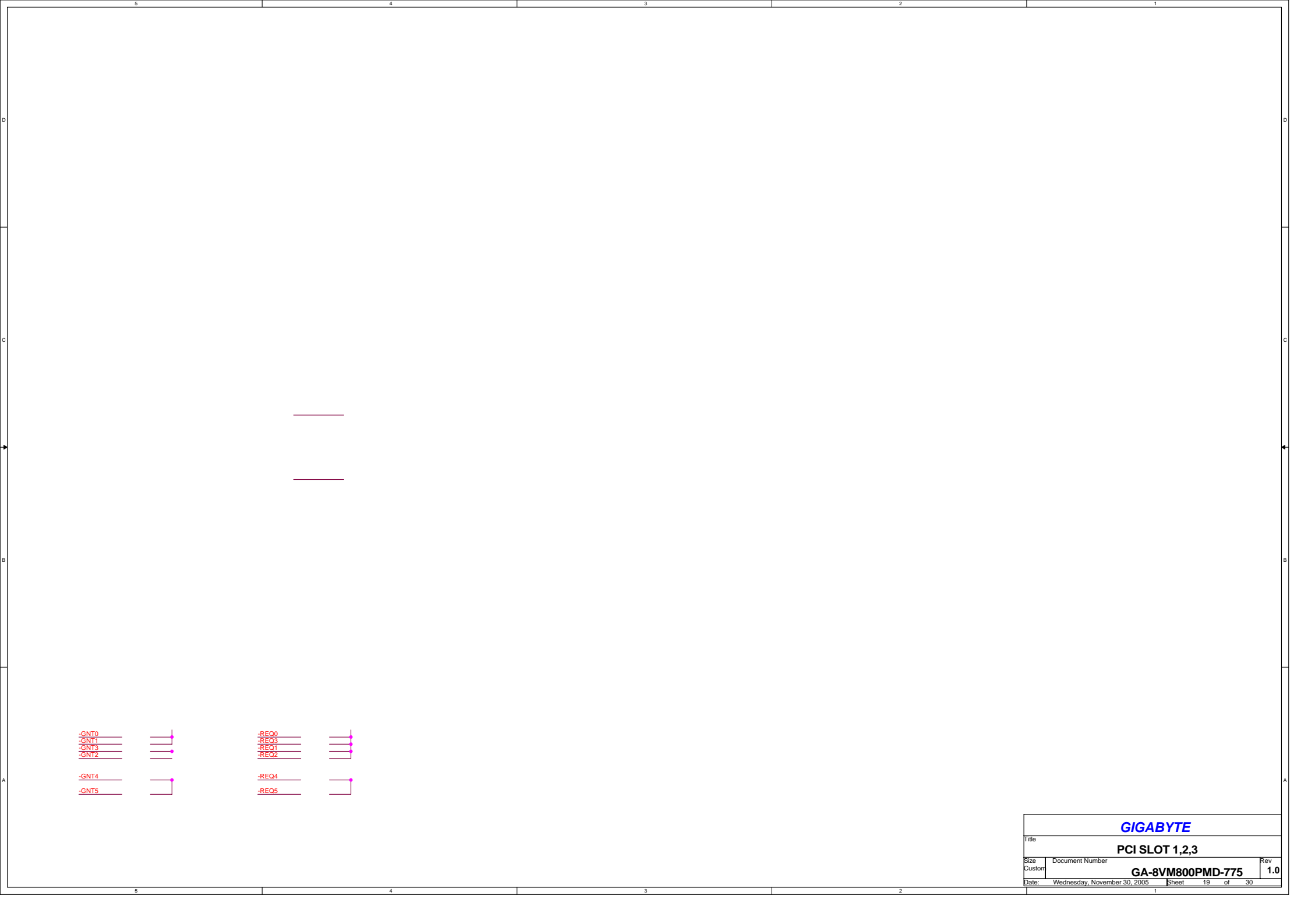
Place 1 at each pair of VDDQ pins
Place an additional for spread from A14 - A33



PLACE THESE BYPASSING CAPACITORS NEAR AGP SLOT

1 : 4X
0 : 8X

GIGABYTE		
AGP SLOT		
Title	Document Number	Rev
	GA-8VM800PMD-775	1.0
Date:	Wednesday, November 30, 2005	Sheet 18 of 30



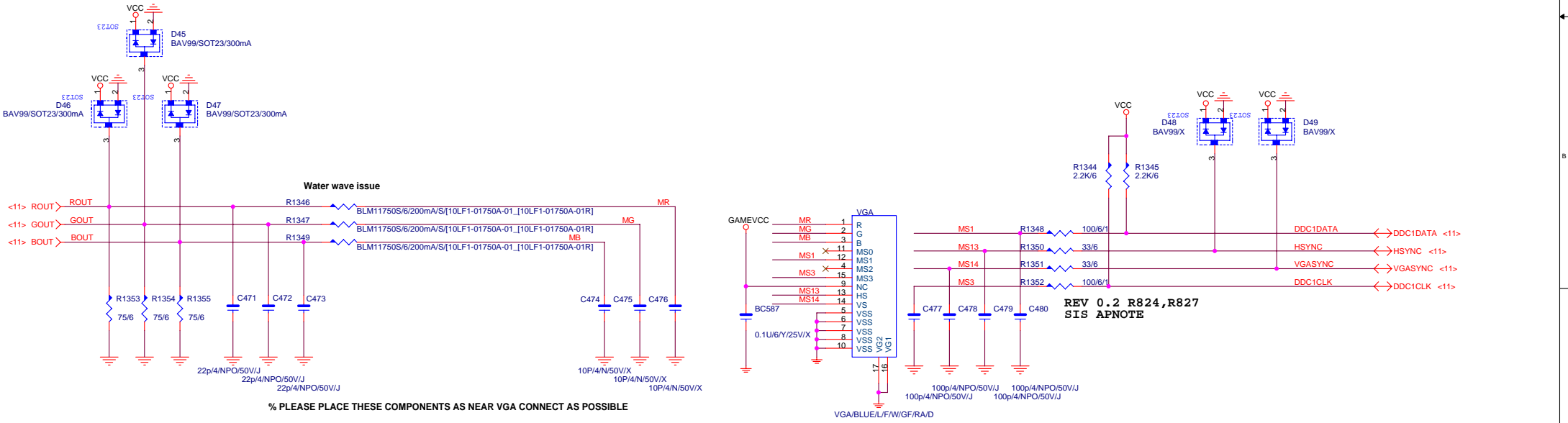
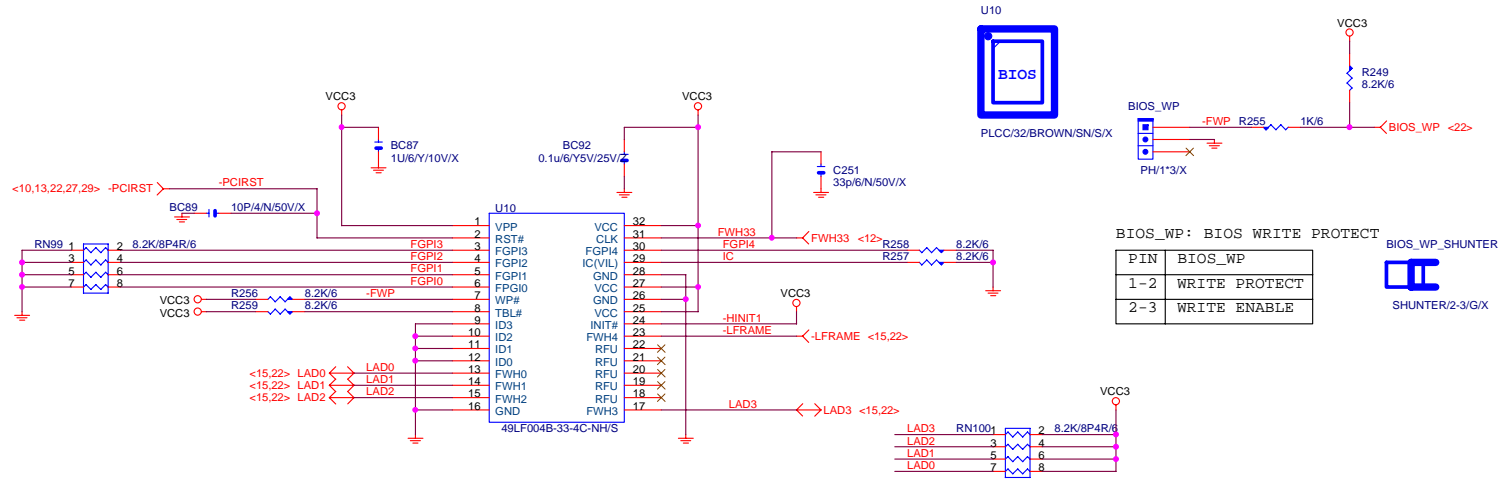
-GNT0
-GNT1
-GNT3
-GNT2

-GNT4
-GNT5

-REQ0
-REQ3
-REQ1
-REQ2

-REQ4
-REQ5

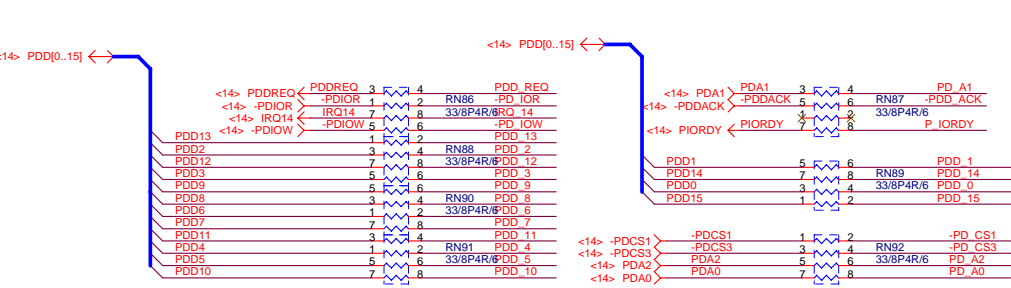
GIGABYTE		
Title PCI SLOT 1,2,3		
Size	Document Number	Rev
Custom	GA-8VM800PMD-775	1.0
Date:	Wednesday, November 30, 2005	Sheet 19 of 30



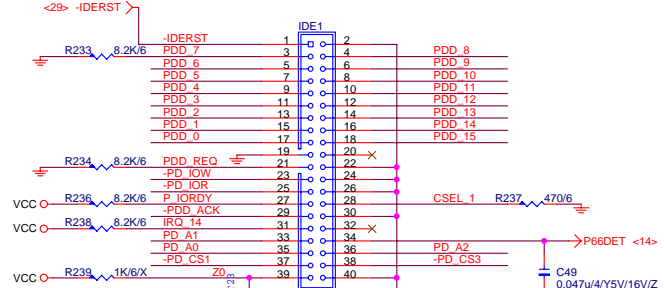
% PLEASE PLACE THESE COMPONENTS AS NEAR VGA CONNECT AS POSSIBLE

PRIMARY IDE

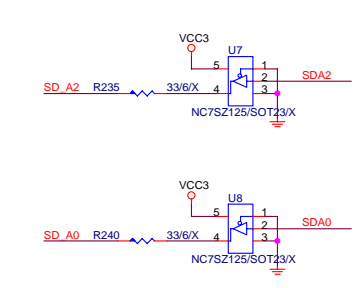
CD,DVD ROM STRAPPING ISSUE



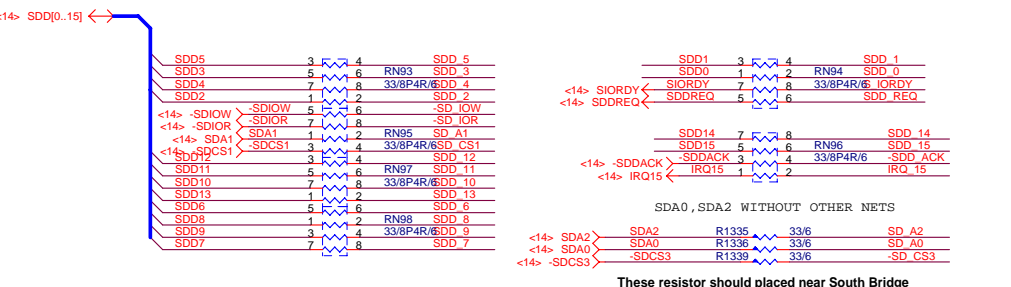
These resistor should placed near South Bridge



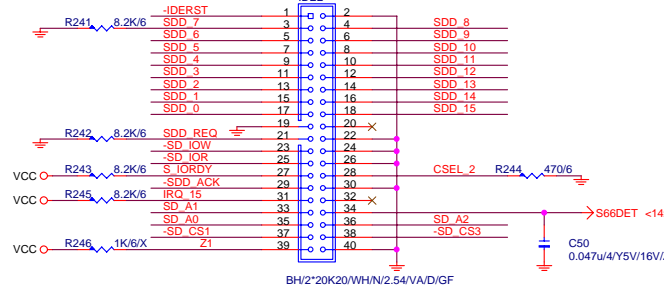
These resistor should placed near South Bridge



SECONDARY IDE

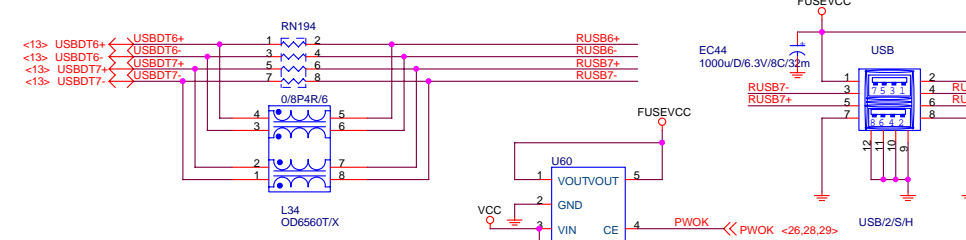
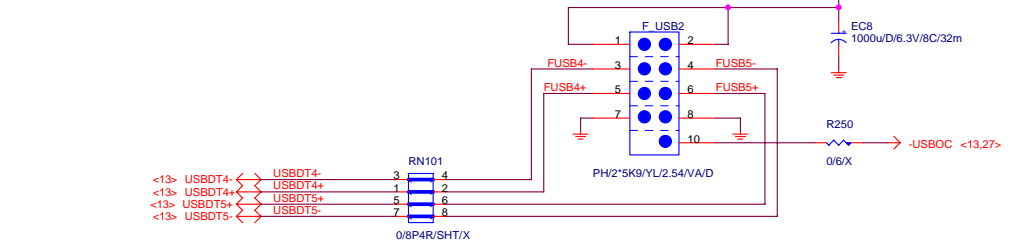


These resistor should placed near South Bridge

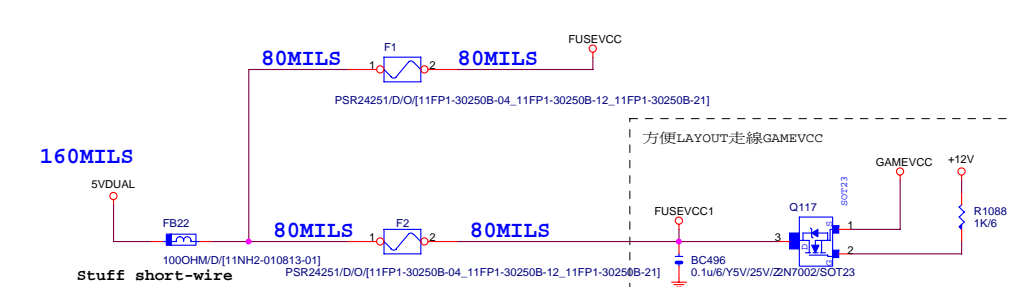


These resistor should placed near South Bridge

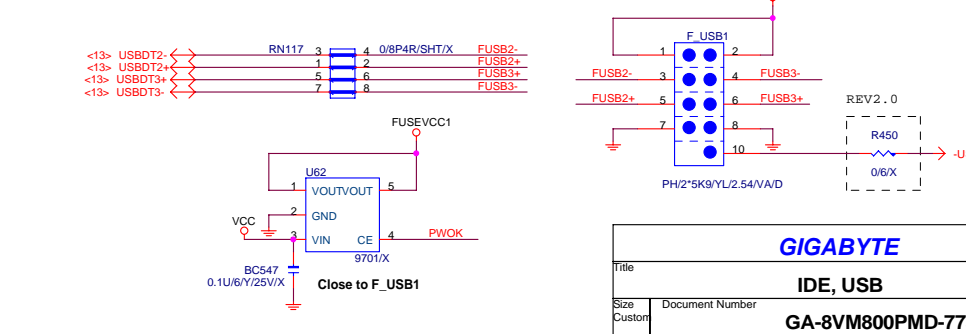
FRONT SIDE USB2



FUSEVCC, GAMEVCC



FRONT SIDE USB1



GIGABYTE

IDE, USB

Title: IDE, USB

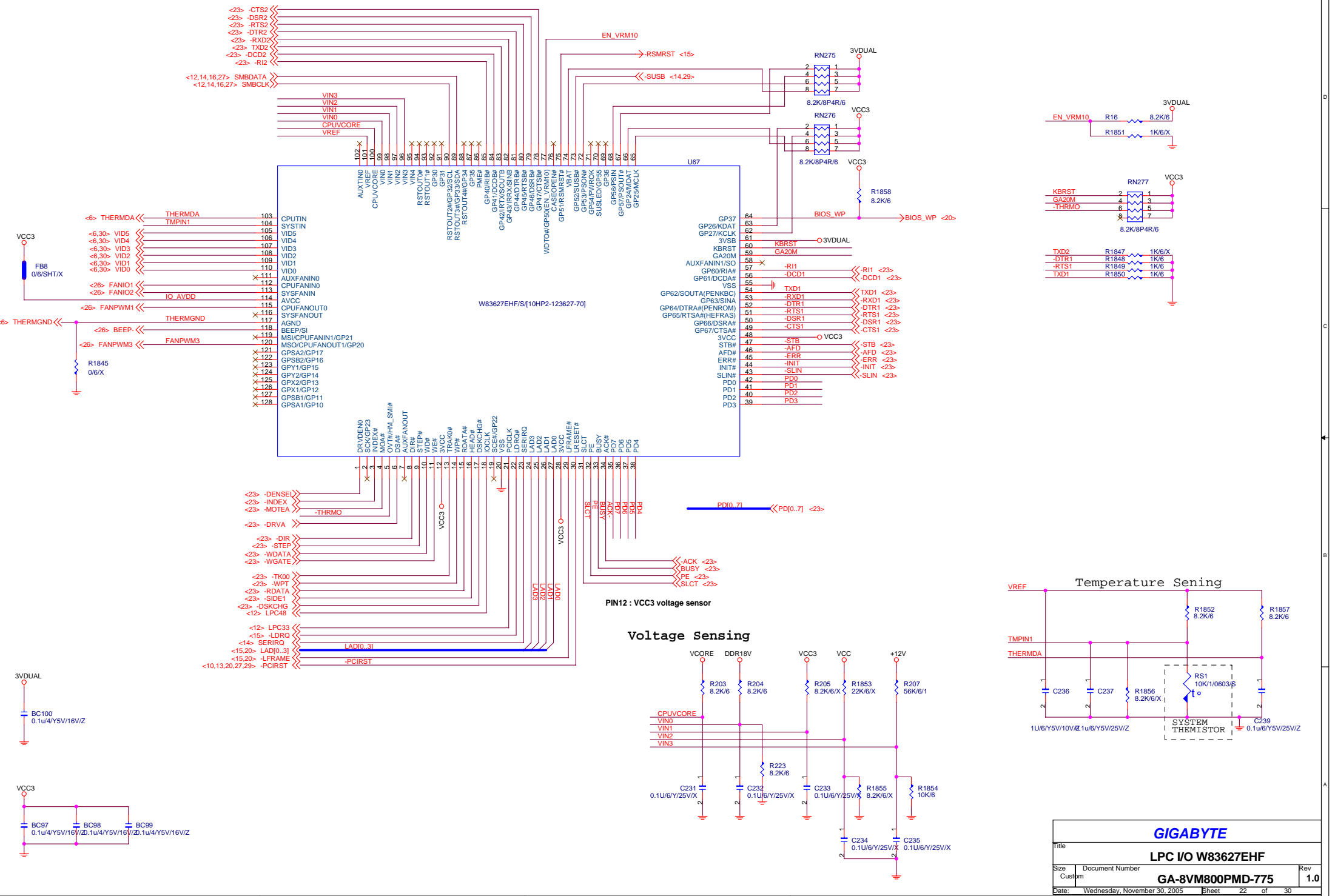
Size: Custom

Document Number: **GA-8VM800PMD-775**

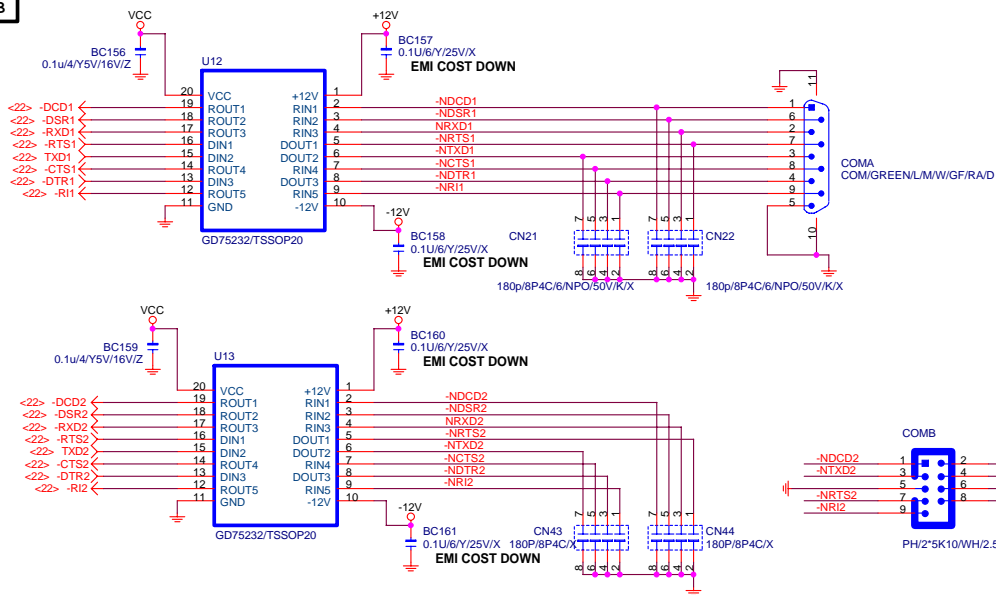
Date: Wednesday, November 30, 2005

Sheet: 21 of 30

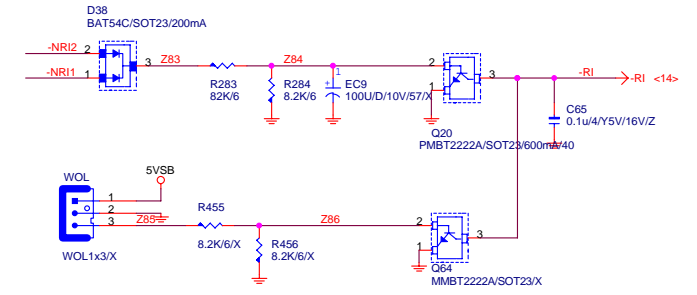
Rev: 1.0



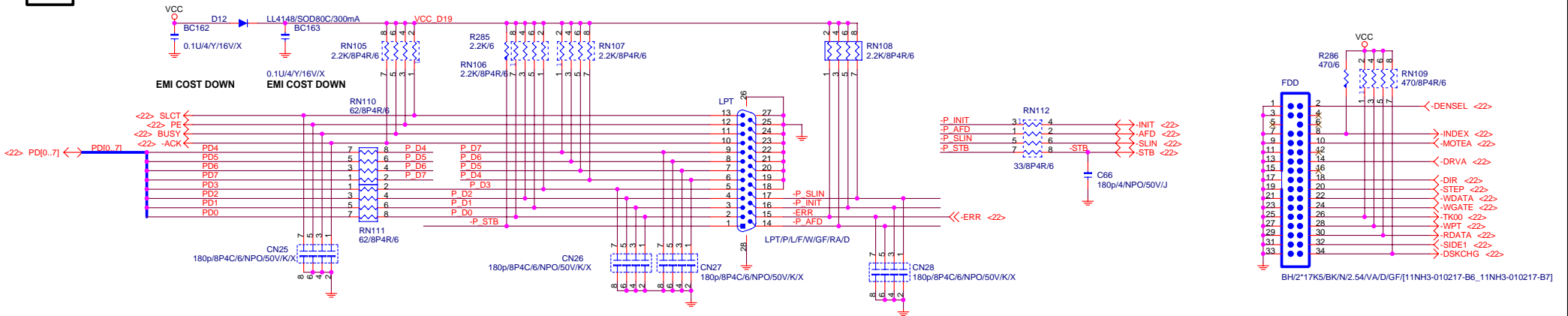
COM A, B



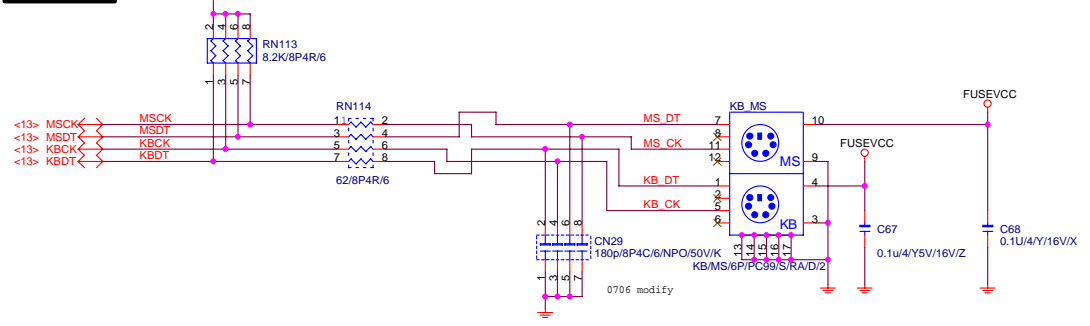
WAKE UP



LPT



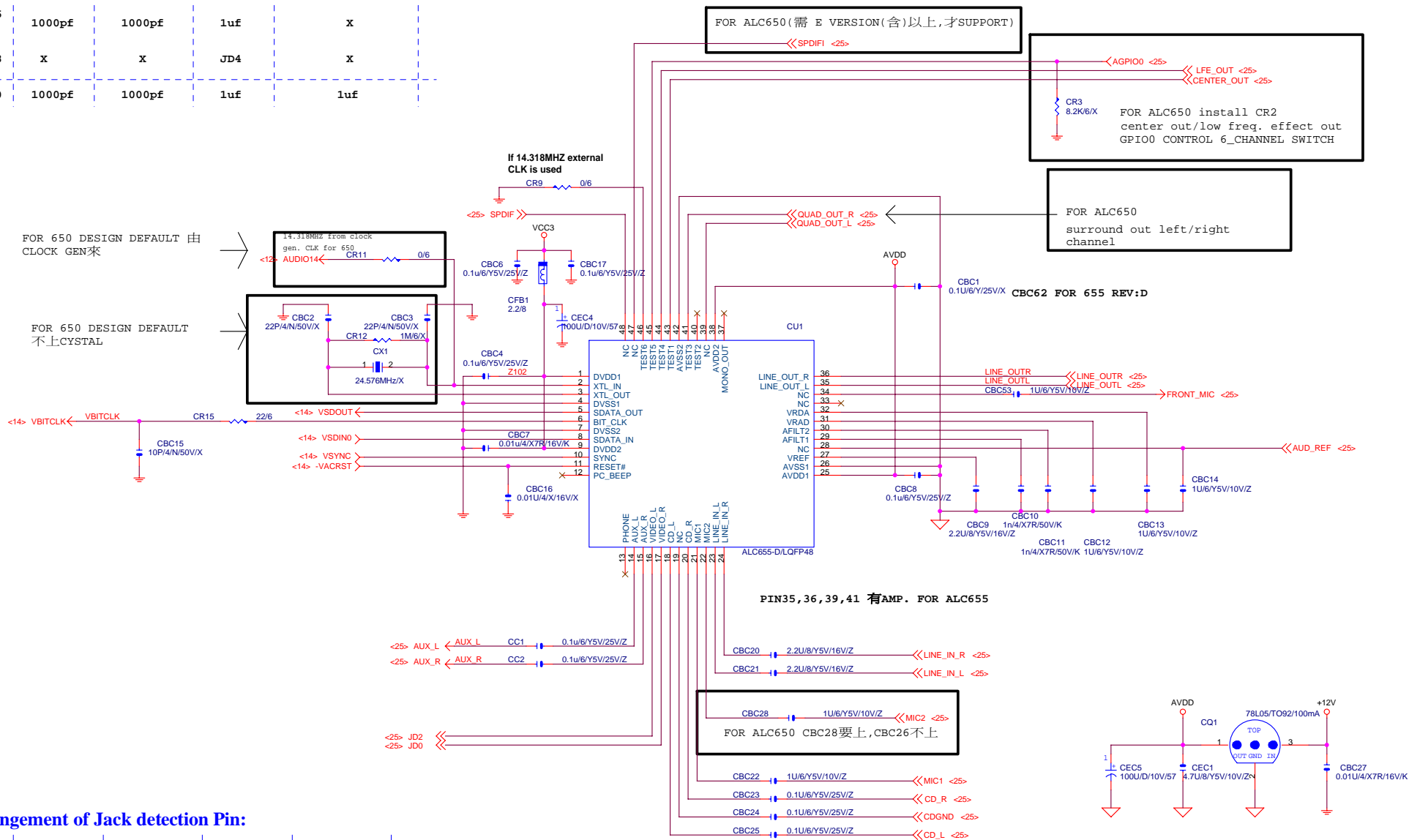
KBC/PS2



GIGABYTE		
COM,PRT,FDD,KB,IR		
Title	Document Number	Rev
	GA-8VM800PMD-775	1.0
Size		
Custom		
Date:	Wednesday, November 30, 2005	Sheet 23 of 30

Filter Cap design:

	Pin-29	Pin-30	Pin-31	Pin-32
ALC655 Rev D	1000pf	1000pf	1uf	Front-MIC2
ALC655 Rev C	1000pf	1000pf	1uf	X
ALC658	X	X	JD4	X
ALC650	1000pf	1000pf	1uf	1uf



Arrangement of Jack detection Pin:

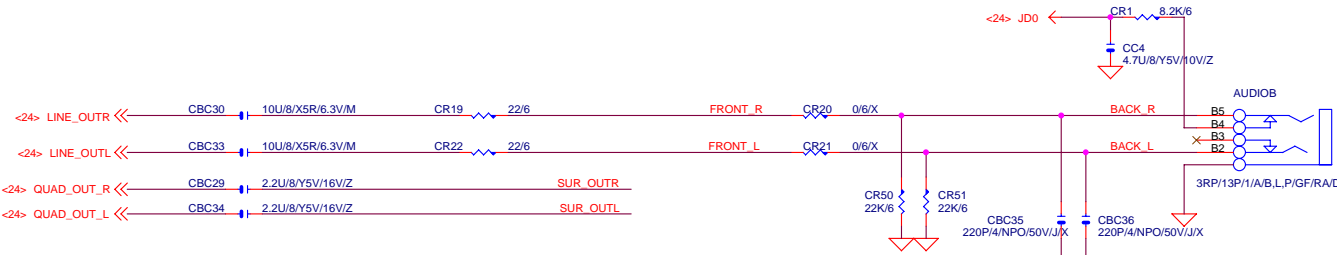
	Pin-45(JD0)	Pin-17(JD1)	Pin-16(JD2)	Pin-40(JD3)	Pin-31(JD4)
ALC655	for MIC-IN	for FRONT-OUT	for LINE-IN		
ALC658	for MIC-IN	for UAJ1	for UAJ2	for FRONT-OUT	for LINE-IN
				External pull high is needed	External pull high is needed

GIGABYTE		
Title ALC655		
Size	Document Number	Rev
Custom	GA-8VM800PMD-775	1.0
Date:	Wednesday, November 30, 2005	Sheet 24 of 30

LINE OUT

JDO,JD2,GPIO0 爲偵測DEVICE INPUT 時由LOW TO HIGH Edge trigger(pop manual)

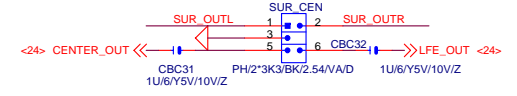
1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ以上AC 信號全部衰減 TO 0V 不會造成JDO 誤動作(無device 時play wav)



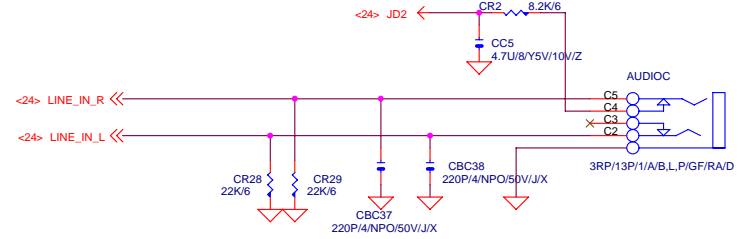
LINE OUT SENSING
 R>4K OHM=>POWER SPEAKER
 4K OHM>R>400 OHM=>MICROPHONE
 R<400 OHM=>HEADPHONE

FOR SUPPORT 6 CHANNEL,
SURROUND OUT

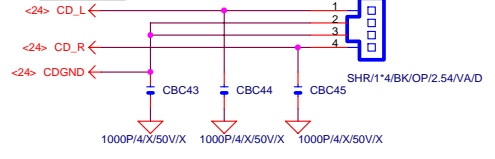
CENTER OUT,LOW
FREQUENCY EFFECT OUT



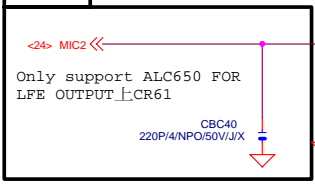
LINE-IN



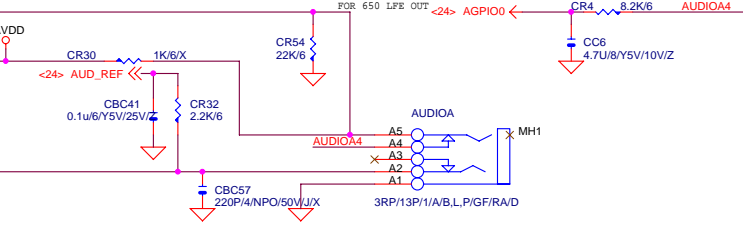
CD IN



MIC



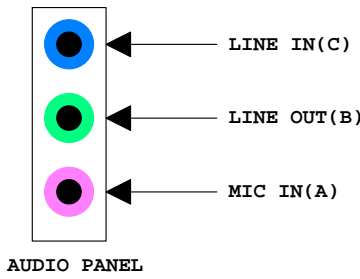
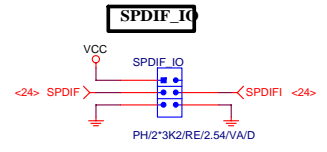
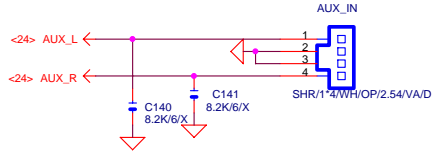
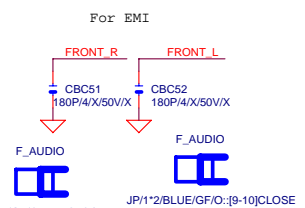
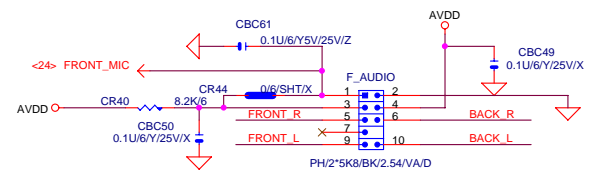
FOR 650 CENTER OUT
FOR ALC650 CR52=22 OHM



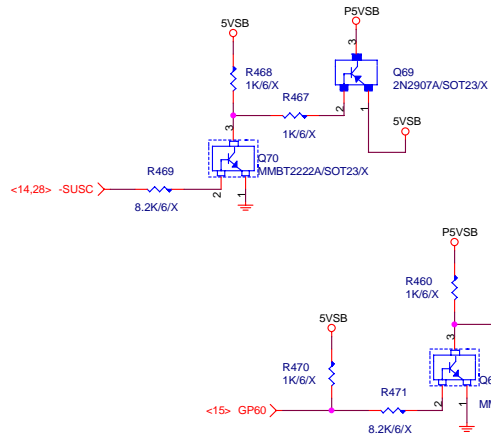
MICROPHONE IN SENSING(當INPUT)(利用vref 偏壓與CR43,CR32 並聯求出阻抗)
 7.1k ohm>R>2.3k ohm==>microphone in
 R<2.3k ohm or R>7.1k ohm==>unknown device

MICROPHONE IN SENSING(當OUTPUT)
 R>4K OHM=>POWER SPEAKER
 4K OHM>R>400 OHM=>MICROPHONE
 R<400 OHM=>HEADPHONE

INTEL FRONT AUDIO



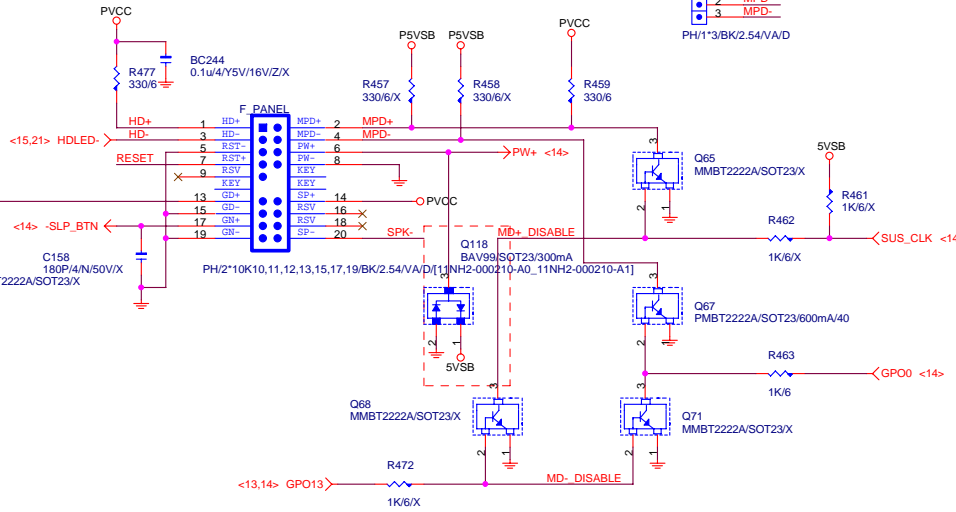
GIGABYTE			
AUDIO OUTPUT, GAME PORT			
Title	Document Number		Rev
Size	GA-8VM800PMD-775		1.0
Customer			
Date:	Wednesday, November 30, 2005	Sheet	25 of 30



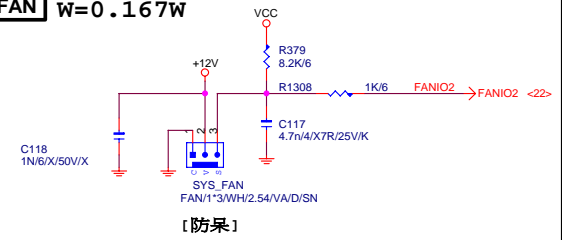
States for a single-color power LED

LED States	ACPI States		
OFF	S1, S3, S5		
Steady Green	S0		
Blinking Green	S0(message waiting)		

3 PIN POWER LED LAYOUT
PLACE CLOSE TO F_PANEL

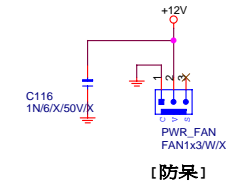


SYSTEM FAN w=0.167W



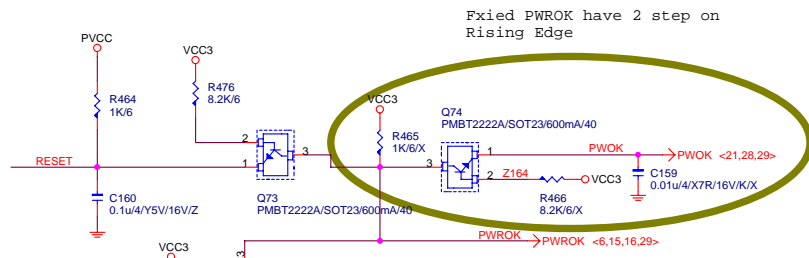
【防呆】

POWER FAN



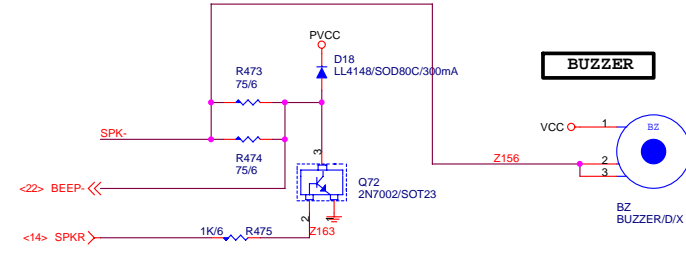
【防呆】

RESET

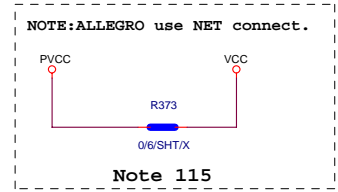


Fixed PWROK have 2 step on Rising Edge

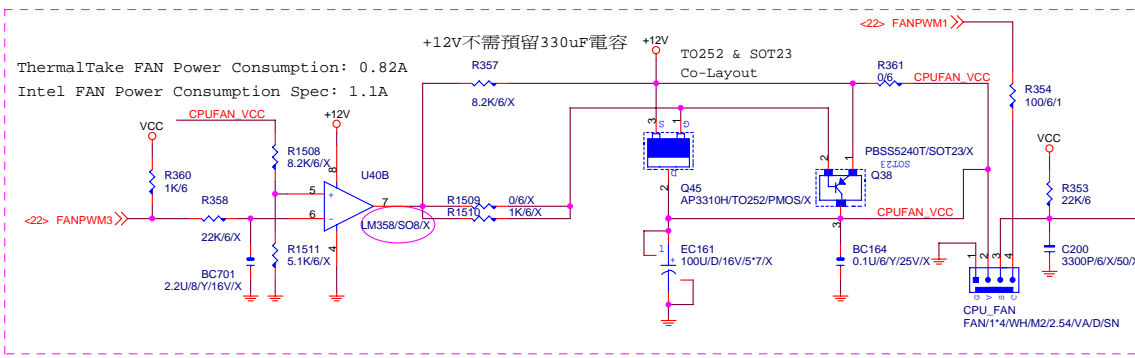
BUZZER



PVCC 在VCC PLANE 切割互連



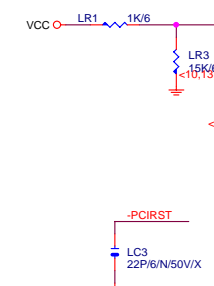
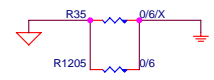
CPU FAN



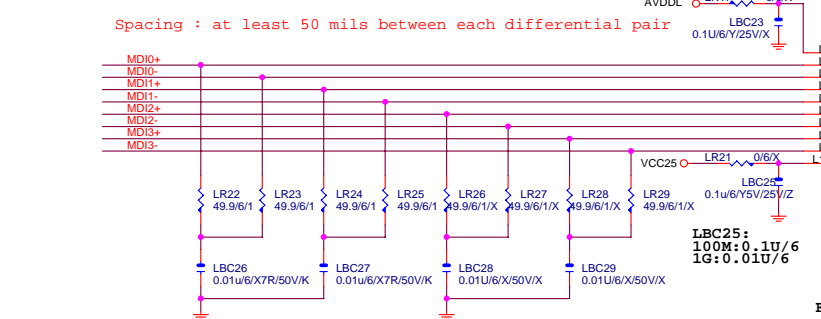
ThermalTake FAN Power Consumption: 0.82A
Intel FAN Power Consumption Spec: 1.1A

	10/100	Giga	Giga
	8100C	8110S	8110SB
AVDDH	N/A	3.3V	3.3V
V_12P	2.5V	N/A	3.3V
AVDDL	3.3V	2.5V	2.5V
V_DAC	N/A	2.5V	2.5V
DVDD	2.5V	1.8V	1.3V
DVDD_A	2.5V	1.8V	1.3V

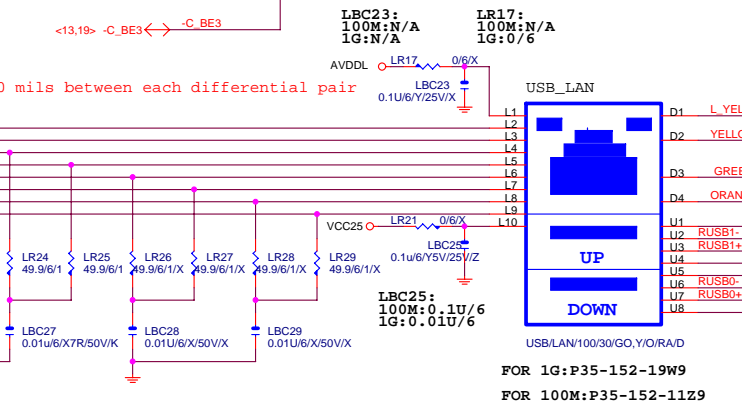
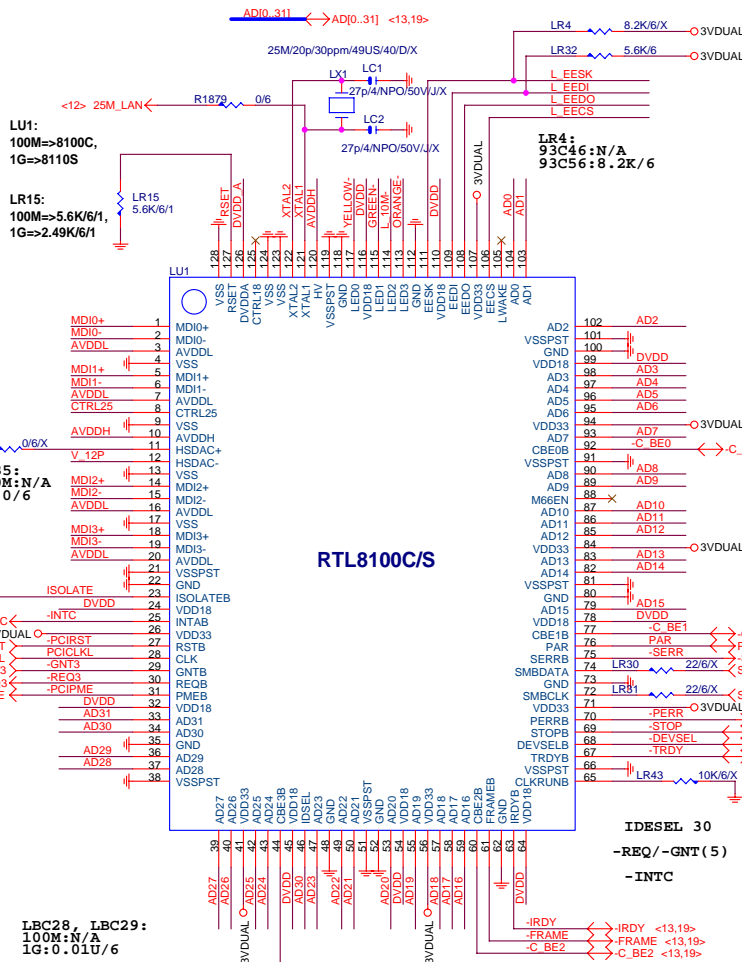
	Yellow	Dual Color LED	
		Green	Orange
10Mb	---	OFF	OFF
100Mb	---	ON	OFF
1Gb	---	OFF	ON
Link	ON	---	---
Active	Blink	---	---



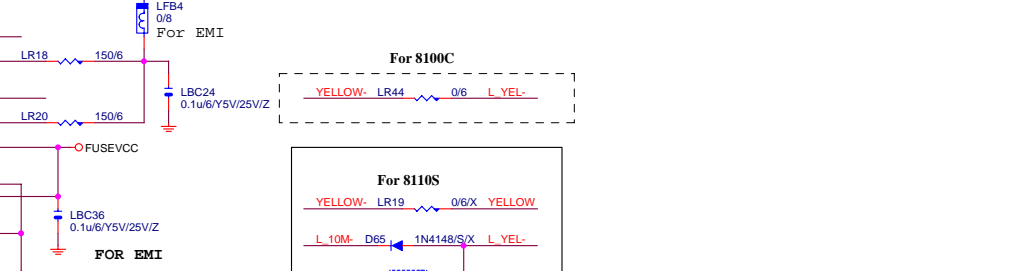
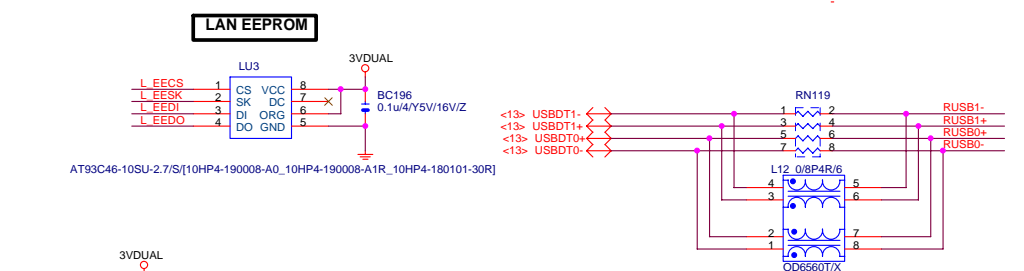
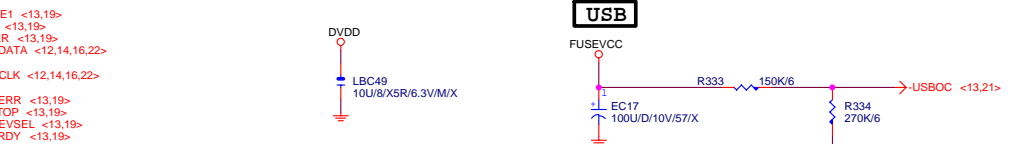
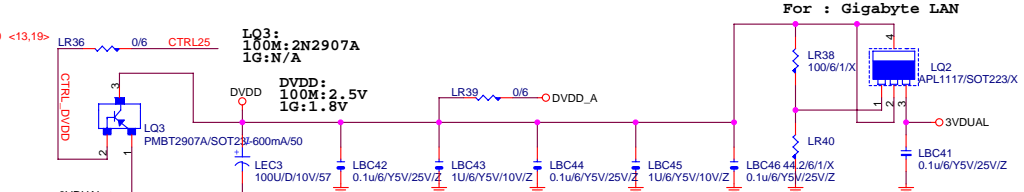
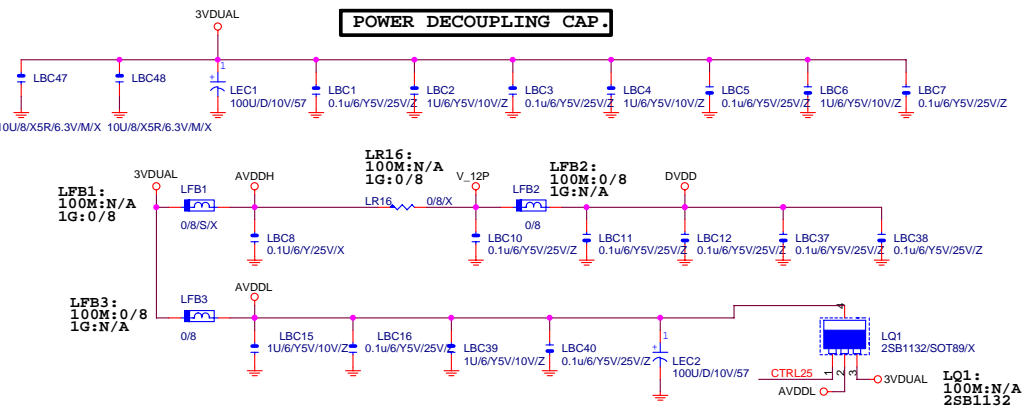
LR26-LR29: 100M:N/A, 1G:49.9/6/1
 LBC28, LBC29: 100M:N/A, 1G:0.01U/6



LR22, LR23, LBC26 Close to Lan Chip
 Realtek suggestion (TX+, TX-).
 LR24, LR25, LBC27 Close to Lan Chip
 Realtek Suggestion (RX+, RX-).

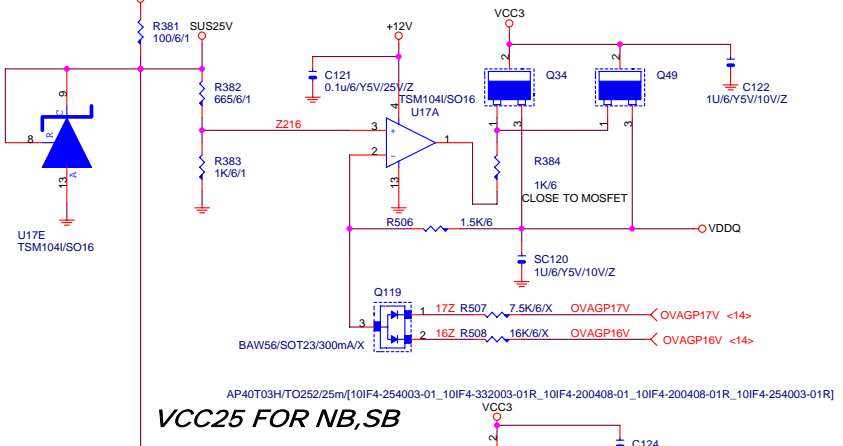
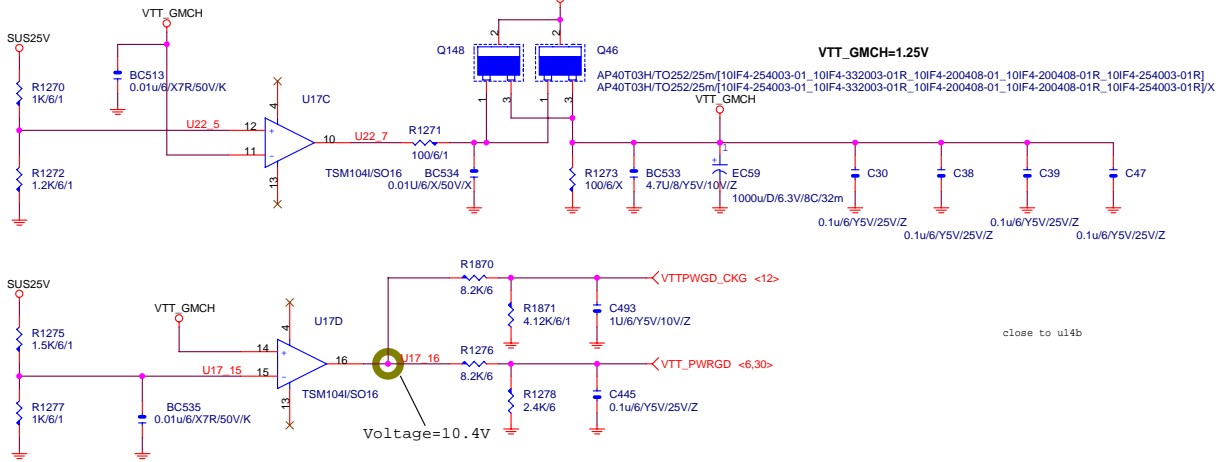


FOR 1G:P35-152-19W9
 FOR 100M:P35-152-11Z9

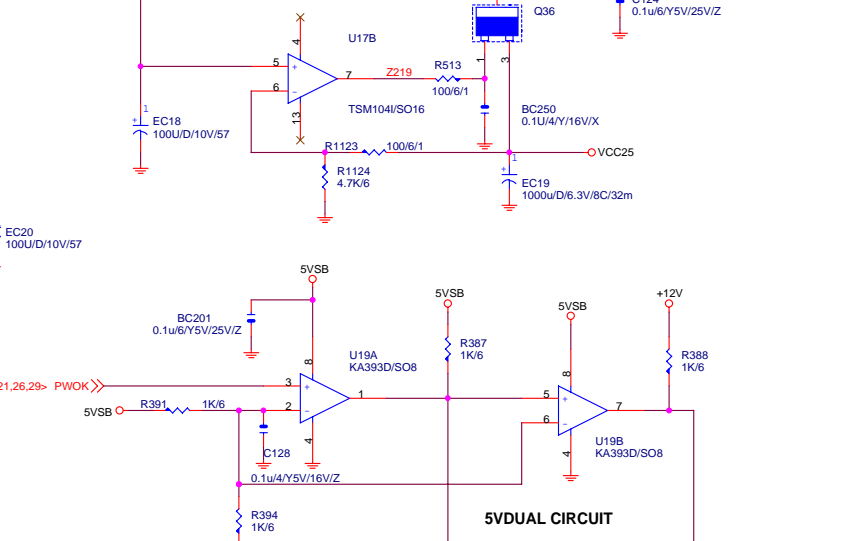
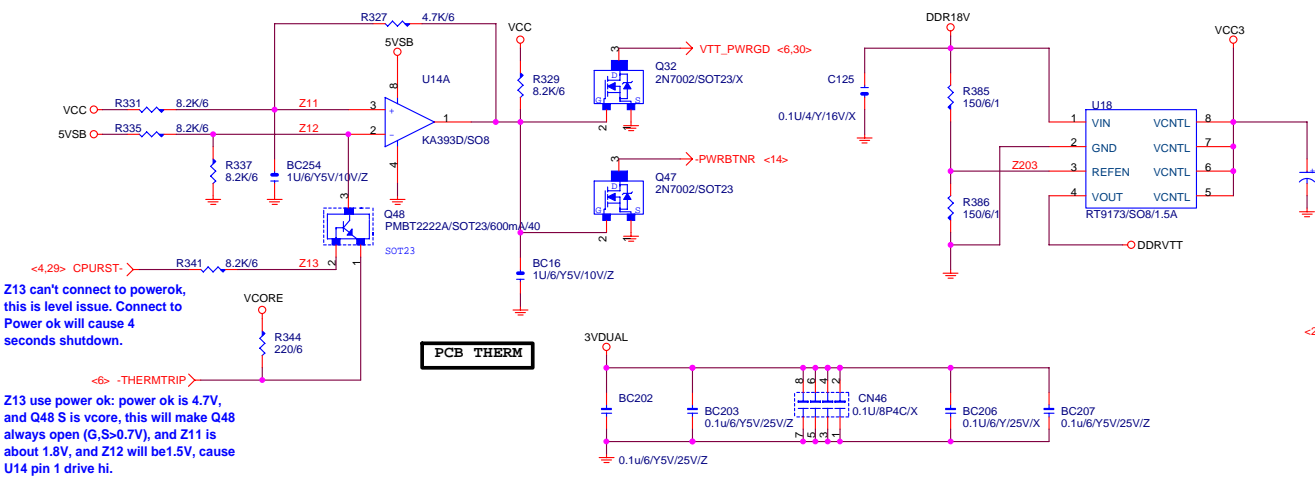


GIGABYTE		
LAN RTL8110C		
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Size	Date	Sheet
Custom	Wednesday, November 30, 2005	27 of 30

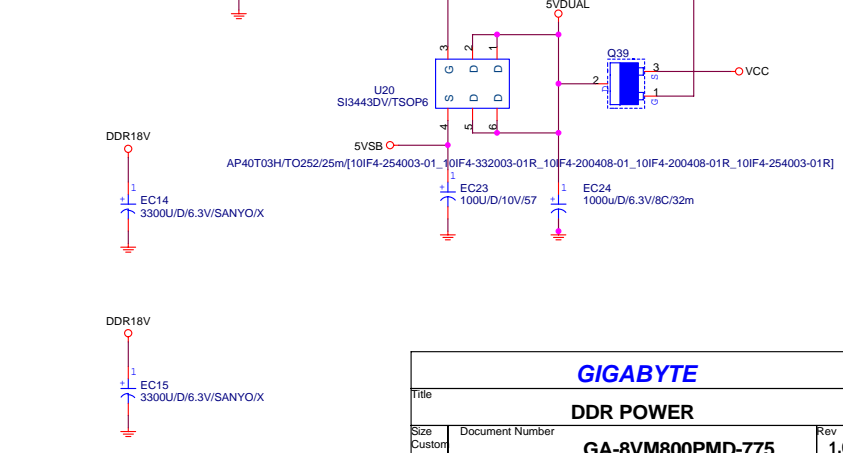
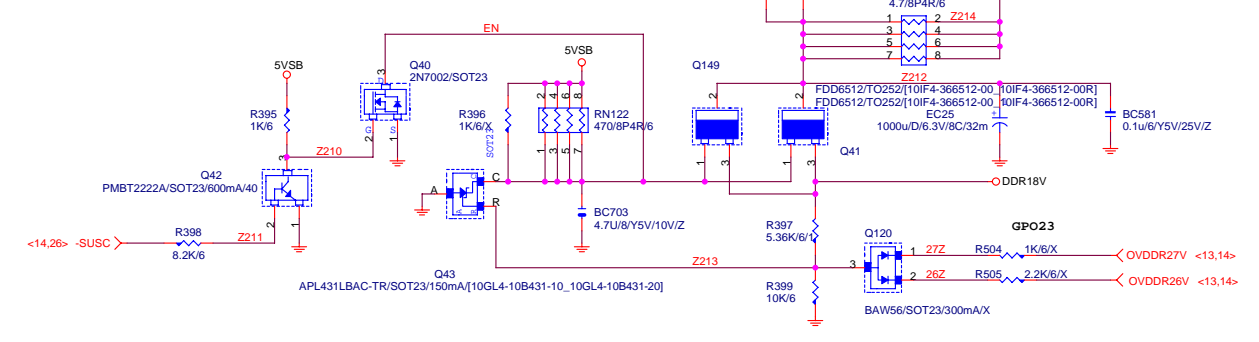
VDDQ FOR AGP 4X/8X



0.9V DDRVTT SWITCHING SOLUTION



DDR25V FOR DDR DIMM & NB



GIGABYTE		
DDR POWER		
Title	Document Number	Rev
	GA-8VM800PMD-775	1.0
Size	Date	Sheet
Custom	Wednesday, November 30, 2005	28 of 30

