





**INTEL PENTIUM 4 (NORTHWOOD) PROCESSOR WITH  
 INTEL 845 (BROOKDALE) CHIPSET  
 SUPPORT 2 UNBUFFERED DDR200 DIMM UP TO 2GB  
 INTEL I/O CONTROLLER HUB 2 (ICH2)  
 ONBOARD ATI RAGE XL WITH 8MB FRAME MEMORY  
 DUAL INTEL 82550 ETHERNET CONTROLLER (SERVER ADAPTER)  
 ONBOARD PROMISE PDC20276 IDE RAID  
 ONBOARD WINBOND IPMI CIRCUIT**

**GA-8IRXRR FOR SR113  
 Revision 1.0  
 4/18/2002**

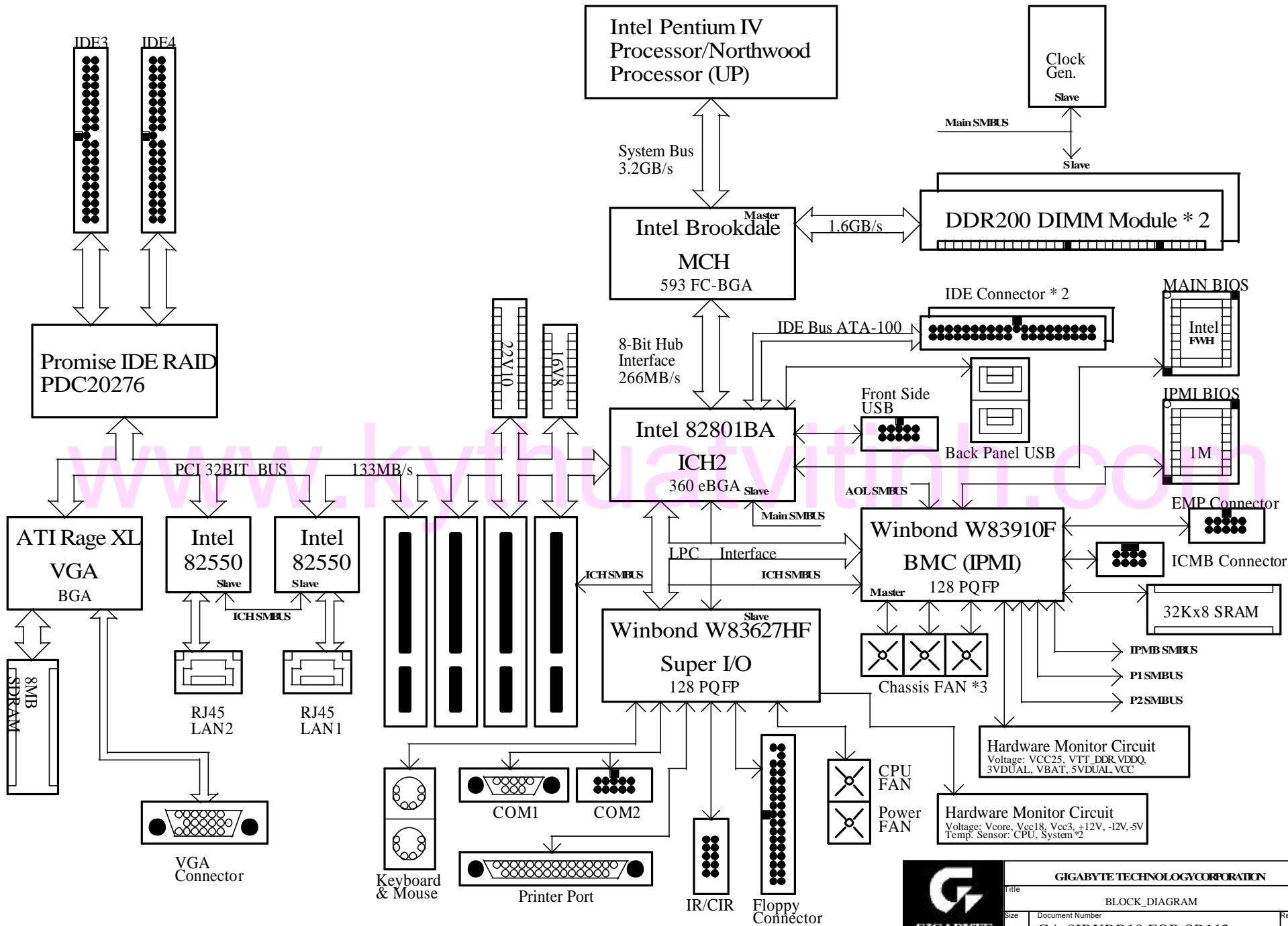
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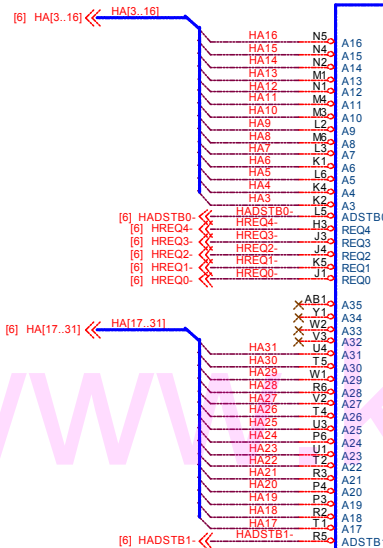
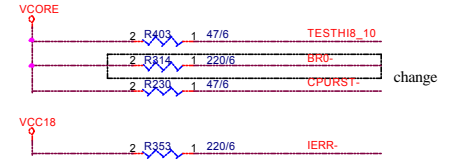
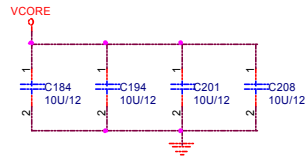
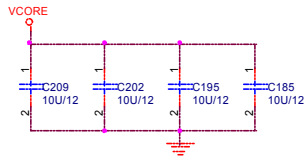
	COMPONENT SIDE (0.5 oz. Copper)
	VCC SIDE (1 oz. Copper)
	GND SIDE (1 oz. Copper)
	SOLDER SIDE (0.5 oz. Copper)



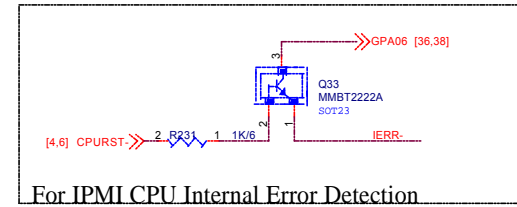
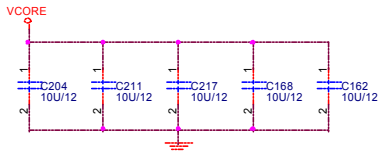
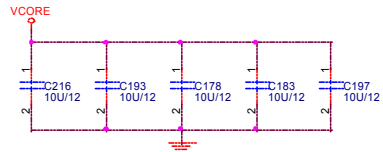
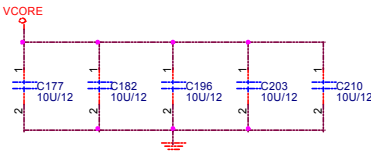
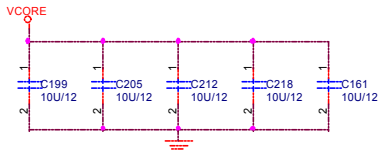
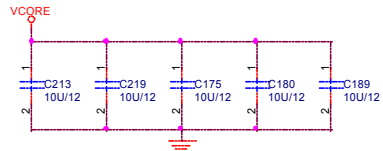
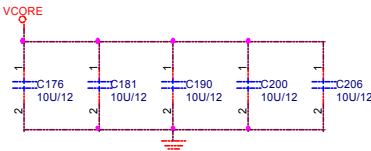
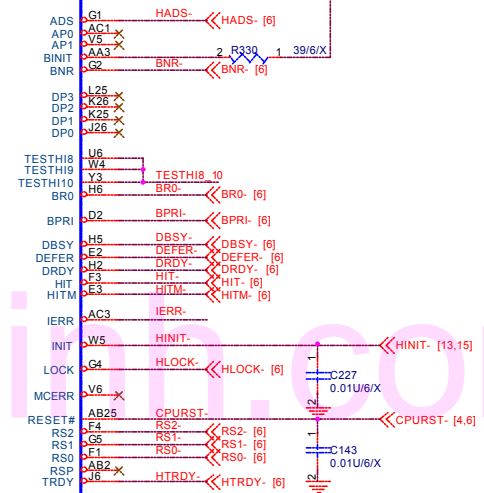
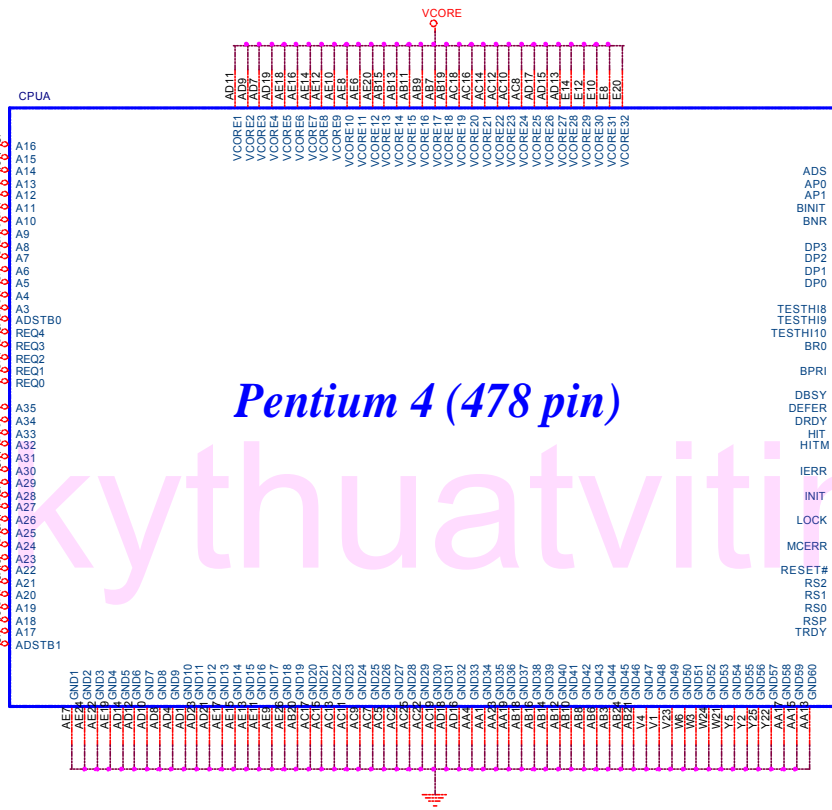
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<b>GIGABYTE TECHNOLOGY CORPORATION</b>			
Title		BLOCK_DIAGRAM	
Size	Document Number	Rev	
	<b>GA-8IRXRR10 FOR SR113</b>	1.0	
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Pentium 4 (478 pin)



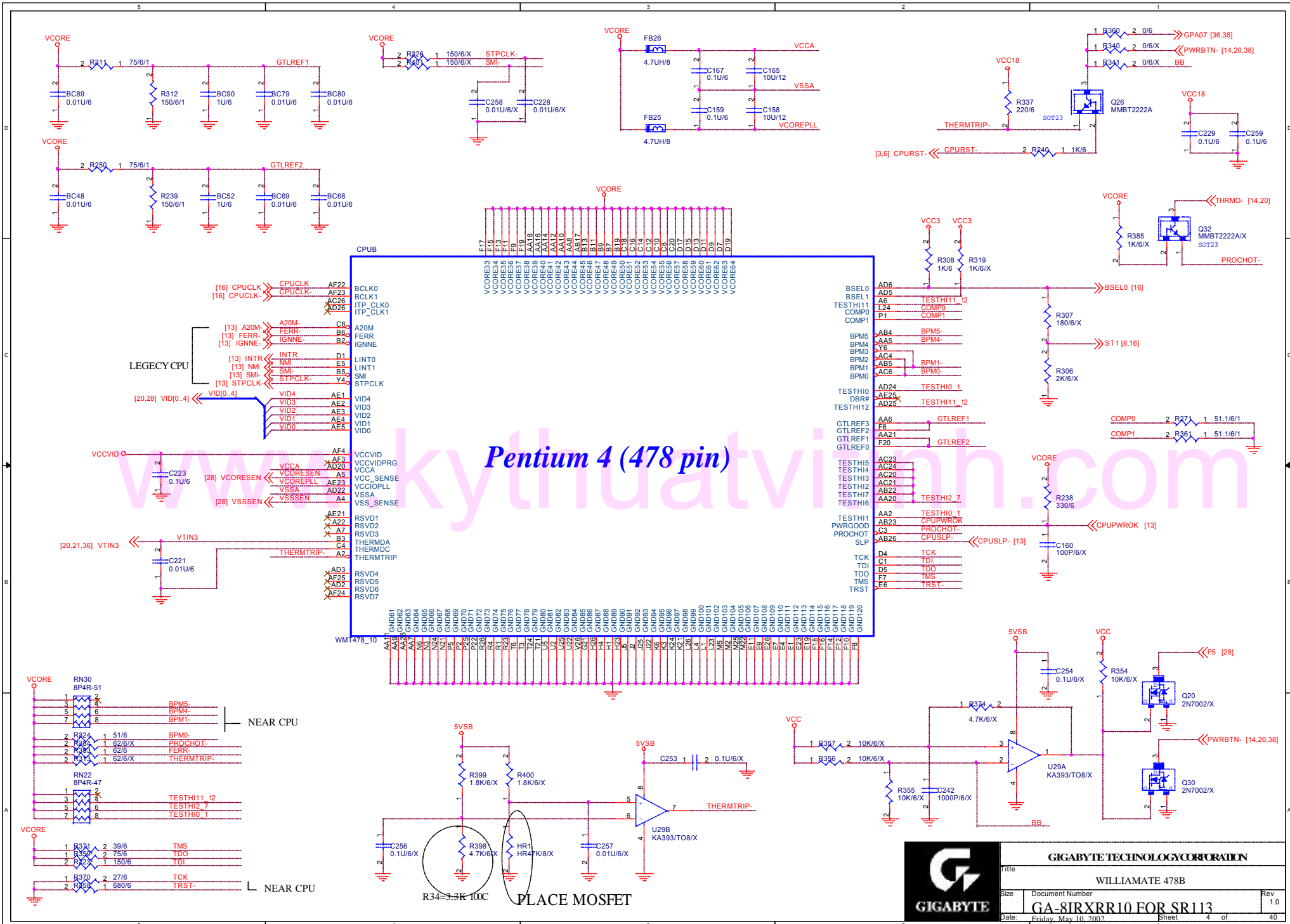
**GIGABYTE TECHNOLOGY CORPORATION**

WILLIAMATE 478A

GA-8IRXRR10 FOR SR113

Rev 1.0

Date: Friday, May 10, 2002



# Pentium 4 (478 pin)

LEGACY CPU

- [13] A20M → A20M-
- [13] FERR → FERR-
- [13] IGNE → IGNE-
- [13] INTR → INTR-
- [13] NM → NM-
- [13] SMI → SMI-
- [13] STPCLK → STPCLK-
- [20,28] VID[0..4] → VID[0..4]

- VCCVID → AF4
- VCCVIDPRG → AF3
- VCC → AF20
- VCC\_SENSE → AE23
- VCCIOPLL → AD22
- VSSA → AD22
- VSS\_SENSE → A4
- RSVD1 → AE21
- RSVD2 → A22
- RSVD3 → A7
- RSVD4 → B3
- RSVD5 → C4
- RSVD6 → AF24
- RSVD7 → AF24

- VTIN3 → [20,21,36] VTIN3
- THERMTRIP → A2
- THERMDA → B3
- THERMDC → A2
- THERMTRIP → A2
- RSVD4 → AF25
- RSVD5 → AF26
- RSVD6 → AF27
- RSVD7 → AF28

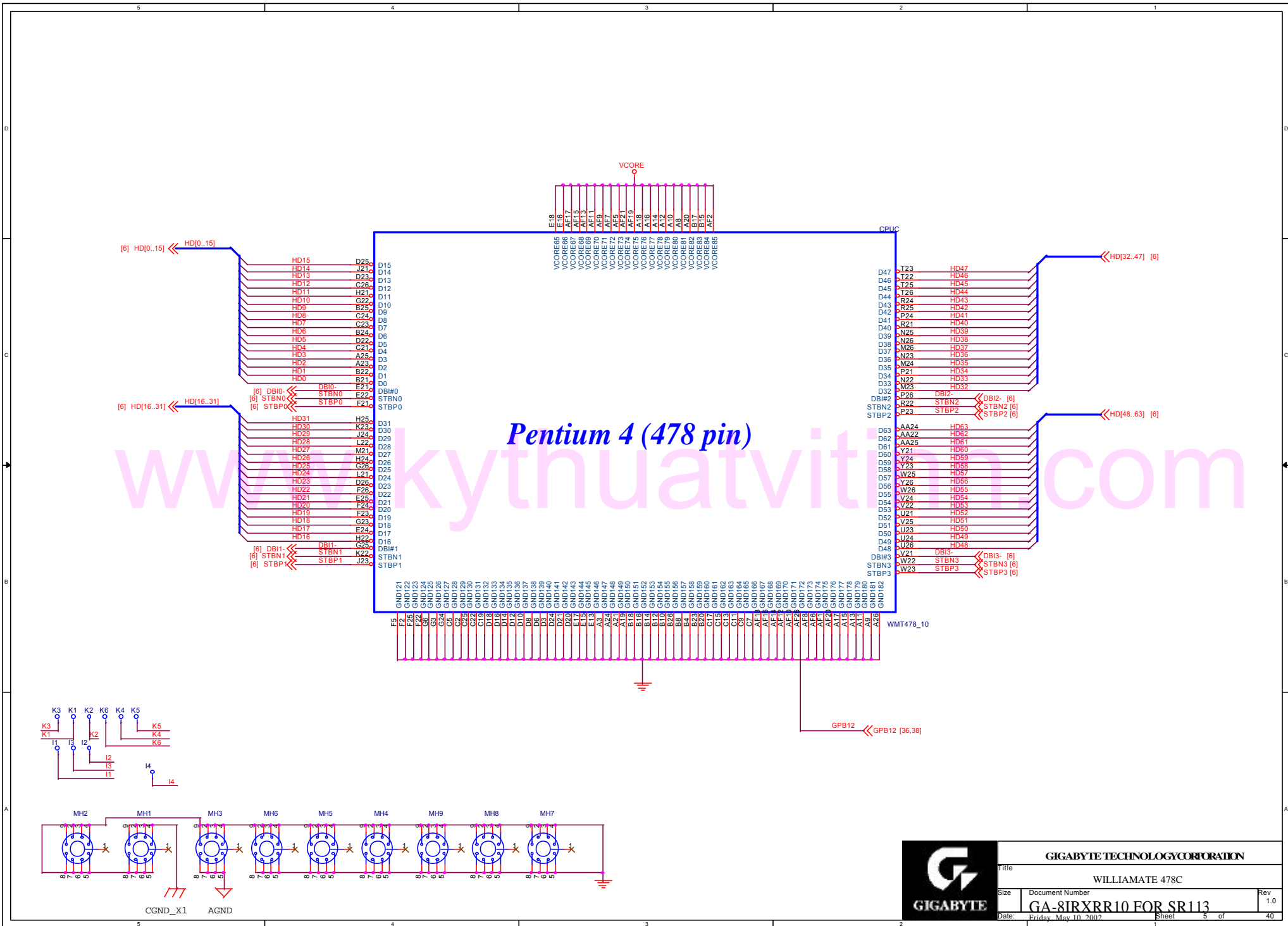
- NEAR CPU
- RN30 8P4R-51
- BPM5- → 2
- BPM4- → 3
- BPM1- → 4
- R324 1 51/6
- R325 2 62/6/X
- R326 1 62/6/X
- R327 1 62/6/X
- RN22 8P4R-47
- TESTHI11\_12 → 1
- TESTHI2\_7 → 2
- TESTHI0\_1 → 3

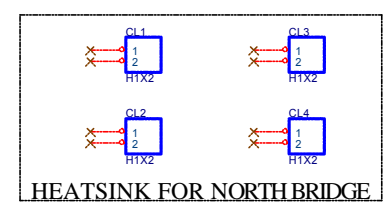
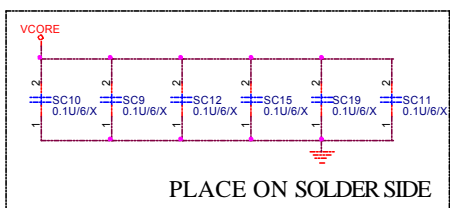
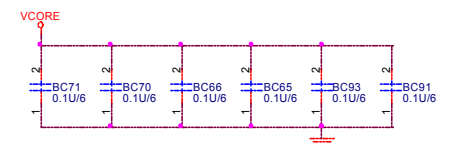
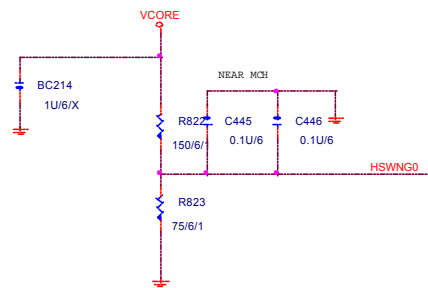
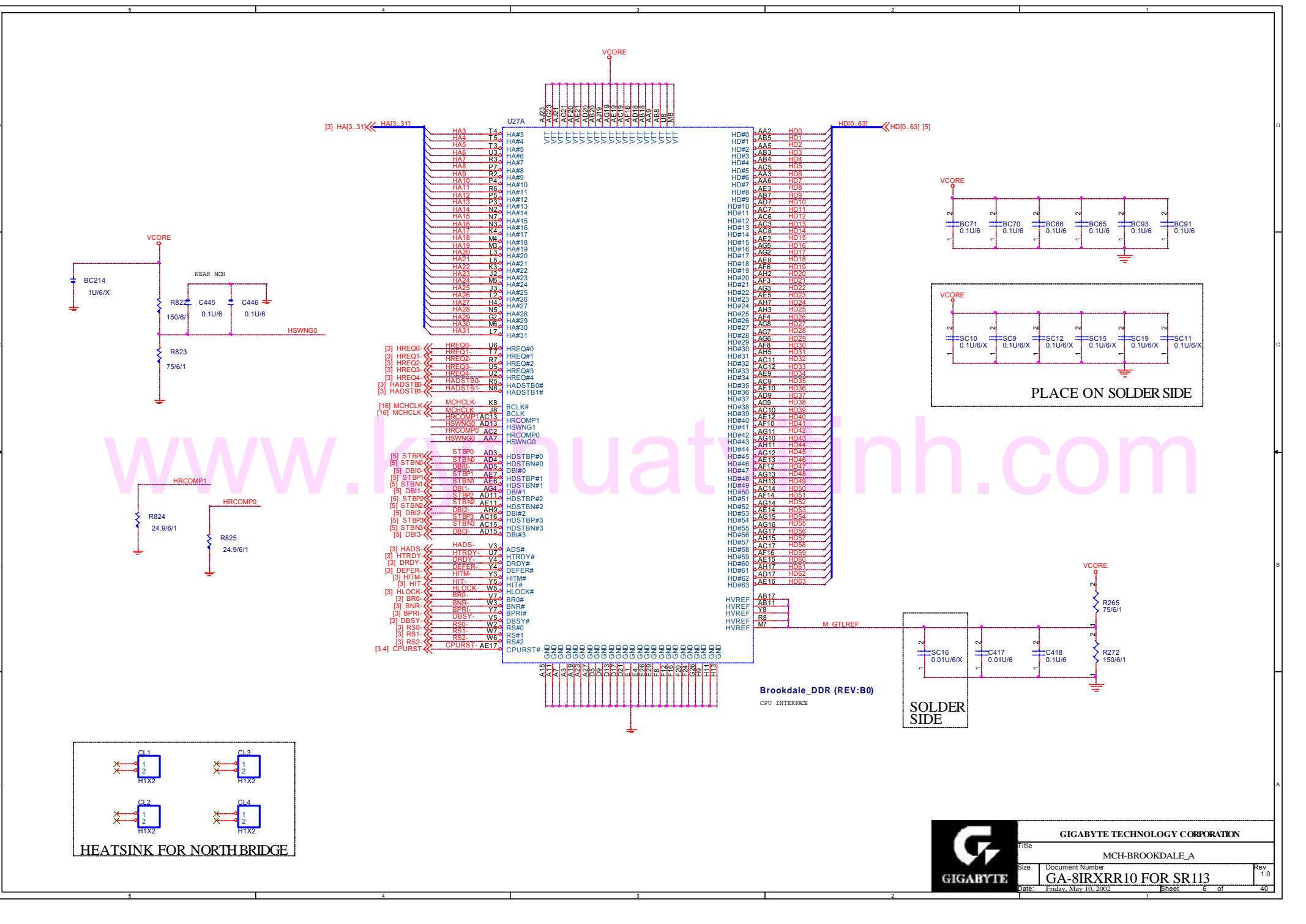
- NEAR CPU
- R371 1 39/6
- R372 2 75/6
- R373 1 150/6
- R370 1 27/6
- R374 1 680/6
- TCK → 1
- TRST → 2

R34=3.3K 100C PLACE MOSFET



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Title: WILLIAMATE 478B			
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Brookdale\_DDR (REV:B0)  
CPU INTERFACE



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Title: MCH-BROOKDALE\_A

Size: GA-8IRXRR10 FOR SR113

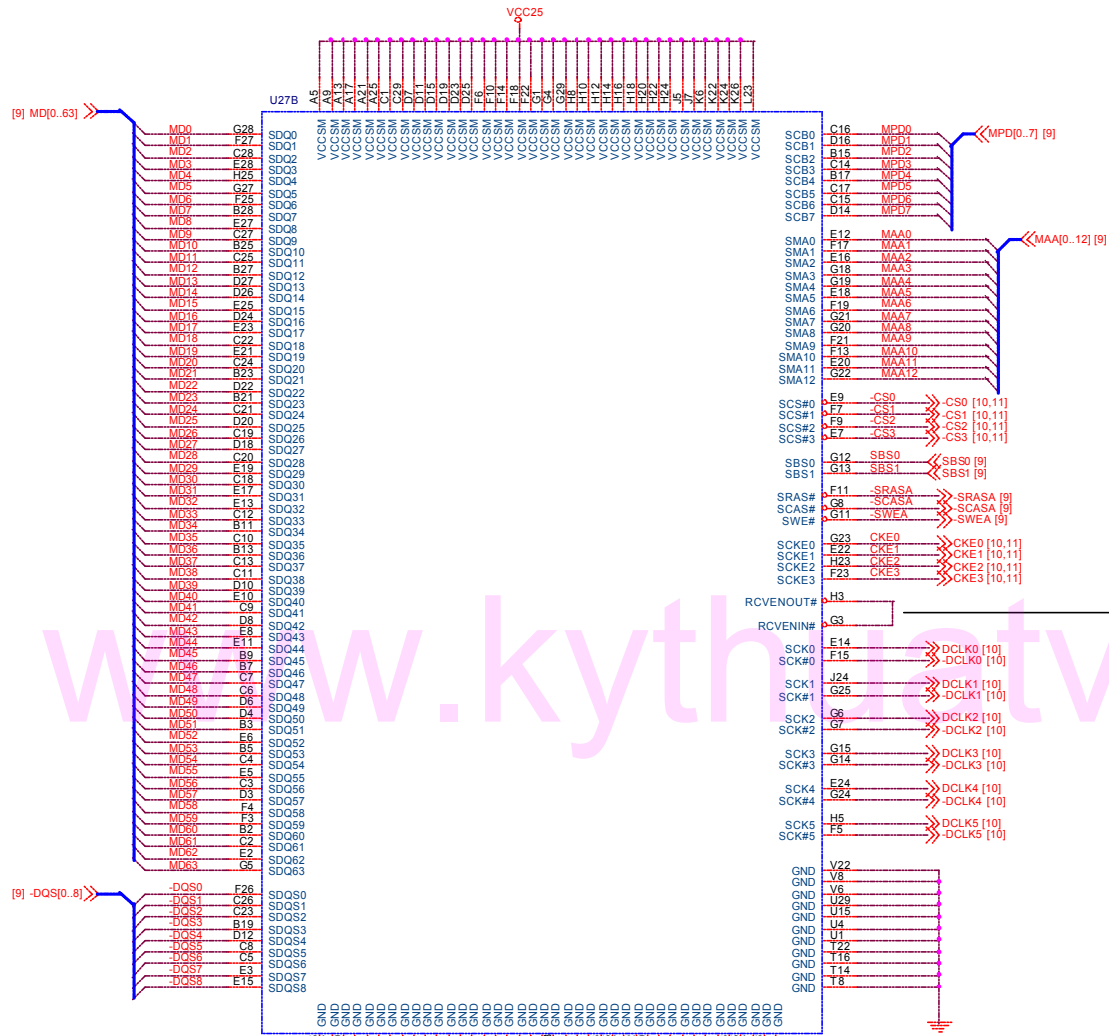
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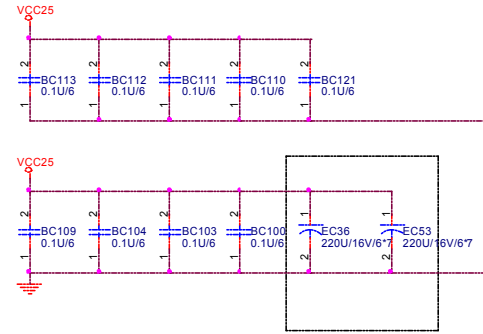
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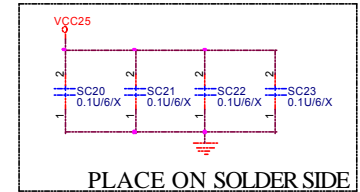


Brookdale\_DDR (REV:B0)  
DDR INTERFACE

注意:DDR版要在SOLDER SIDE  
LAY 1 INCH 長度 (5MIL)

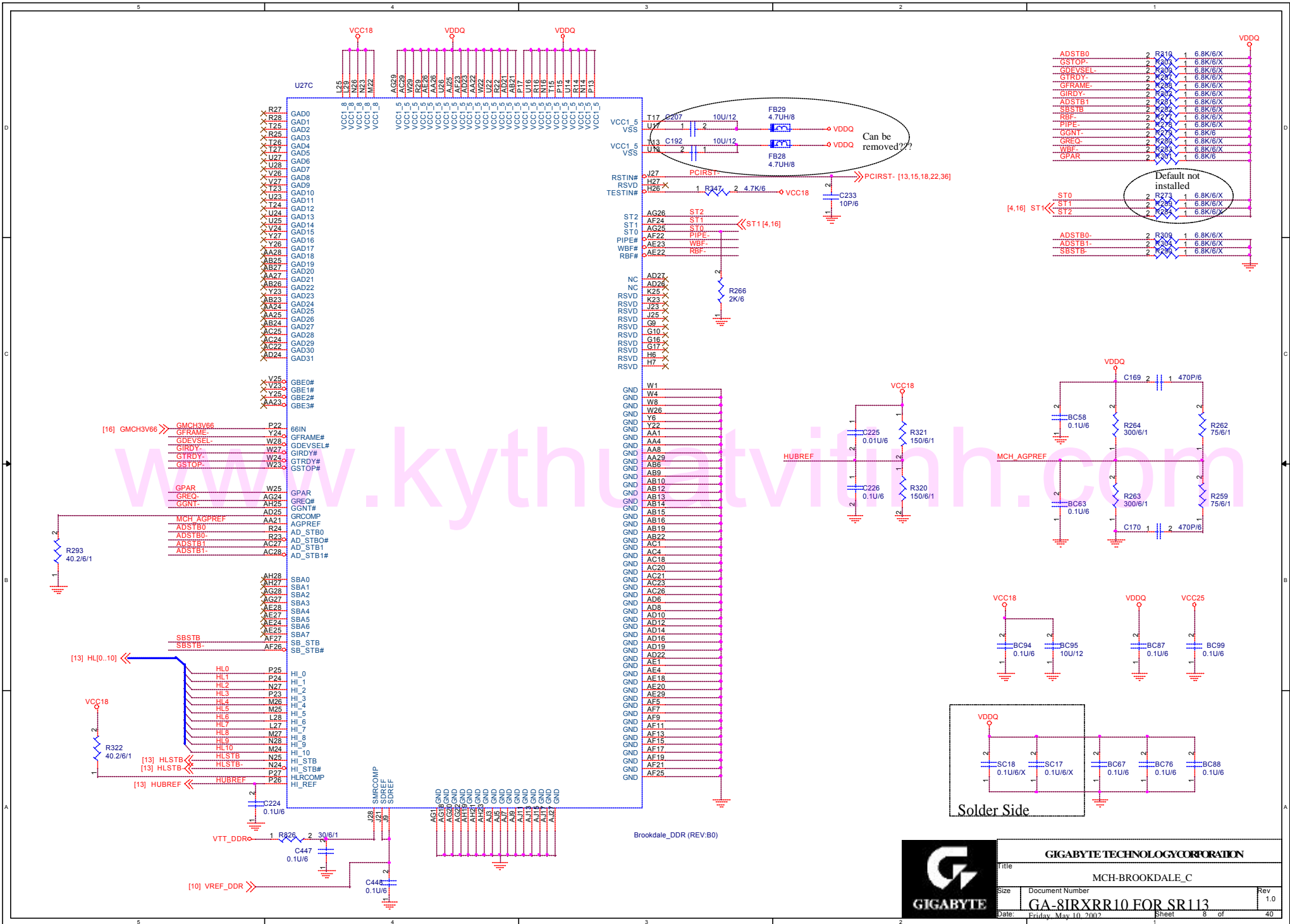


BETWEEN NB AND THE FIRST DIMM



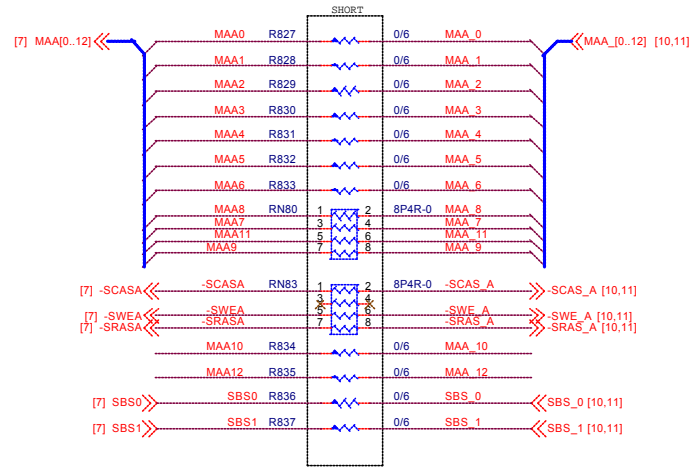
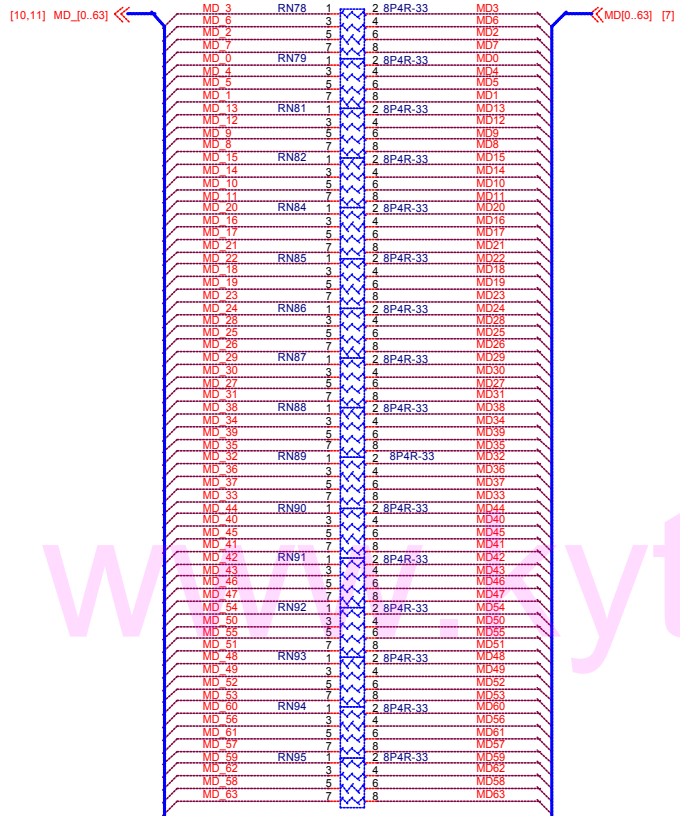
PLACE ON SOLDER SIDE



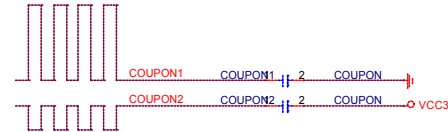
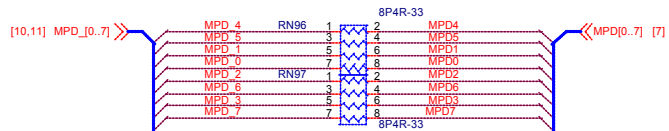
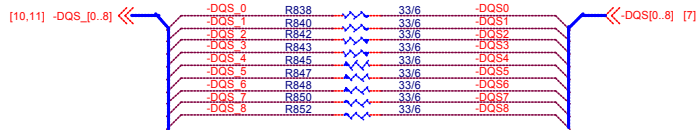
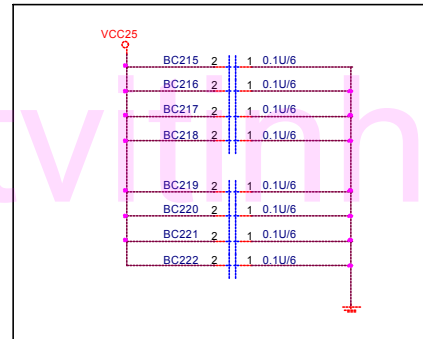


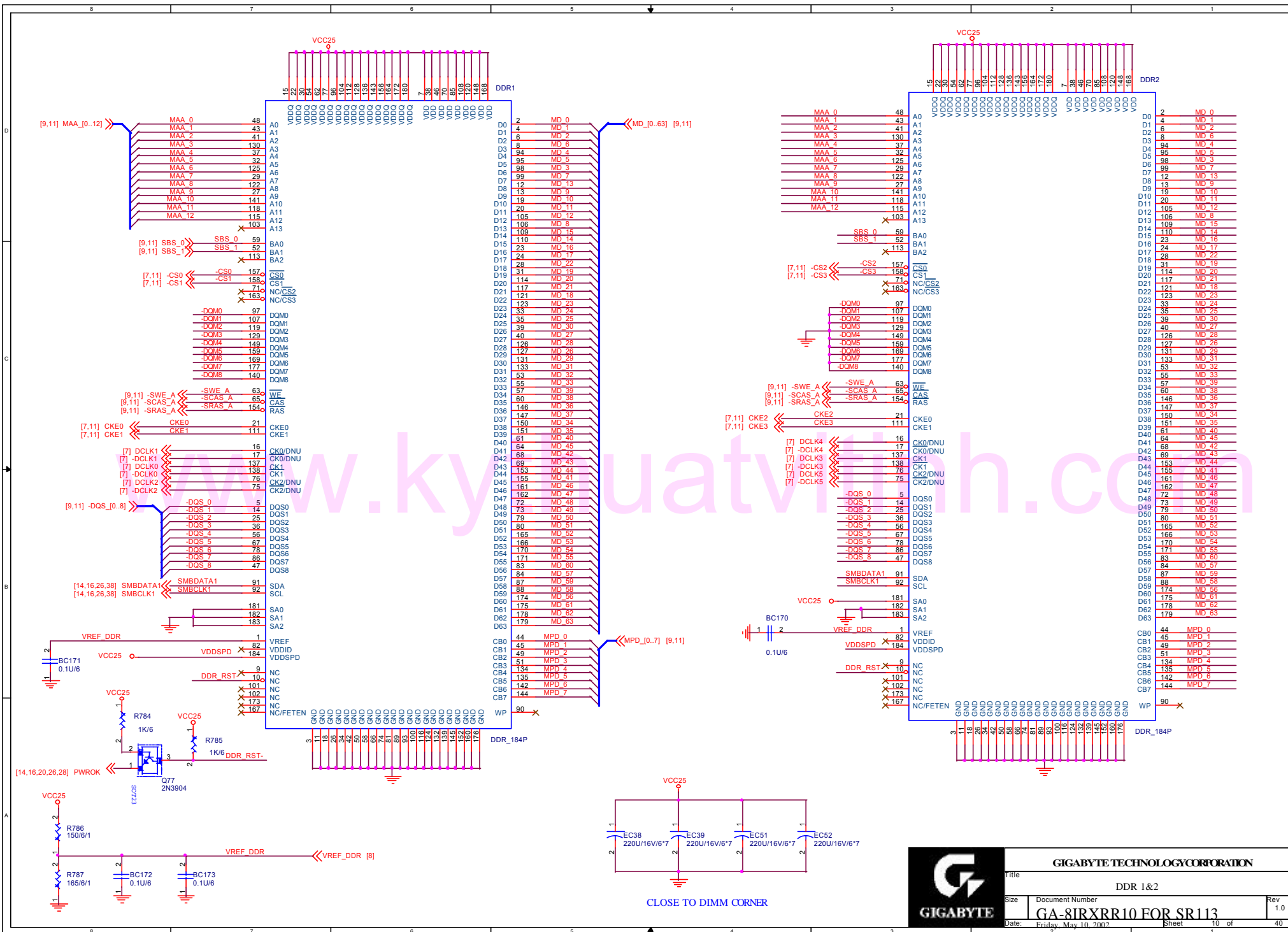
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BETWEEN DIMM1 & DIMM2





**GIGABYTE TECHNOLOGY CORPORATION**

Title: **DDR 1&2**

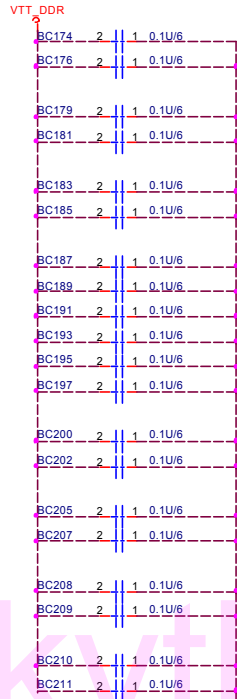
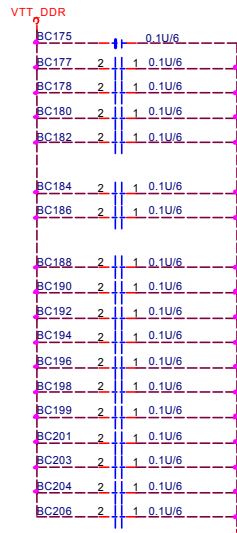
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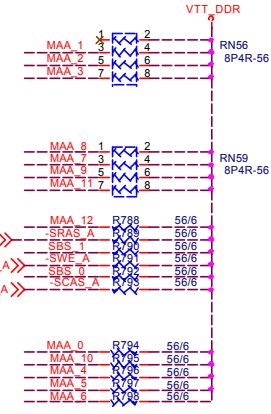
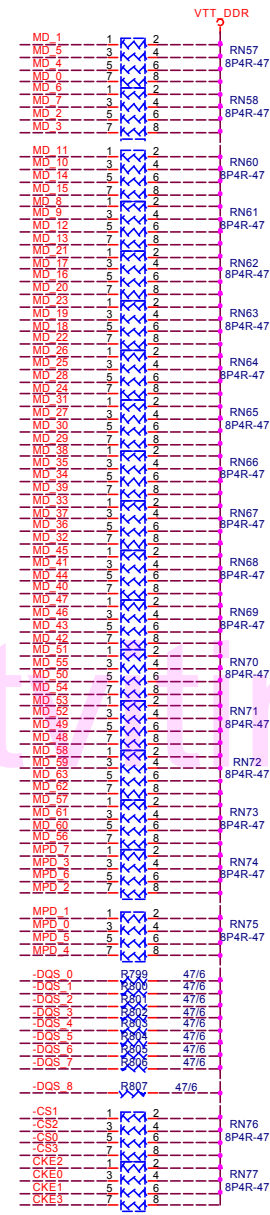
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CLOSE TO DDR TERMINATOR(1 CAP. FOR 2R-PACKS)



- [9,10] MD[0..63] << MD [0..63]
- [9,10] MPD[0..7] << MPD [0..7]
- [9,10] -DQS[0..8] << -DQS [0..8]
- [9,10] MAA[0..12] << MAA[0..12]
- [7,10] -CS[0..3] << -CS[0..3]
- [7,10] CKE[0..3] << CKE[0..3]
- [9,10] SBS[0..1] << SBS [0..1]

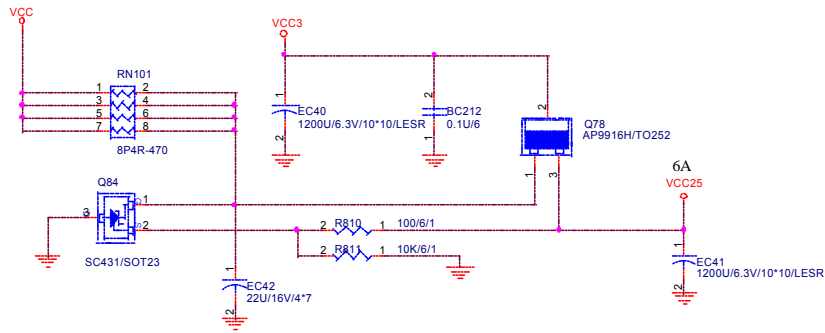
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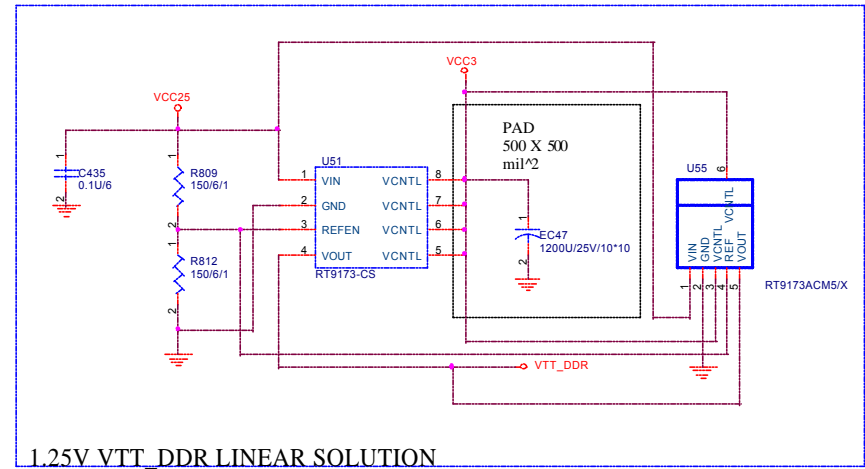
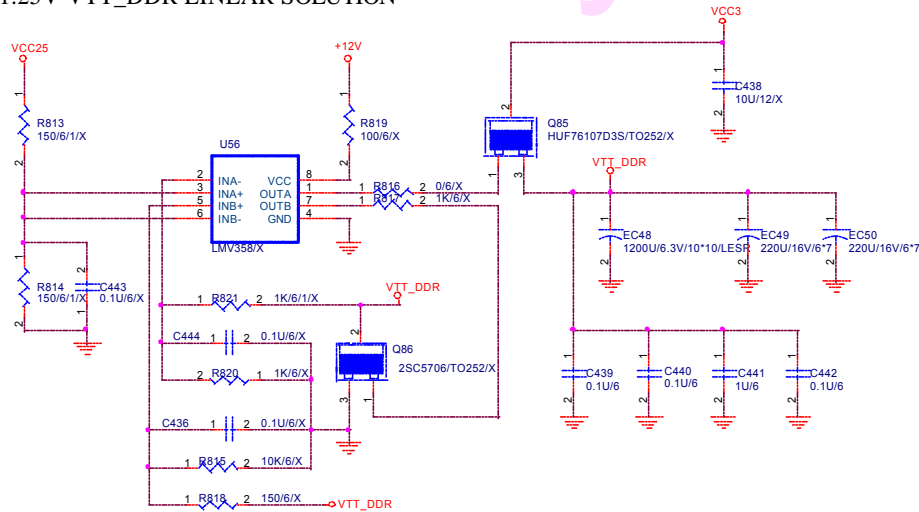
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Size	Document Number	Rev	
	GA-8IRXRR10 FOR SR113	1.0	
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### 2.5V POWER



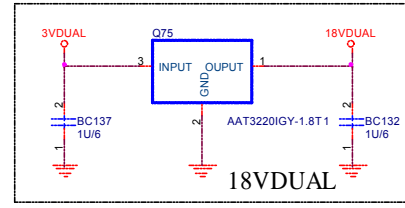
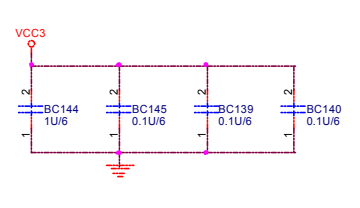
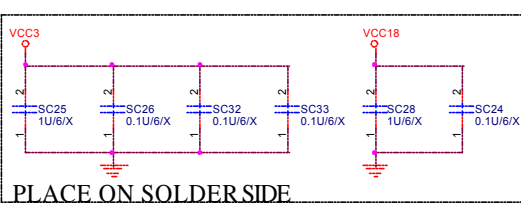
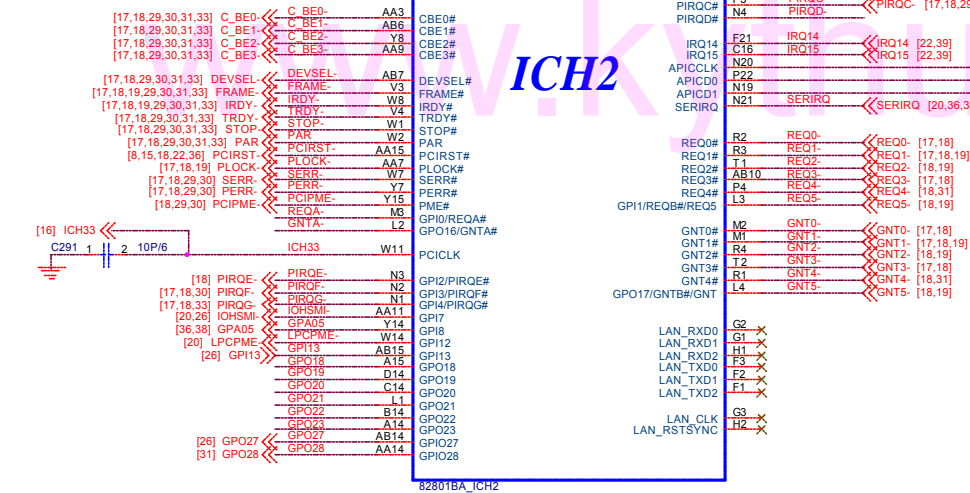
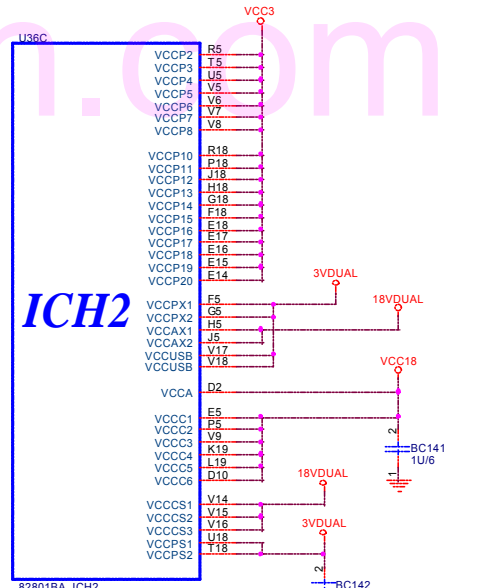
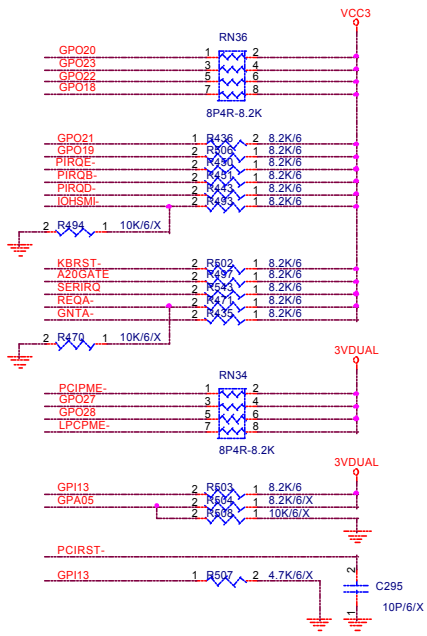
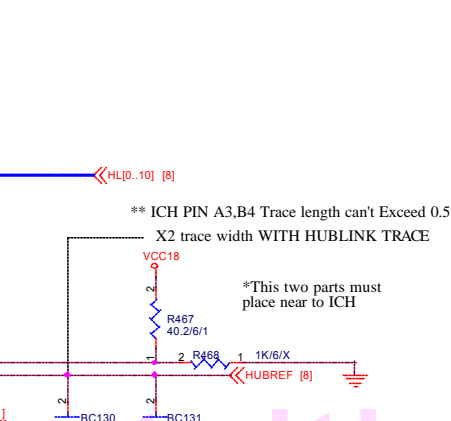
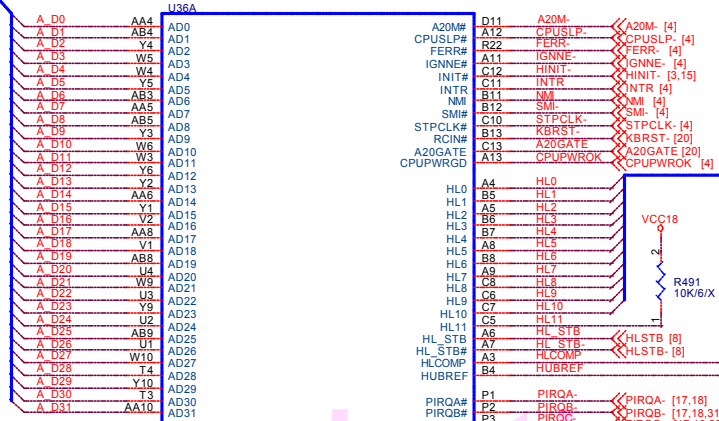
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### 1.25V VTT\_DDR LINEAR SOLUTION



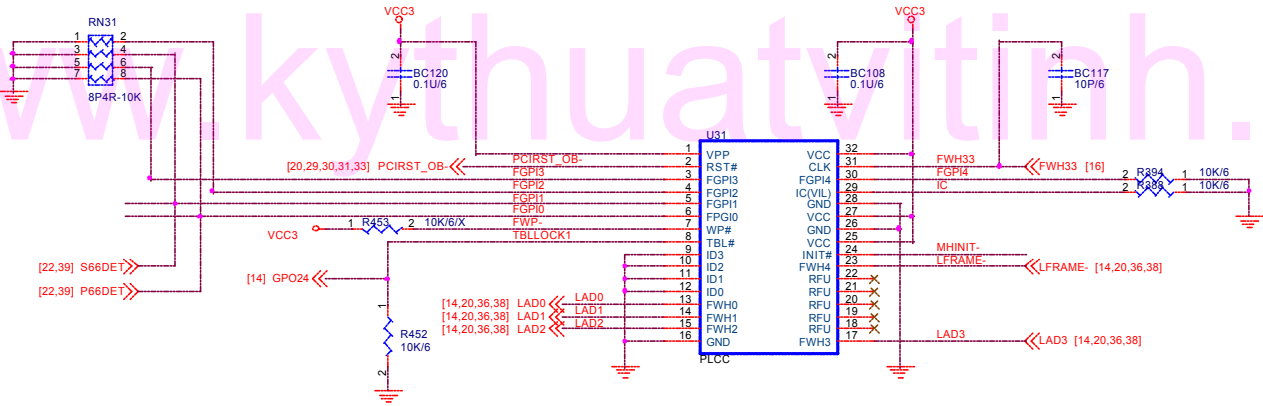
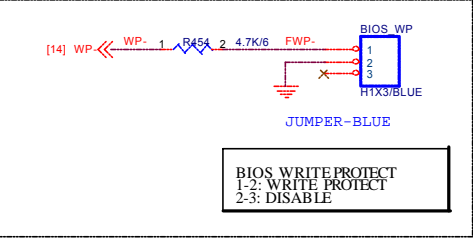
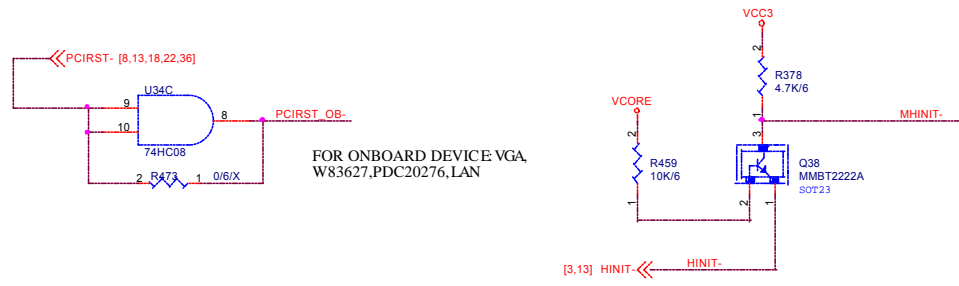
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Size:	Document Number:	Rev: 1.0
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[17,18,29,30,31,33] A\_D[0..31] << A\_D[0..31]



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Title ICH2_A		
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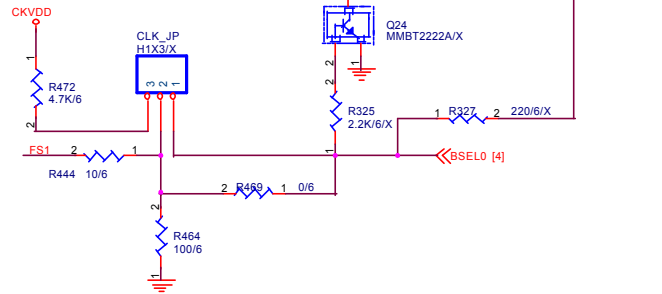
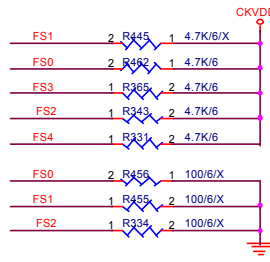
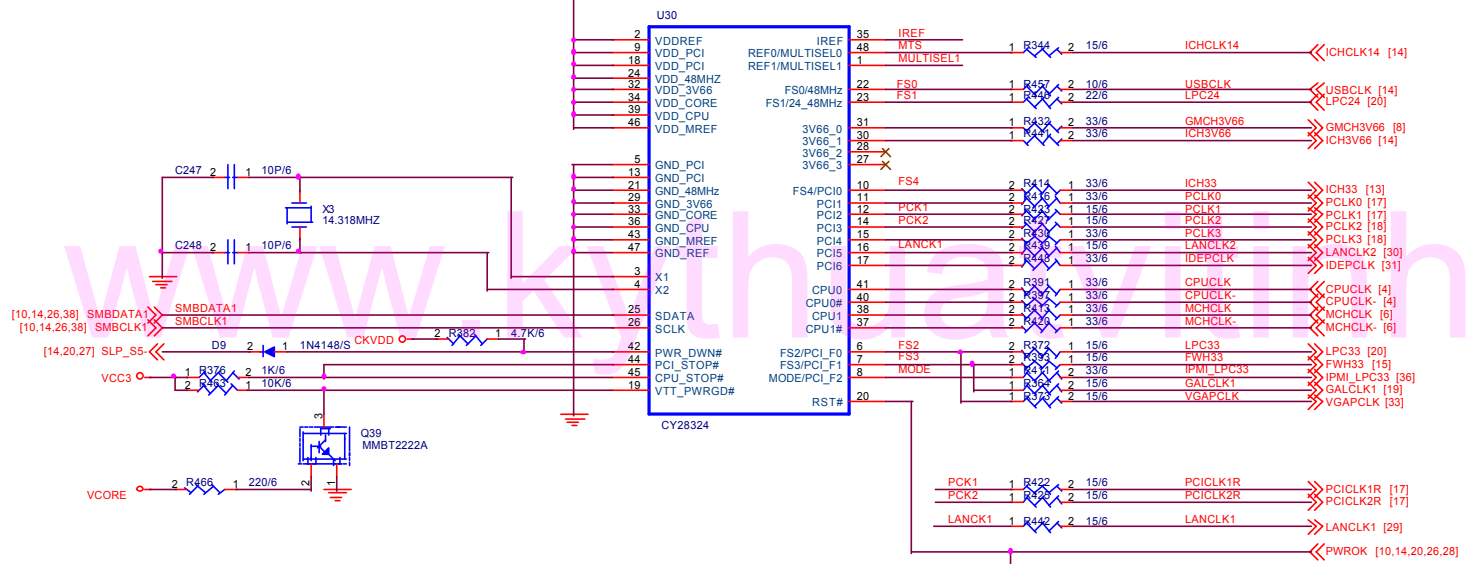
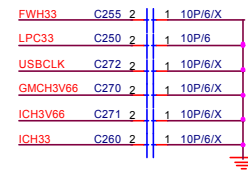
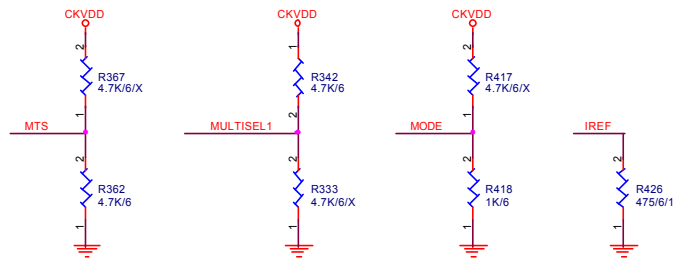
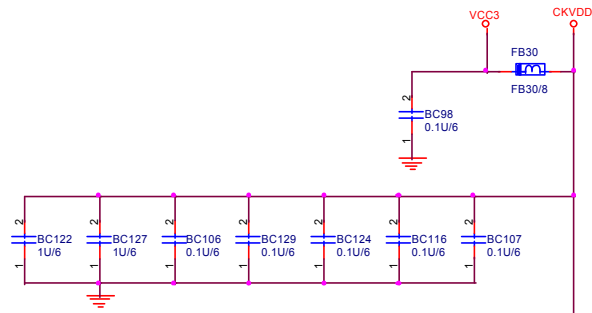




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Size	Document Number	Rev
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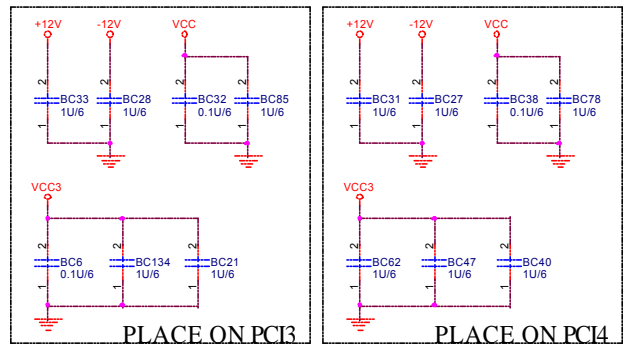
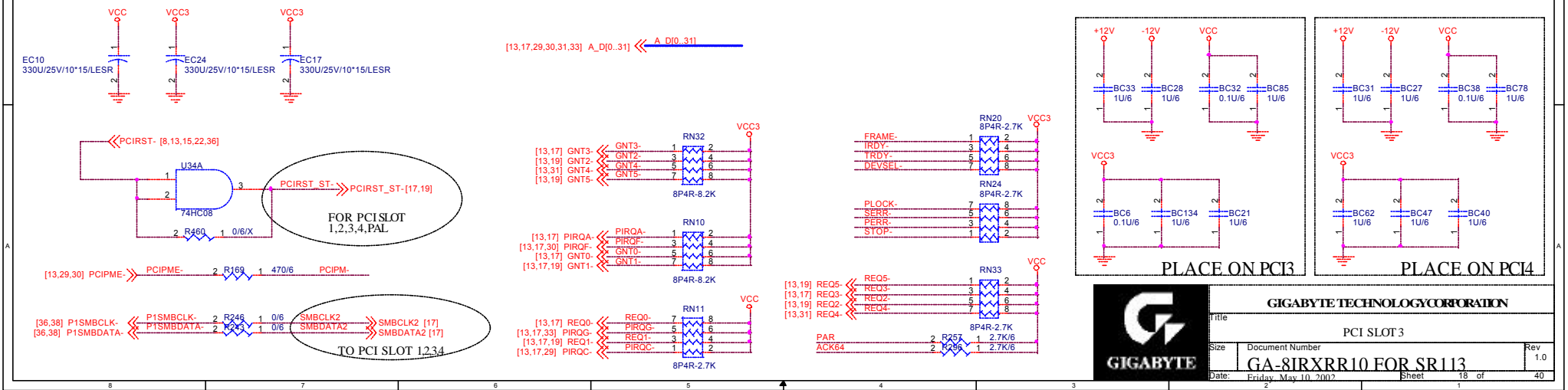
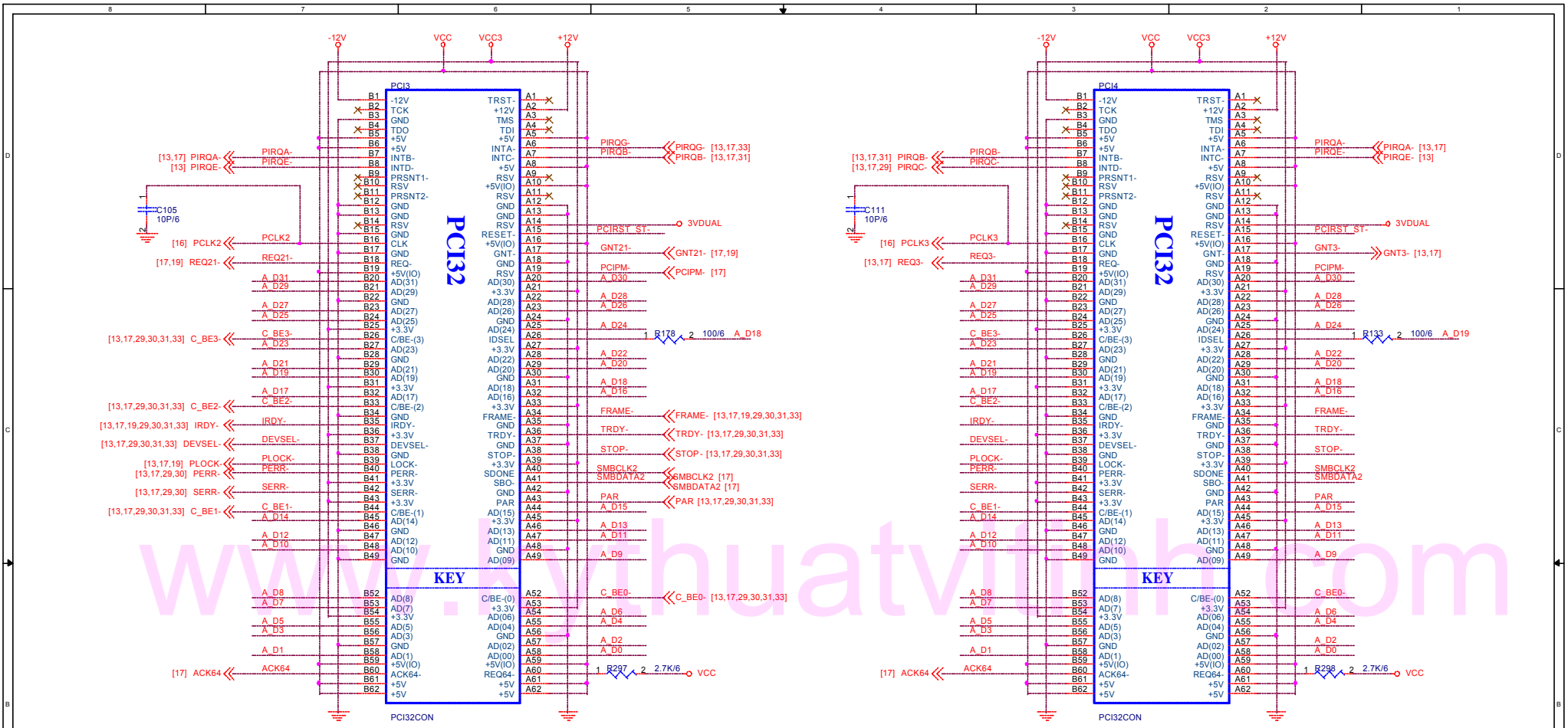
RST- pin  
Iol=5mA  
Vol=0.8mV

CPU	CLK_JP1
AUTO	1-2
133	2-3

1-2:DEFAULT  
2-3:NOT SUPPORT NOW



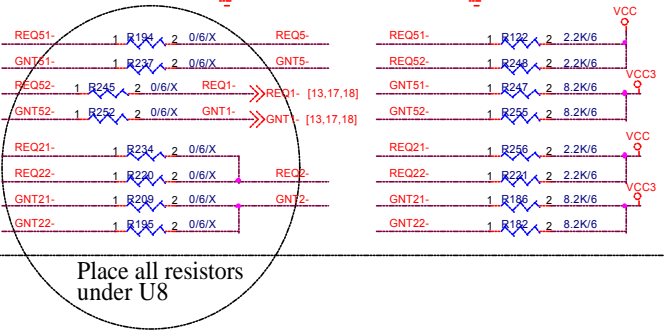
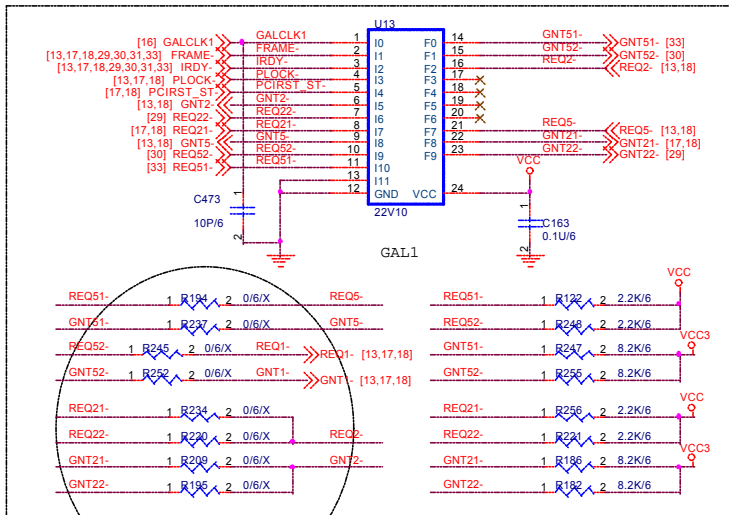




**GIGABYTE TECHNOLOGY CORPORATION**

PCI SLOT 3

Title	PCI SLOT 3		
Size	Document Number	Rev	
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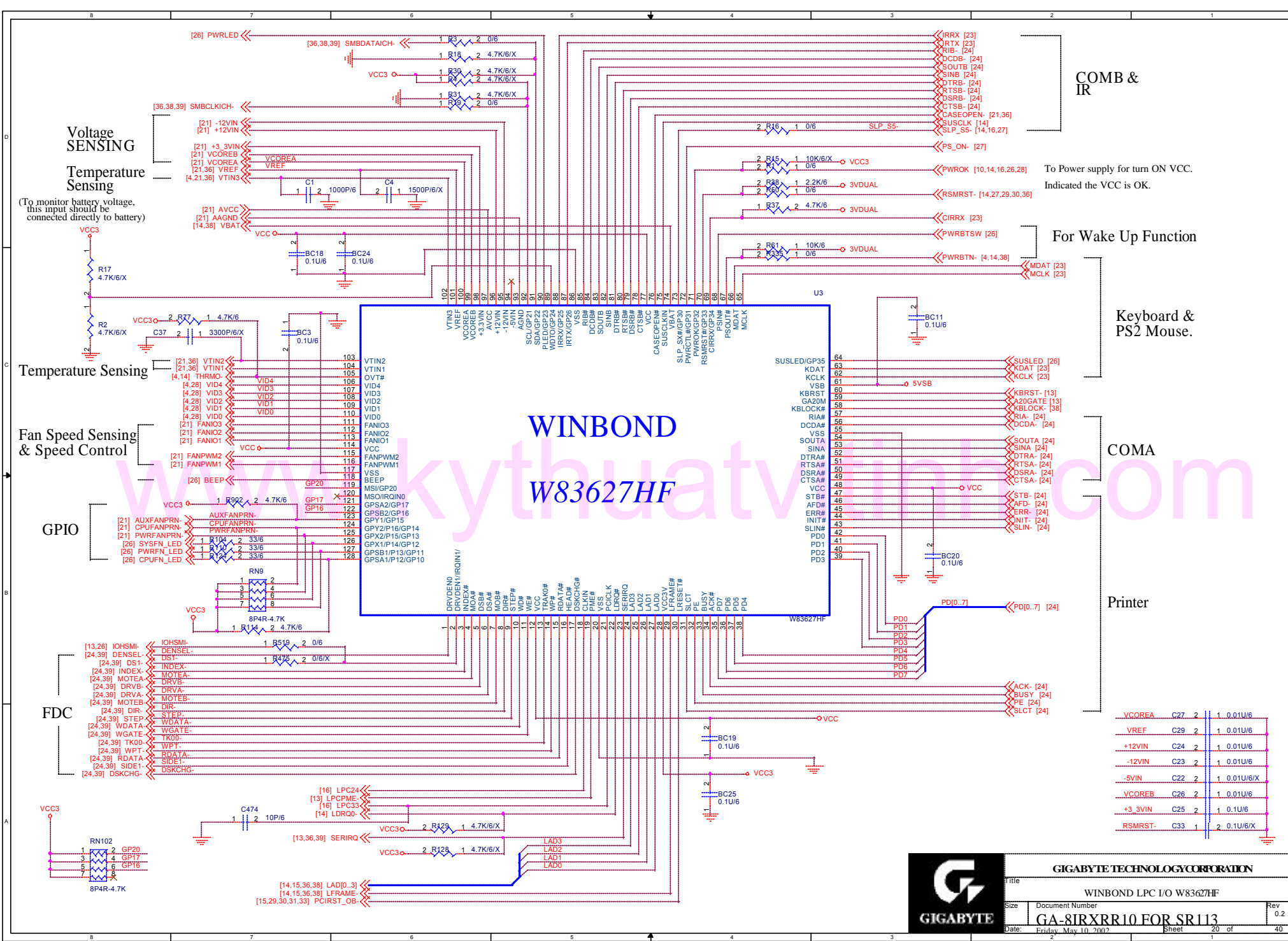
**PCI DEVICE REQ,GNT AND INTR ROUTING**

PCI DEVICE	IDSEL	REQ&GNT (4U)	REQ&GNT (1U)	REQ&GNT (2U)	IRQ ROUTING
PCII	AD16	REQ0-,GNT0-	REQ0-,GNT0-	REQ0-,GNT0-	PIRQC-,PIRQF-,PIRQG-,PIRQA-
PCI2	AD17	REQ1-,GNT1-	NA	REQ1-,GNT1-	PIRQF-,PIRQG-,PIRQA-,PIRQB-
PCI3	AD18	REQ21-,GNT21- [GAL1]	NA	REQ21-,GNT21- [GAL1]	PIRQG-,PIRQA-,PIRQB-,PIRQE-
PCI4	AD19	REQ3-,GNT3-	NA	NA	PIRQA-,PIRQB-,PIRQE-,PIRQC-
PROMISE RAID	AD20	REQ4-,GNT4-	REQ4-,GNT4-	REQ4-,GNT4-	PIRQB-
LAN1	AD22	REQ22-,GNT22- [GAL1]	REQ2-,GNT2-	REQ22-,GNT22- [GAL1]	PIRQC-
LAN2	AD23	REQ52-,GNT52- [GAL1]	REQ1-,GNT1-	REQ52-,GNT52- [GAL1]	PIRQF-
ATI VGA	AD24	REQ51-,GNT51- [GAL1]	REQ5-,GNT5-	REQ51-,GNT51- [GAL1]	PIRQG-

**ICH2 INTR ROUTING**

ITEM	INTERRUPT	REMARK
USB1	PIRQD-	
USB2	PIRQH-	





**WINBOND**  
**W83627HF**

**COMB & IR**

To Power supply for turn ON VCC.  
Indicated the VCC is OK.

For Wake Up Function

**Keyboard & PS2 Mouse.**

**COMA**

**Printer**

VCOREA	C27	2	1	0.01U/6
VREF	C29	2	1	0.01U/6
+12VIN	C24	2	1	0.01U/6
-12VIN	C23	2	1	0.01U/6
-5VIN	C22	2	1	0.01U/6/X
VCOREB	C26	2	1	0.01U/6
+3_3VIN	C25	2	1	0.1U/6
RSMRST	C33	1	2	0.1U/6/X



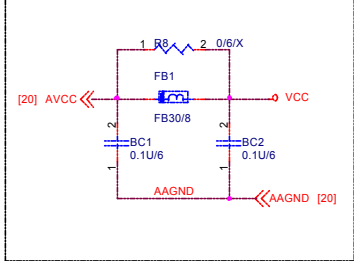
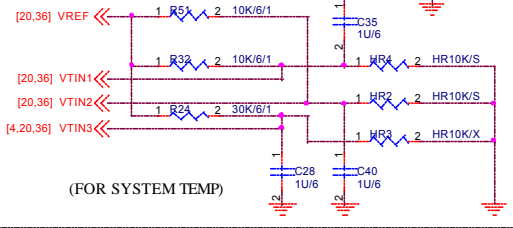
**GIGABYTE TECHNOLOGY CORPORATION**

Title		WINBOND LPC I/O W83627HF	
Size	Document Number	Rev 0.2	
Date		Friday, May 10, 2002	
Sheet		2 of 40	

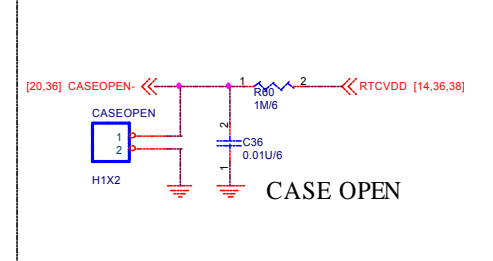
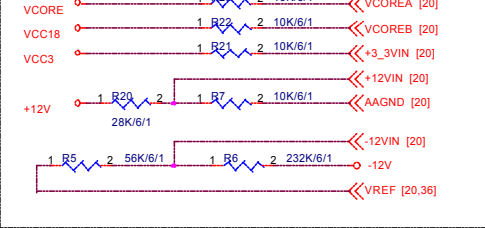
**GA-8IRXRR10 FOR SR113**

# Hardware Monitor circuits

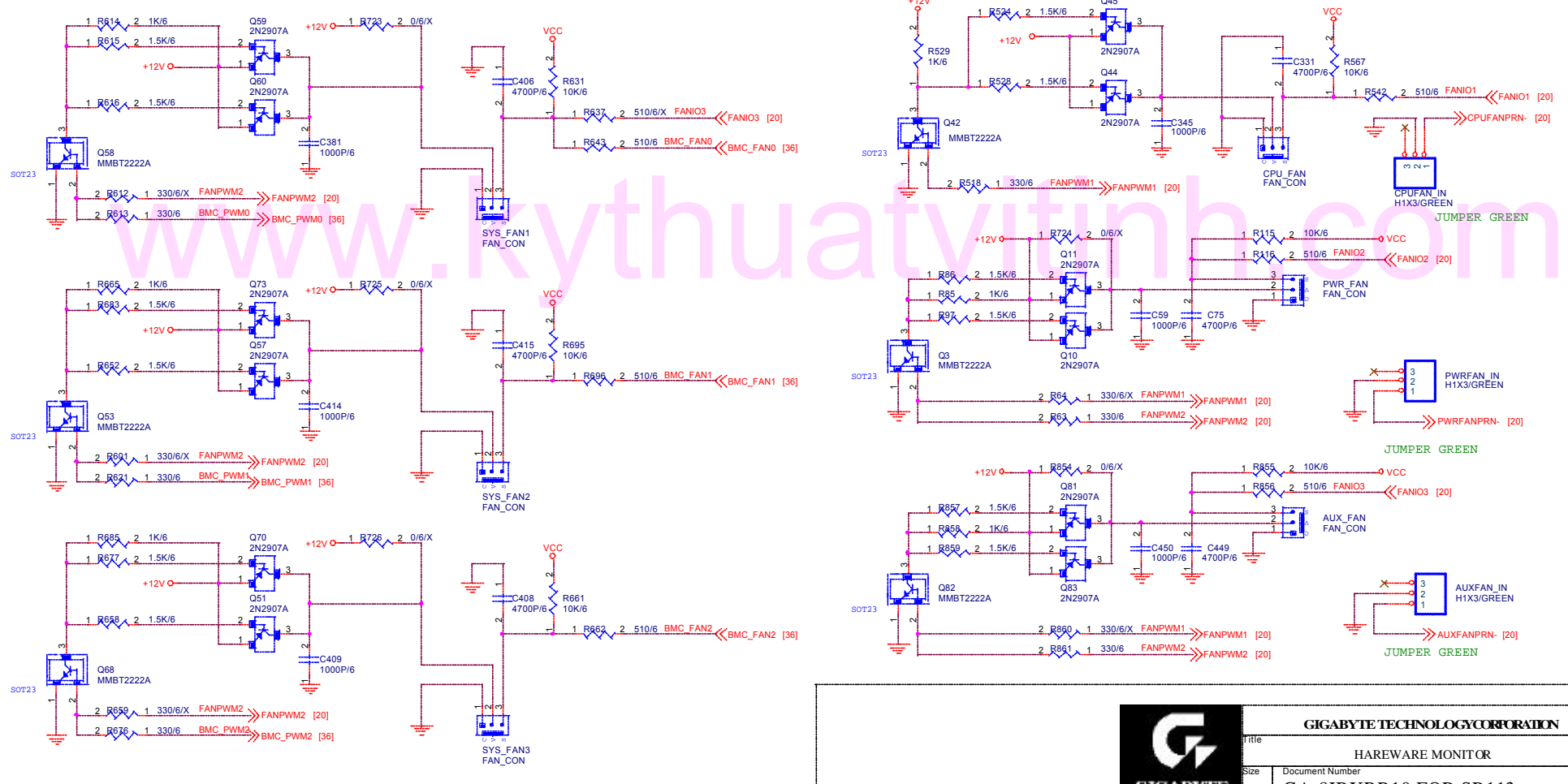
## Temperature Sensing



## Voltage Sensing

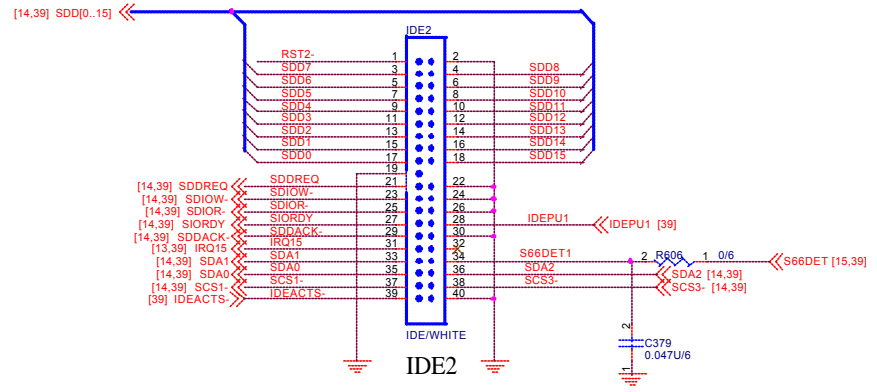
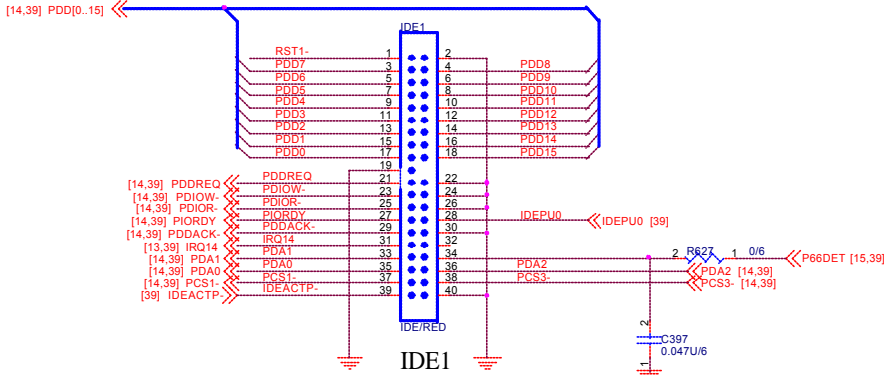
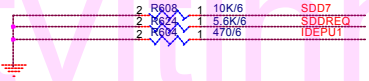
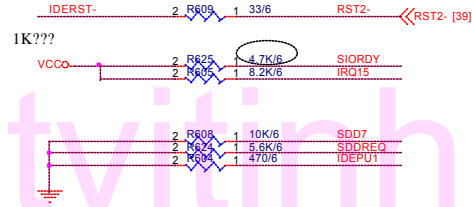
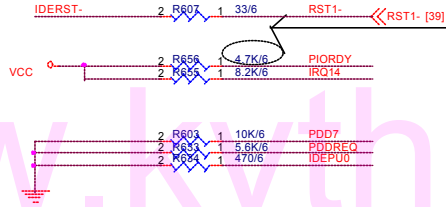
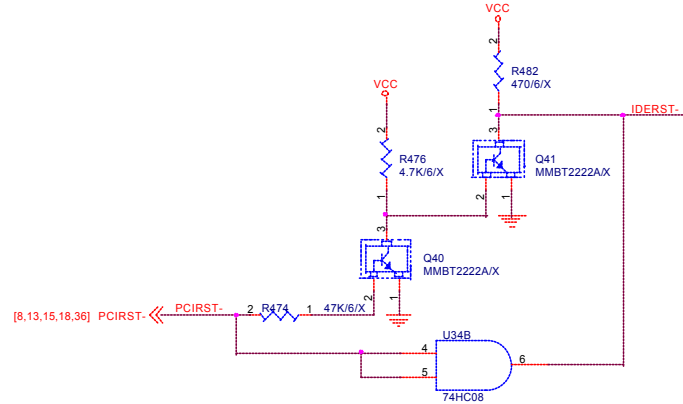
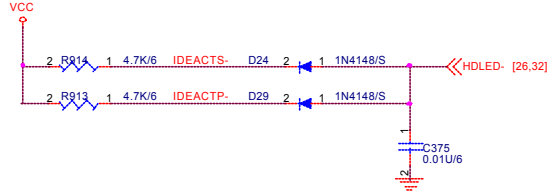
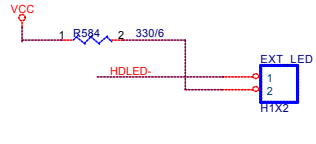


## FAN CONNECTOR



GIGABYTE TECHNOLOGY CORPORATION		
Title HAREWARE MONITOR		
Size	Document Number	Rev 0.2
GA-8IRXRR10 FOR SR113		
Date	Friday, May 10, 2007	Sheet 21 of 40

### EXTERNAL HD\_LED CONNECTOR



PRIMARY IDE CONNECTOR

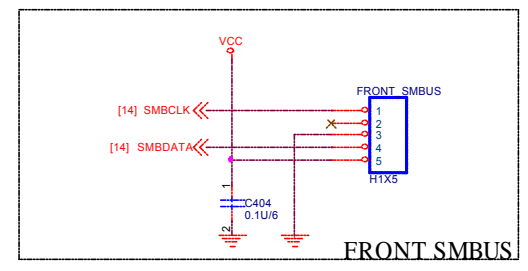
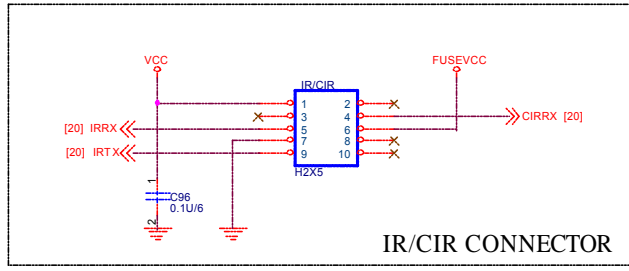
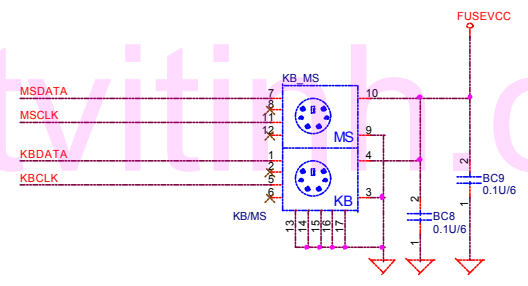
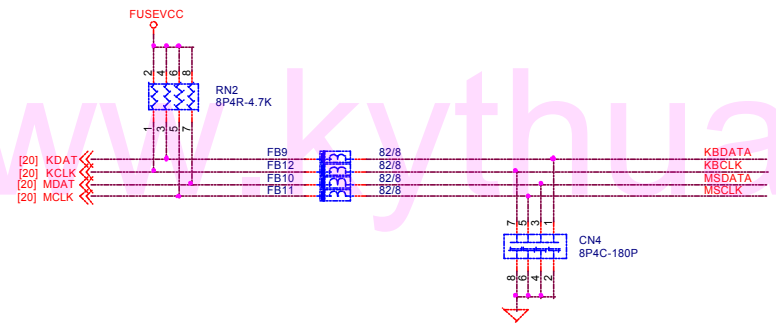
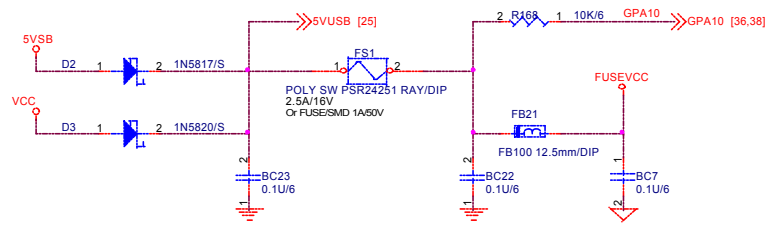
SECONDARY IDE CONNECTOR



GIGABYTE TECHNOLOGY CORPORATION

Title			IDE CONNECTOR		
Size	Document Number		Rev		0.2
Date	Friday, May 10, 2002		Sheet		22 of 40

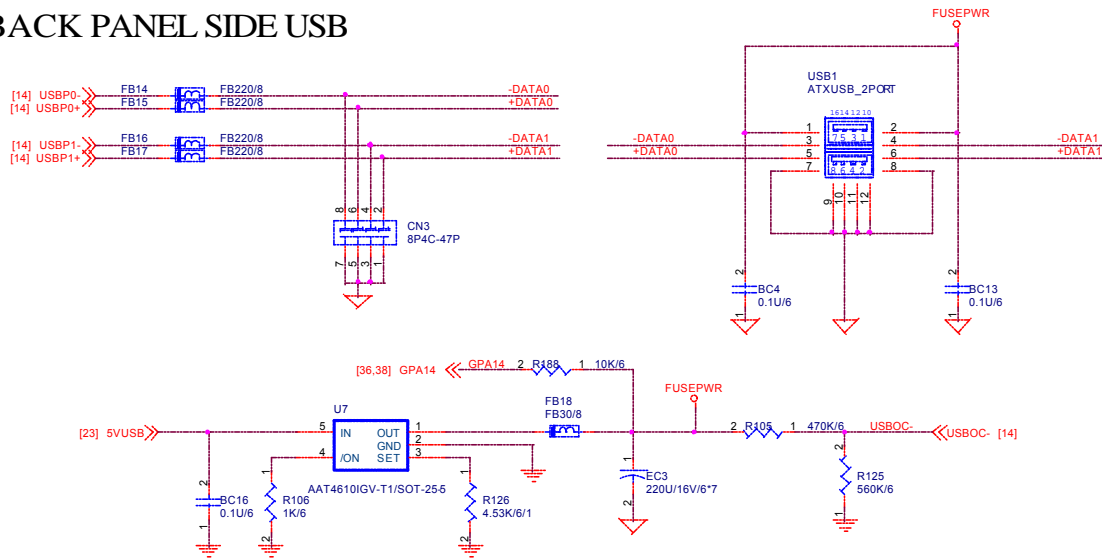
GA-8IRXRR10 FOR SR113



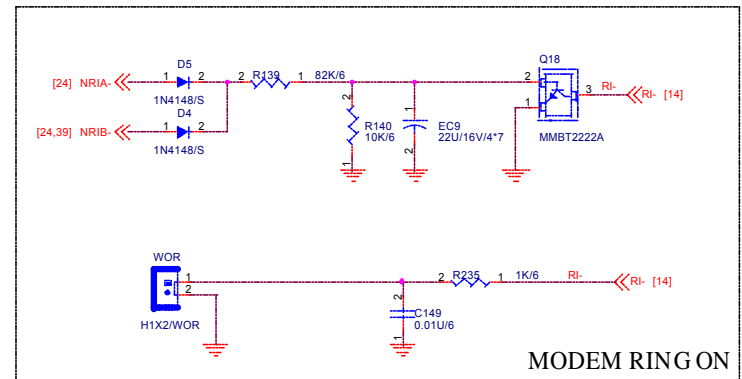
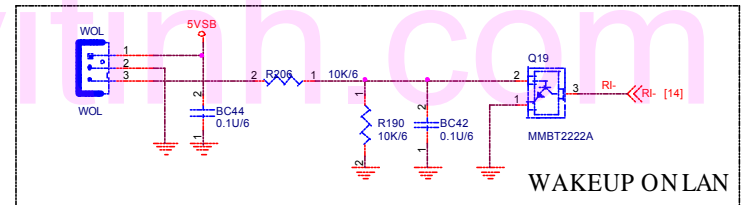
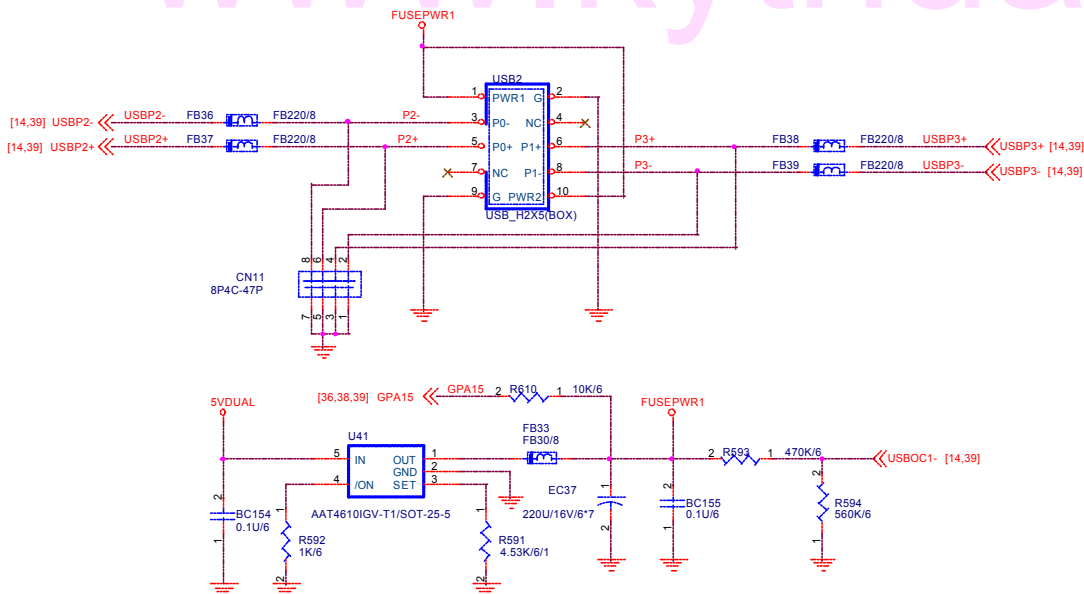
<b>GIGABYTE TECHNOLOGY CORPORATION</b>		
Title KB & PS2 MOUSE&IR		
Size	Document Number	Rev
	<b>GA-8IRXRR10 FOR SR113</b>	0.2
Date	Friday, May 10, 2002	Sheet 23 of 40



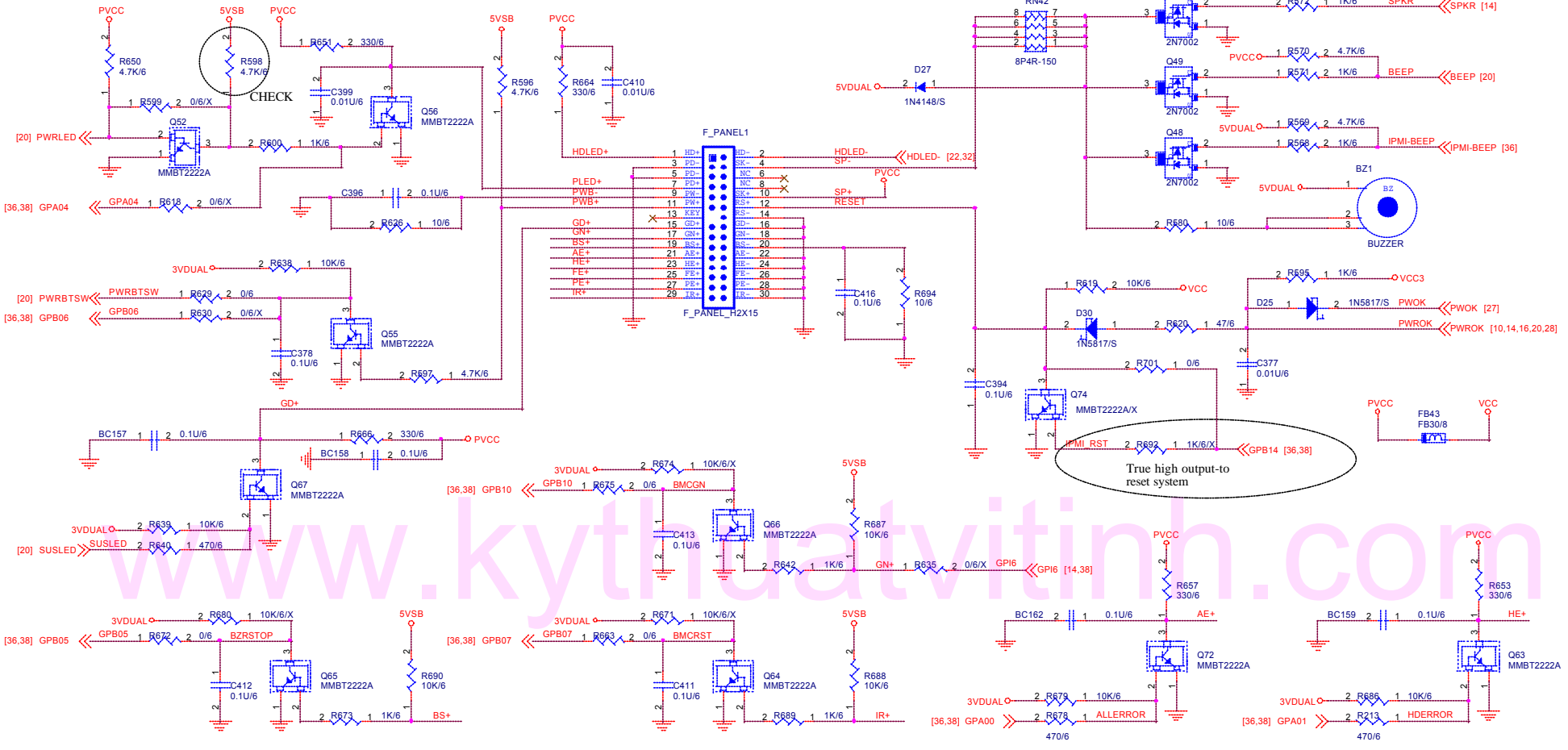
# BACK PANEL SIDE USB



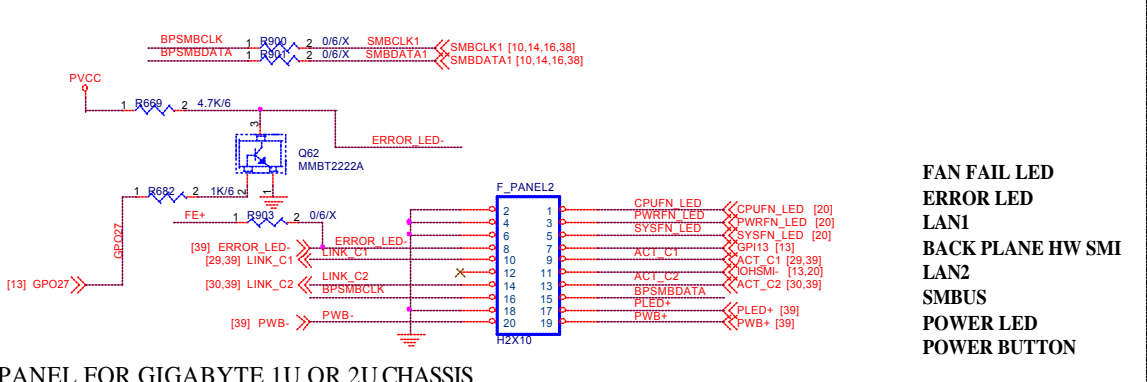
# FRONT SIDE USB



GIGABYTE TECHNOLOGY CORPORATION		
Title	ICH USB+LAN PORT	
Size	Document Number	Rev
	GA-8IRXRR10 FOR SR113	0.2
Date	Friday, May 10, 2002	Sheet 25 of 40

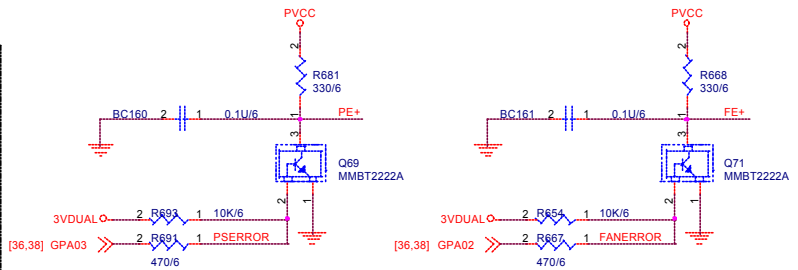


True high output-to-reset system



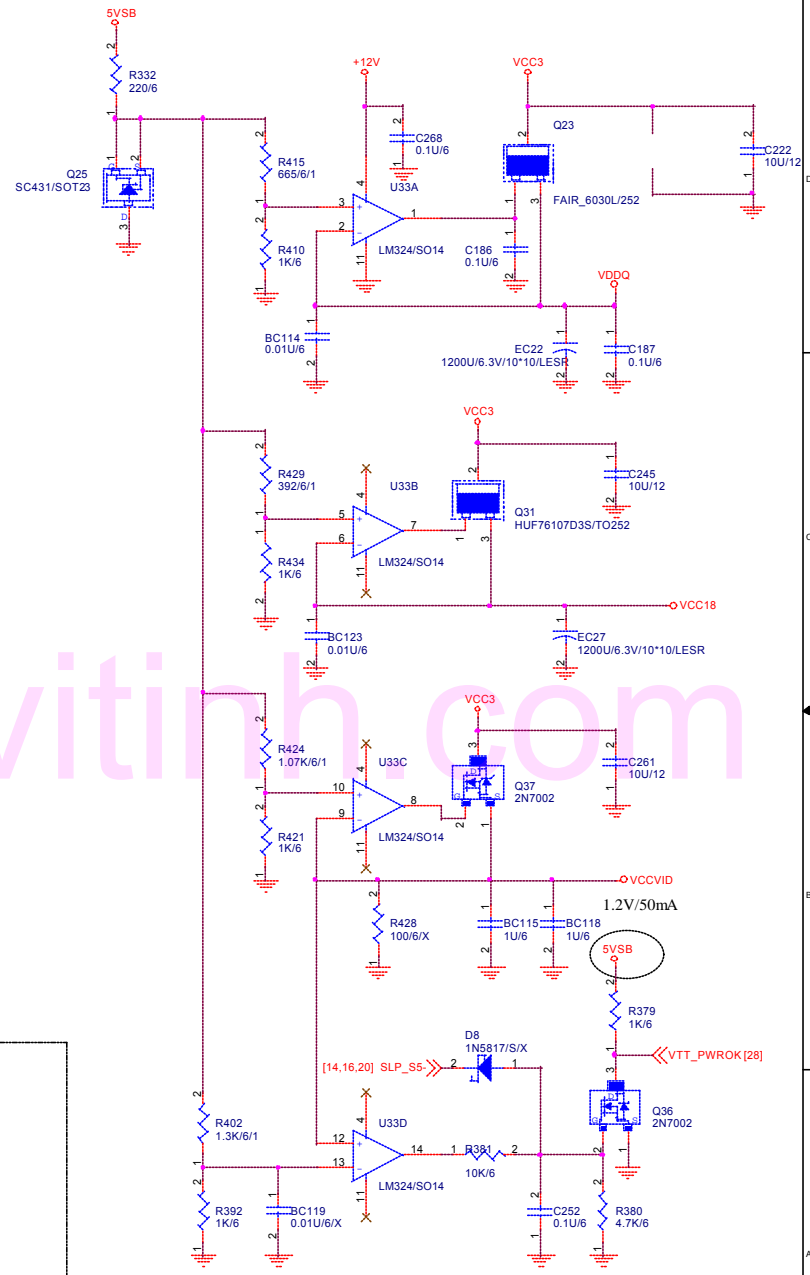
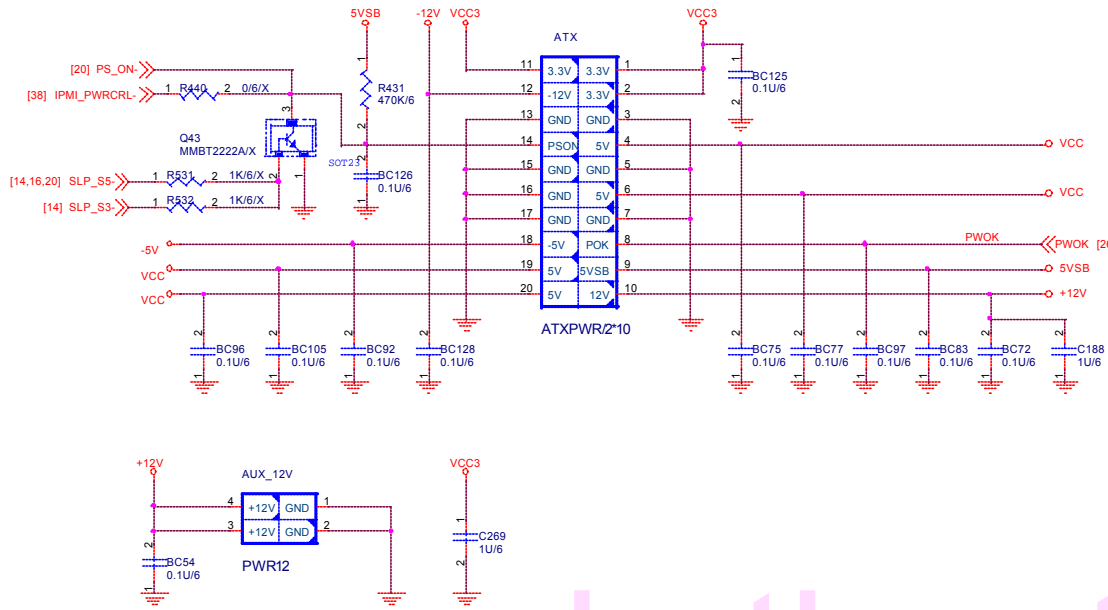
PANEL FOR GIGABYTE 1U OR 2U CHASSIS

- FAN FAIL LED
- ERROR LED
- LAN1
- BACK PLANE HW SMI
- LAN2
- SMBUS
- POWER LED
- POWER BUTTON

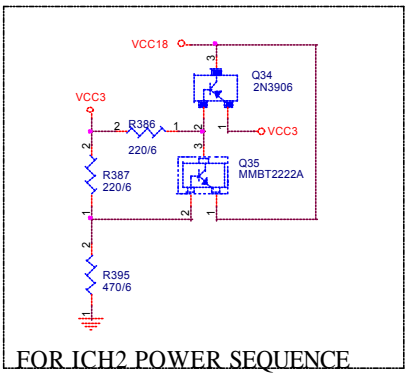
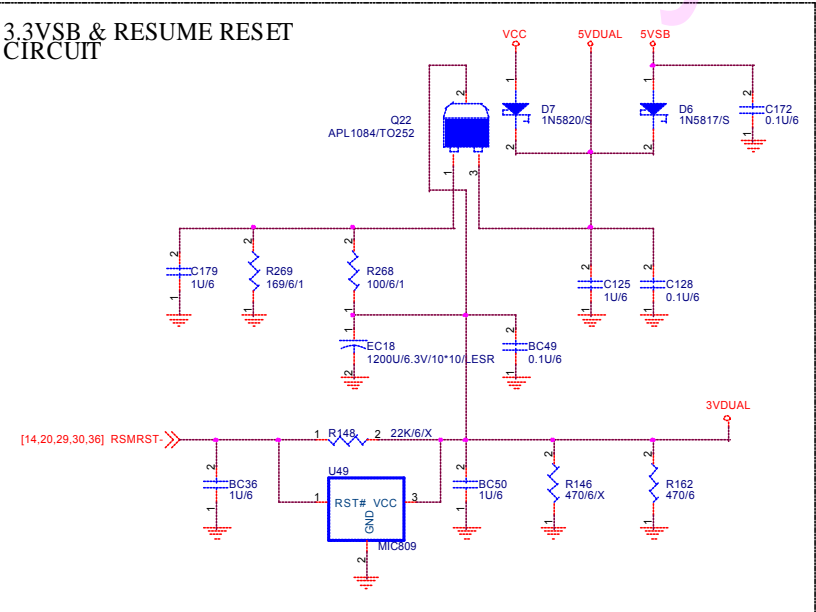


<b>GIGABYTE TECHNOLOGY CORPORATION</b>		
Title: PANEL & RI		
Size: Document Number	Rev: 0.2	
Date: Friday, May 10, 2007		Sheet: 26 of 40

# ATX POWER CONNECTOR



## 3.3VSB & RESUME RESET CIRCUIT

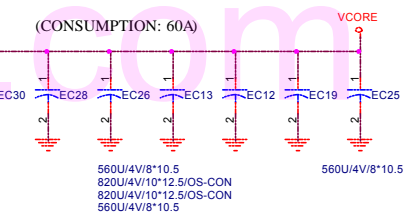
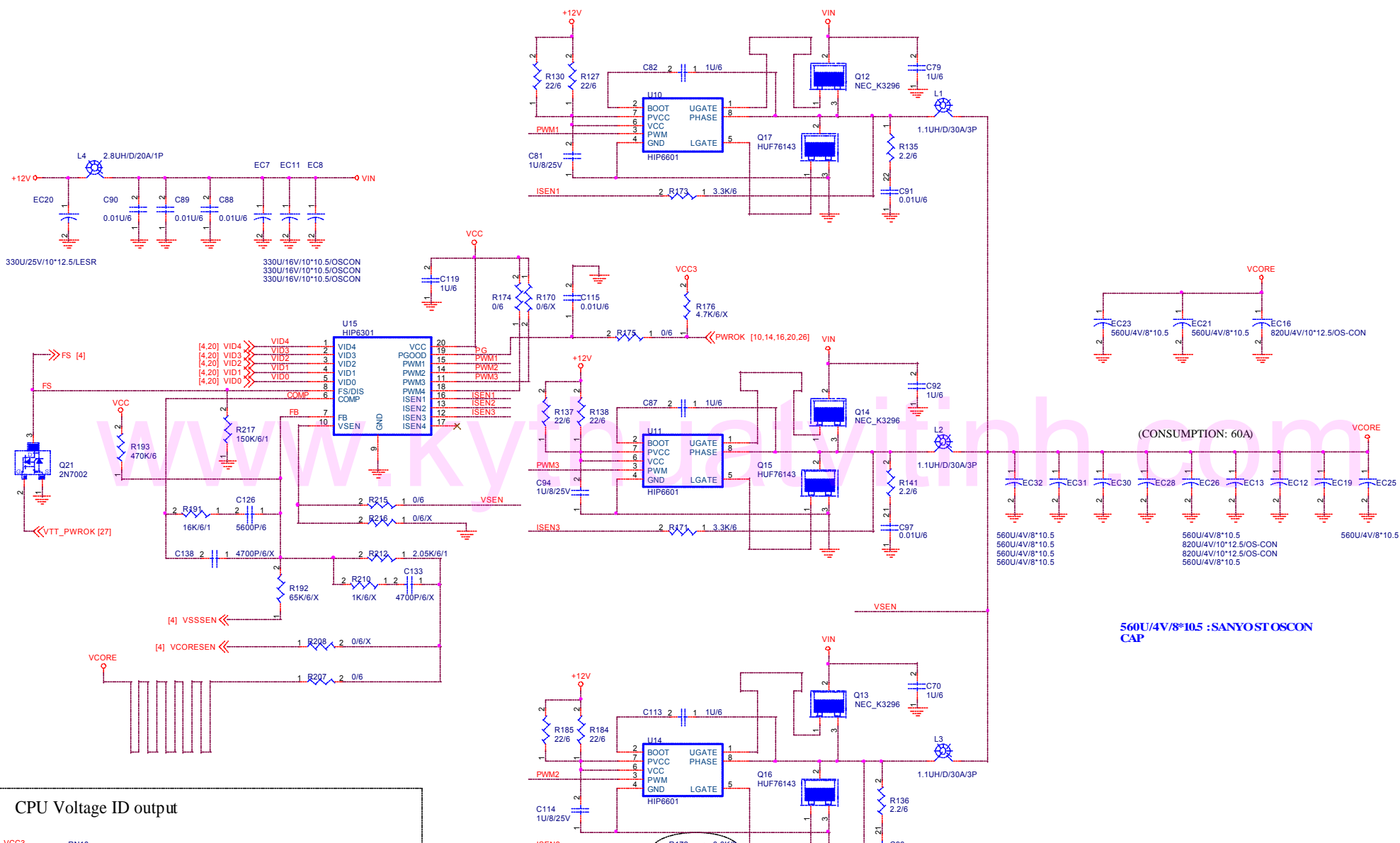


FOR ICH2 POWER SEQUENCE

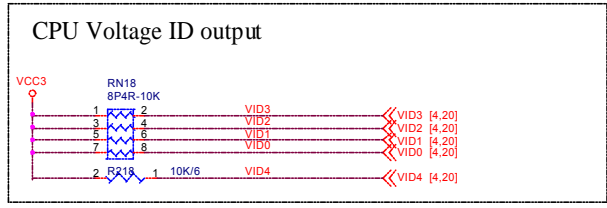


GIGABYTE TECHNOLOGY CORPORATION

Title		
Misc. PWR & ATX CONN.		
Size	Document Number	Rev
	GA-8IRXRR10 FOR SR113	0.2
Date	Friday, May 10, 2002	Sheet 27 of 40

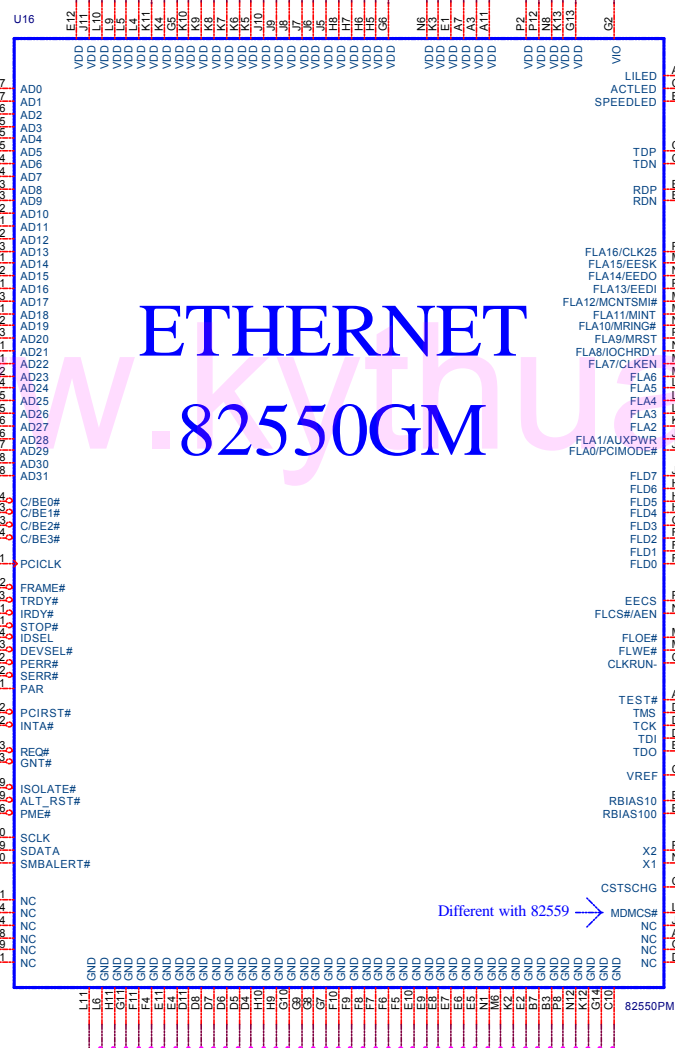
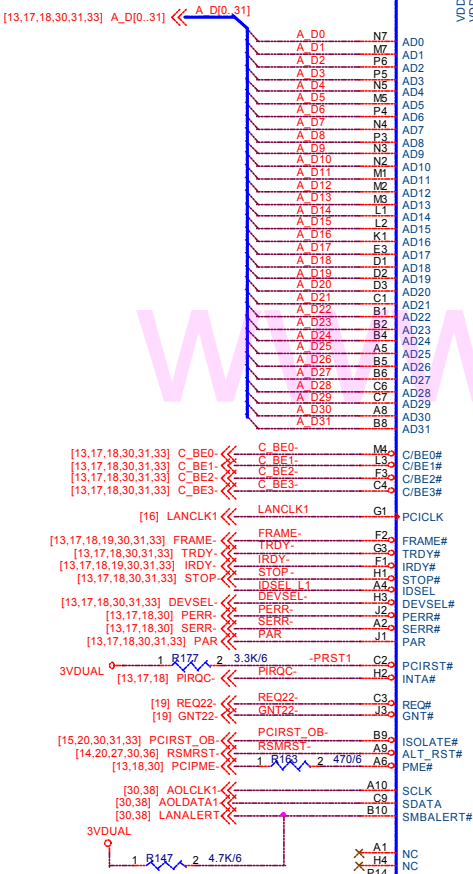
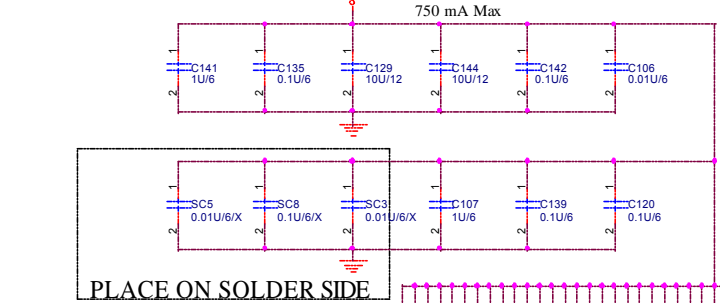


560U/4V/8\*10.5 : SANYO ST OSCON CAP

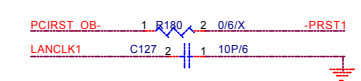
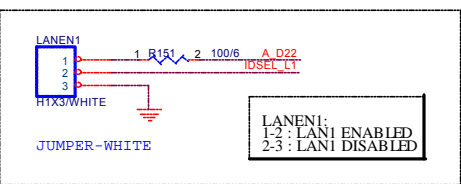
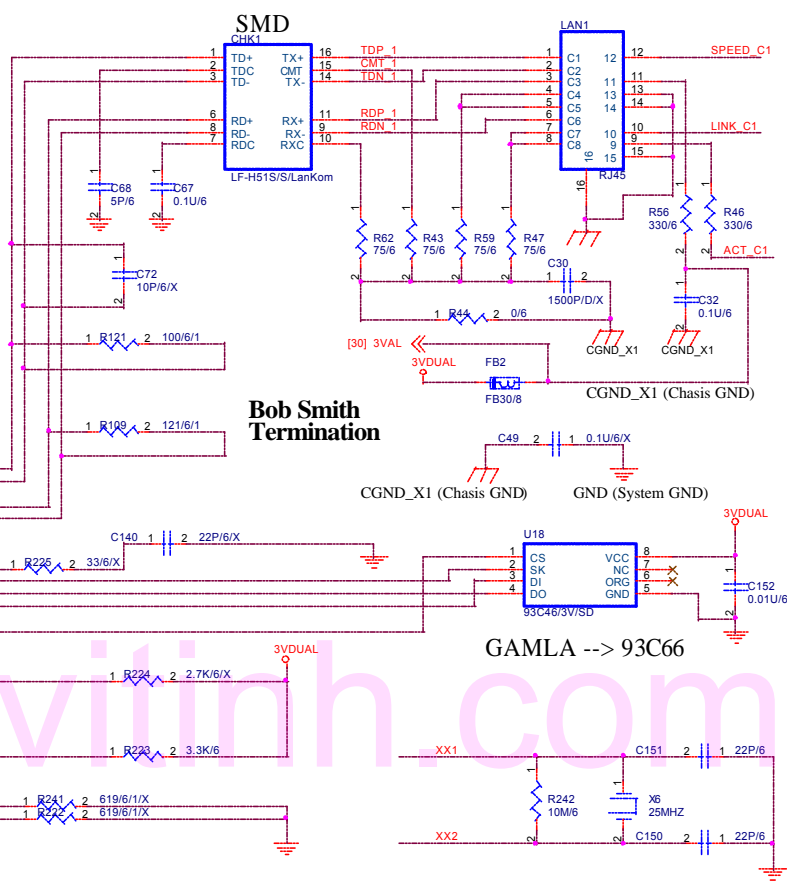


<b>GIGABYTE TECHNOLOGY CORPORATION</b>		
Title: Vcore PWM HIP6301+6601		
Size:	Document Number:	Rev: 0.2
<b>GA-8IRXRR10 FOR SR113</b>		
Date: Friday, May 10, 2002	Sheet: 28	of 40

# ETHERNET 82550GM

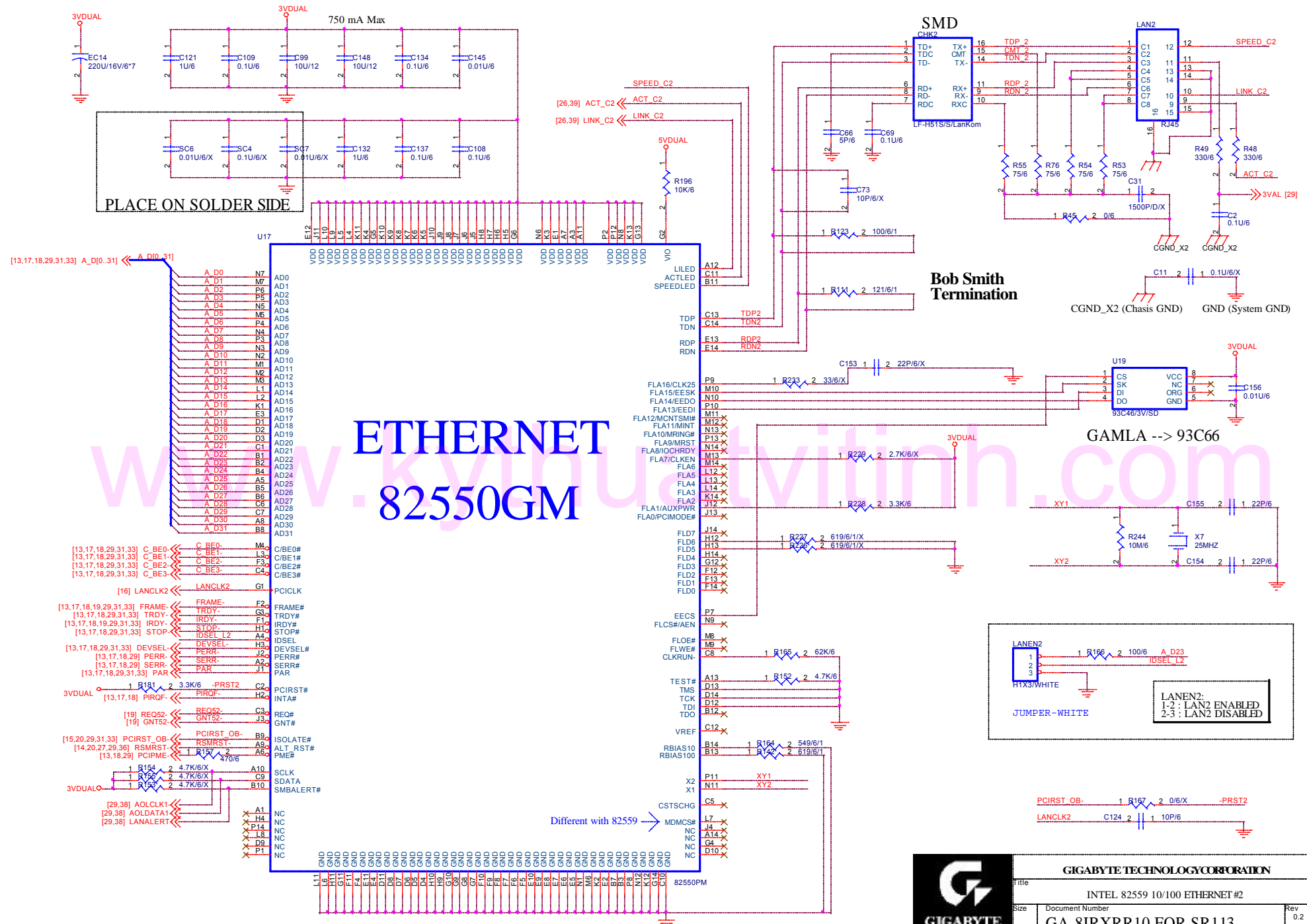


Different with 82559 →



GIGABYTE TECHNOLOGY CORPORATION		
Title	INTEL 82559 10/100 ETHERNET #1	
Size	Document Number	Rev
	<b>GA-8IRXRR10 FOR SR113</b>	0.2
Date	Friday, May 10, 2002	Sheet 29 of 40

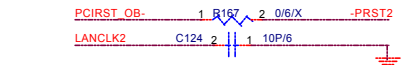
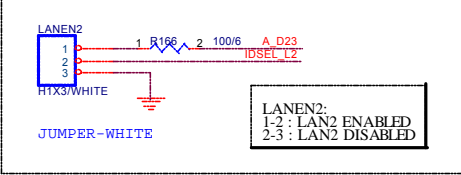
# ETHERNET 82550GM



PLACE ON SOLDER SIDE

Bob Smith Termination

GAMLA --> 93C66

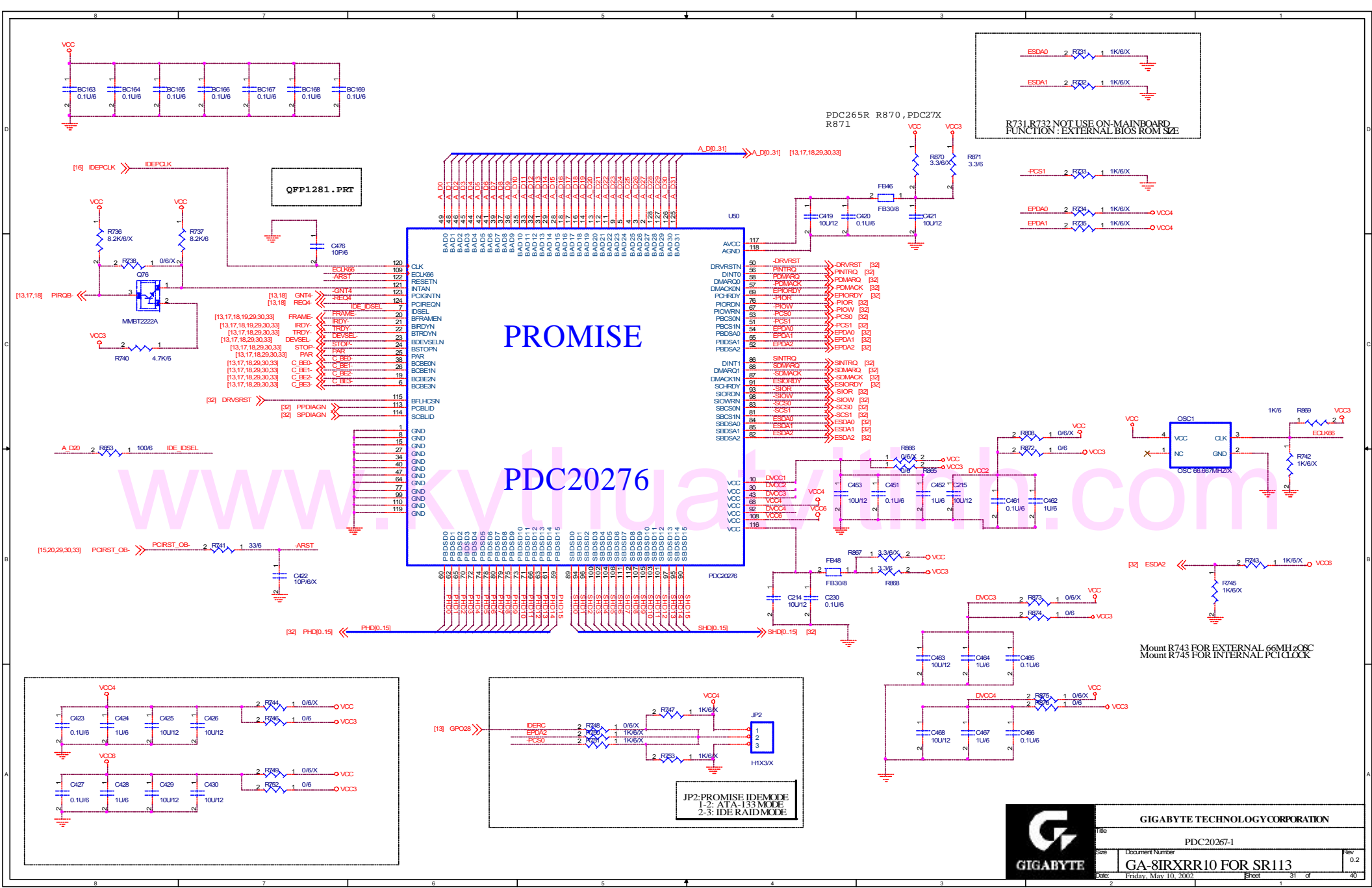


Different with 82559



GIGABYTE TECHNOLOGY CORPORATION

Title			INTEL 82559 10/100 ETHERNET #2
Size	Document Number	Rev 0.2	
Date		Friday, May 10, 2002	Sheet 30 of 40
GA-8IRXRR10 FOR SR113			



PROMISE

PDC2076

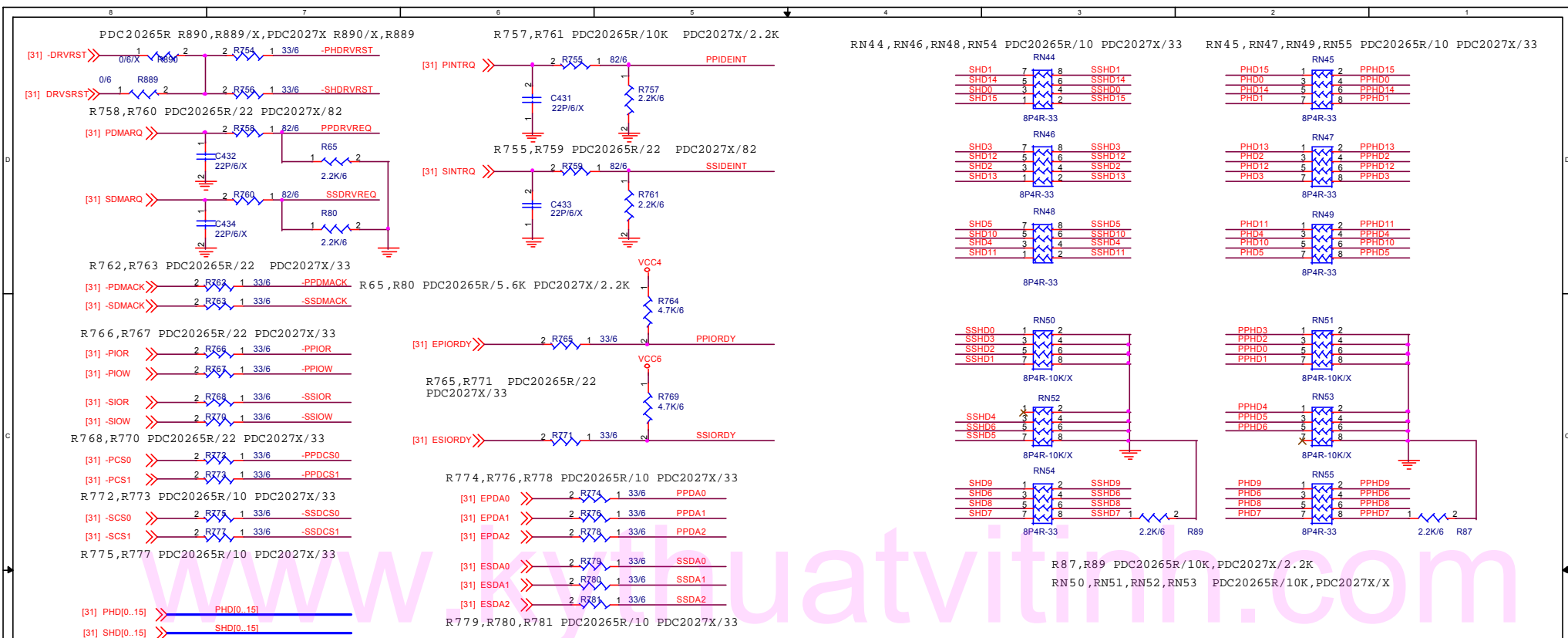
ESDA0 2 R731 1 1K/6X  
 ESDA1 2 R732 1 1K/6X

R731, R732 NOT USE ON-MAINBOARD  
 FUNCTION: EXTERNAL BIOS ROM SIZE

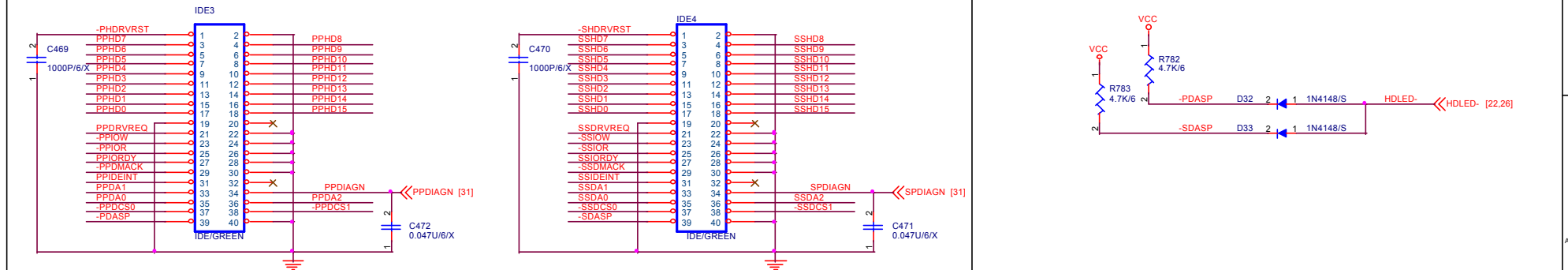
Mount R743 FOR EXTERNAL 66MHZ OSC  
 Mount R745 FOR INTERNAL PCI CLOCK

JP2: PROMISE IDE MODE  
 1-2: AT 133 MODE  
 2-3: IDE RAID MODE





### PROMISE IDE RAID 2 ATA-133 E-IDE CHANNEL



**POWER REQUIREMENT**  
Rage XL

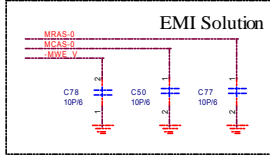
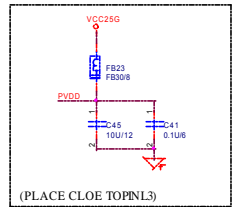
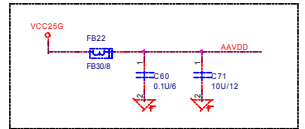
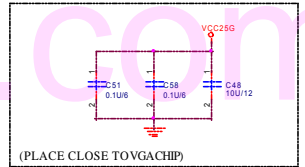
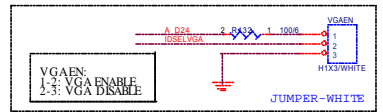
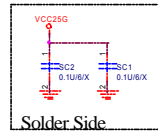
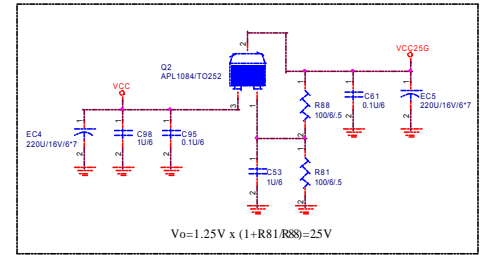
VDDR = 3.3V  $\approx$  0.25A  
 VDDC = 2.8V  $\approx$  1.5A  
 PVDD = 2.5V  $\approx$  0.08A  
 AVDD = 2.5V  $\approx$  0.07A

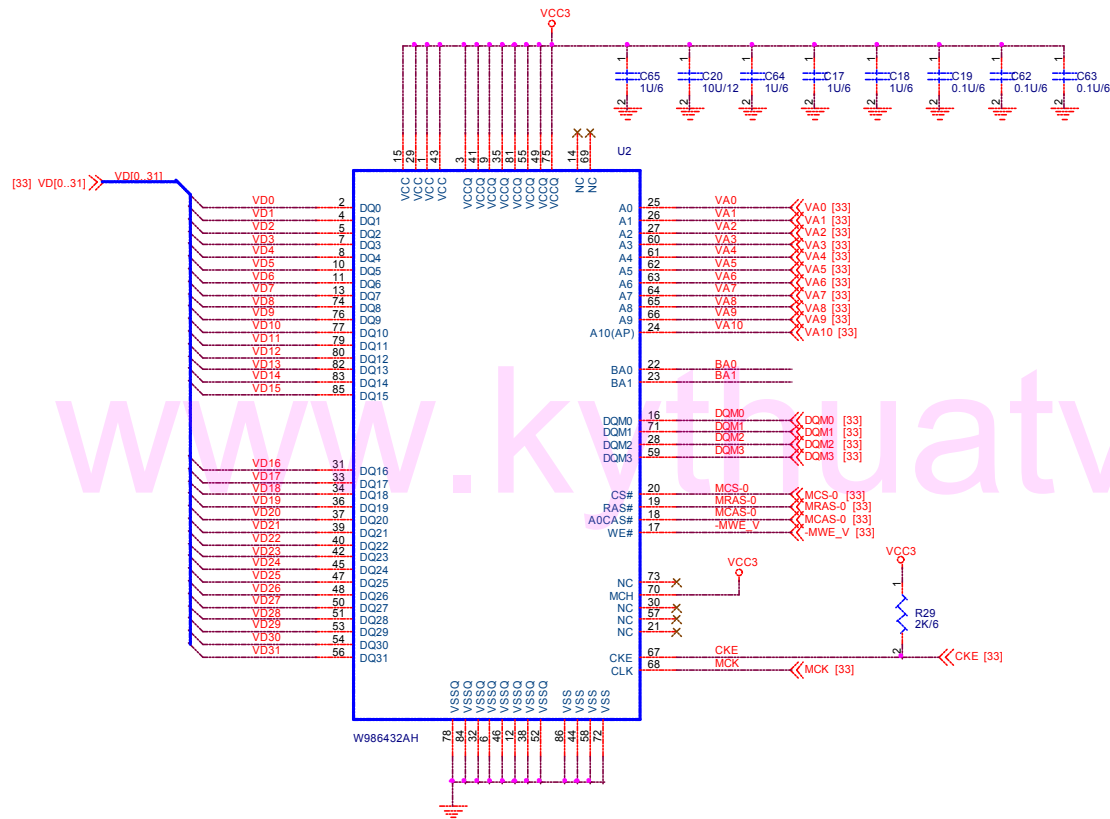
**POWER REQUIREMENT**  
Rage 128VR

VDDR = 3.3V  $\approx$  0.25A  
 VDDC = 2.8V  $\approx$  1.5A  
 PVDD = 2.5V  $\approx$  0.08A  
 AVDD = 2.5V  $\approx$  0.07A

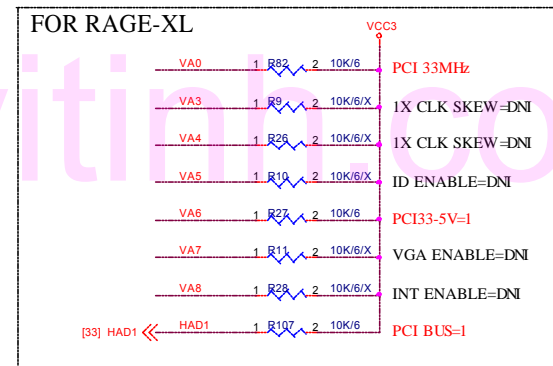
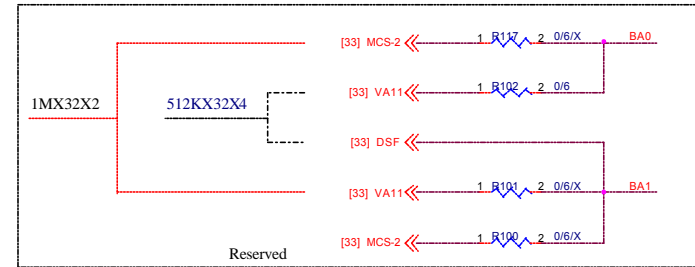
# Rage - XL (215R3LASB22)

## ATI RAGE 128 VR



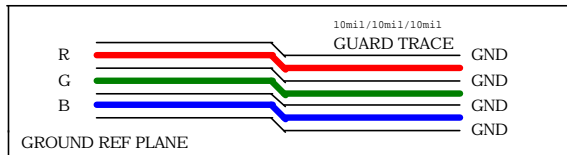


1M x 32 x 2 = R101, R117 Install  
 512K x 32 x 4 = R102, R79 Install

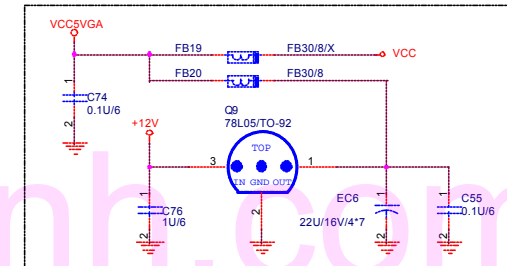
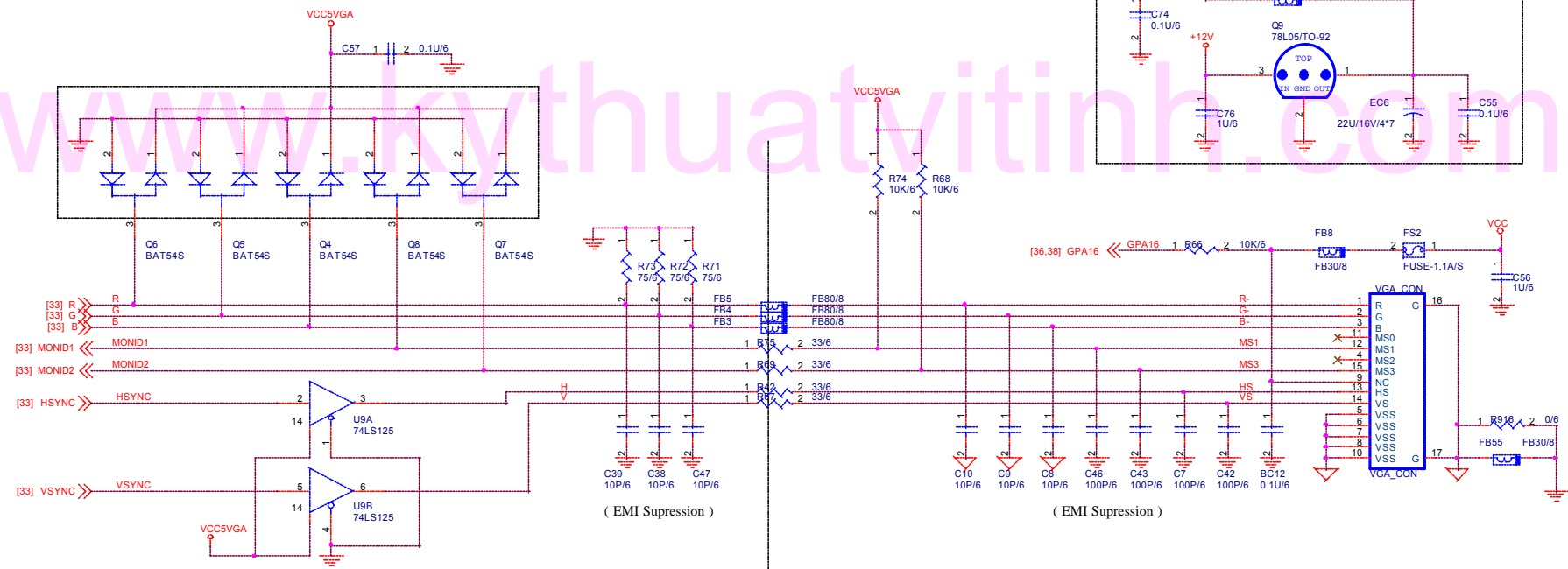


www.kvtpuatinh.com



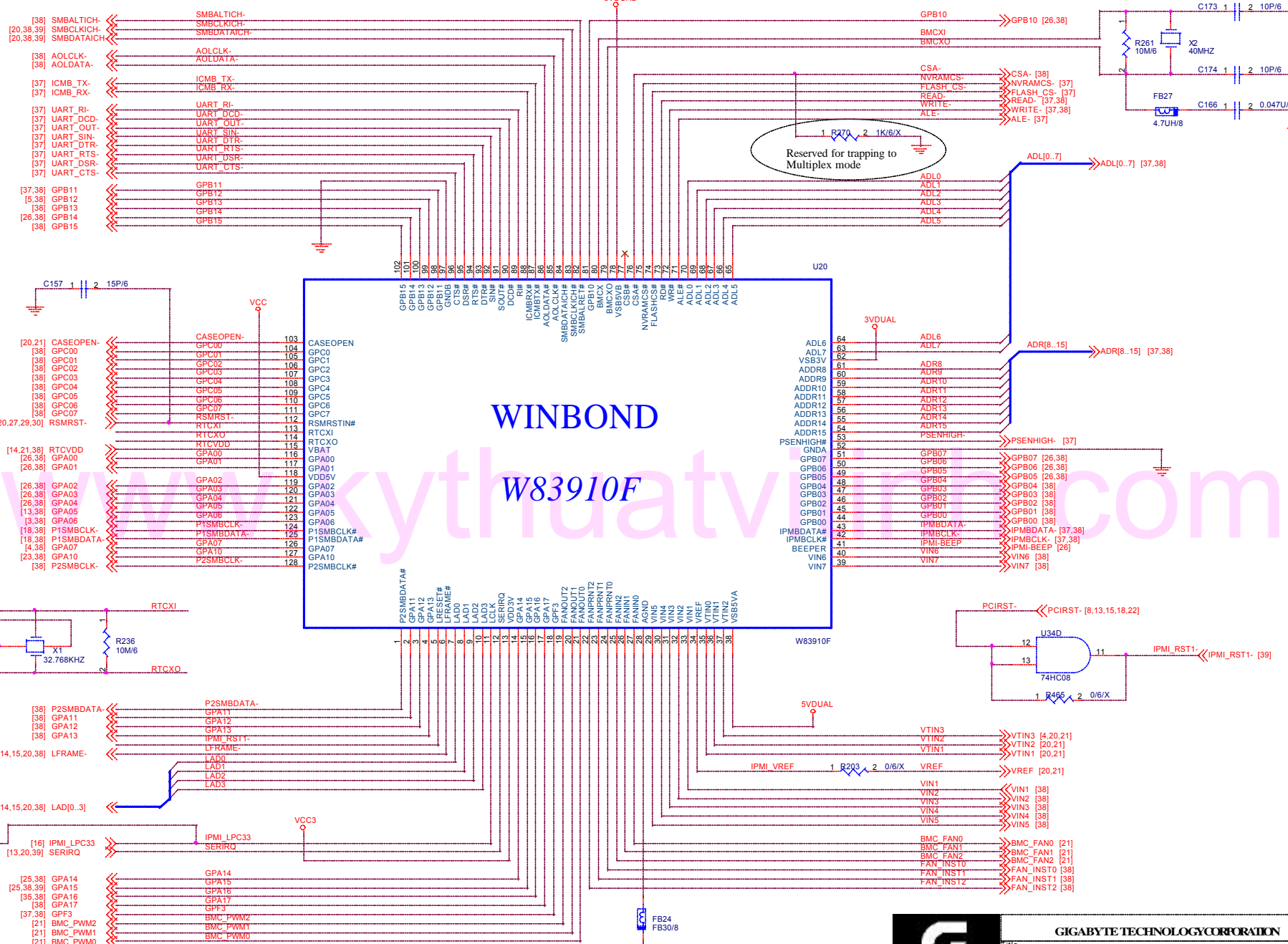


The analog DAC outputs R, G and B are designed to drive a 37.5 Ohm equivalent load.



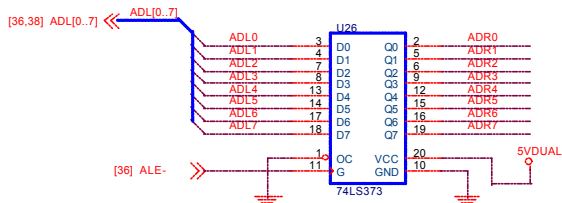
CUT PLANE

		<b>GIGABYTE TECHNOLOGY CORPORATION</b>	
		Title: VGA CON	
Size:	Document Number:	Rev: 0.2	
Date:	Friday, May 10, 2007	Sheet 35 of 40	

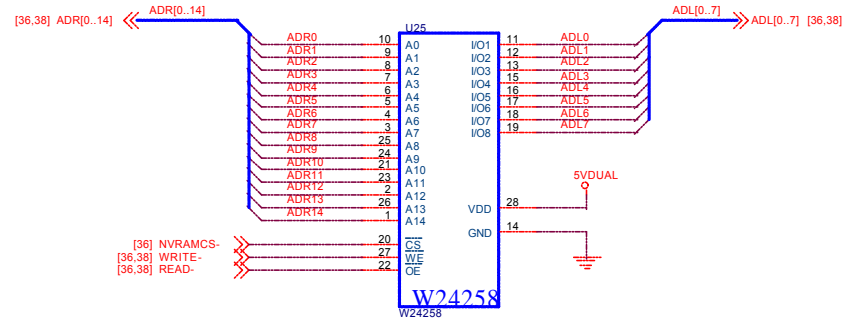
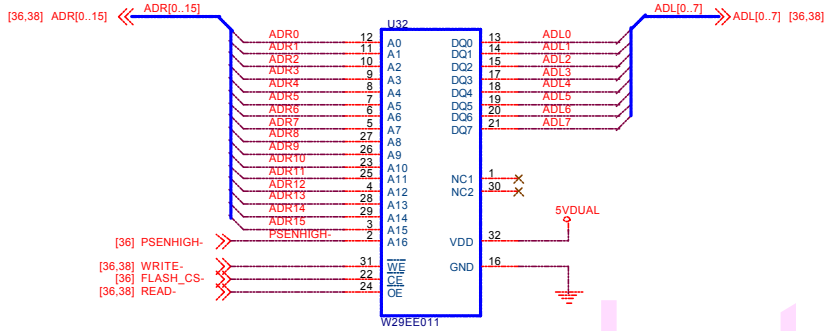


GIGABYTE TECHNOLOGY CORPORATION

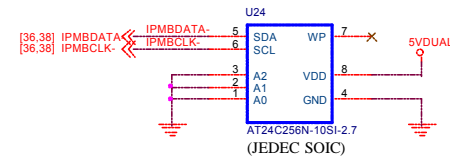
Title		WINBOND IPMI #1
Size	Document Number	GA-8IRXRR10 FOR SR113
Date	Friday, May 10, 2002	Sheet 36 of 40
Rev	0.2	



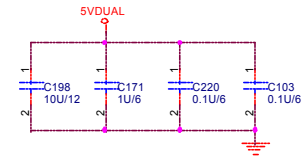
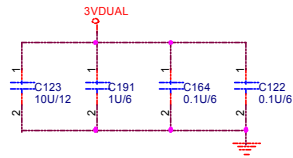
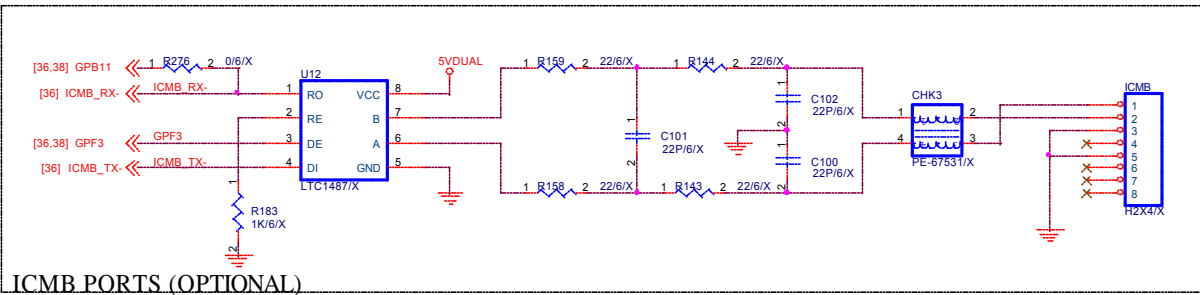
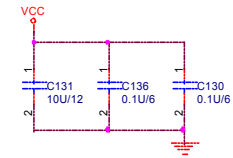
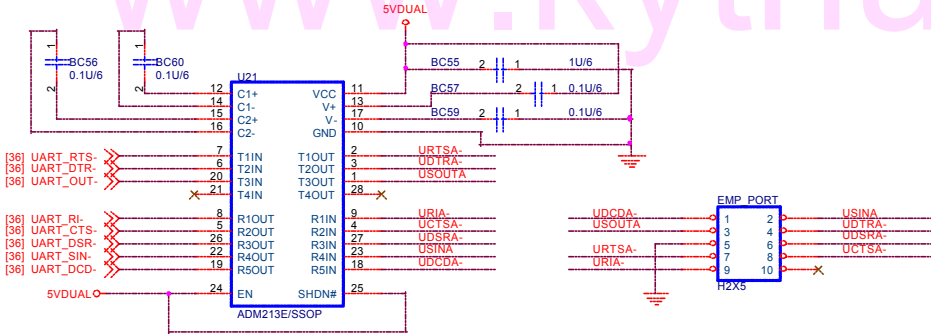
**FLASH ROM**



Note: The Flash and RAM access time should be within 70 ns.



**EMP (Emergency Management Port) Connect to MODEM**

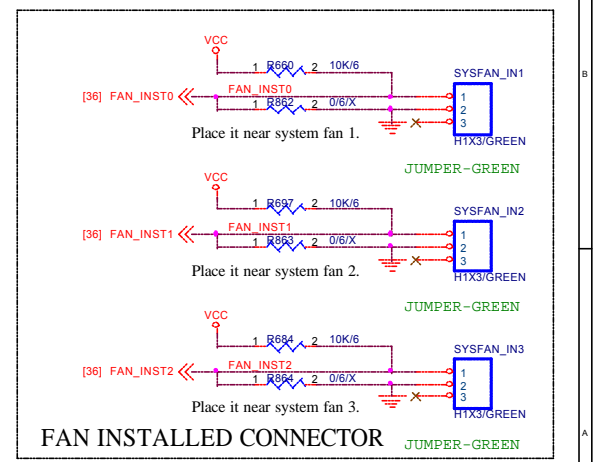
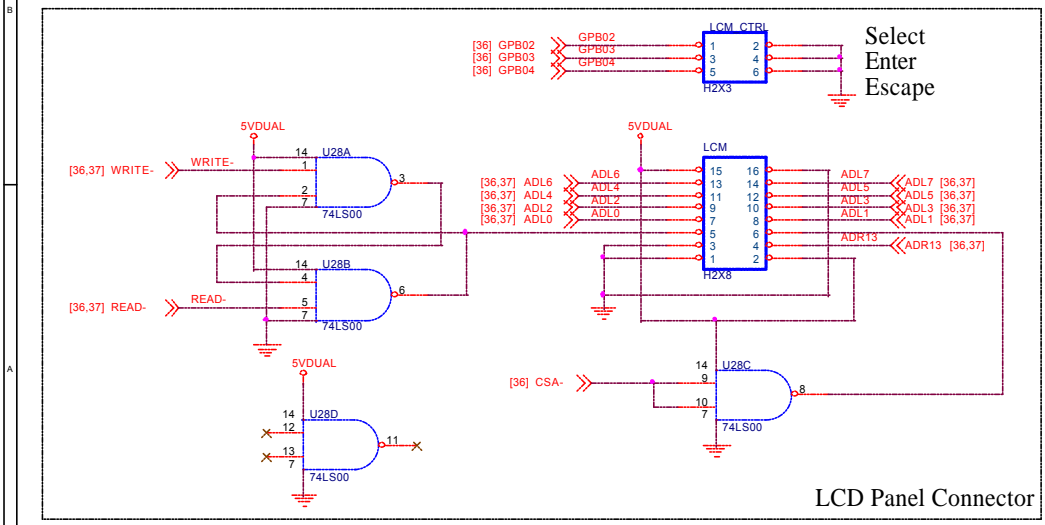
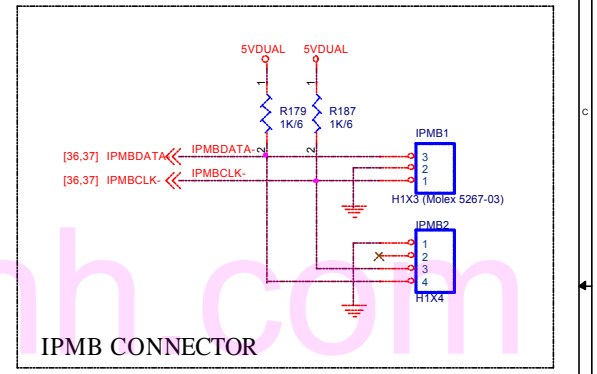
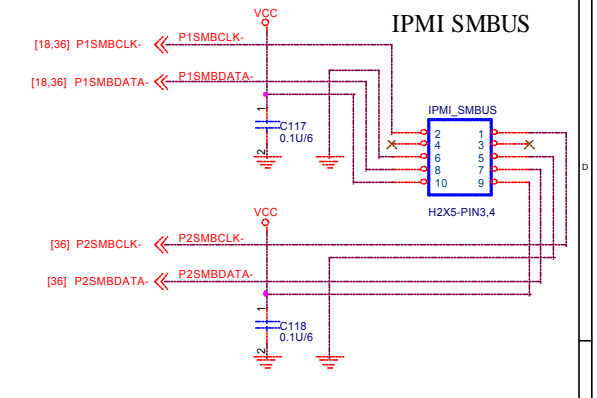
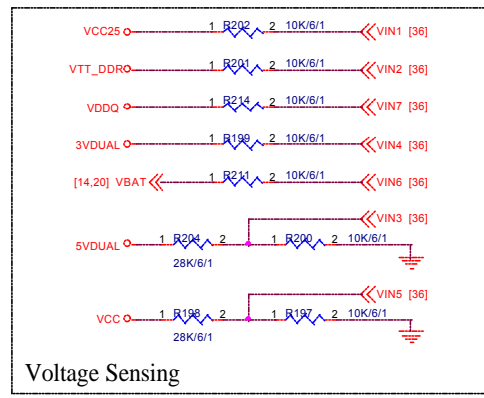


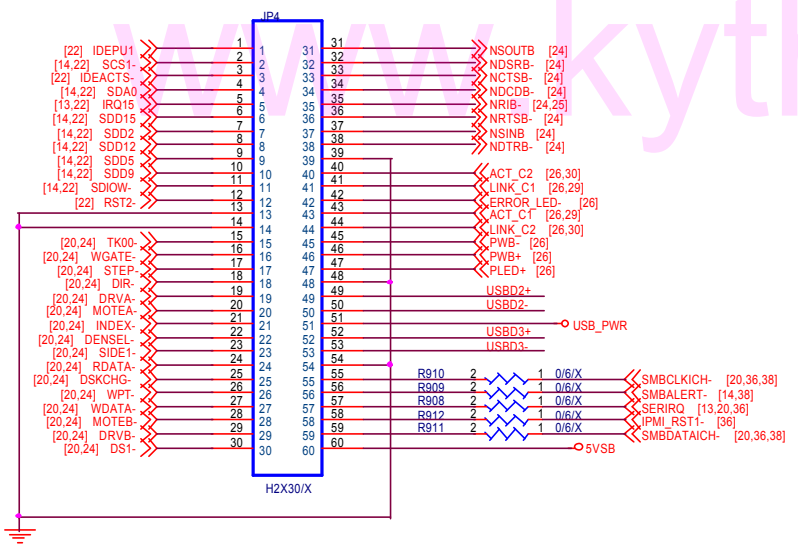
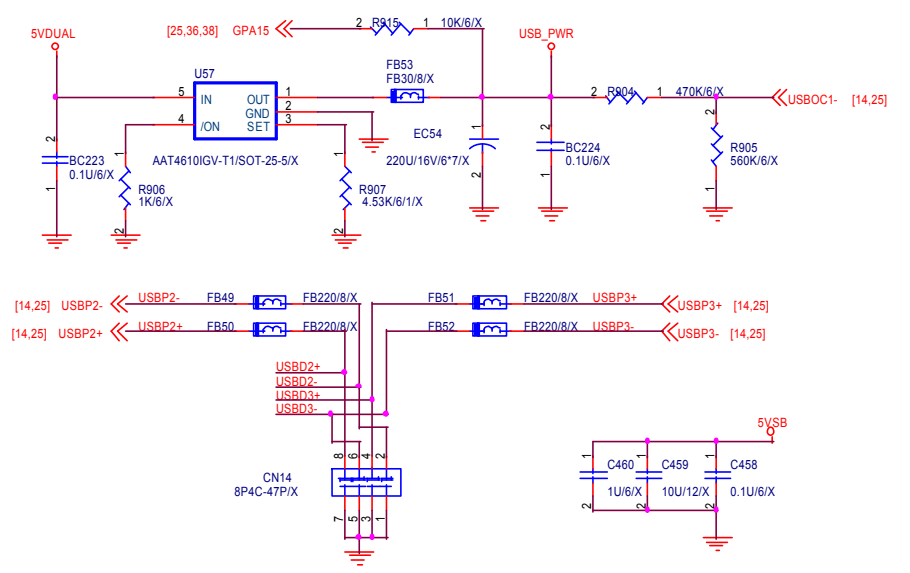
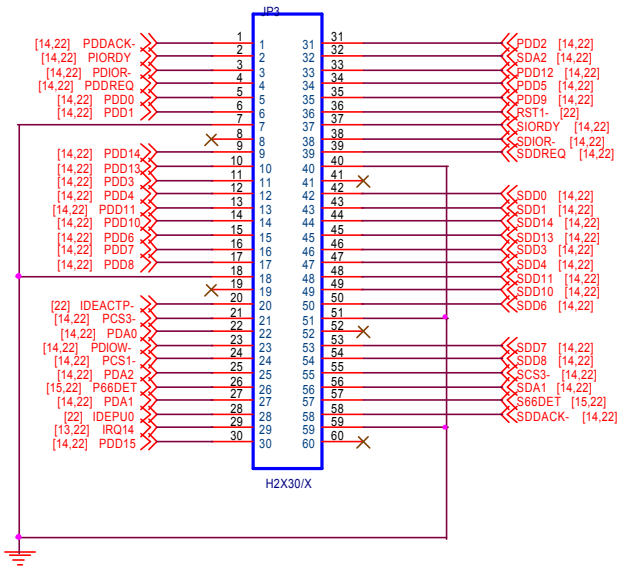
**ICMB PORTS (OPTIONAL)**



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# Model Name : GA-8IRXRR

## Version: 0.2

### GPIO ASSIGN LIST

CHIPSET	GPIO	DESCRIPTION
ICH2	GPI0	REQA- (Pull up only)
ICH2	GPI1	REQ5-
ICH2	GPI2	PIRQE- For PCI Device
ICH2	GPI3	PIRQF- For PCI Device
ICH2	GPI4	PIRQG- For PCI Device
ICH2	GPI5	PIRQH- For USB2
ICH2	GPI6	Green Button
ICH2	GPI7	Super I/O Hardware Monitor SMI-
ICH2	GPI8	NMI Signal From IPMI BMC
ICH2	GPI11	SMBALERT- (Pull up only)
ICH2	GPI12	LPCPME-
ICH2	GPI13	BACK PLANE HW SMI INPUT
ICH2	GPO16	GNTA- (Pull up only)
ICH2	GPO17	GNT5- For PCI Device
ICH2	GPO18	(Pull up only)
ICH2	GPO19	(Pull up only)
ICH2	GPO20	(Pull up only)
ICH2	GPO21	(Pull up only)
ICH2	GPO22	(Pull up only)
ICH2	GPO23	(Pull up only)
ICH2	GPO24	BIOS Table Lock
ICH2	GPO25	BIOS Write Protect
ICH2	GPO27	Error LED
ICH2	GPO28	Promise IDE Mode

### GPIO ASSIGN LIST

CHIPSET	GPIO	DESCRIPTION
W83627HF	GP10	CPU Fan Fail LED
W83627HF	GP11	Power Fan Fail LED
W83627HF	GP12	System Fan Fail LED
W83627HF	GP13	Power Fan Present Input Connector
W83627HF	GP14	CPU Fan Present Input Connector
W83627HF	GP15	Model Identification Switch For Hitachi.
W83627HF	GP16	Model Identification Switch For Hitachi.
W83627HF	GP17	Model Identification Switch For Hitachi.
W83627HF	GP20	
W83627HF	GP21	SMBUS CLK (SLAVE)
W83627HF	GP22	SMBUS DATA (SLAVE)
W83627HF	GP23	Power LED
W83627HF	GP24	
W83627HF	GP25	IR RX
W83627HF	GP26	IR TX
W83627HF	GP30	SLP_S5
W83627HF	GP31	Power Control: Power Supply ON "PS_ON-"
W83627HF	GP32	Power OK
W83627HF	GP33	Resume Reset
W83627HF	GP34	CIR RX
W83627HF	GP35	SUSLED (Green LED)

### GPIO ASSIGN LIST

CHIPSET	GPIO	POWER	DESCRIPTION
W83910F	GPA00	VCC	For All Error LED: Active Low
W83910F	GPA01	VCC	For Hard Disk Error LED: Active Low
W83910F	GPA02	VCC	For Fan Error LED: Active Low
W83910F	GPA03	VCC	For Power Supply Error LED: Active Low
W83910F	GPA04	VCC	For System Power LED: Active Low
W83910F	GPA05	VCC	For System NMI: Active High
W83910F	GPA06	VCC	For CPU Internal Error Detect: Active Low
W83910F	GPA07	VCC	For CPU Thermal Trip Signal Detect: Active Low
W83910F	GPA10	VCC	For Keyboard/Mouse Fuse Error
W83910F	GPA11	VCC	For IDE Fuse Error
W83910F	GPA12	VCC	For SCSI Channel1 Fuse Error
W83910F	GPA13	VCC	For SCSI Channel2 Fuse Error
W83910F	GPA14	VCC	For Backplane USB Fuse Error
W83910F	GPA15	VCC	For Front Side USB Fuse Error
W83910F	GPA16	VCC	For VGA Connector Power Fuse Error
W83910F	GPA17	VCC	
W83910F	GPB00	VSBS	For System Green Function: Active Low to ICH2
W83910F	GPB01	VSBS	For Power Button Signal to ICH2: Active Low
W83910F	GPB02	VSBS	For LCD Panel "SELECT"
W83910F	GPB03	VSBS	For LCD Panel "ENTER"
W83910F	GPB04	VSBS	For LCD Panel "ESCAPE"
W83910F	GPB05	VSBS	For Buzzer Stop Button: Active High
W83910F	GPB06	VSBS	For System Power Button: Active High
W83910F	GPB07	VSBS	For System Reset Button: Active High
W83910F	GPB10	VSBS	For System Green Function Button: Active High
W83910F	GPB11	VSBS	For ICMB Port
W83910F	GPB12	VSBS	For CPU Present Detection: Low--> CPU Installed
W83910F	GPB13	VSBS	For KBLock Function From BMC: Active Low
W83910F	GPB14	VSBS	For System Reset From BMC: Active High
W83910F	GPB15	VSBS	For System Power Control From BMC: Active Low
W83910F	GPC00	VBAT	
W83910F	GPC01	VBAT	
W83910F	GPC02	VBAT	
W83910F	GPC03	VBAT	
W83910F	GPC04	VBAT	
W83910F	GPC05	VBAT	
W83910F	GPC06	VBAT	
W83910F	GPC07	VBAT	



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