

**INTEL PENTIUM 4 (NORTHWOOD) PROCESSOR WITH
 INTEL 845 (BROOKDALE) CHIPSET
 SUPPORT 2 NON-ECC & ECC DDR
 INTEL I/O CONTROLLER HUB 2 (ICH2)
 ONBOARD ATI RAGE XL (PCI BUS) SVGA
 DUAL INTEL LOM 82550 SERIES (SERVER ADAPTER)
 ONBOARD ADAPTEC DUAL CHANNEL ULTRA 160 SCSI
 SUPPORT ZERO CHANNEL RAID
 (ADAPTEC RAPTOR OR INTEL RAIDIOS)
 ONBOARD WINBOND IPMI CIRCUIT**

**GA-8IRXR-A
 Revision 1.00
 01/20/2001**

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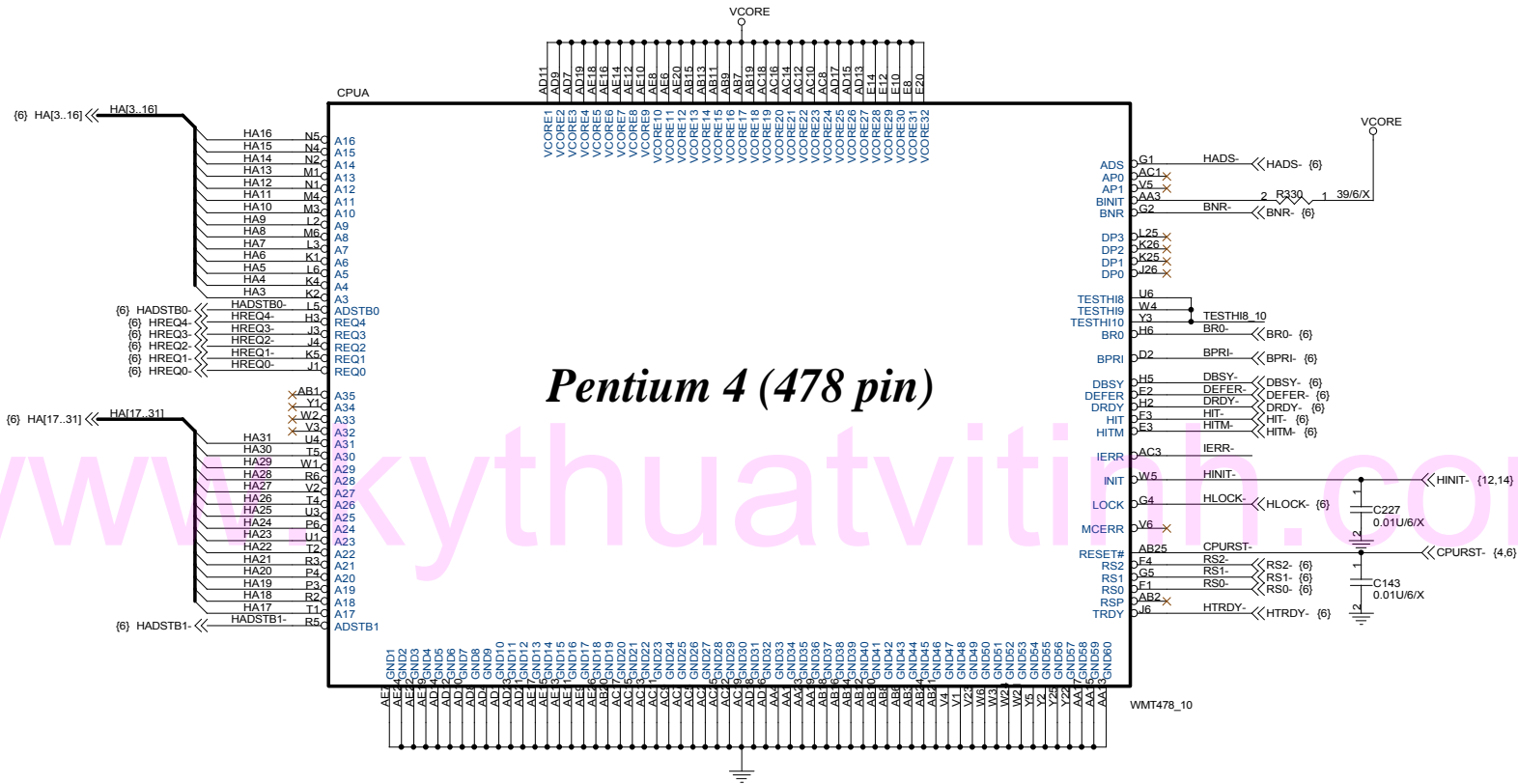
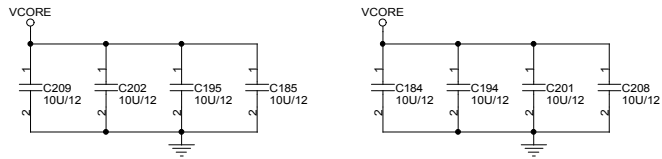


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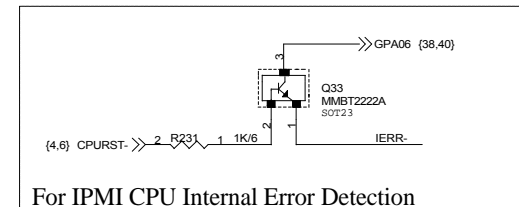
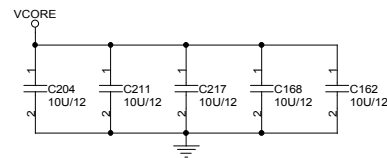
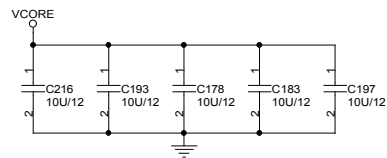
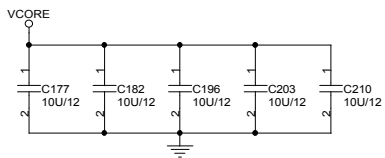
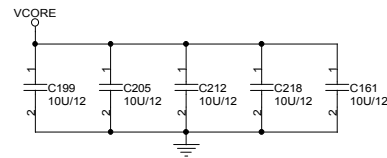
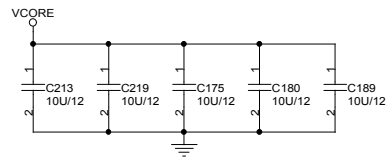
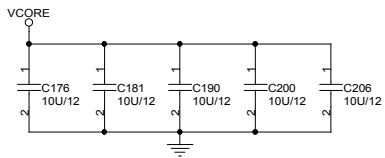
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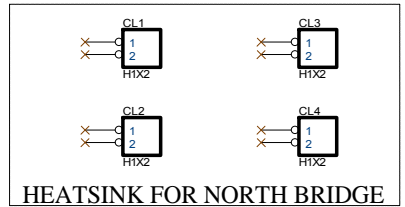
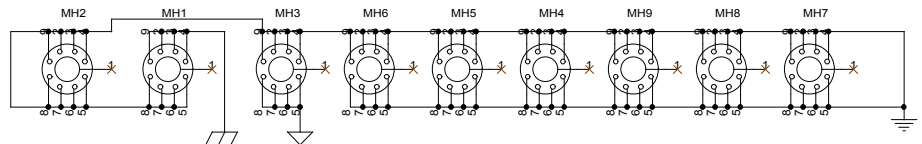
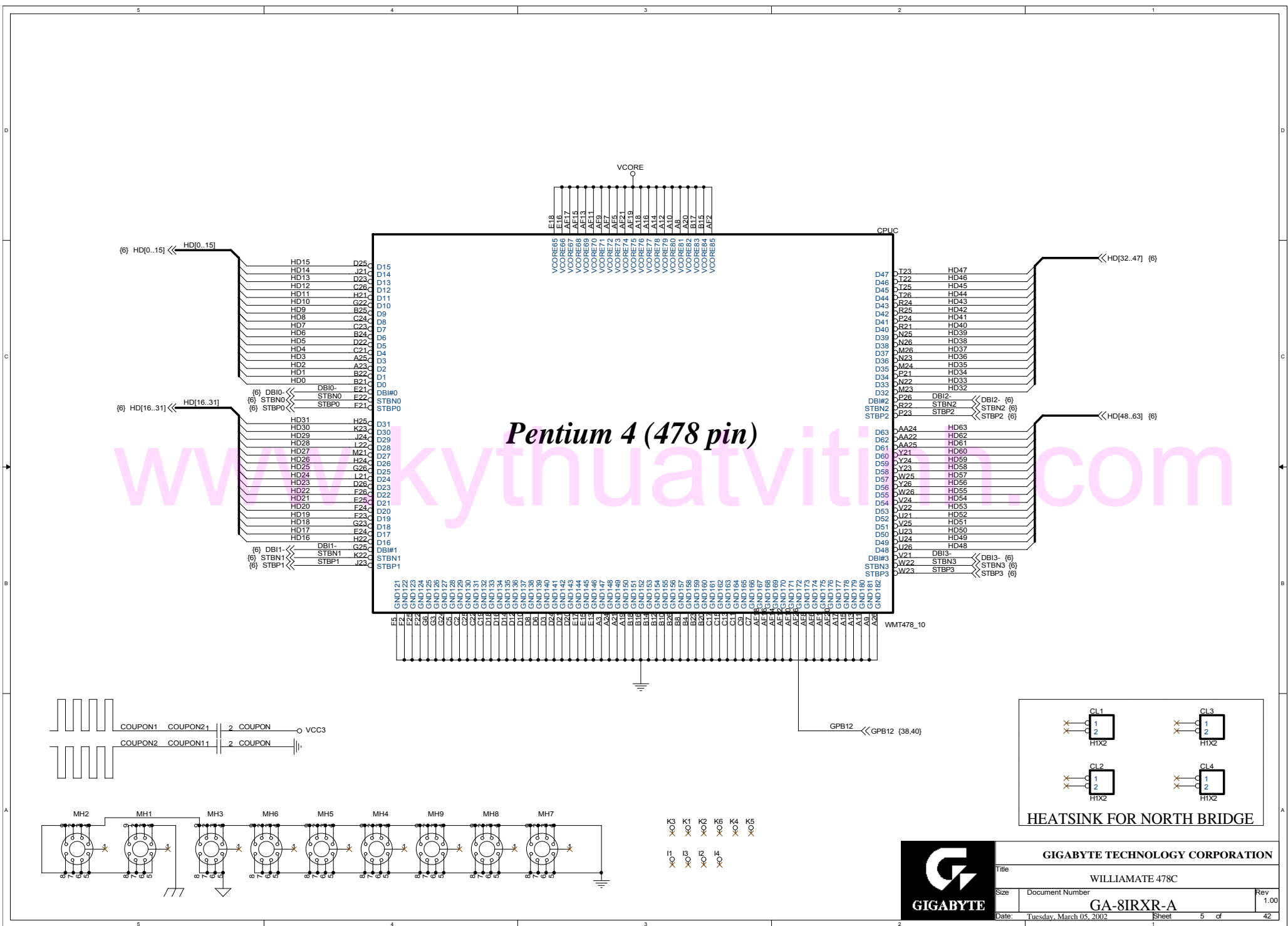
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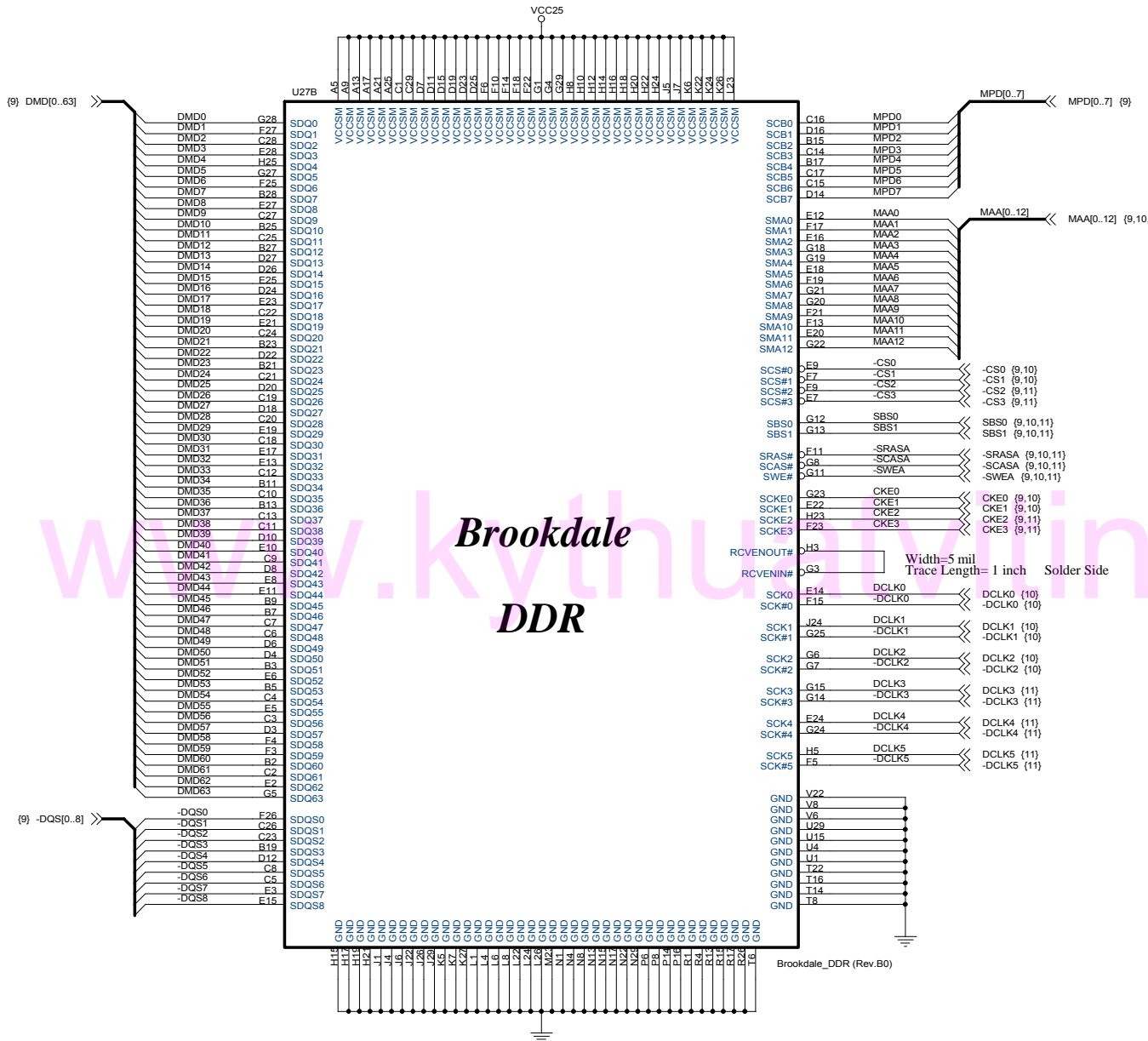


Pentium 4 (478 pin)



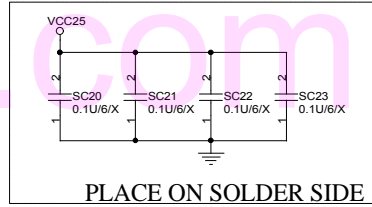


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WILLIAMATE 478C			
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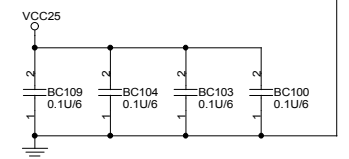
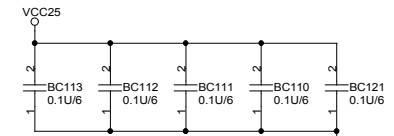


**Brookdale
DDR**

Width=5 mil
Trace Length= 1 inch Solder Side



PLACE ON SOLDER SIDE



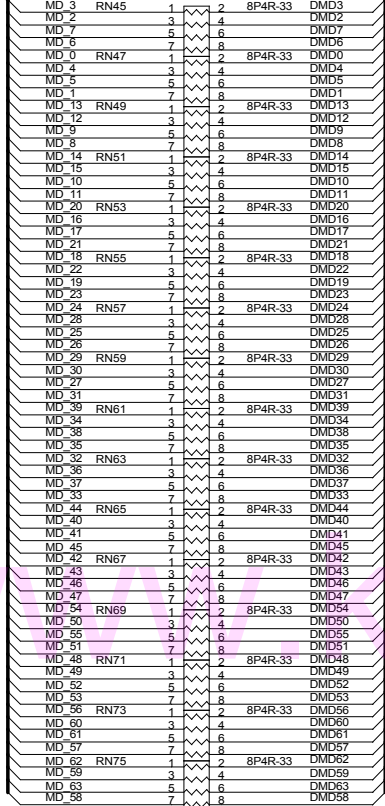
MEMORY INTERFACE

Brookdale_DDR (Rev.B0)

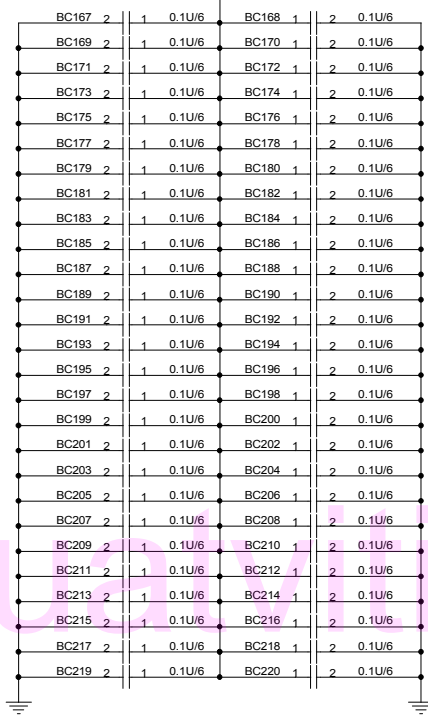


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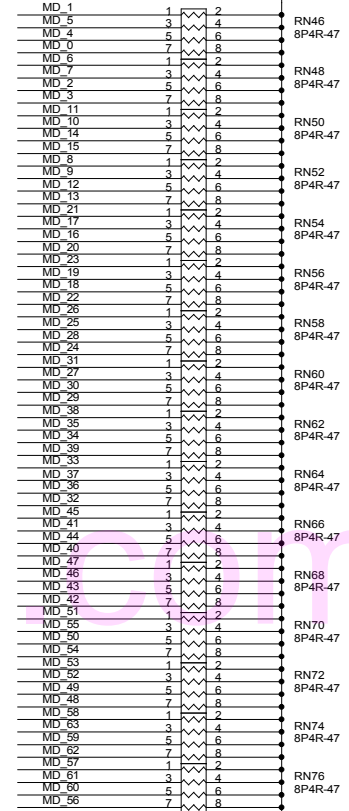
{10,11} MD[0..63] << << DMD[0..63] (7) >>



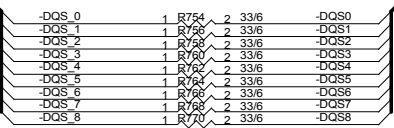
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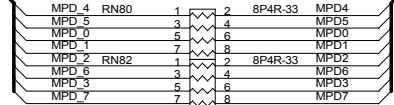
VTT_DDR



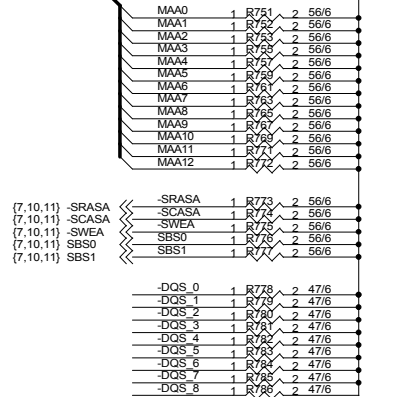
{10,11} -DQS[0..8] << -DQS[0..8] >> DQS[0..8] (7)



{10,11} MPD[0..7] << MPD[0..7] >> MPD[0..7] (7)



{7,10,11} MAA[0..12] << MAA[0..12] >>



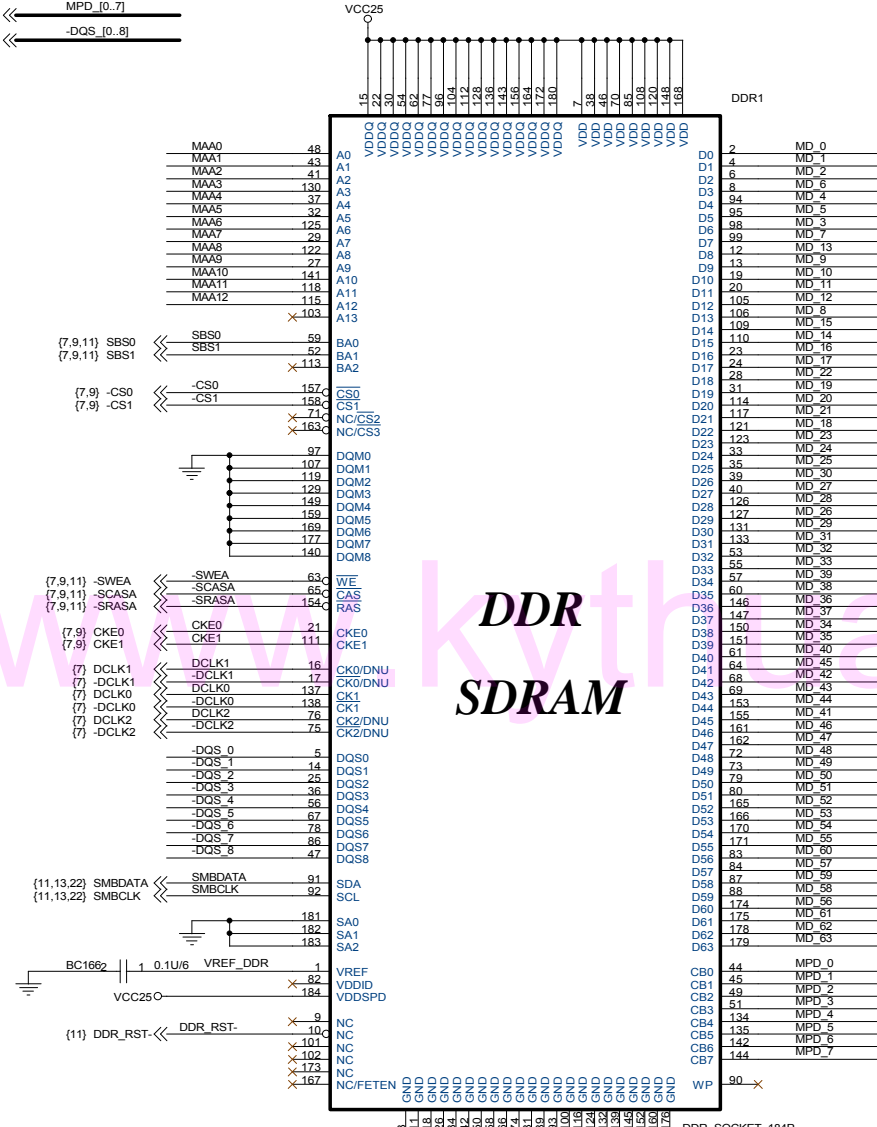
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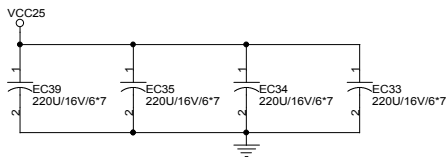
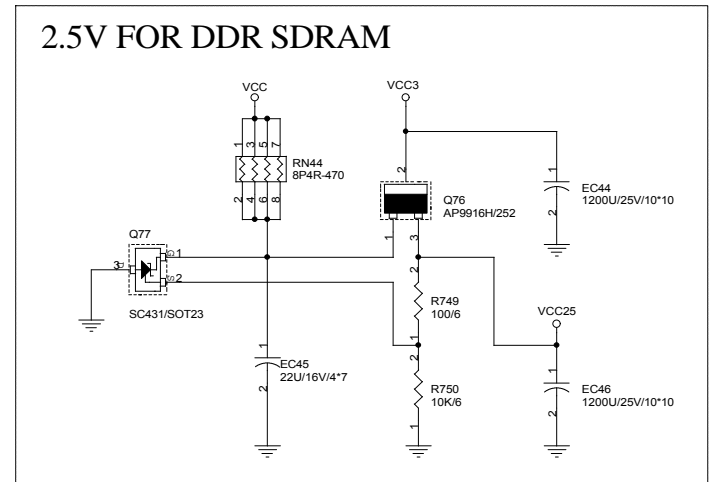
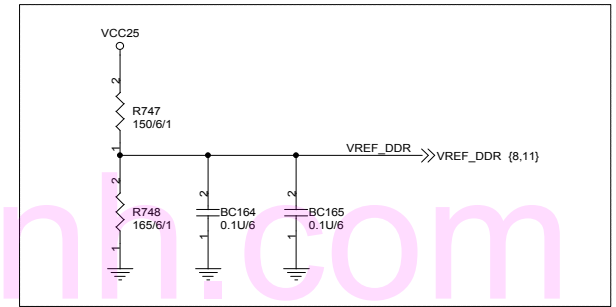
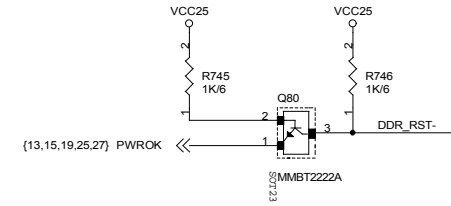
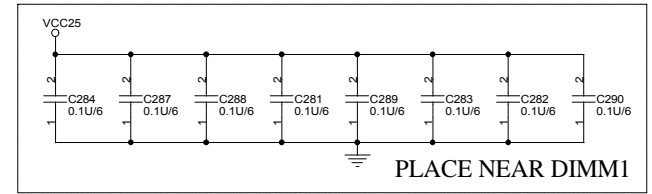
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(9,11) MD_[0..63] << MD_[0..63]
 (7,9,11) MAA[0..12] << MAA[0..12]
 (9,11) MPD_[0..7] << MPD_[0..7]
 (9,11) -DQS_[0..8] << -DQS_[0..8]



**DDR
SDRAM**



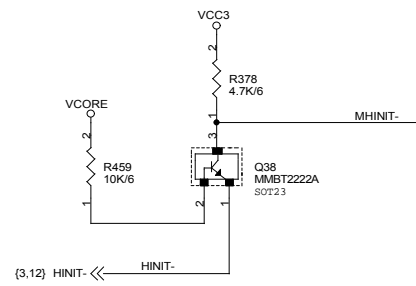
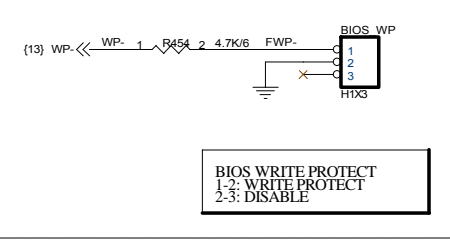
REV 1.0 MODIFY LIST
 1. Modify the library of SC431. The pin1 & pin2 reverse!



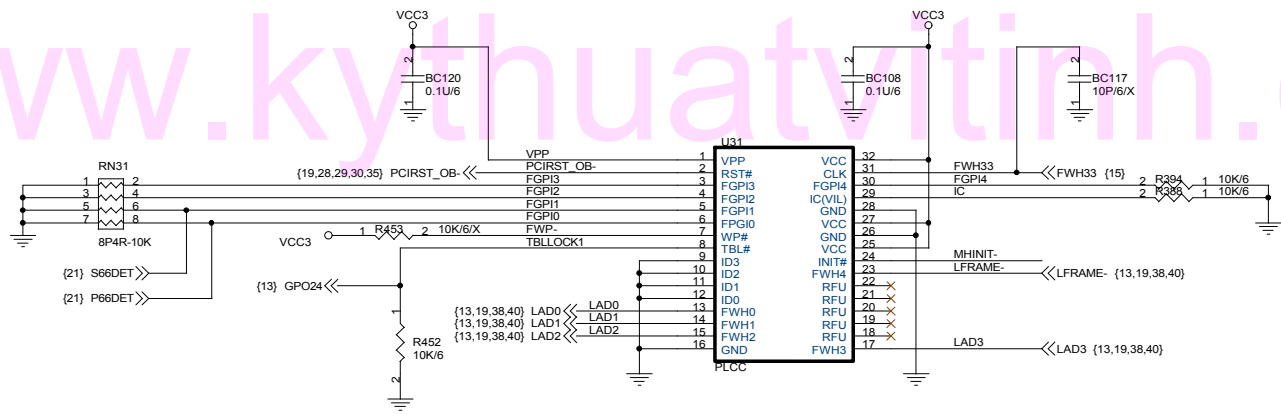
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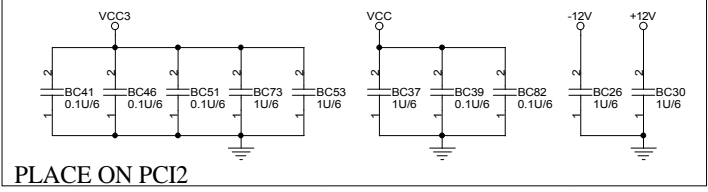
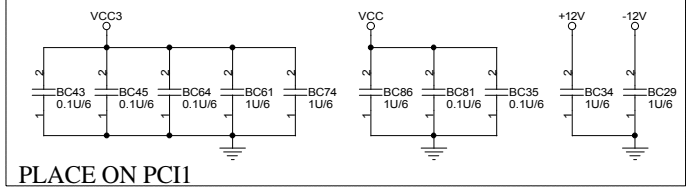
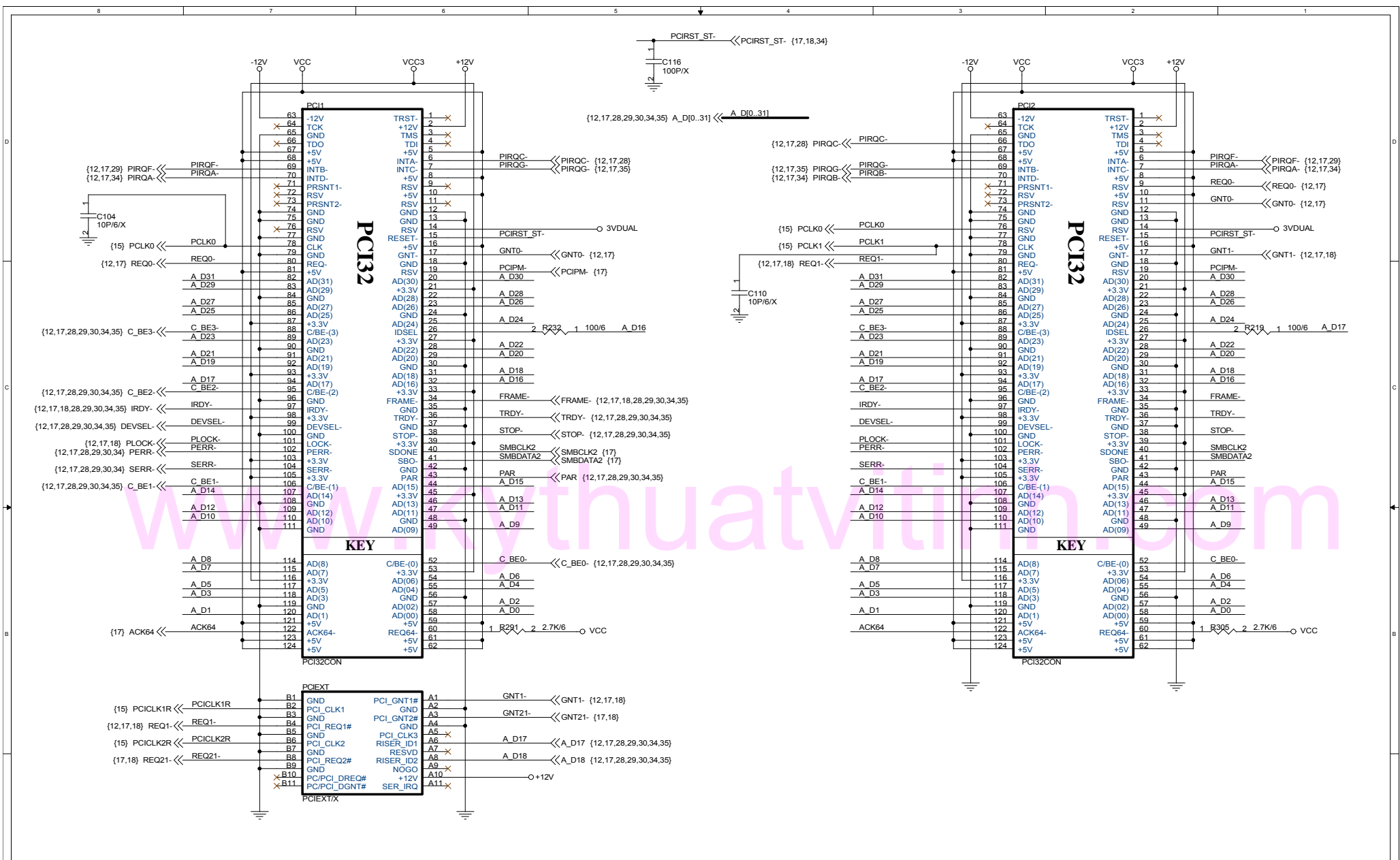


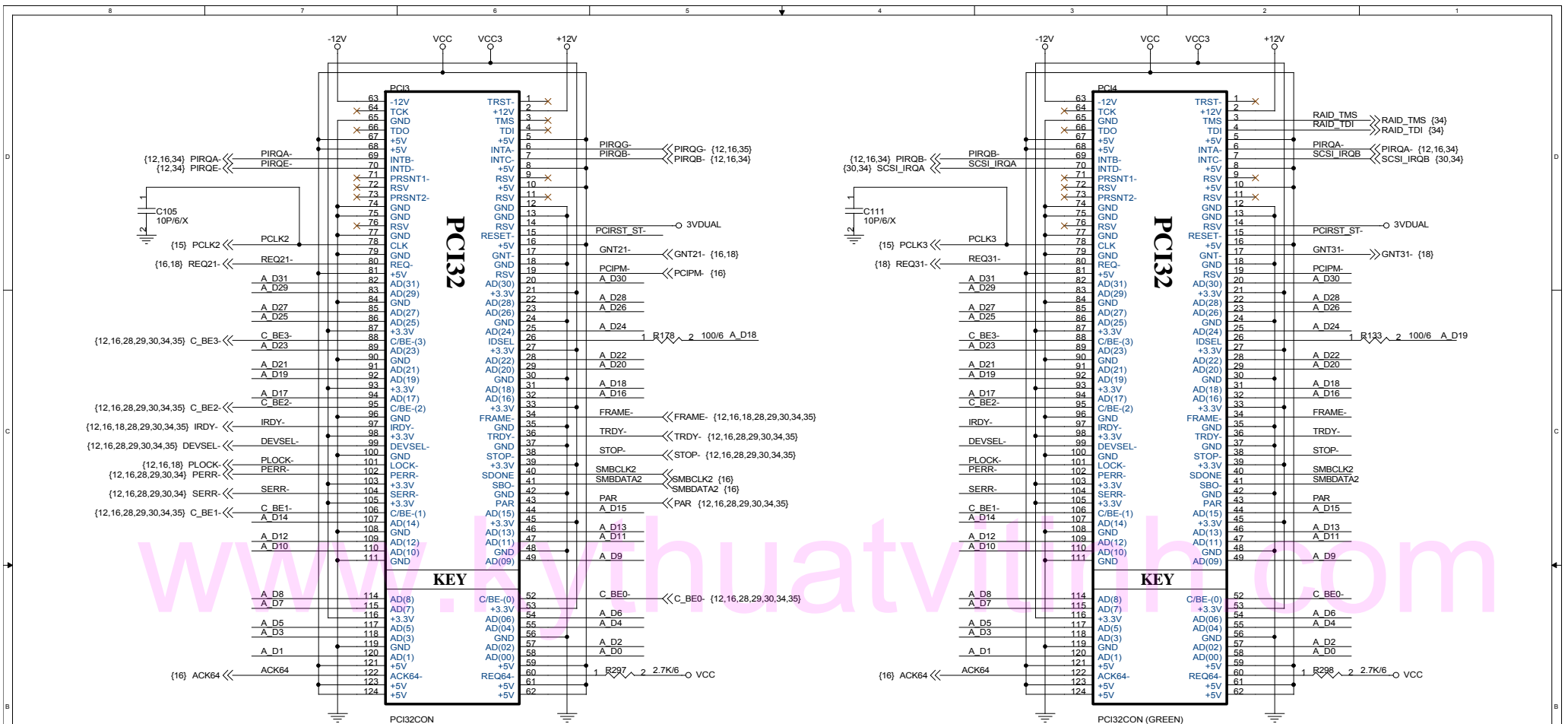
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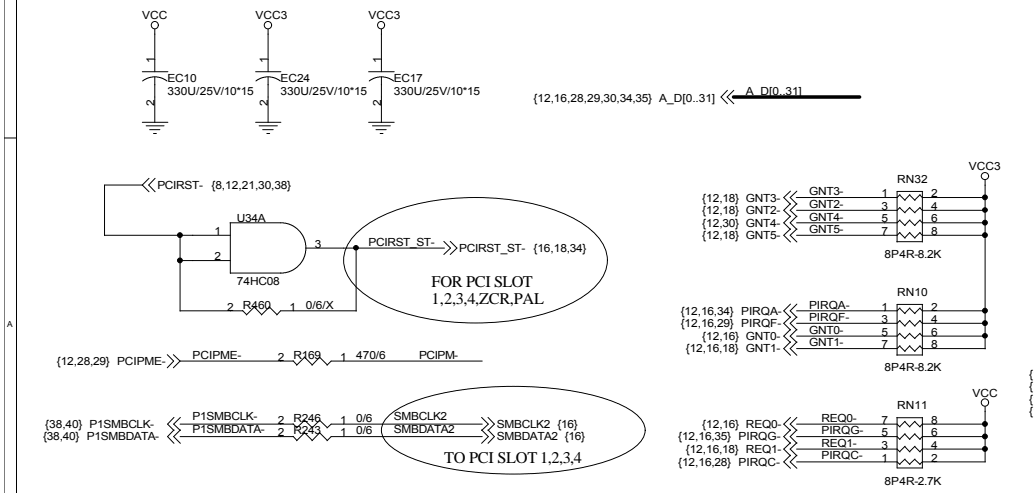
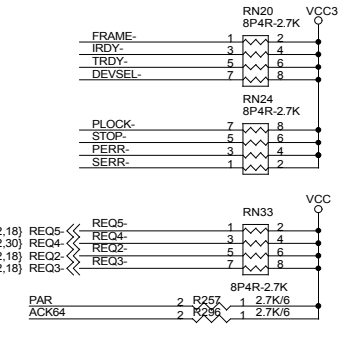
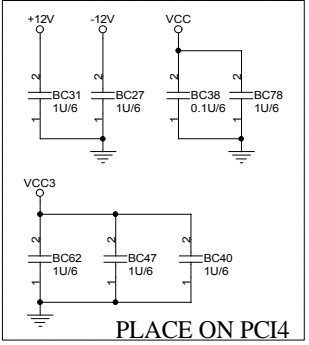
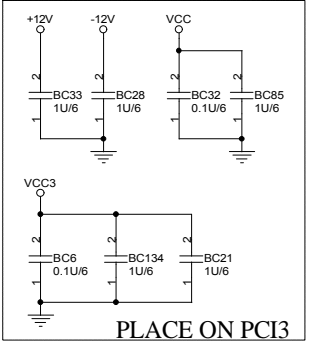
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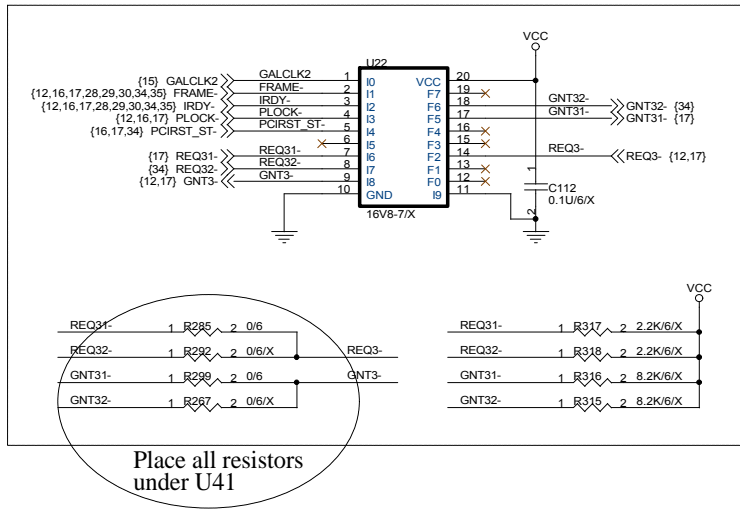
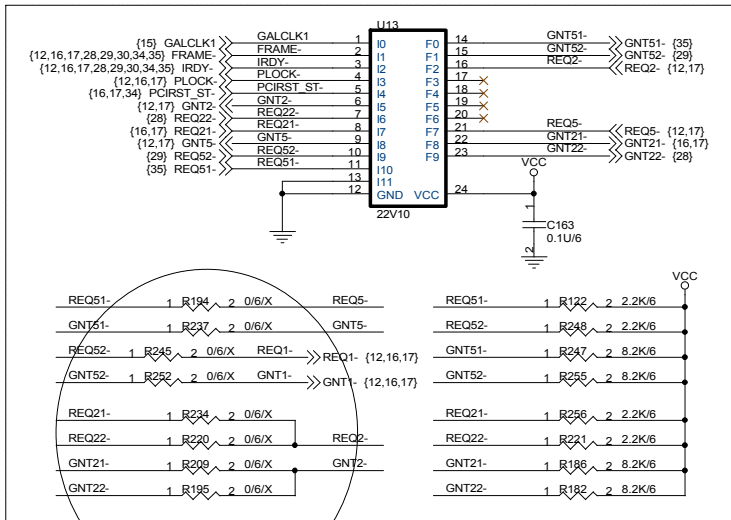
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REV 1.0 MODIFY LIST
1. PCI4 add circuit for support Intel RAIDIOS.





Place all resistors under U8

Place all resistors under U41

PCI DEVICE REQ,GNT AND INTR ROUNTING

PCI DEVICE	IDSEL	REQ&GNT (4U)	REQ&GNT (1U)	REQ&GNT (2U)	IRQ ROUNTING
PCI1	AD16	REQ0-,GNT0-	REQ0-,GNT0-	REQ0-,GNT0-	PIRQC-,PIRQF-,PIRQG-,PIRQA-
PCI2	AD17	REQ1-,GNT1-	NA	REQ1-,GNT1-	PIRQF-,PIRQG-,PIRQA-,PIRQB-
PCI3	AD18	REQ21-,GNT21- [GAL1]	NA	REQ21-,GNT21- [GAL1]	PIRQG-,PIRQA-,PIRQB-,PIRQE-
PCI4	AD19	REQ31-,GNT31- [GAL2]	NA	NA	PIRQA-,PIRQB-,PIRQE-,PIRQC-
SCSI	AD20	REQ4-,GNT4-	REQ4-,GNT4-	REQ4-,GNT4-	PIRQB-,PIRQE-
LAN1	AD22	REQ22-,GNT22- [GAL1]	REQ2-,GNT2-	REQ22-,GNT22- [GAL1]	PIRQC-
LAN2	AD23	REQ52-,GNT52- [GAL1]	REQ1-,GNT1-	REQ52-,GNT52- [GAL1]	PIRQF-
ATI VGA	AD24	REQ51-,GNT51- [GAL1]	REQ5-,GNT5-	REQ51-,GNT51- [GAL1]	PIRQG-
ZCR	AD25	REQ32-,GNT32- [GAL2]	REQ3-,GNT3-	REQ3-,GNT3-	PIRQA-,PIRQB-,PIRQE-

ICH2 INTR ROUNTING

ITEM	INTERRUPT	REMARK
USB1	PIRQD-	
USB2	PIRQH-	



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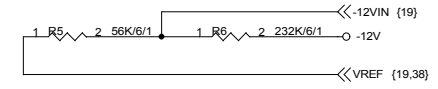
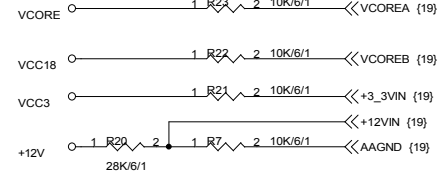
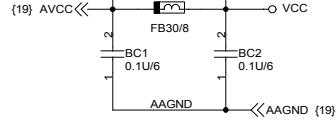
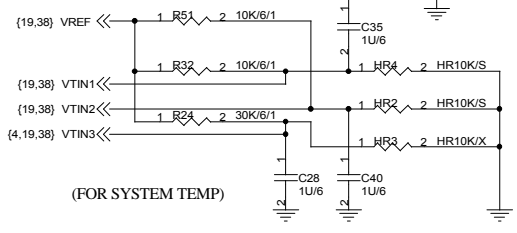
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Size Document Number GA-8IRXR-A Rev 1.00

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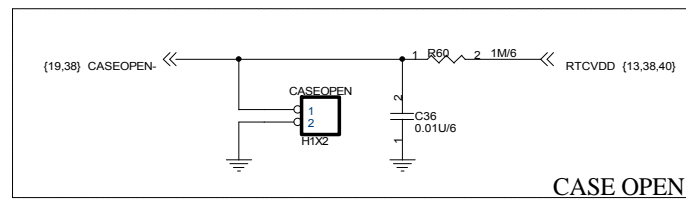
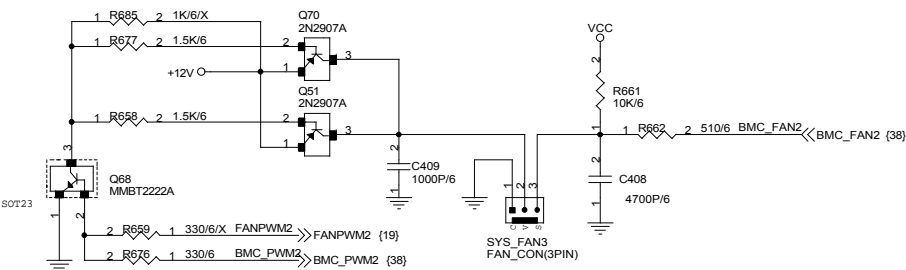
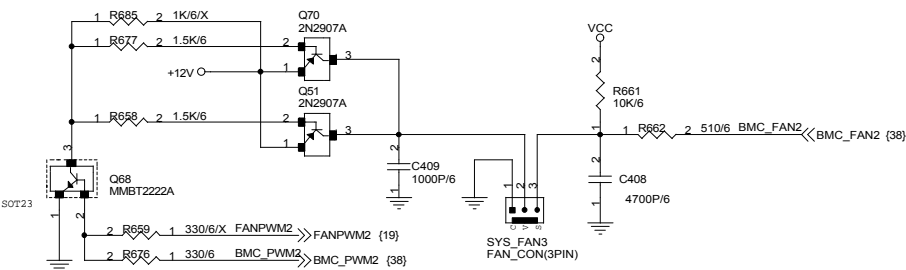
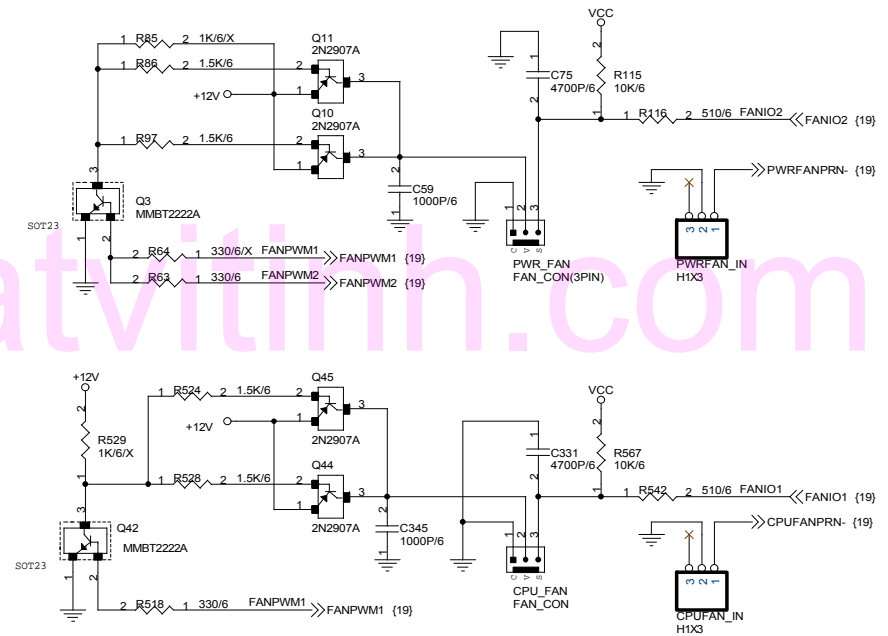
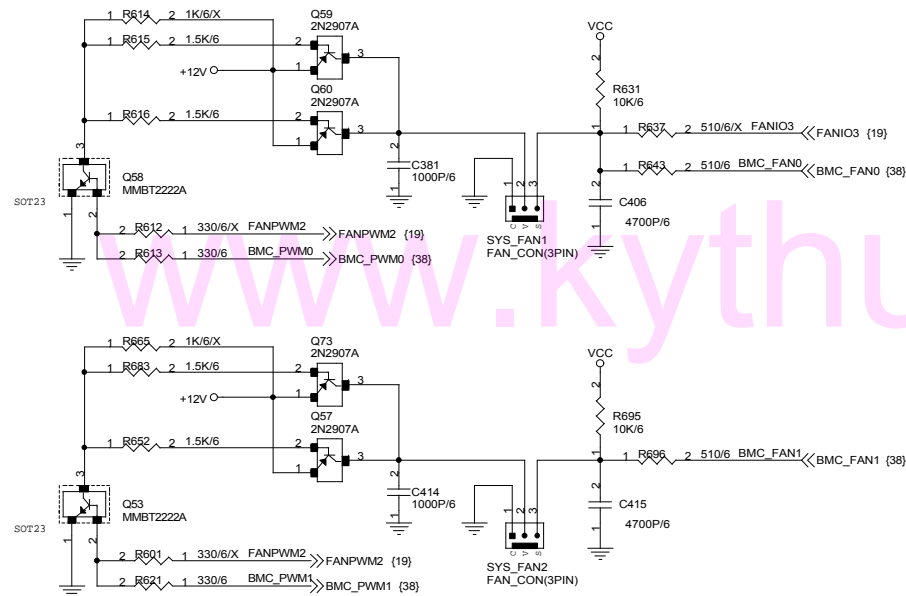
Hardware Monitor circuits

Temperature Sensing

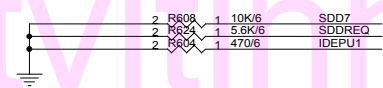
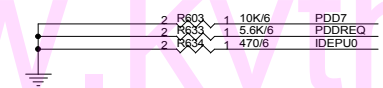
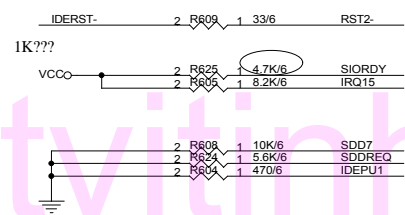
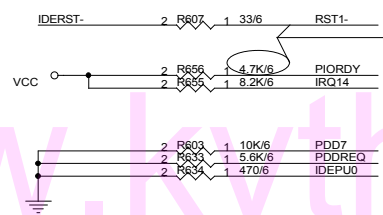
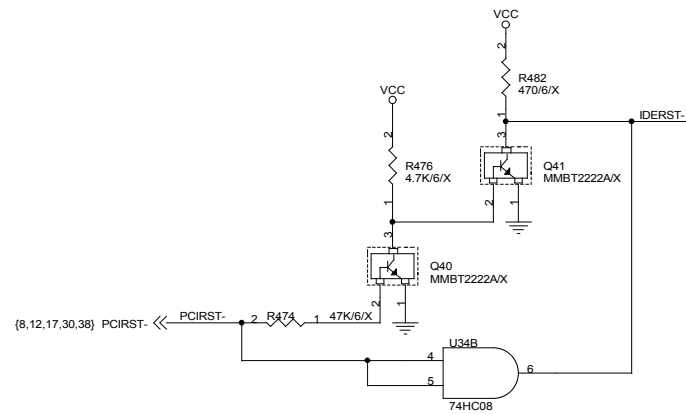
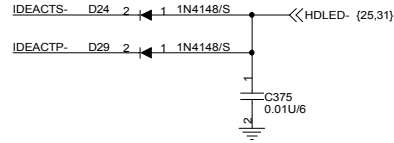
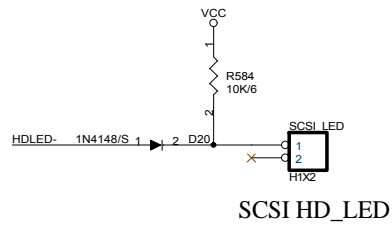


Voltage Sensing

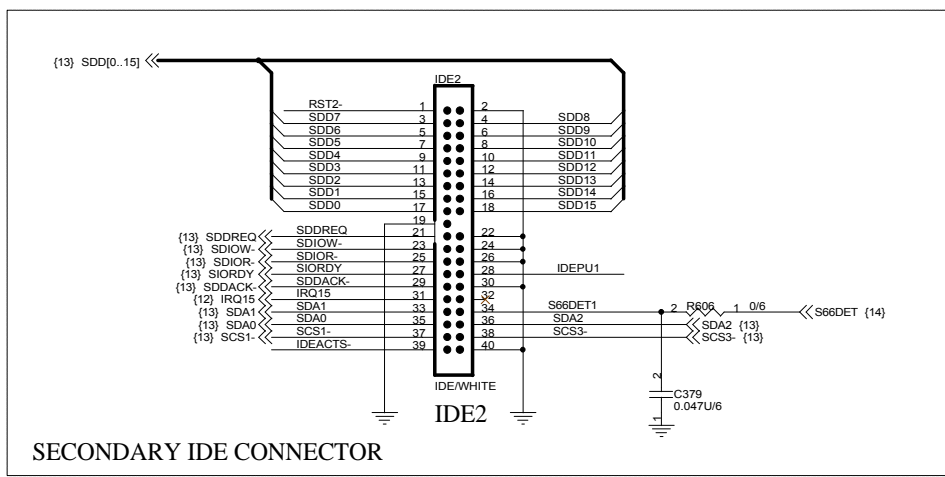
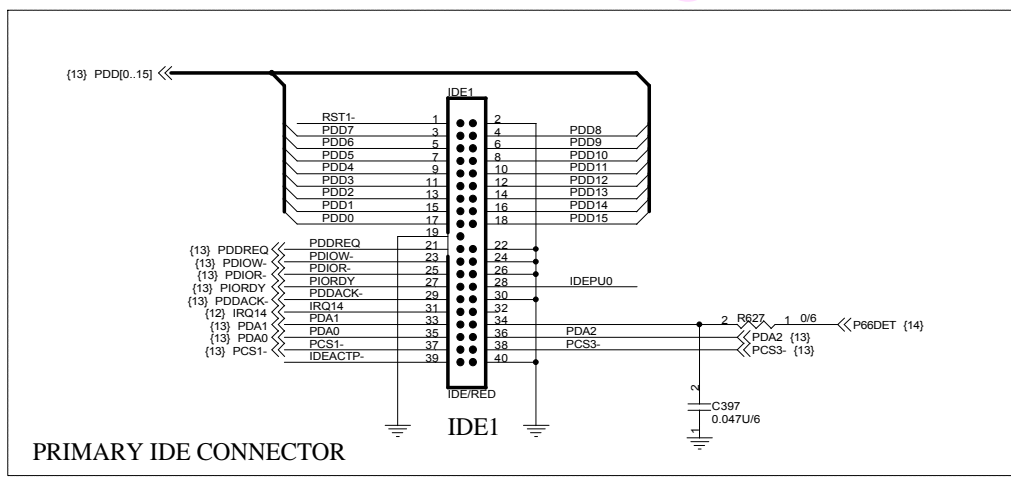
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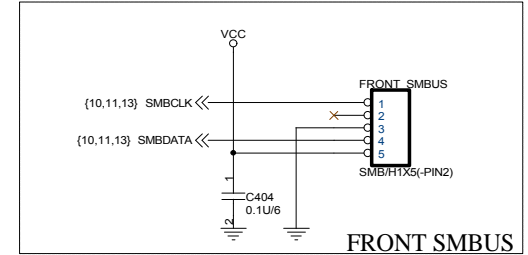
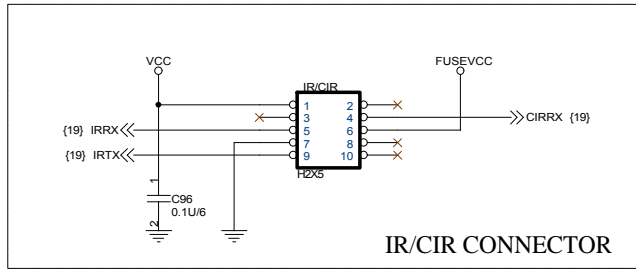
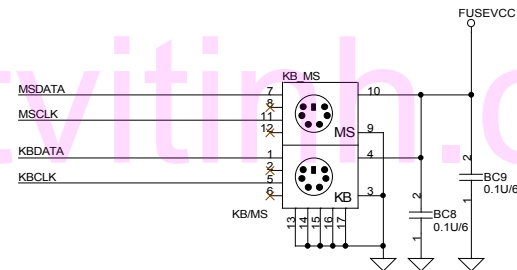
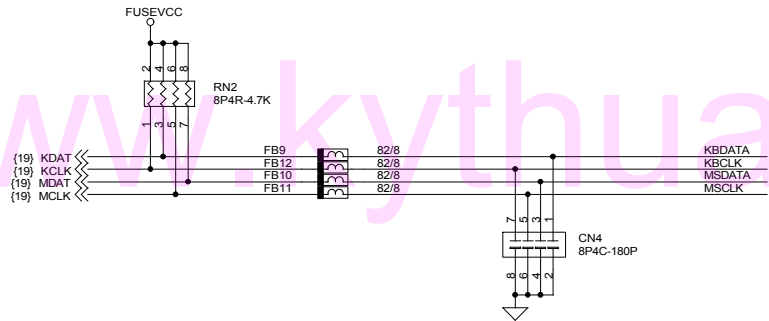
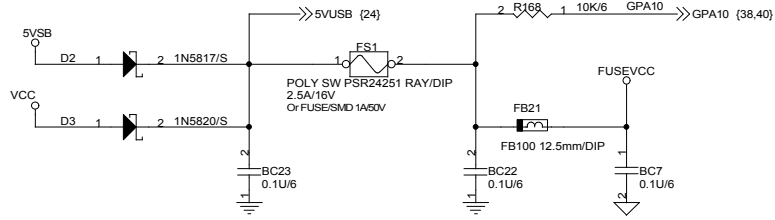


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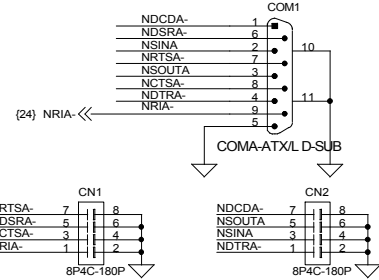
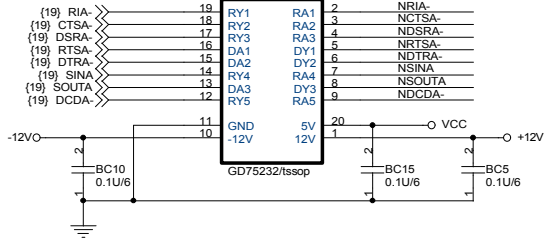


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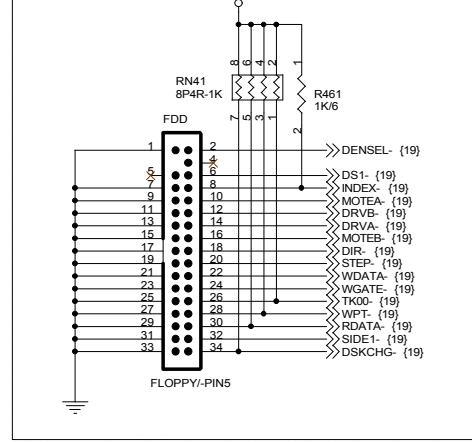




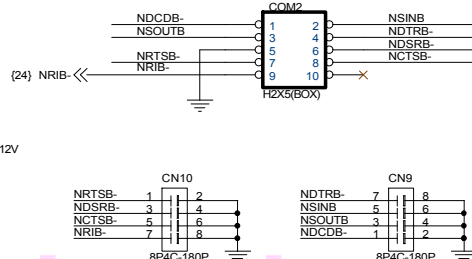
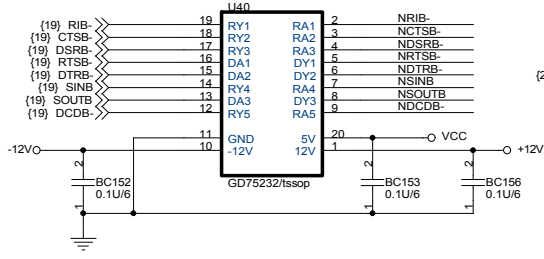
COMA



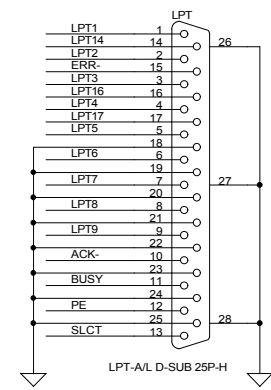
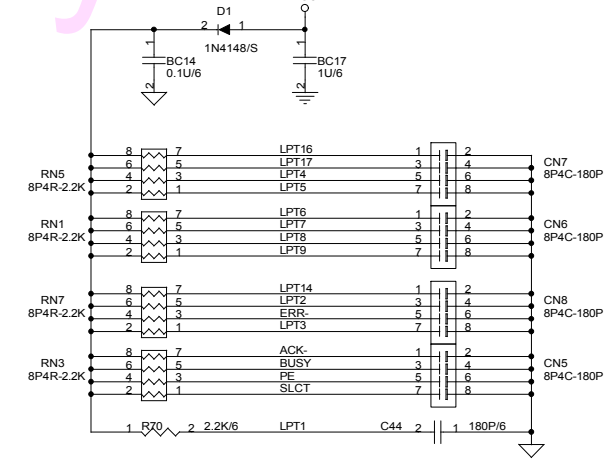
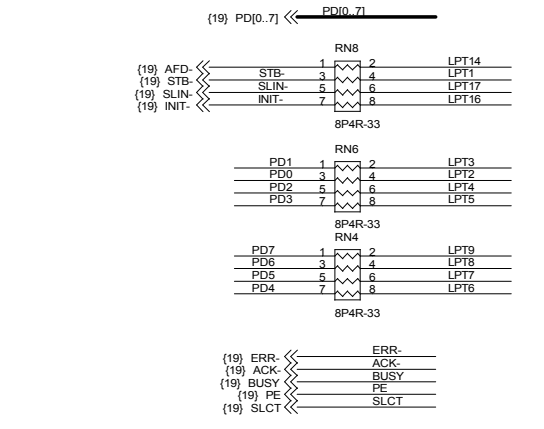
FLOPPY



COMB



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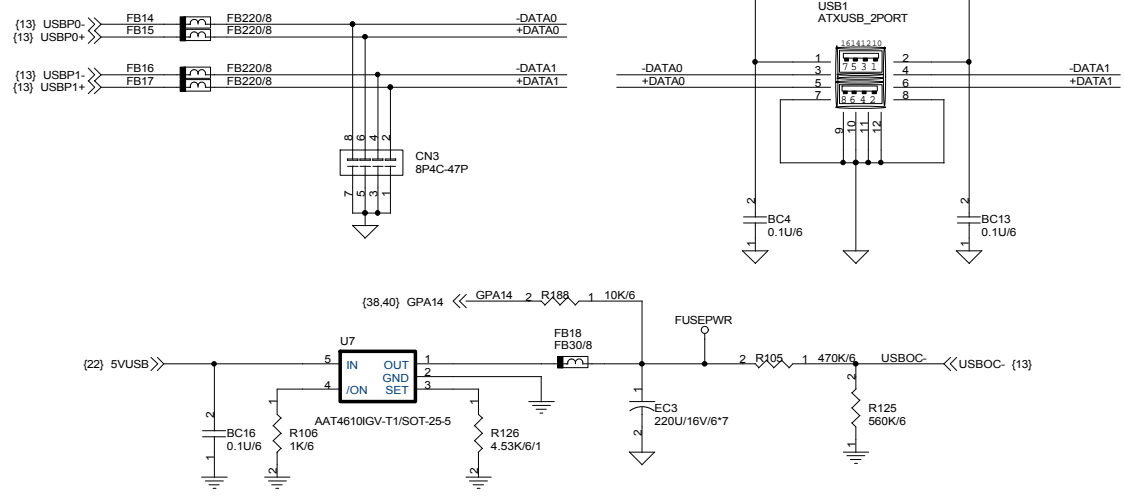
PRINTER PORT



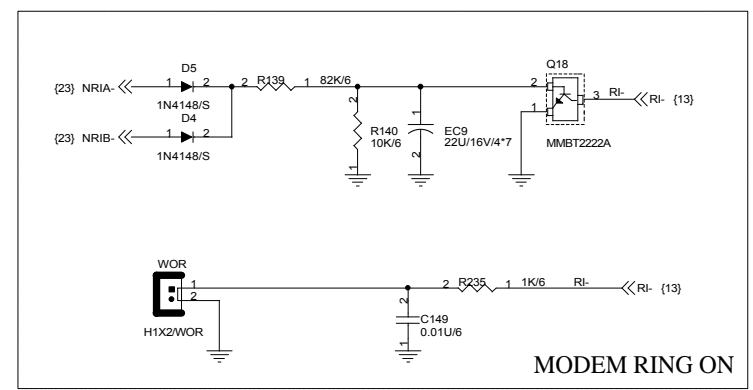
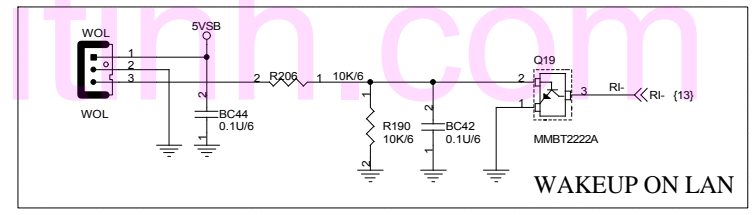
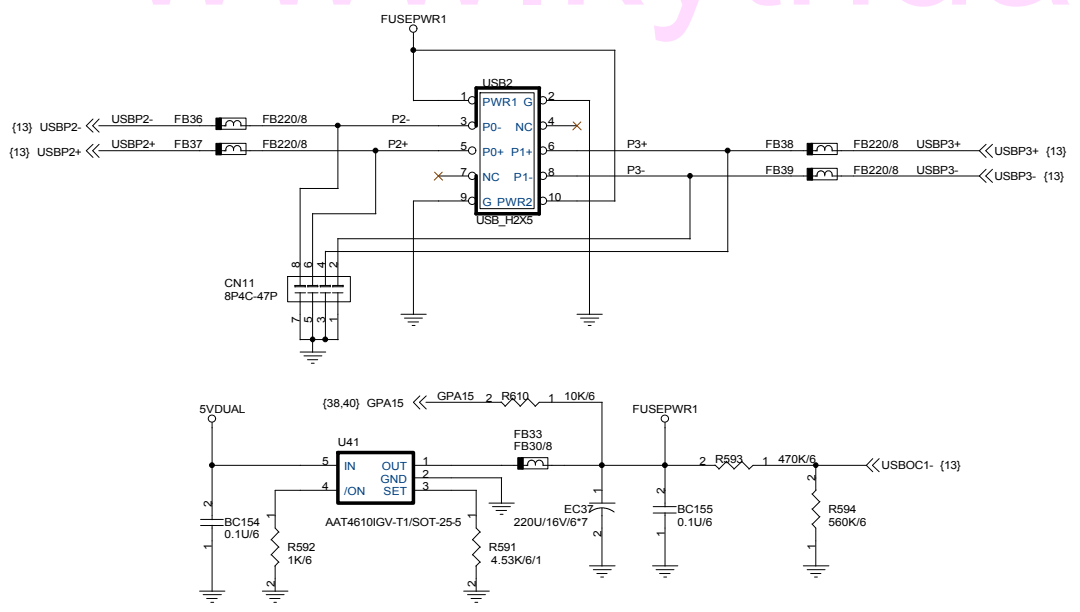
GIGABYTE TECHNOLOGY CORPORATION

Title	COM & IR & LPT PORT & FLOOPY		Rev	1.00
Size	Document Number	GA-8IRXR-A		
Date:	Tuesday, March 05, 2002	Sheet	23 of	42

BACK PANEL SIDE USB



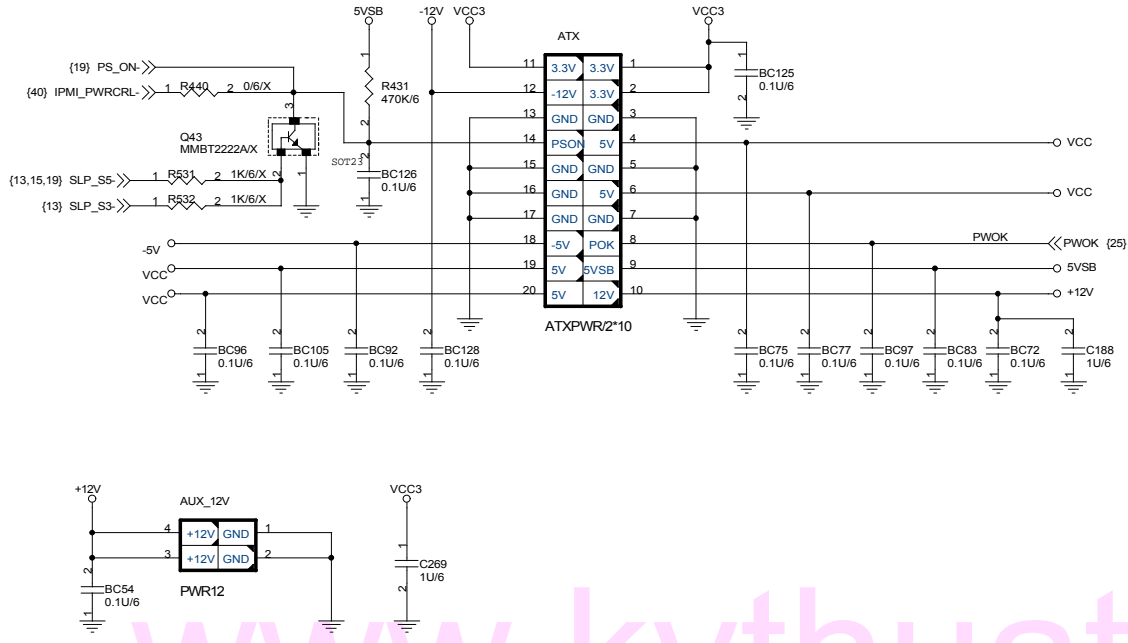
FRONT SIDE USB



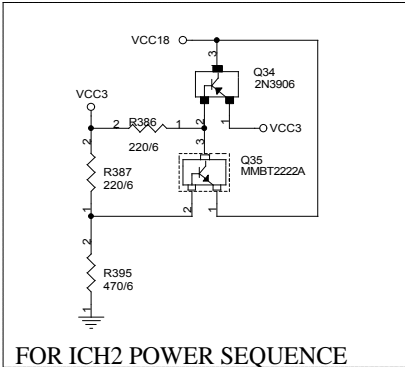
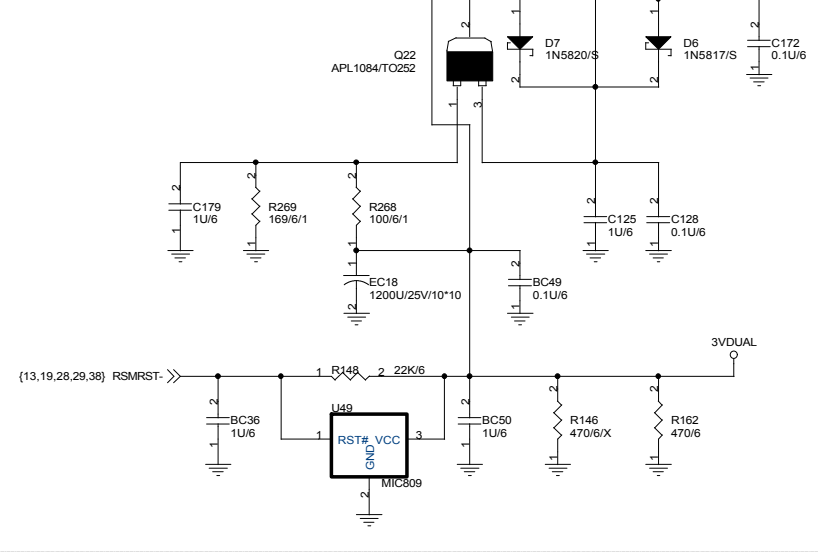
GIGABYTE TECHNOLOGY CORPORATION			
Title: ICH USB+LAN PORT			
Size	Document Number	GA-8IRXR-A	
Date: Tuesday, March 05, 2002	Sheet	24	of 42

Rev 1.00

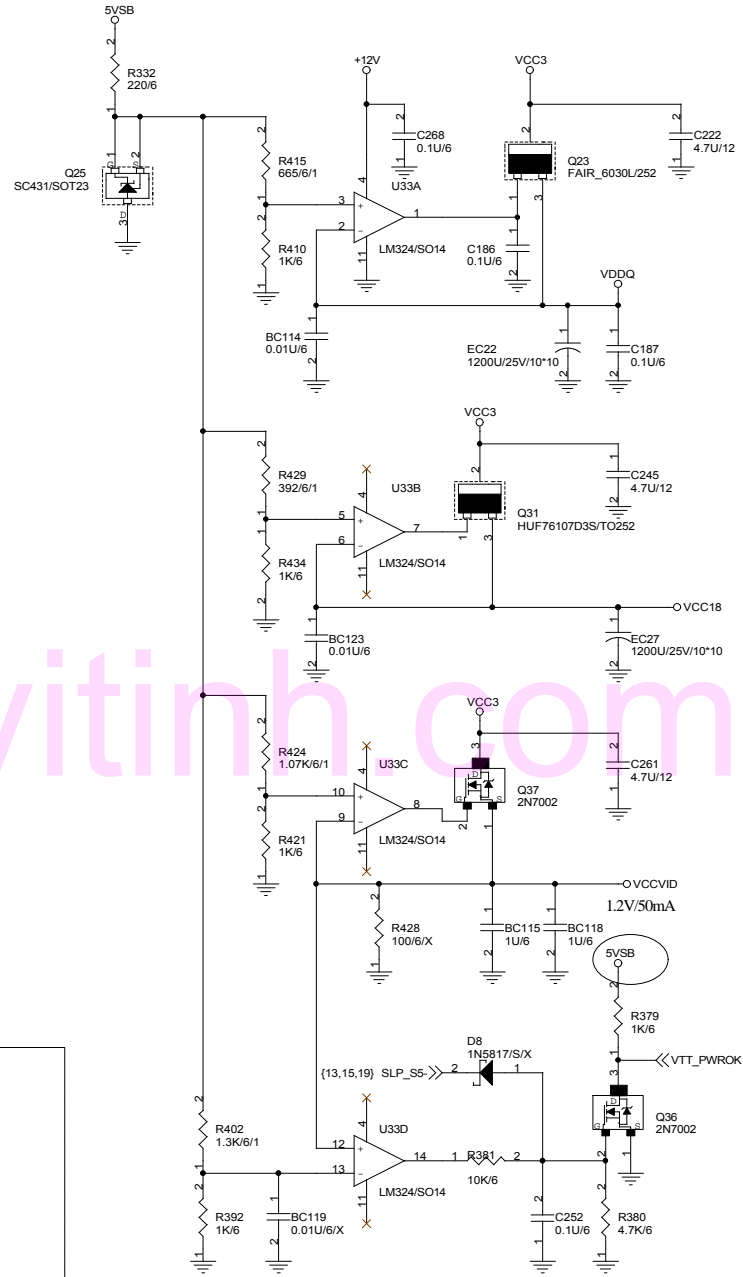
ATX POWER CONNECTOR



3.3VSB & RESUME RESET CIRCUIT

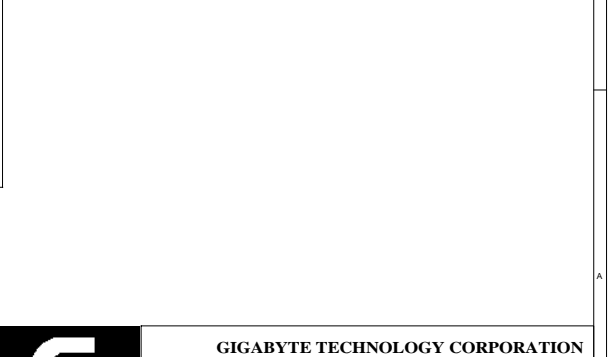
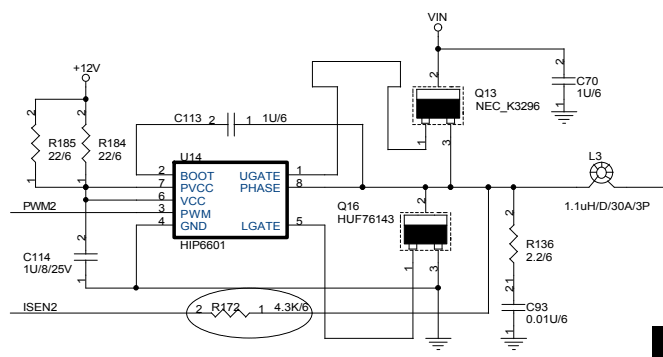
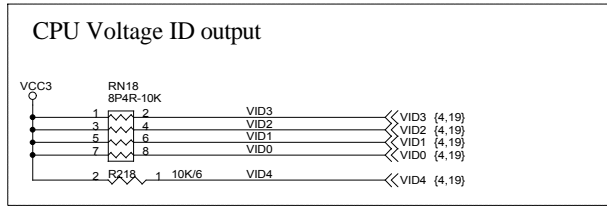
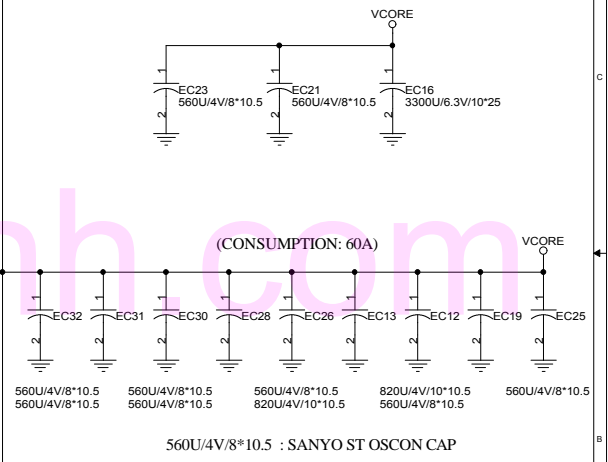
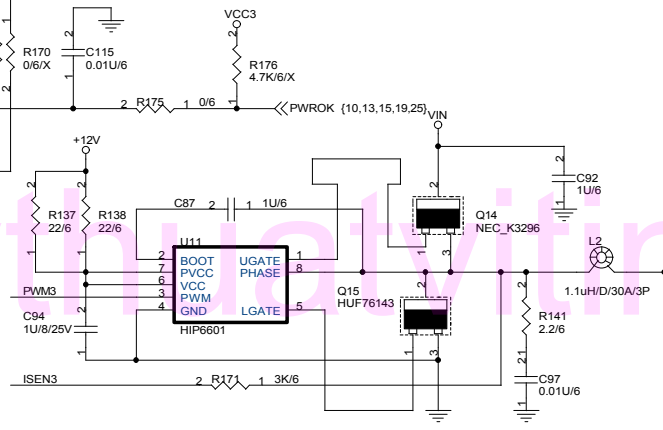
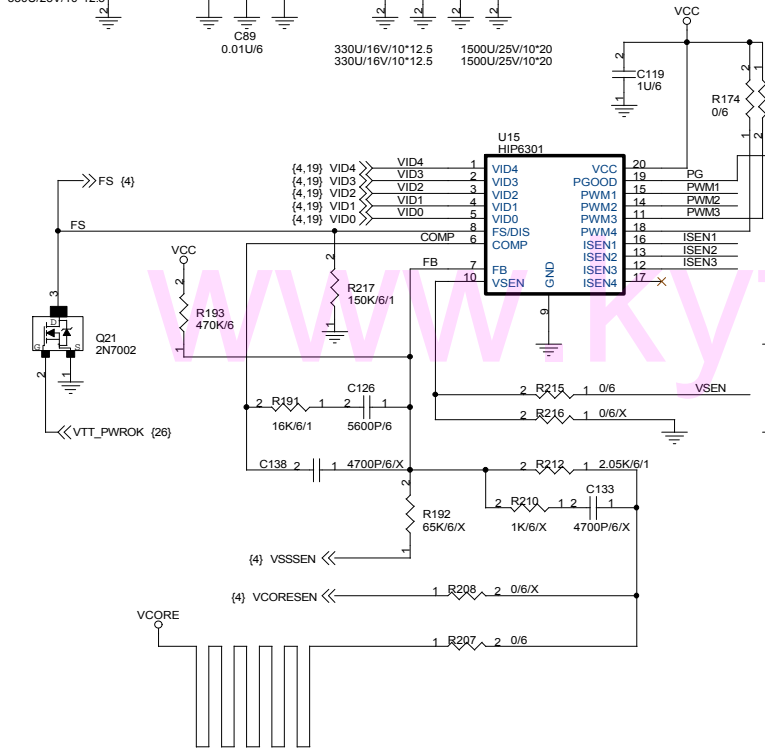
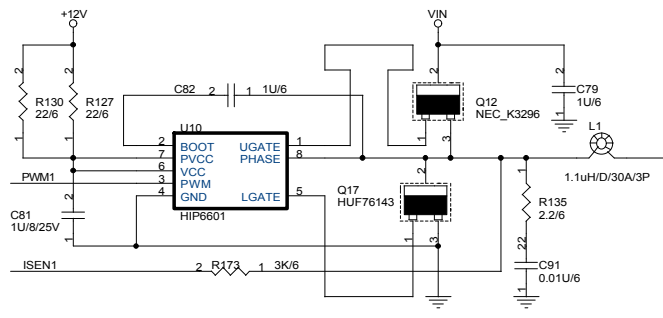
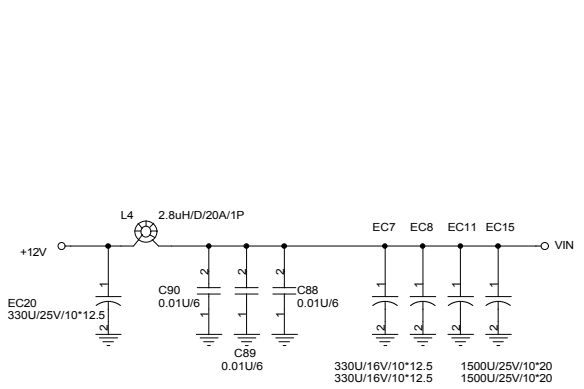


FOR ICH2 POWER SEQUENCE

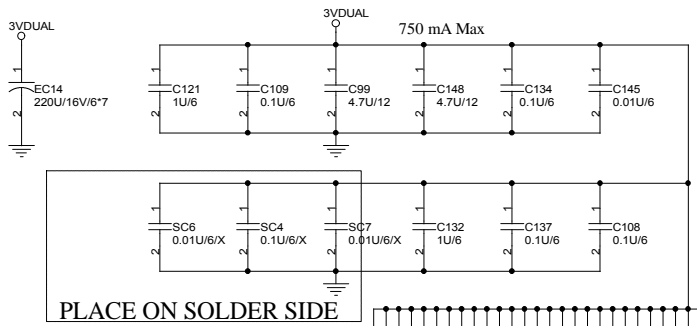


GIGABYTE TECHNOLOGY CORPORATION

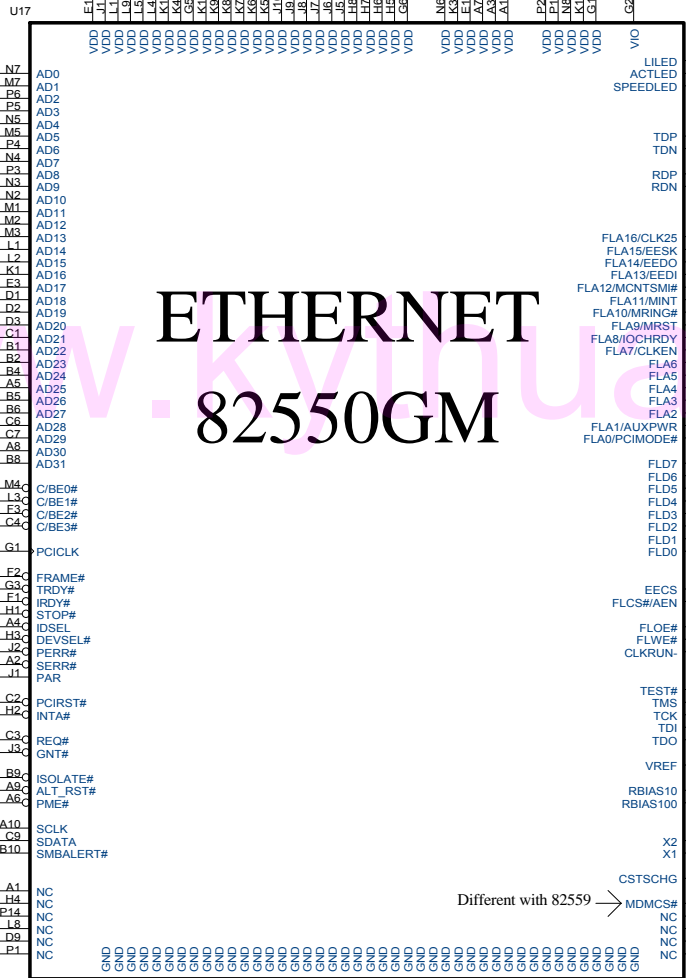
Title	Misc. PWR & ATX CONN.		
Size	Document Number	GA-8IRXR-A	
Date:	Tuesday, March 05, 2002	Sheet	26 of 42
Rev	1.00		



GIGABYTE TECHNOLOGY CORPORATION			
Title Vcore PWM HIP6301+6601			
Size	Document Number	Rev	
	GA-8IRXR-A	1.00	
Date:	Tuesday, March 05, 2002	Sheet	27 of 42

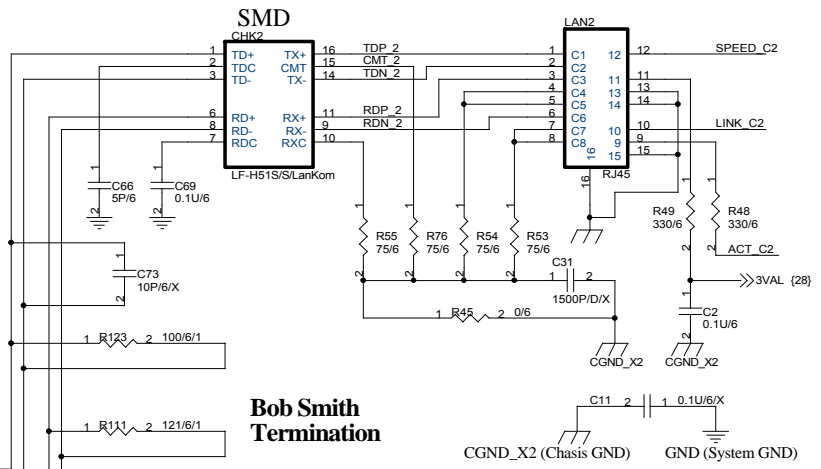


PLACE ON SOLDER SIDE



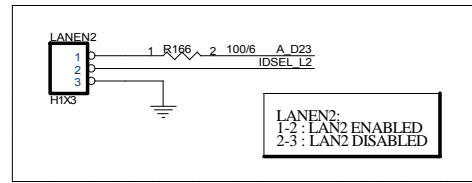
ETHERNET 82550GM

Different with 82559 →



Bob Smith Termination

GAMLA --> 93C66

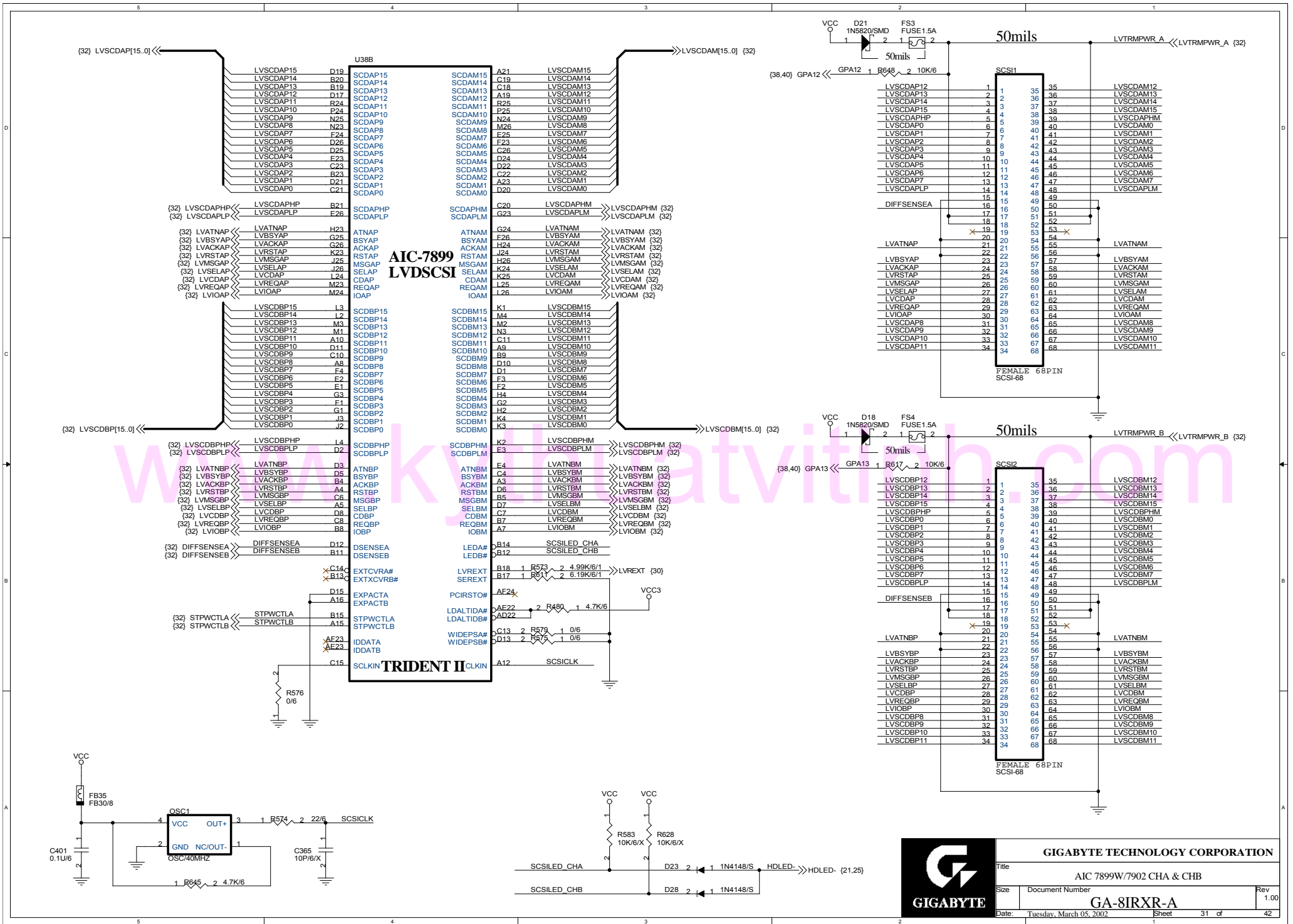


{12,16,17,28,30,34,35} A_D[0..31] << A_D[0..31]

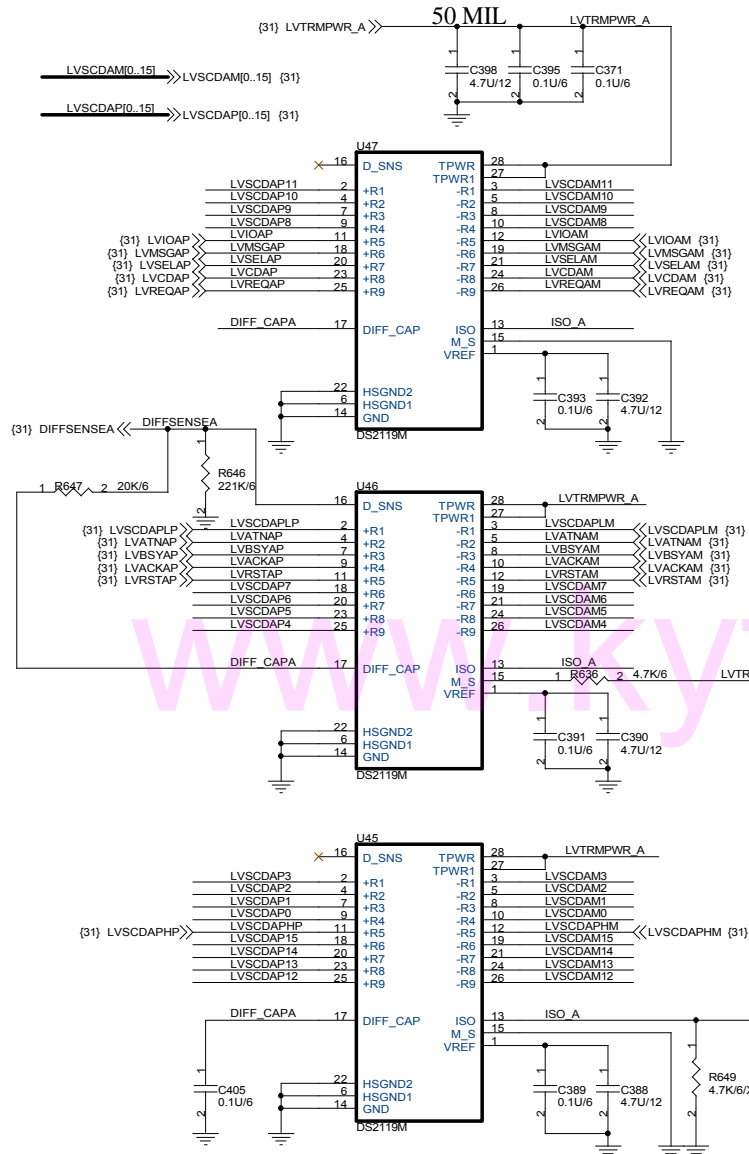
{12,16,17,28,30,34,35} C_BE0 << C_BE0- M4
 {12,16,17,28,30,34,35} C_BE1 << C_BE1- L3
 {12,16,17,28,30,34,35} C_BE2 << C_BE2- C4
 {12,16,17,28,30,34,35} C_BE3 << C_BE3- C4
 {15} LANCLK2 << LANCLK2 G1
 {12,16,17,18,28,30,34,35} FRAME- << FRAME- E2
 {12,16,17,28,30,34,35} TRDY- << TRDY- G3
 {12,16,17,18,28,30,34,35} IRDY- << IRDY- F1
 {12,16,17,28,30,34,35} STOP- << STOP- H1
 {12,16,17,28,30,34,35} DEVSEL- << DEVSEL- A4
 {12,16,17,28,30,34} PERR- << PERR- H3
 {12,16,17,28,30,34} SERR- << SERR- A2
 {12,16,17,28,30,34,35} PAR << PAR J1
 3VDUAL 1 R181 2 3.3K/6 -PRST2 C2
 {12,16,17} PIRQF- << PIRQF- H2
 {18} REQ52 << REQ52 C3
 {18} GNT52 << GNT52 J3
 {14,19,28,30,35} PCIRST-OB- << PCIRST-OB- B9
 {13,19,26,28,38} RSMRST- << RSMRST- A9
 {12,17,28} PCIPME- << PCIPME- A6
 1 R154 2 4.7K/6/X A10
 1 R152 2 4.7K/6/X C9
 3VDUALO 1 R153 2 4.7K/6/X B10
 {28,40} AOLCLK1 << A1
 {28,40} AOLDATA1 << H4
 {28,40} LANALERT- << P14
 X L8
 X G4
 X D9
 X P1

REV 1.0 MODIFY LIST
 1. Connect LANALERT- to BMC.

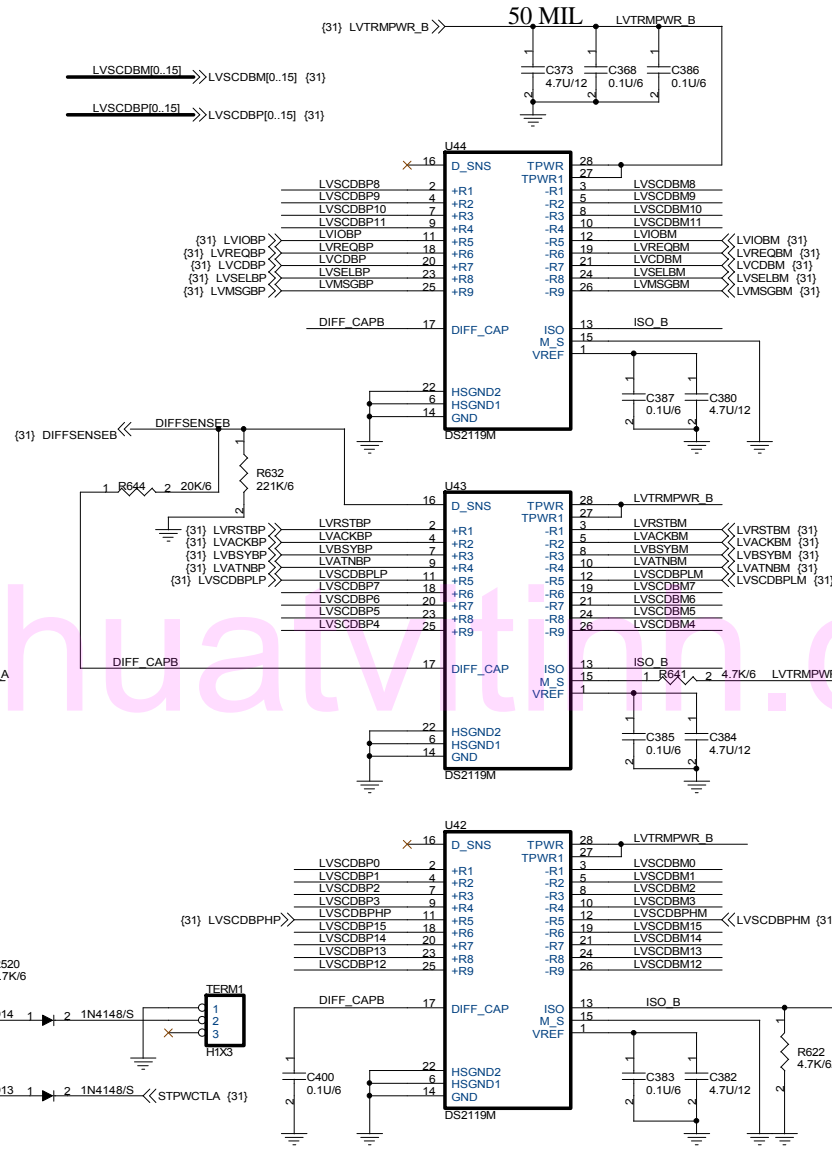




SCSI- Channel A



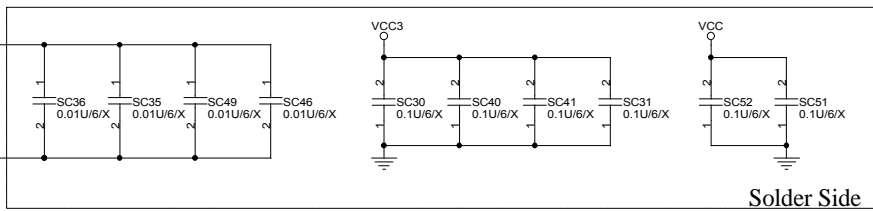
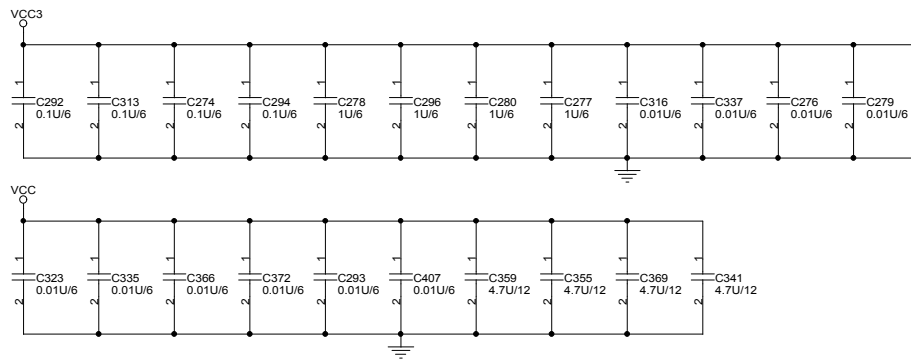
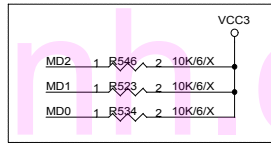
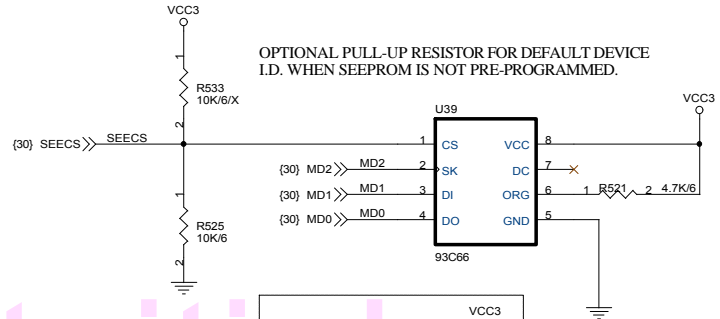
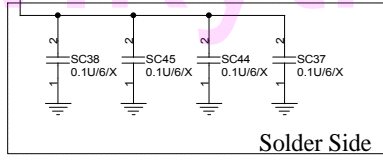
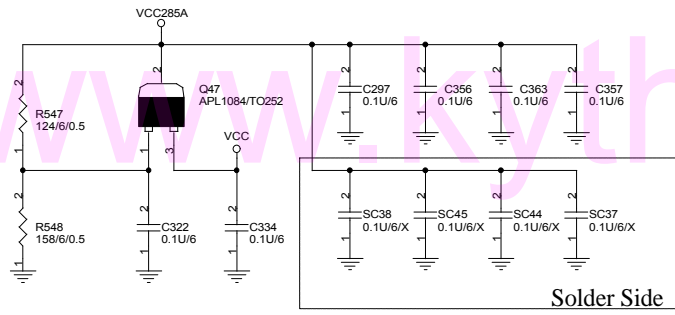
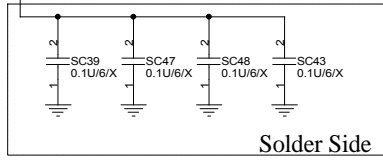
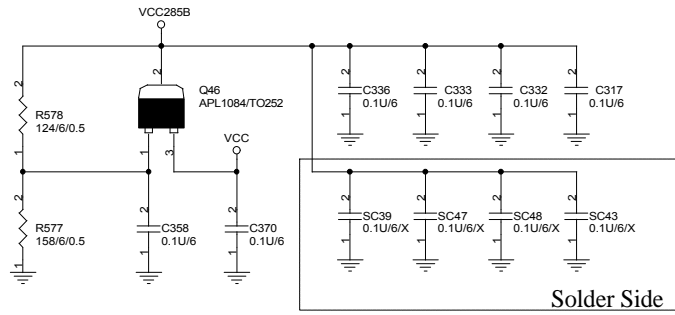
SCSI- Channel B



VIAS CHANGE LAYER

- @ 5mil/5mil/15mil
- @ Route +S/-S in parallel and Keep them equal length
- @ Routing +S/-S on the outer layer is preferred, Hi Zo/Lo Cap
- @ REQ, ACK, DATA, PARITY signal pairs are critical traces

TERMI, TERM2:
1-2: SCSI TERM. ENABLED
2-3: SCSI TERM. AUTO



GIGABYTE TECHNOLOGY CORPORATION			
Title: AIC 7899W/7902 POWER			
Size	Document Number		Rev
	GA-8IRXR-A		1.00
Date:	Tuesday, March 05, 2002		Sheet 33 of 42

POWER REQUIREMENT
Rage XL

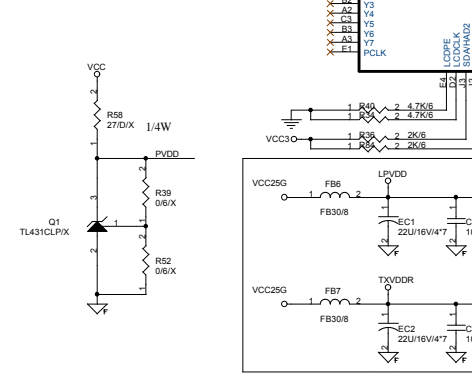
VDDR = 3.3V \Rightarrow 0.25A
 VDDO = 3.3V \Rightarrow 0.15A
 VDDC = 2.8V \Rightarrow 1.5A
 PVDD = 2.8V \Rightarrow 0.04A
 AVDD = 2.5V \Rightarrow 0.07A

POWER REQUIREMENT
Rage 128VR

VDDR = 3.3V \Rightarrow 0.25A
 VDDO = 3.3V \Rightarrow 0.15A
 VDDC = 2.8V \Rightarrow 1.5A
 PVDD = 2.8V \Rightarrow 0.04A
 AVDD = 2.5V \Rightarrow 0.07A

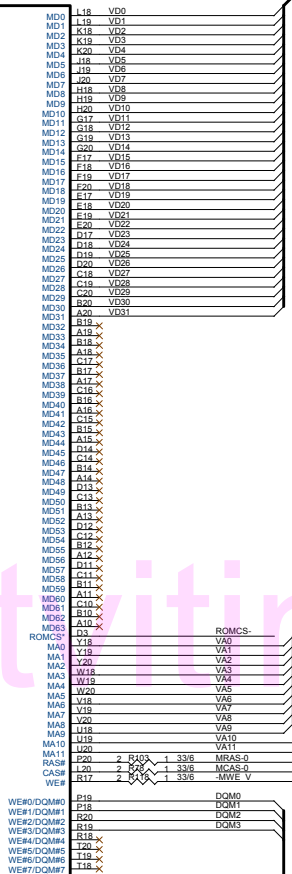
(12,16,17,28,29,30,34) A_D0_31
 (12,16,17,28,29,30,34) C_BED
 (12,16,17,28,29,30,34) C_BE1
 (12,16,17,28,29,30,34) C_BE2
 (12,16,17,28,29,30,34) C_BE3
 (15) VGAPCLK
 (14,19,28,29,30) PCIRST_OB
 (12,16,17) PIRQG
 (8) HAD1
 (12,16,17,18,28,29,30,34) FRAME#
 (12,16,17,18,28,29,30,34) IRDY#
 (12,16,17,28,29,30,34) TRDY#
 (12,16,17,28,29,30,34) DEVSEL#
 (12,16,17,28,29,30,34) STOP#
 (12,16,17,28,29,30,34) PAR#
 (18) GNTS1#
 (18) REQ01#

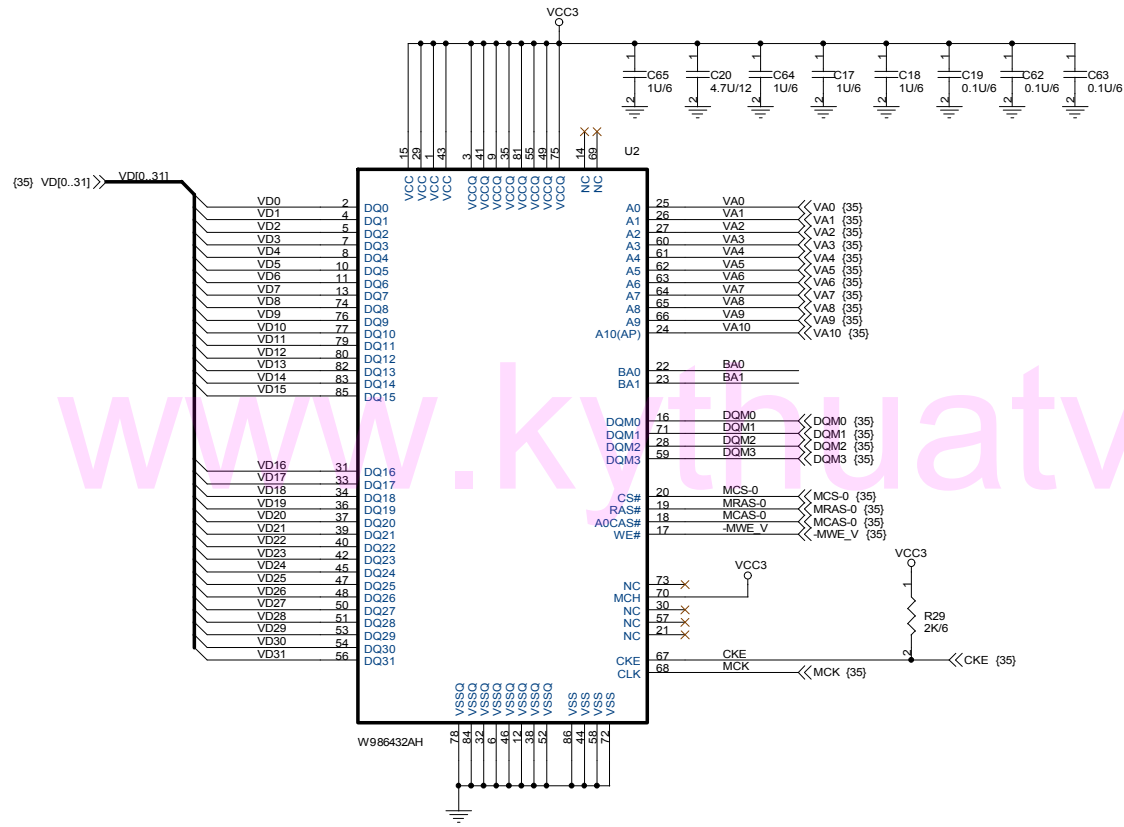
(12,16,17,28,29,30,34) C_BED0
 (12,16,17,28,29,30,34) C_BE1#0
 (12,16,17,28,29,30,34) C_BE2#0
 (12,16,17,28,29,30,34) C_BE3#0
 (15) VGAPCLK
 (14,19,28,29,30) PCIRST_OB
 (12,16,17) PIRQG
 (8) HAD1
 (12,16,17,18,28,29,30,34) FRAME#
 (12,16,17,18,28,29,30,34) IRDY#
 (12,16,17,28,29,30,34) TRDY#
 (12,16,17,28,29,30,34) DEVSEL#
 (12,16,17,28,29,30,34) STOP#
 (12,16,17,28,29,30,34) PAR#
 (18) GNTS1#
 (18) REQ01#



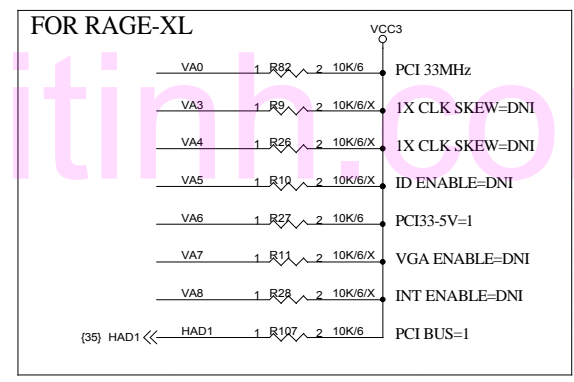
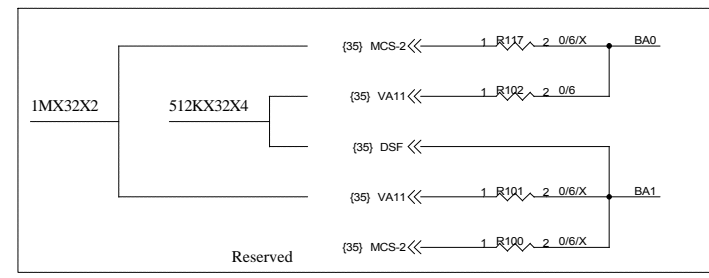
REV 1.0 MODIFY LIST
 1. Add C432,C433,C434 for EMI.

Rage - XL (215R3LASB22) ATI RAGE 128 VR



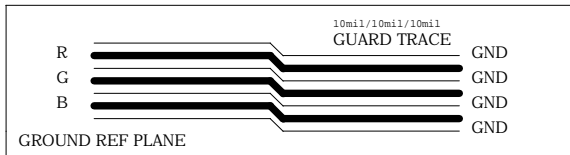


1M x 32 x 2 = R101, R117 Install
 512K x 32 x 4 = R102, R79 Install

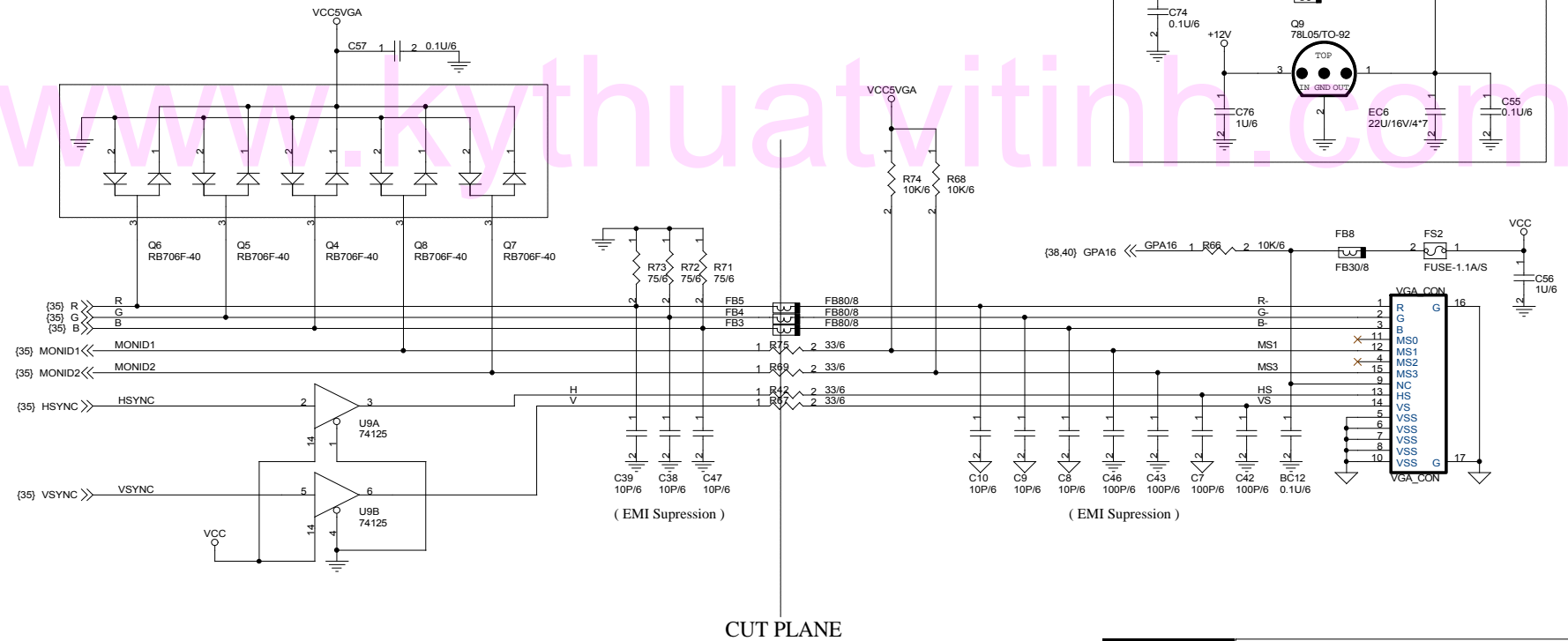


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The analog DAC outputs R, G and B are designed to drive a 37.5 Ohm equivalent load.



- REV 1.0 MODIFY LIST
1. Modify Layout and move 74125 to connector side.

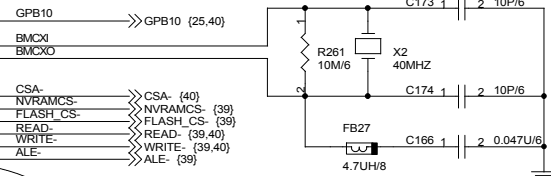


GIGABYTE TECHNOLOGY CORPORATION

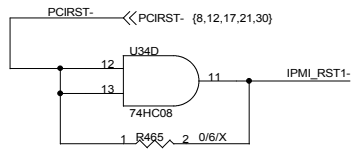
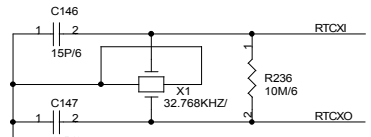
Title		VGA CON	
Size	Document Number	GA-8IRXR-A	
Date:	Tuesday, March 05, 2002	Sheet	37 of 42
Rev		1.00	

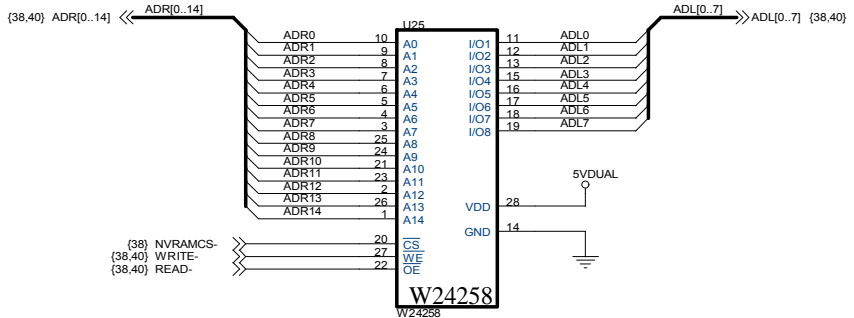
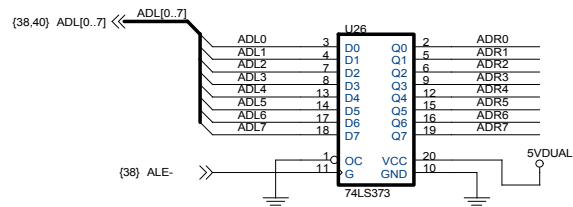
(40) SMBALTICh- SMBALTICh-
 (19,40) SMBCLKICh- SMBCLKICh-
 (19,40) SMBDATAICh- SMBDATAICh-
 (40) AOLCLK- AOLCLK-
 (40) AOLDATA- AOLDATA-
 (39) ICMB_TX- ICMB_TX-
 (39) ICMB_RX- ICMB_RX-
 (39) UART_RI- UART_RI-
 (39) UART_DCD- UART_DCD-
 (39) UART_OUT- UART_OUT-
 (39) UART_SIN- UART_SIN-
 (39) UART_DTR- UART_DTR-
 (39) UART_RTS- UART_RTS-
 (39) UART_DSR- UART_DSR-
 (39) UART_CTS- UART_CTS-
 (39,40) GPB11- GPB11-
 (5,40) GPB12- GPB12-
 (40) GPB13- GPB13-
 (25,40) GPB14- GPB14-
 (40) GPB15- GPB15-

(19,20) CASEOPEN- CASEOPEN-
 (40) GPC00- GPC00-
 (40) GPC01- GPC01-
 (40) GPC02- GPC02-
 (40) GPC03- GPC03-
 (40) GPC04- GPC04-
 (40) GPC05- GPC05-
 (40) GPC06- GPC06-
 (40) GPC07- GPC07-
 (13,19,26,28,29) RSMRST- RSMRST-
 (13,20,40) RTCVDD- RTCVDD-
 (25,40) GPA00- GPA00-
 (25,40) GPA01- GPA01-
 (25,40) GPA02- GPA02-
 (25,40) GPA03- GPA03-
 (25,40) GPA04- GPA04-
 (12,40) GPA05- GPA05-
 (3,40) GPA06- GPA06-
 (17,40) P1SMBCLK- P1SMBCLK-
 (17,40) P1SMBDATA- P1SMBDATA-
 (4,40) GPA07- GPA07-
 (22,40) GPA10- GPA10-
 (40) P2SMBCLK- P2SMBCLK-
 (40) P2SMBDATA- P2SMBDATA-
 (40) GPA11- GPA11-
 (31,40) GPA12- GPA12-
 (31,40) GPA13- GPA13-
 (13,14,19,40) LFRAME- LFRAME-
 (13,14,19,40) LAD[0..3]- LAD[0..3]-
 (15) IPMI_LPC33- IPMI_LPC33-
 (12,19) SERIRQ- SERIRQ-
 (24,40) GPA14- GPA14-
 (24,40) GPA15- GPA15-
 (37,40) GPA16- GPA16-
 (40) GPA17- GPA17-
 (39,40) GPF3- GPF3-
 (20) BMC_PWM2- BMC_PWM2-
 (20) BMC_PWM1- BMC_PWM1-
 (20) BMC_PWM0- BMC_PWM0-

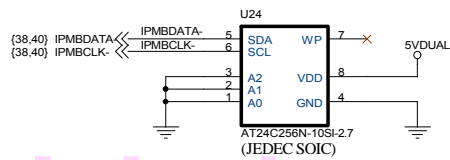
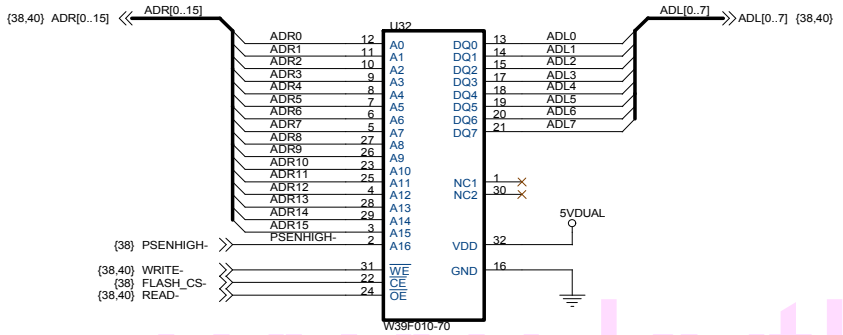


Reserved for trapping to Multiplex mode

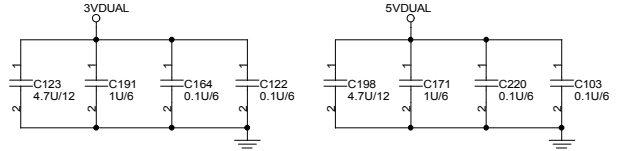
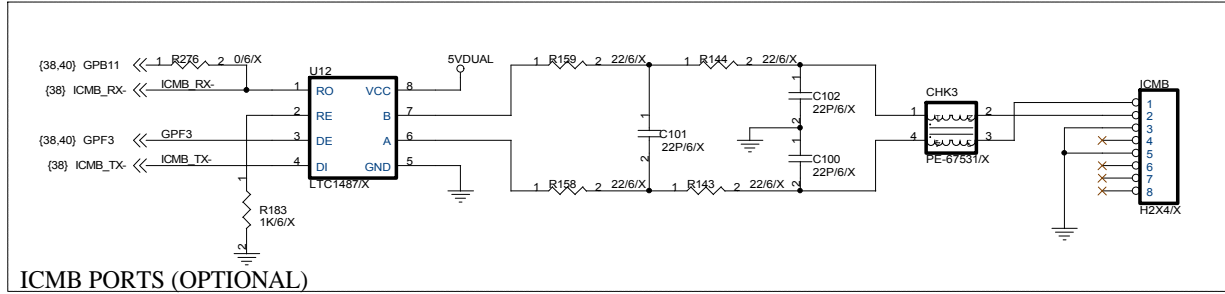
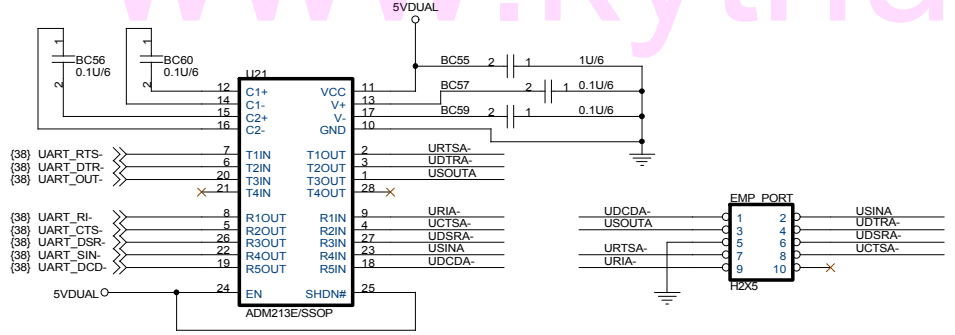


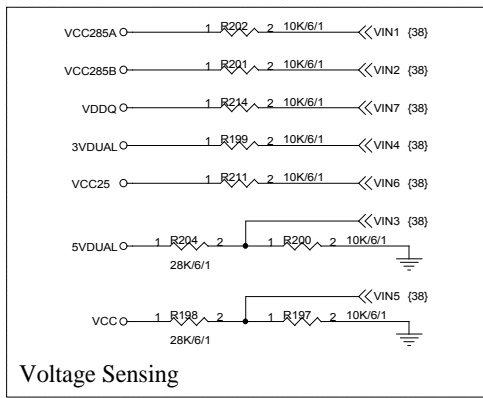
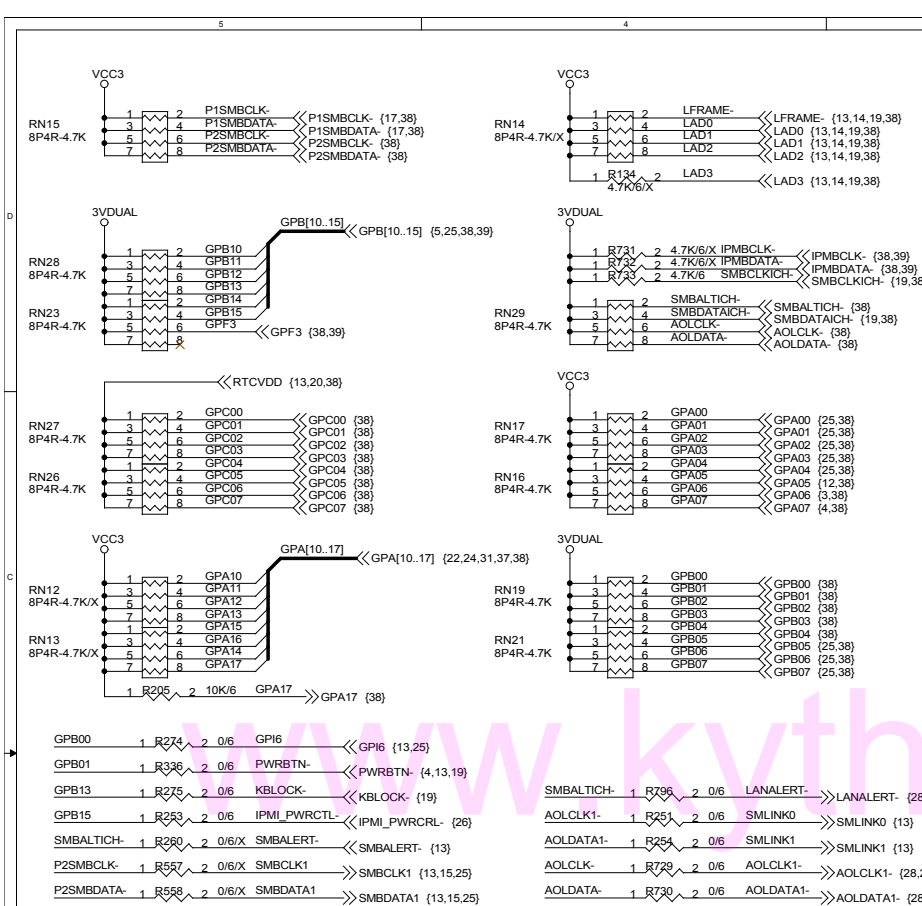


Note: The Flash and RAM access time should be within 70 ns.

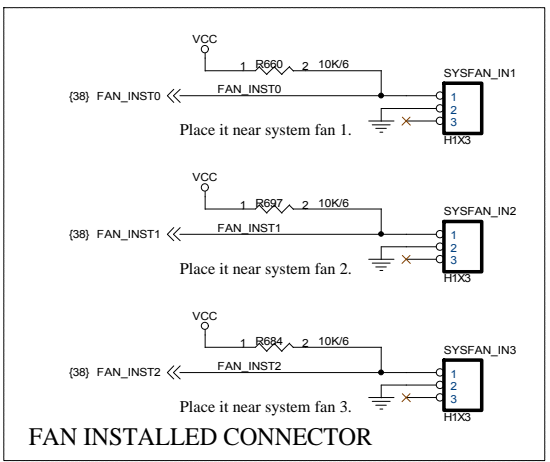
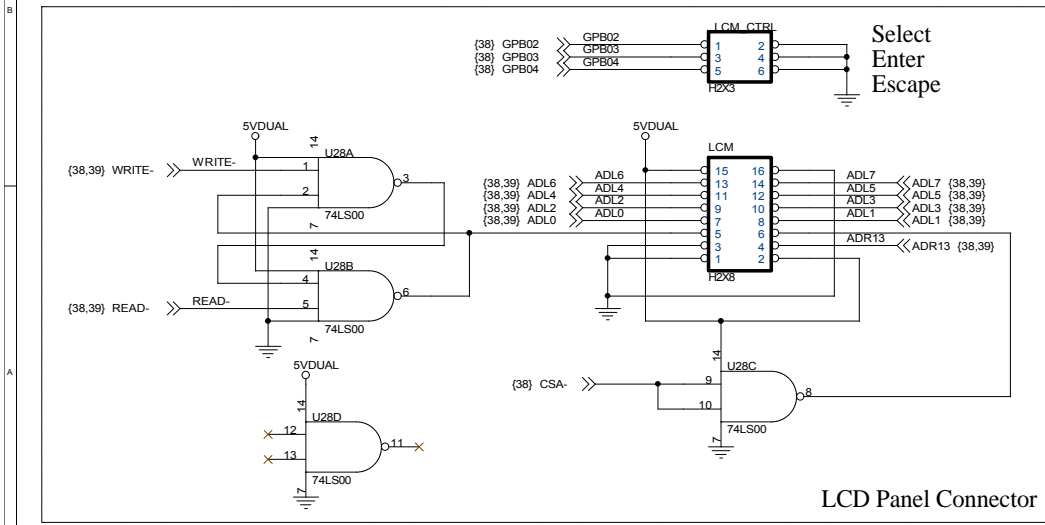
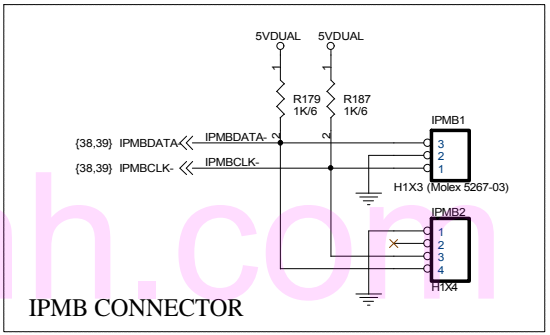
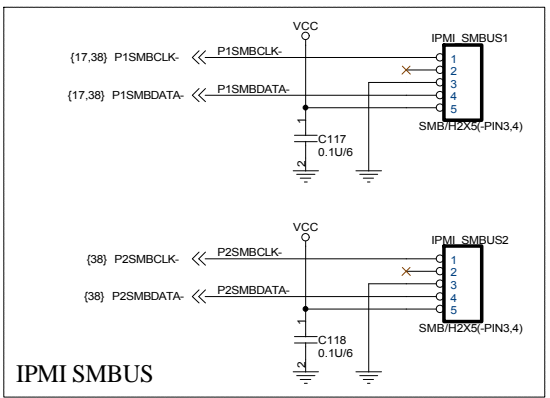


EMP (Emergency Management Port) Connect to MODEM





REV 1.0 MODIFY LIST
 1. Add R976 for connect LANALERT- to SMBALTICh-



Model Name: GA-8IRXR-A

Version: 1.00

GPIO ASSIGN LIST

CHIPSET	GPIO	DESCRIPTION
ICH2	GPI0	REQA- (Pull up only)
ICH2	GPI1	REQ5-
ICH2	GPI2	PIRQE- For PCI Device
ICH2	GPI3	PIRQF- For PCI Device
ICH2	GPI4	PIRQG- For PCI Device
ICH2	GPI5	PIRQH- For USB2
ICH2	GPI6	Green Button
ICH2	GPI7	Super I/O Hardware Monitor SMI-
ICH2	GPI8	NMI Signal From IPMI BMC
ICH2	GPI11	SMBALERT- (Pull up only)
ICH2	GPI12	LPCPME-
ICH2	GPI13	(Pull up only)
ICH2	GPO16	GNTA- (Pull up only)
ICH2	GPO17	GNT5- For PCI Device
ICH2	GPO18	For Backplane Fan Stop on Suspend Stage.
ICH2	GPO19	(Pull up only)
ICH2	GPO20	(Pull up only)
ICH2	GPO21	(Pull up only)
ICH2	GPO22	(Pull up only)
ICH2	GPO23	(Pull up only)
ICH2	GPO24	BIOS Table Lock
ICH2	GPO25	BIOS Write Protect
ICH2	GPO27	Error LED
ICH2	GPO28	(Pull up only)

GPIO ASSIGN LIST

CHIPSET	GPIO	DESCRIPTION
W83627HF	GP10	CPU Fan Fail LED
W83627HF	GP11	Power Fan Fail LED
W83627HF	GP12	System Fan Fail LED
W83627HF	GP13	Power Fan Present Input Connector
W83627HF	GP14	CPU Fan Present Input Connector
W83627HF	GP15	Model Identification Switch For Hitachi.
W83627HF	GP16	Model Identification Switch For Hitachi.
W83627HF	GP17	Model Identification Switch For Hitachi.
W83627HF	GP20	
W83627HF	GP21	SMBUS CLK (SLAVE)
W83627HF	GP22	SMBUS DATA (SLAVE)
W83627HF	GP23	Power LED
W83627HF	GP24	
W83627HF	GP25	IR RX
W83627HF	GP26	IR TX
W83627HF	GP30	SLP_S5
W83627HF	GP31	Power Control: Power Supply ON "PS_ON"
W83627HF	GP32	Power OK
W83627HF	GP33	Resume Reset
W83627HF	GP34	CIR_RX
W83627HF	GP35	SUSLED (Green LED)

GPIO ASSIGN LIST

CHIPSET	GPIO	POWER	DESCRIPTION
W83910F	GPA00	VCC	For All Error LED: Active Low
W83910F	GPA01	VCC	For Hard Disk Error LED: Active Low
W83910F	GPA02	VCC	For Fan Error LED: Active Low
W83910F	GPA03	VCC	For Power Supply Error LED: Active Low
W83910F	GPA04	VCC	For System Power LED: Active Low
W83910F	GPA05	VCC	For System NMI: Active High
W83910F	GPA06	VCC	For CPU Internal Error Detect: Active Low
W83910F	GPA07	VCC	For CPU Thermal Trip Signal Detect: Active Low
W83910F	GPA10	VCC	For Keyboard/Mouse Fuse Error
W83910F	GPA11	VCC	
W83910F	GPA12	VCC	For SCSI Channel1 Fuse Error
W83910F	GPA13	VCC	For SCSI Channel2 Fuse Error
W83910F	GPA14	VCC	For Backplane USB Fuse Error
W83910F	GPA15	VCC	For Front Side USB Fuse Error
W83910F	GPA16	VCC	For VGA Connector Power Fuse Error
W83910F	GPA17	VCC	
W83910F	GPB00	VSBS	For System Green Function: Active Low to ICH2
W83910F	GPB01	VSBS	For Power Button Signal to ICH2: Active Low
W83910F	GPB02	VSBS	For LCD Panel "SELECT"
W83910F	GPB03	VSBS	For LCD Panel "ENTER"
W83910F	GPB04	VSBS	For LCD Panel "ESCAPE"
W83910F	GPB05	VSBS	For Buzzer Stop Button: Active High
W83910F	GPB06	VSBS	For System Power Button: Active High
W83910F	GPB07	VSBS	For System Reset Button: Active High
W83910F	GPB10	VSBS	For System Green Function Button: Active High
W83910F	GPB11	VSBS	For ICMB Port
W83910F	GPB12	VSBS	For CPU Present Detection: Low--> CPU Installed
W83910F	GPB13	VSBS	For KBLock Function From BMC: Active Low
W83910F	GPB14	VSBS	For System Reset From BMC: Active High
W83910F	GPB15	VSBS	For System Power Control From BMC: Active Low
W83910F	GPC00	VBAT	
W83910F	GPC01	VBAT	
W83910F	GPC02	VBAT	
W83910F	GPC03	VBAT	
W83910F	GPC04	VBAT	
W83910F	GPC05	VBAT	
W83910F	GPC06	VBAT	
W83910F	GPC07	VBAT	



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