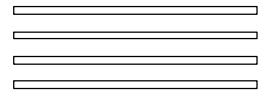


# GIGABYTE GA-8IRM Schematics

Revision 2.2

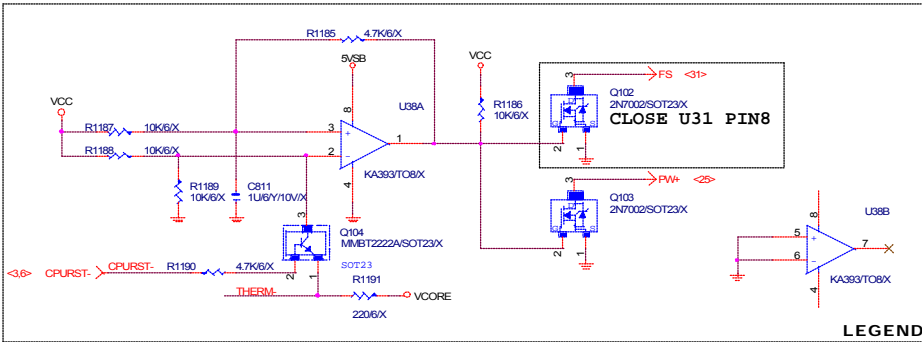
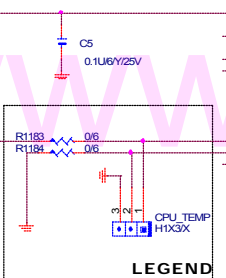
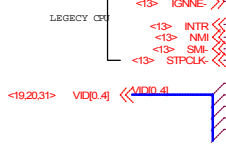
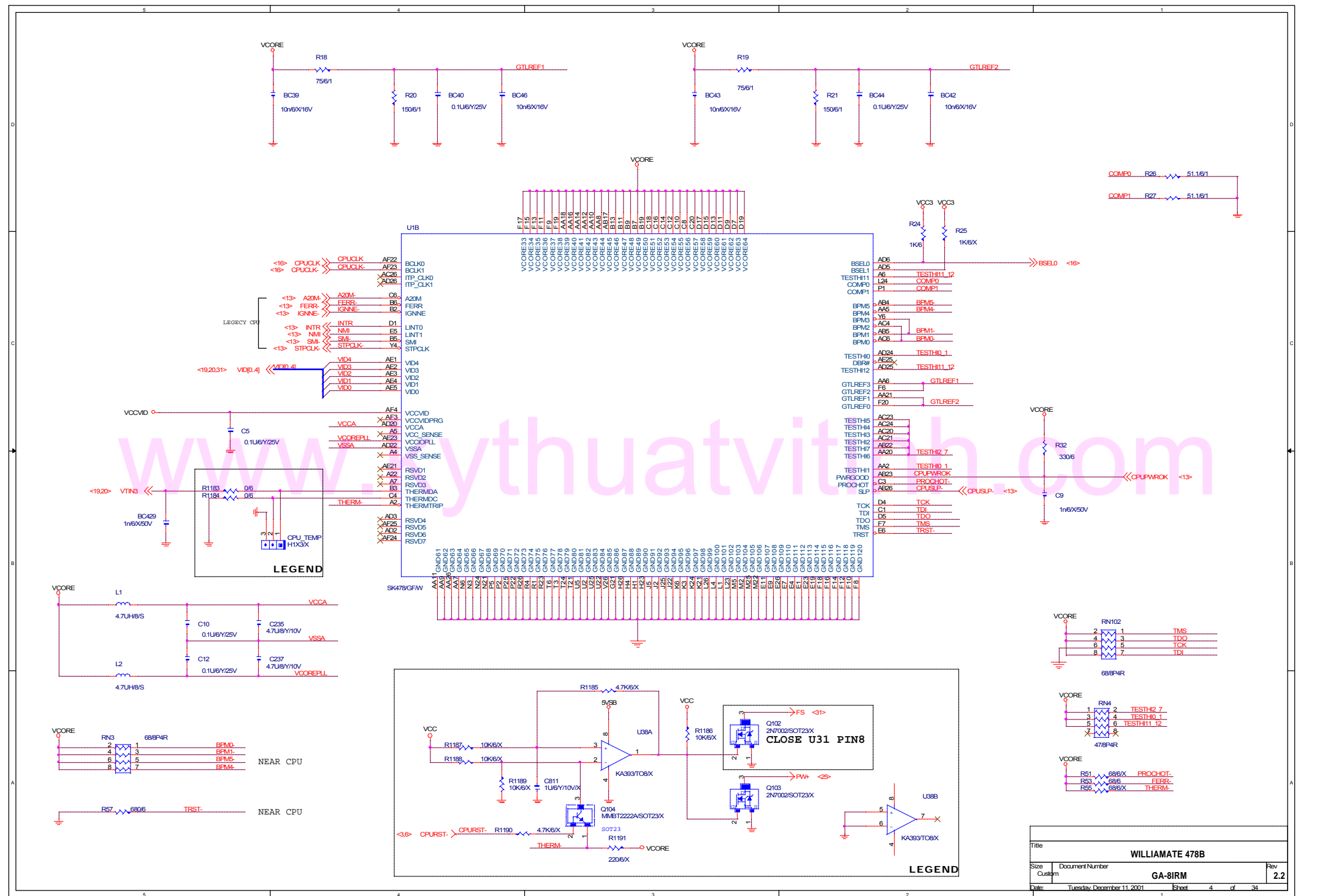
SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	WILLIAMATE_478A
04	WILLIAMATE_478B
05	WILLIAMATE_478C
06	MCH-BROOKDALE_A
07	MCH-BROOKDALE_B
08	MCH-BROOKDALE_C
09	DDR SERIAL TERMINATION
10	DDR 1_2
11	DDR TERMINATOR
12	AGP 4X
13	ICH2_1
14	ICH2_2
15	FWH
16	ICS CLOCK GEN
17	PCI1_2
18	PCI3
19	W83627HF LPC IO
20	H/W MONITOR & FAN
21	IDE
22	KB_PS2

SHEET	TITLE
23	COM_LPT
24	FRONT USB
25	FPANEL
26	CODEC
27	AUDIO_J1
28	AUDIO_J2
29	AUDIO_J3
30	POWER
31	PWM
32	DDR POWER
33	KINNERETH
34	CNR

		COMPONENT SIDE (1 oz. Copper) VCC SIDE (2 oz. Copper) GND SIDE (1 oz. Copper) SOLDER SIDE (0.5 oz. Copper)
<b>GIGABYTE</b>		
Title <b>COVER SHEET</b>		
Size Custom	Document Number <b>GA-8IRM</b>	Rev <b>2.2</b>
Date: Tuesday, December 11, 2001		Sheet 1 of 34

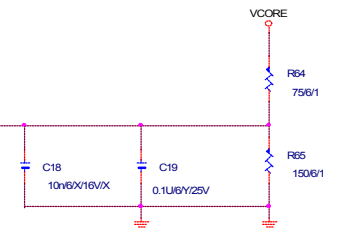
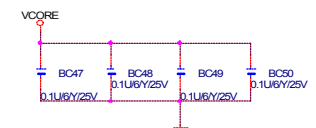
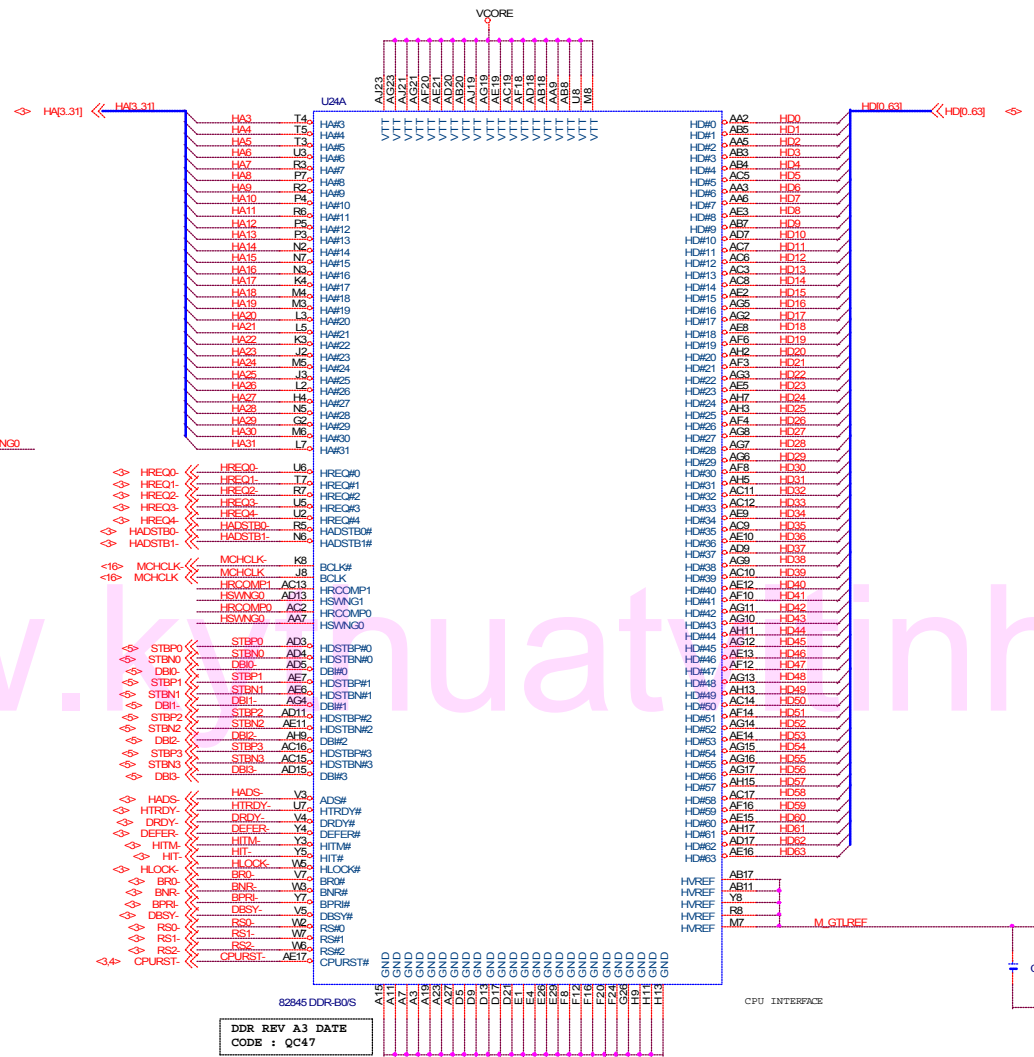
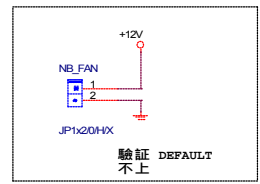
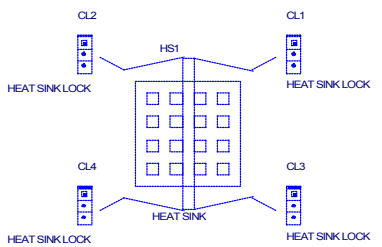
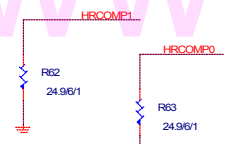
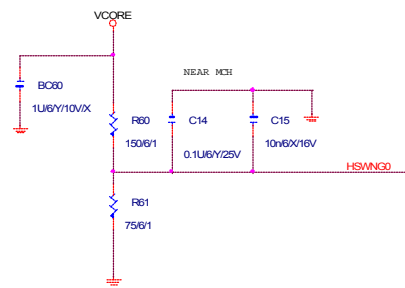






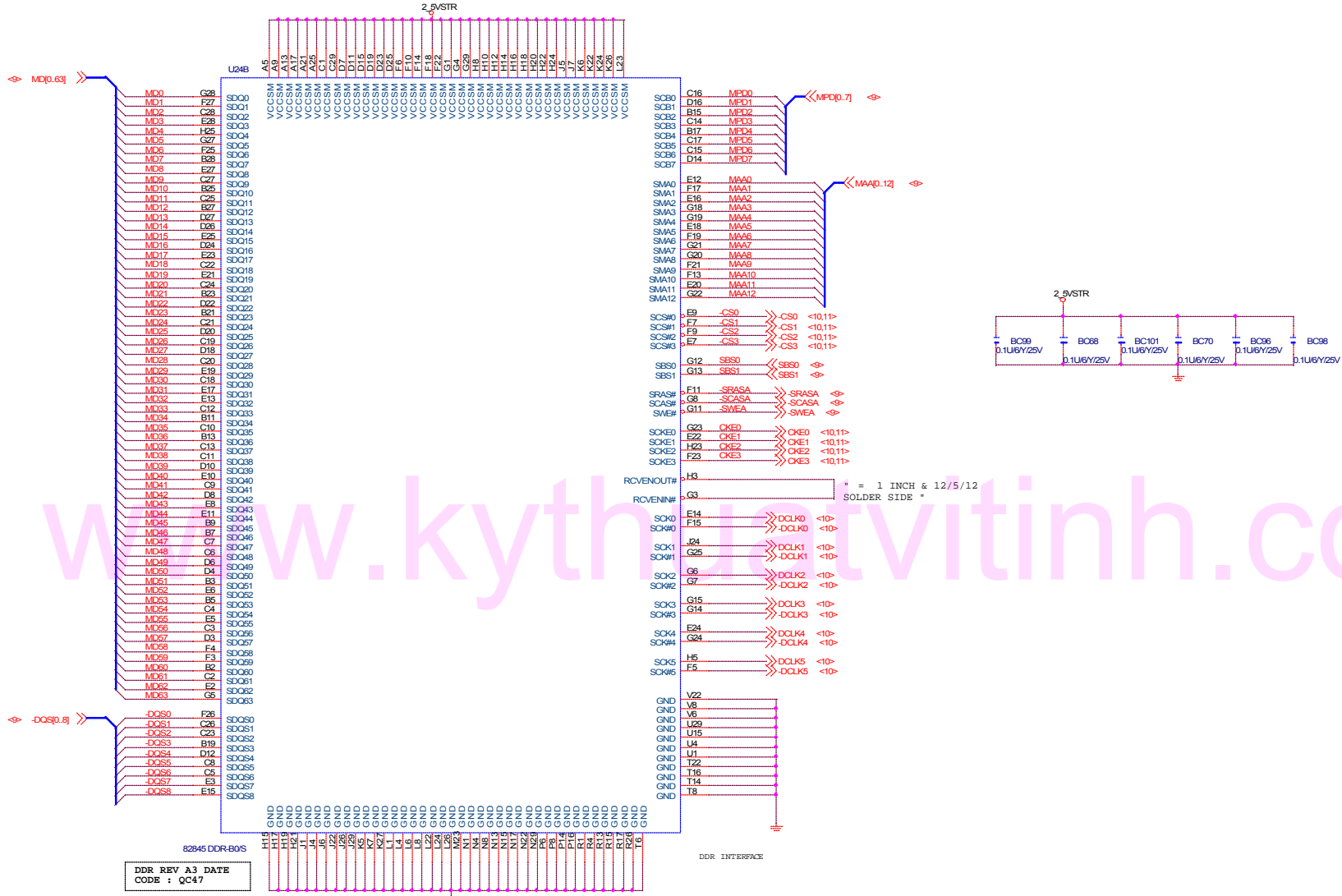
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Size	Document Number				Rev
Custom	GA-BIRM				2.2
Date	Tuesday, December 11, 2001	Sheet	4	of	34

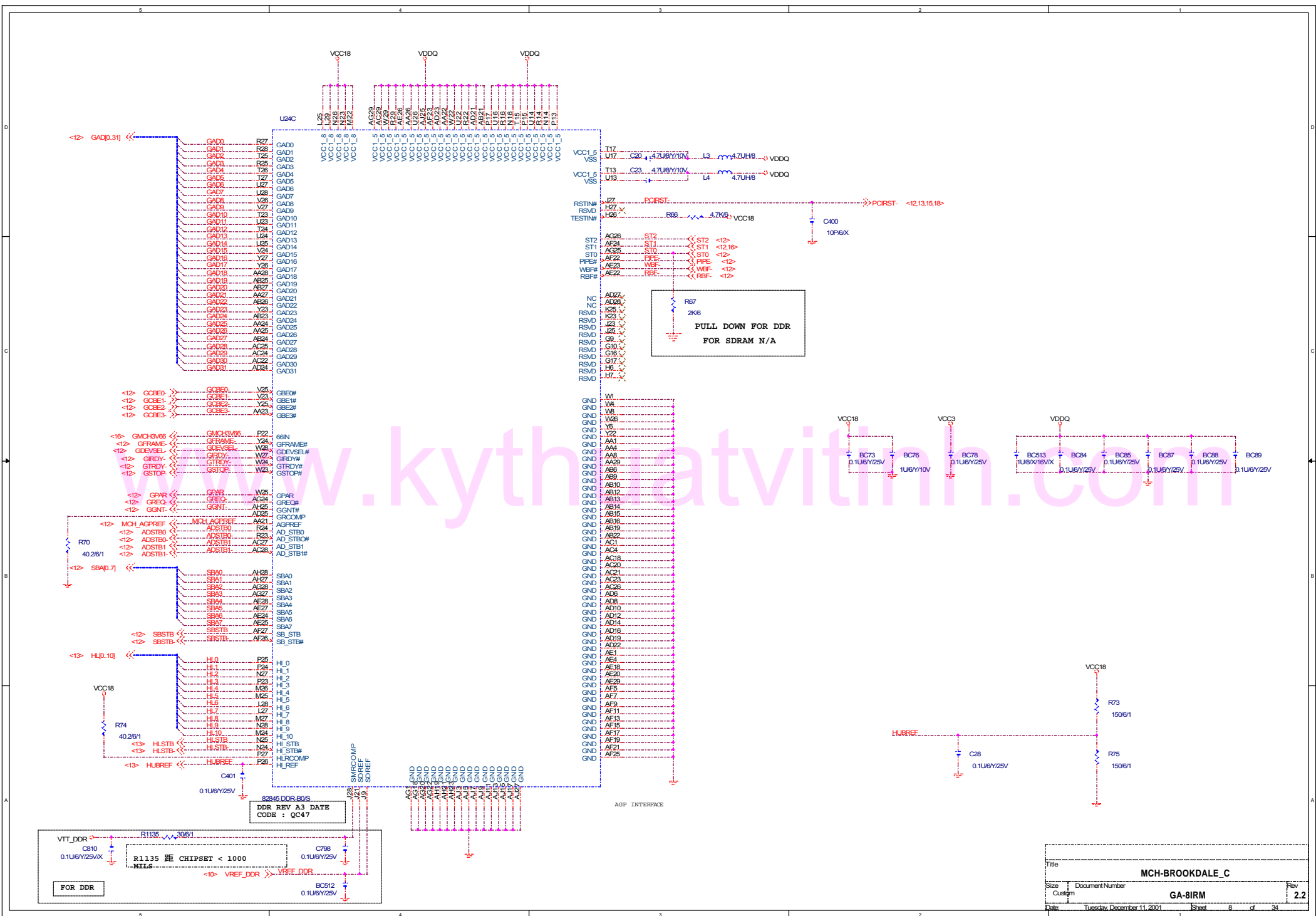




82845 DDR-B0S  
 DDR REV A3 DATE  
 CODE : QC47

Title			
MCH-BROOKDALE_A			
Size	Document Number	Rev	
Custom	GA-8IRM	2.2	
Date:	Tuesday, December 11, 2001	Sheet	6 of 34



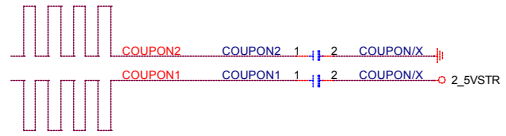
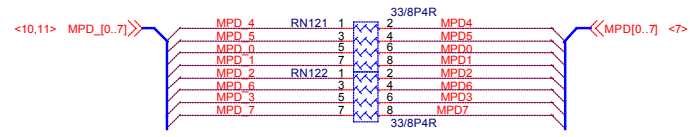
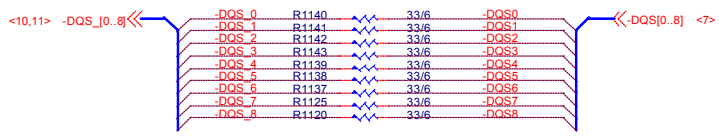
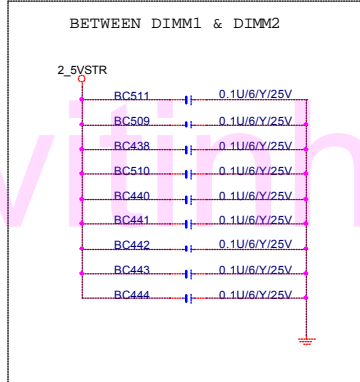
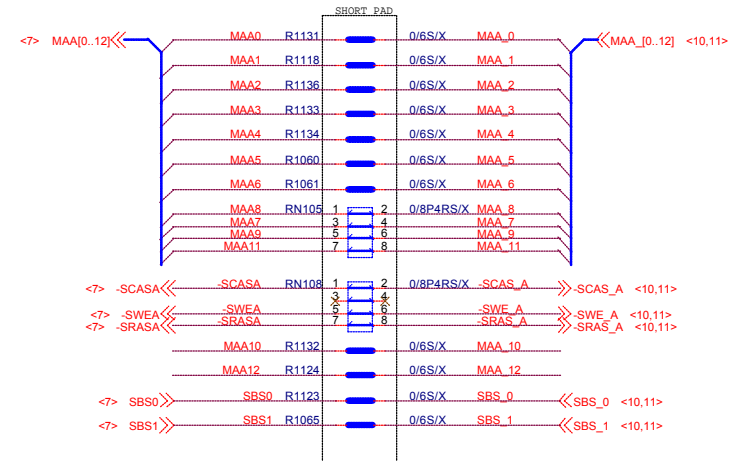
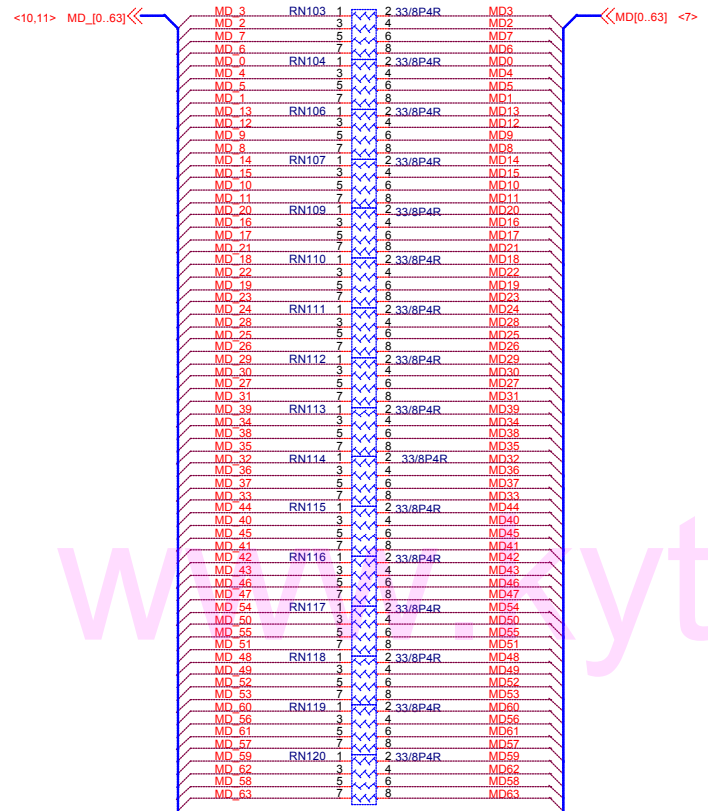


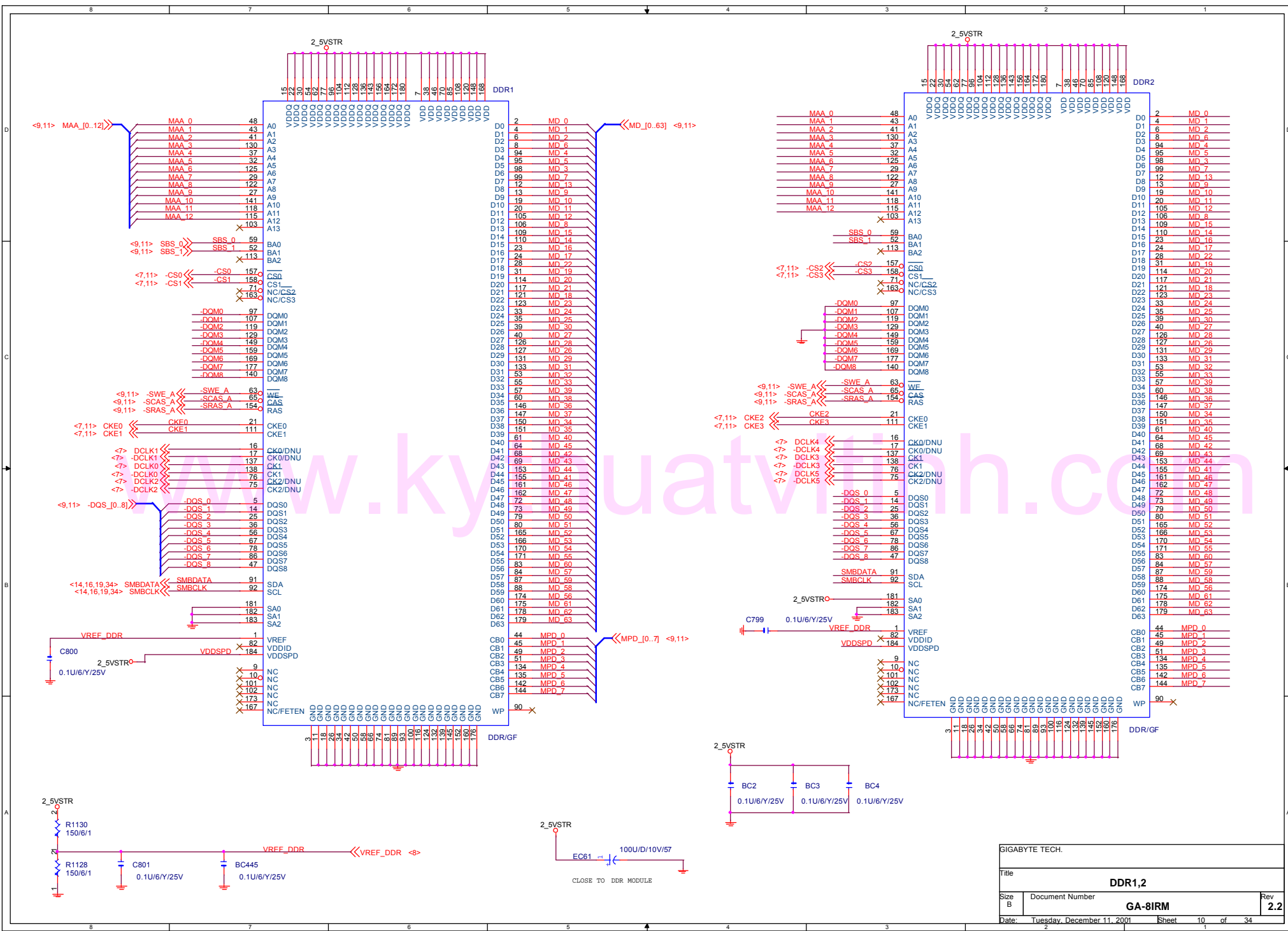
PULL DOWN FOR DDR  
FOR SDRAM N/A

82845 DDR-BOIS  
DDR REV A3 DATE  
CODE : QC47

VTT\_DDR  
C810  
0.1u6y25v/x  
FOR DDR  
R1135 BE CHIPSET < 1000  
MILLS  
VREF\_DDR  
VREF\_DDR  
C798  
0.1u6y25v  
C799  
0.1u6y25v

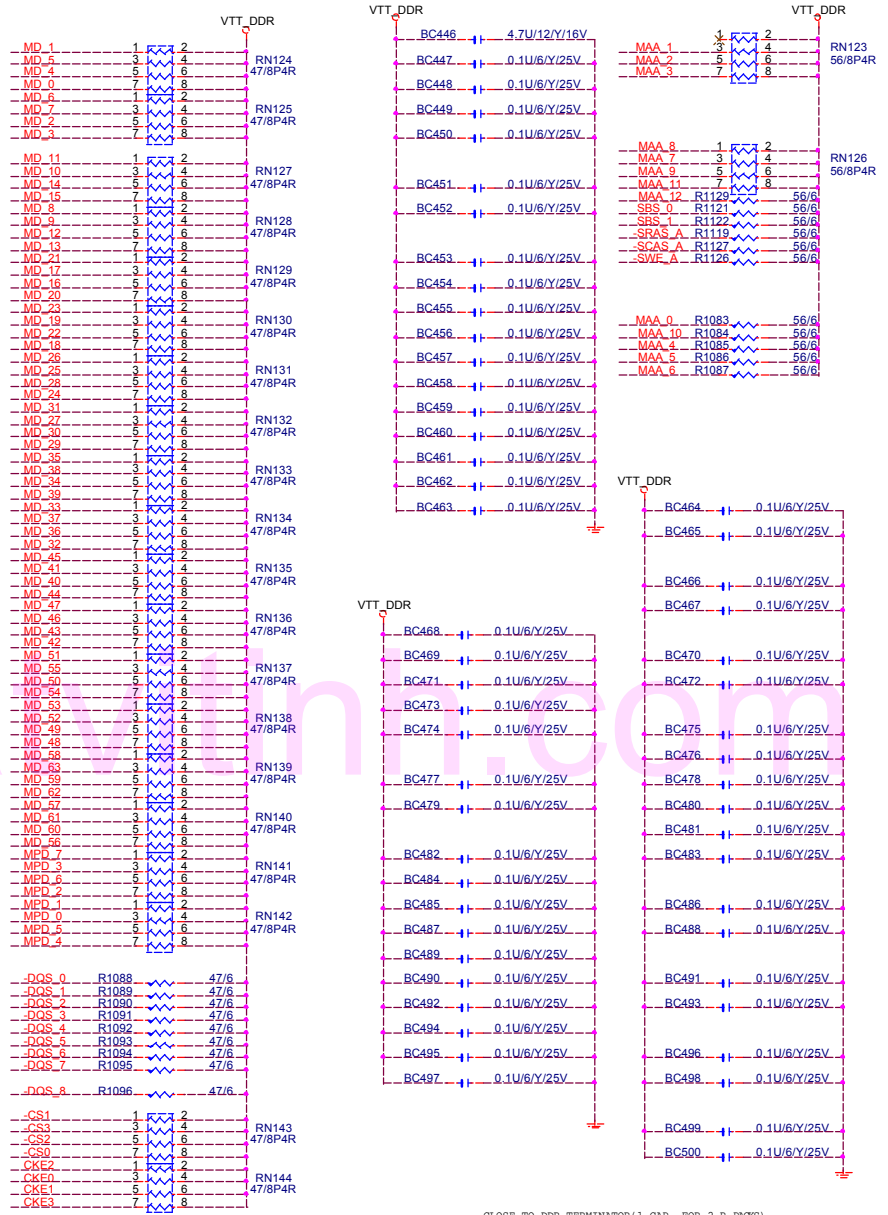
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MCH-BROOKDALE\_C  
Size Custom  
Document Number GA-8IRM  
Date Tuesday, December 11, 2001  
Rev 2.2  
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GIGABYTE TECH.		
Title		DDR1,2
Size	Document Number	GA-BIRM
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		Rev 2.2

<9,10> MD[0..63] << MD[0..63]  
 <9,10> MPD[0..7] << MPD[0..7]  
 <9,10> -DQS[0..8] << -DQS[0..8]  
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 <7,10> -CS[0..3] << -CS[0..3]  
 <7,10> CKE[0..3] << CKE[0..3]  
 <9,10> SBS[0..1] << SBS[0..1]  
 <9,10> -SWE\_A << -SWE\_A  
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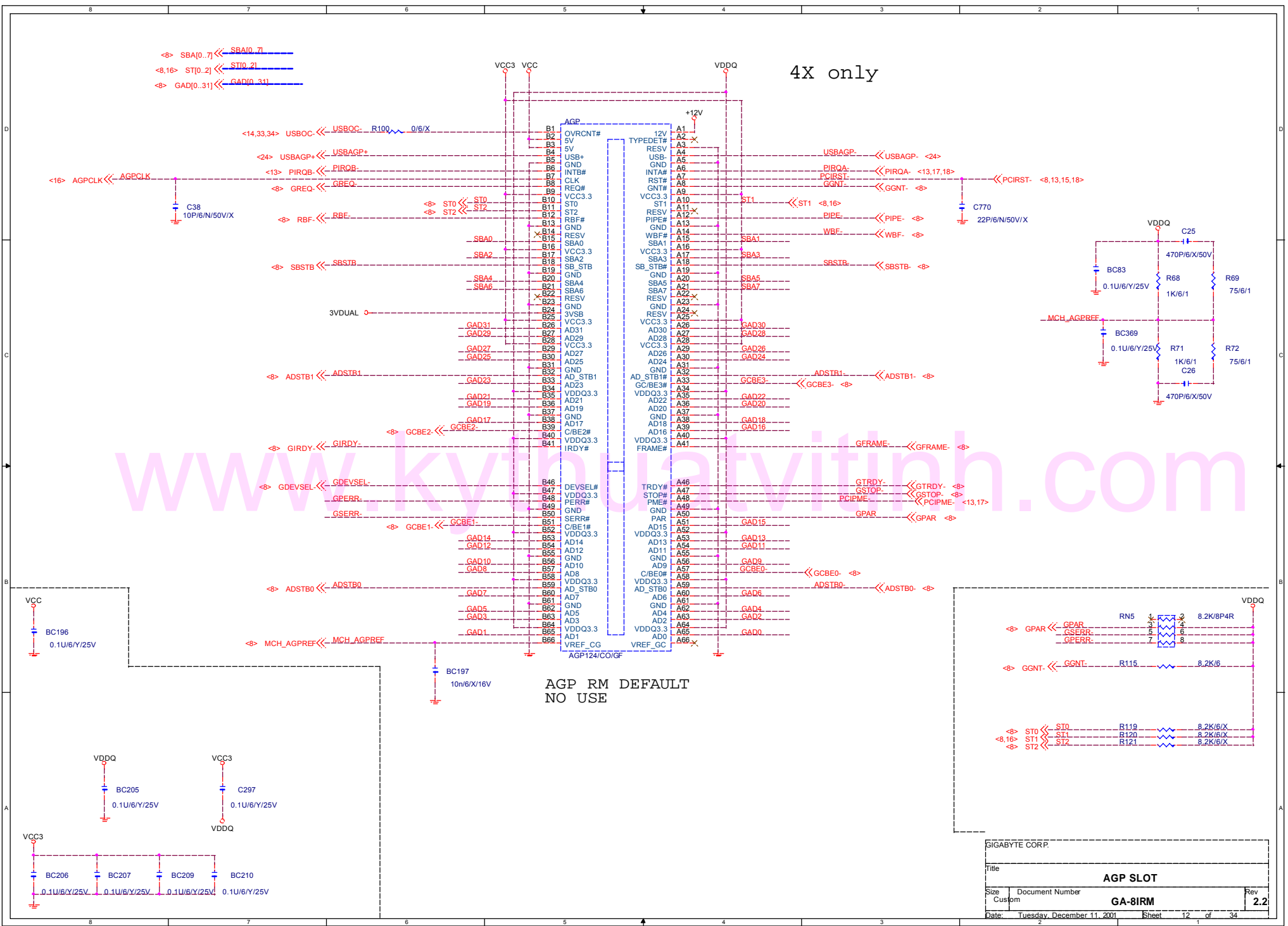
CLOSE TO DDR TERMINATOR(1 CAP. FOR 2 R-FMCS)

GIGABYTE TECH.	
Title	
<b>TERMINATOR</b>	
Size B	Document Number
	<b>GA-8IRM</b>
Date: Tuesday, December 11, 2001	Sheet 11 of 34
	Rev <b>2.2</b>

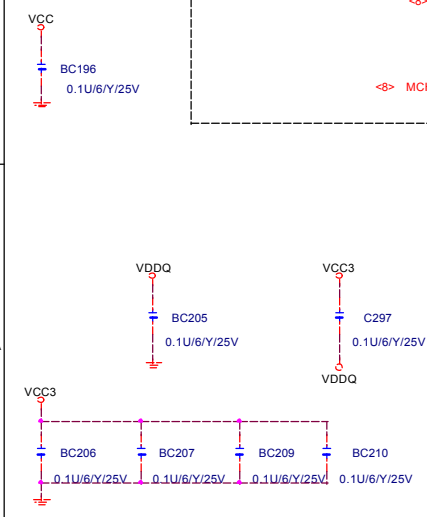
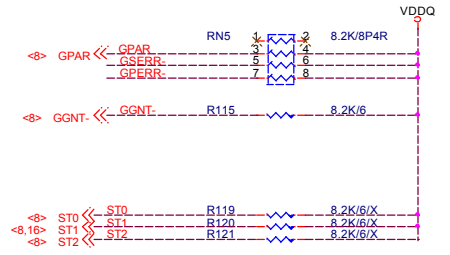
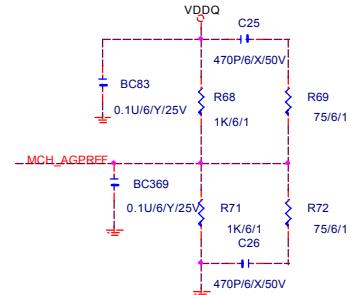
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4x only

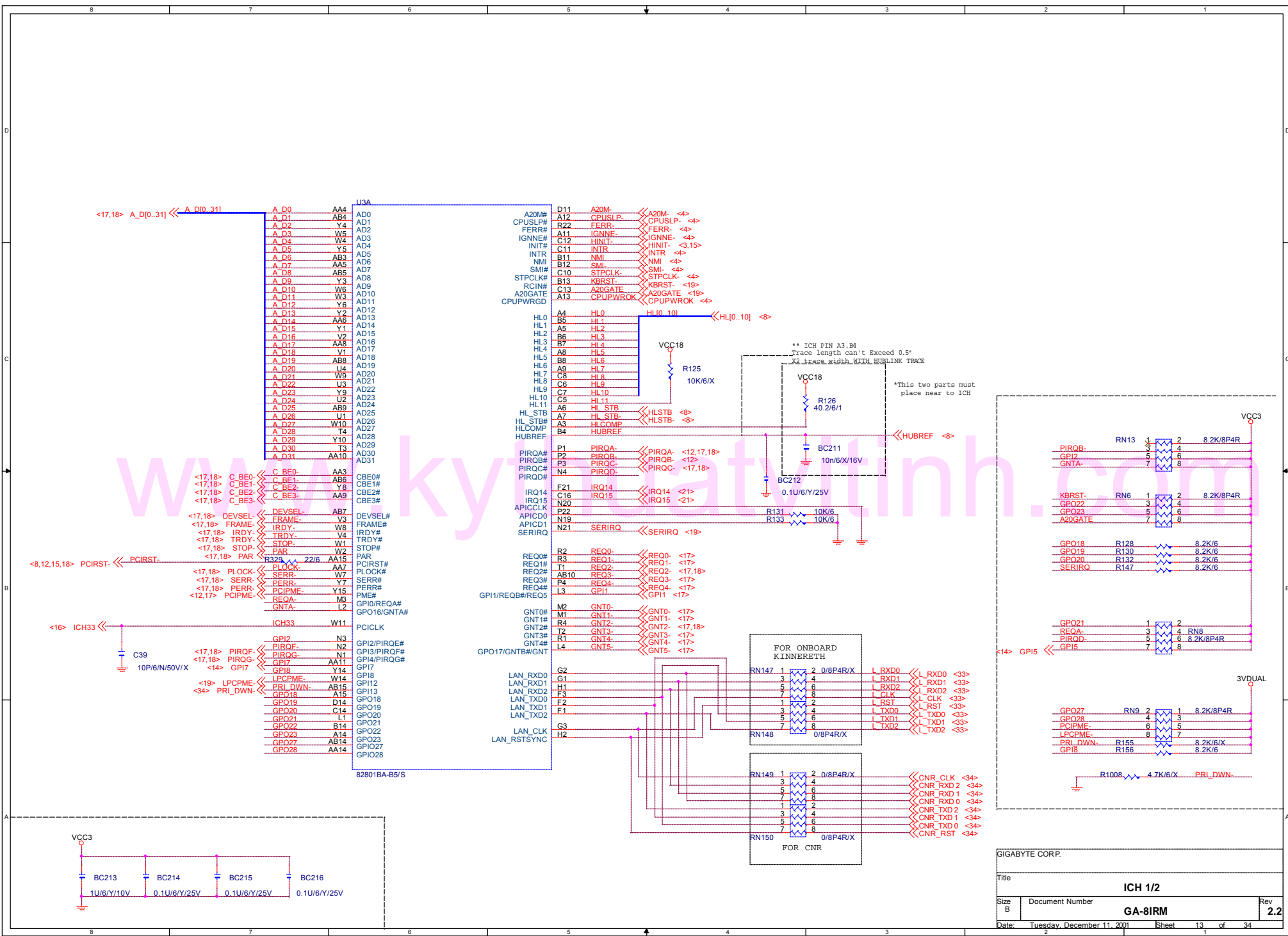
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<8,16> ST[0..2] << ST[0..2]  
<8> GAD[0..31] << GAD[0..31]



AGP RM DEFAULT  
NO USE



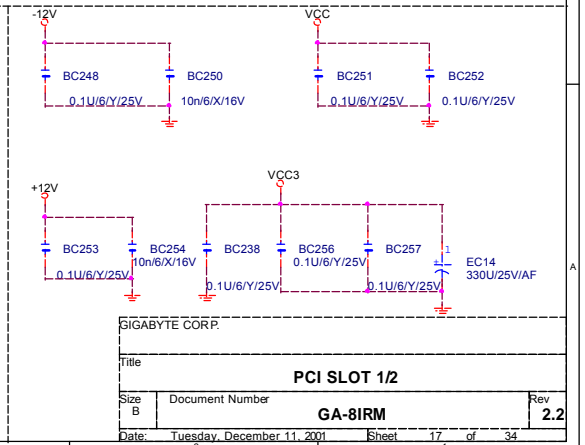
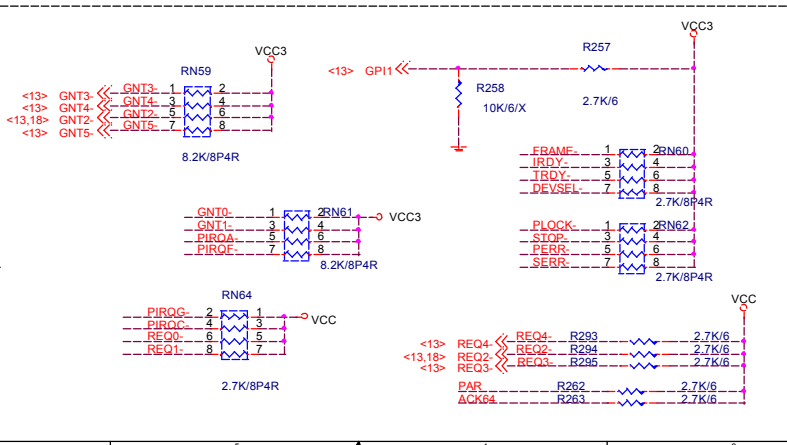
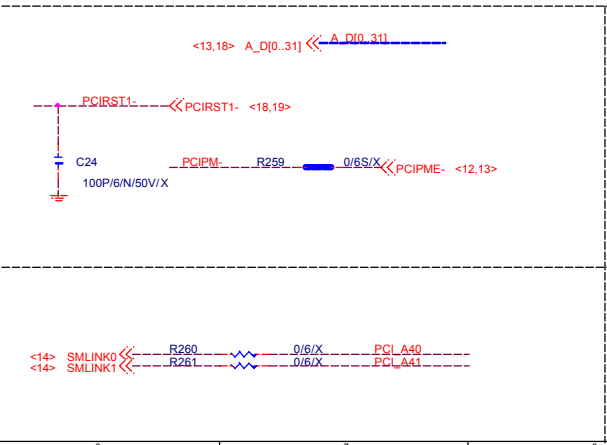
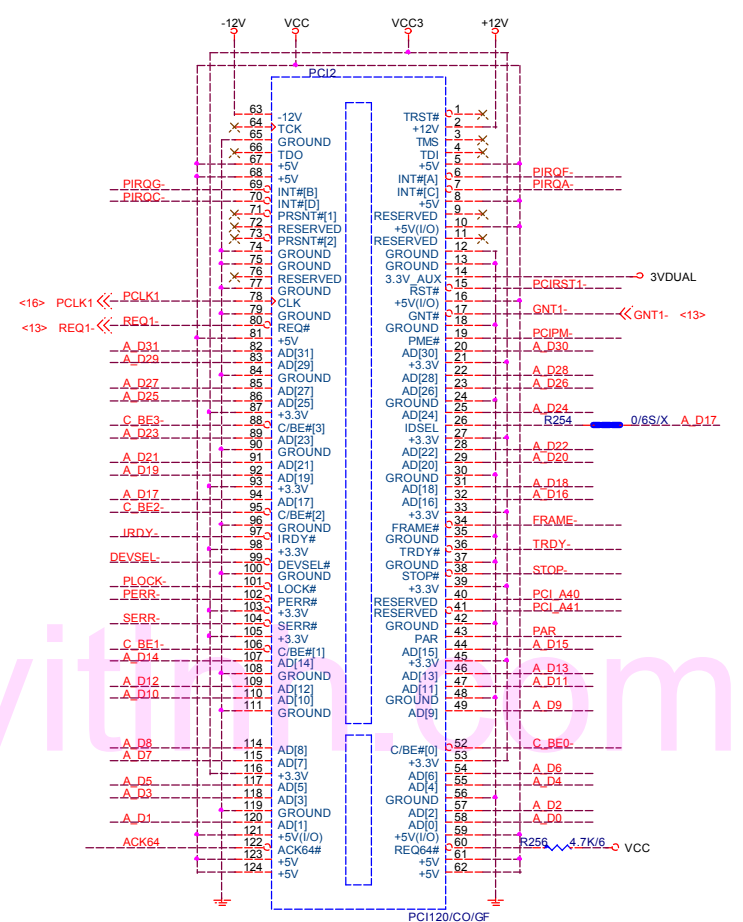
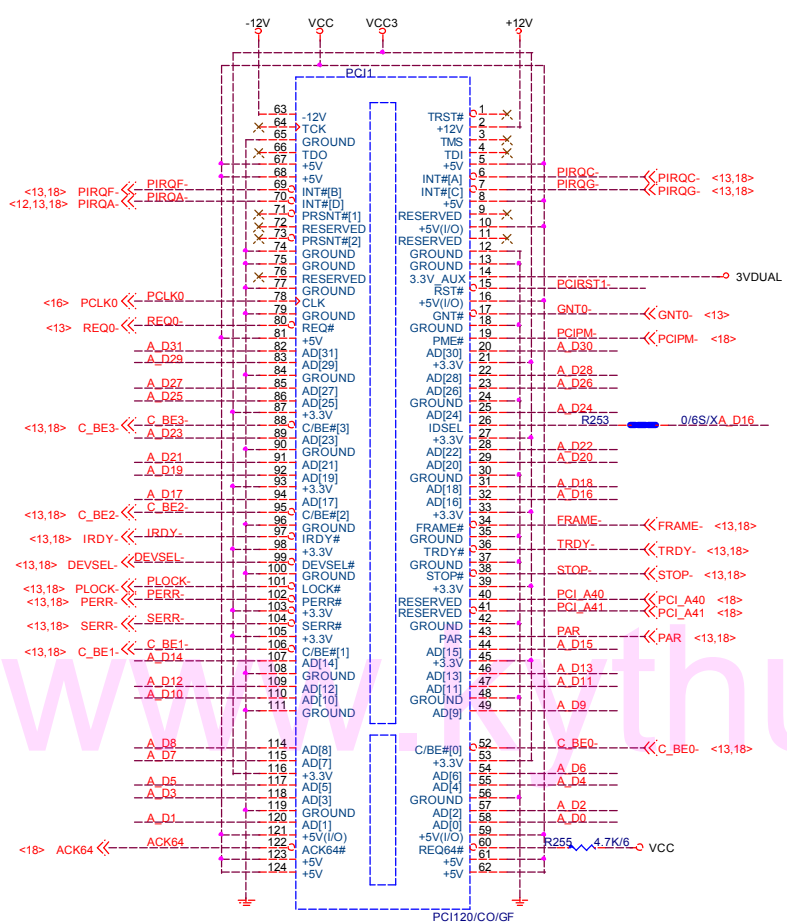
GIGABYTE CORP.			
Title			
AGP SLOT			
Size	Document Number	Rev	
Custom	GA-8IRM	2.2	
Date:	Tuesday, December 11, 2001	Sheet	12 of 34



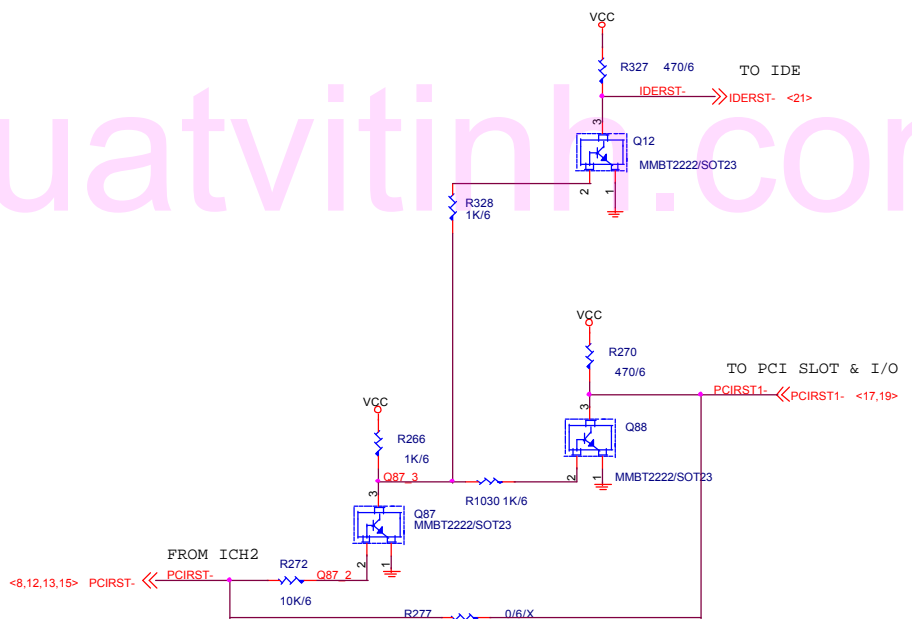
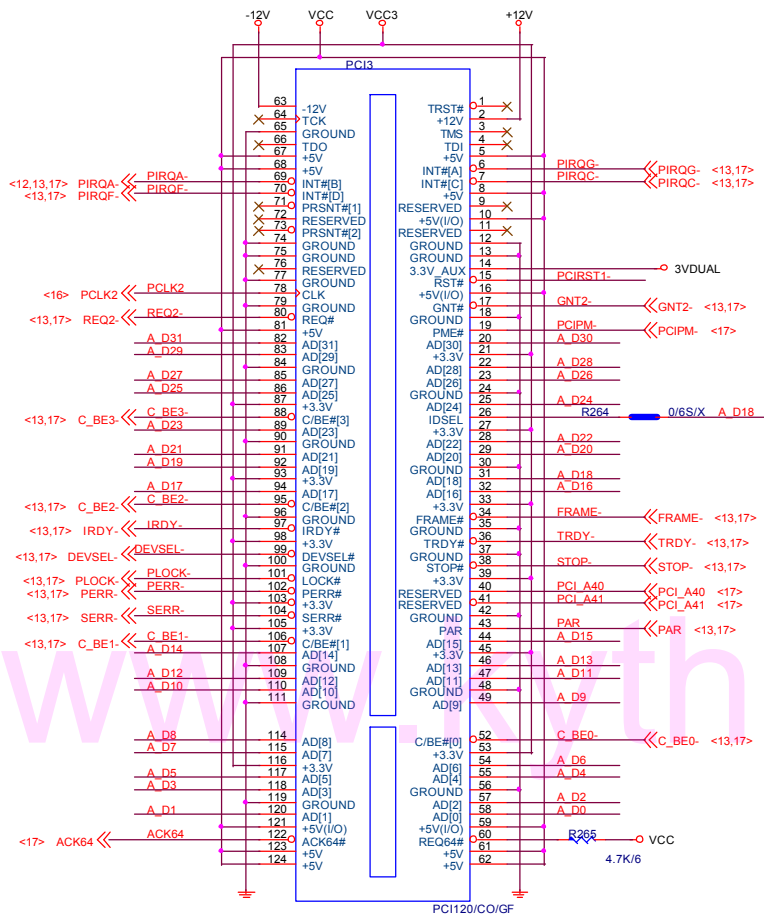








GIGABYTE CORP.	
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Size B	Document Number GA-8IRM
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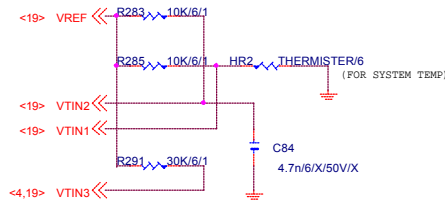


GIGABYTE CORP.			
Title			
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Size	Document Number	GA-8IRM	Rev
B			2.2
Date:	Tuesday, December 11, 2001	Sheet	18 of 34

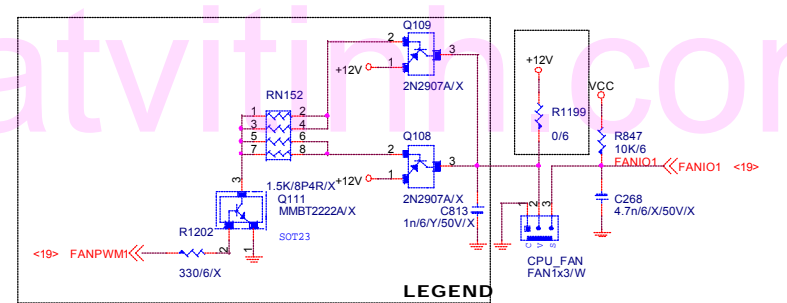
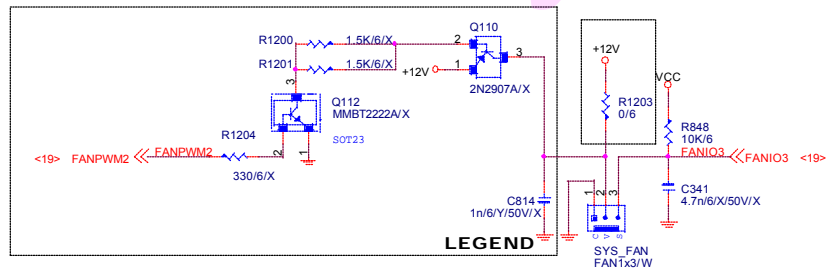
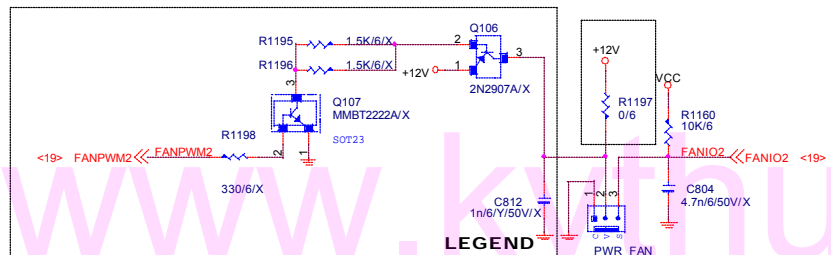
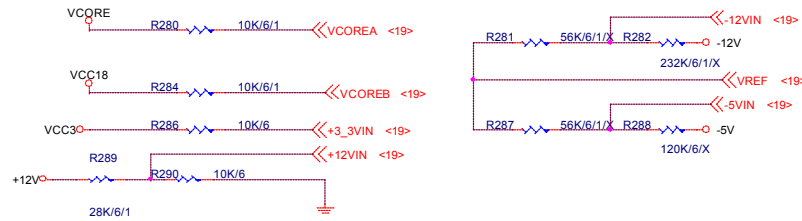


# Hardware Monitor circuits

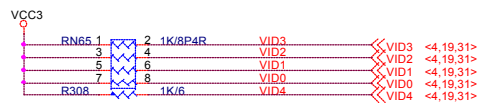
## Temperature Sensing



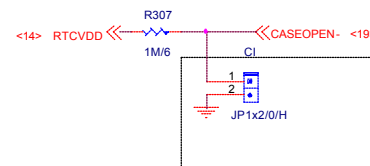
## Voltage Sensing



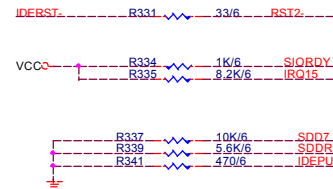
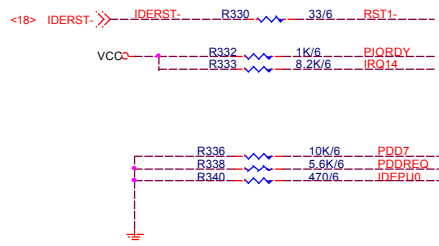
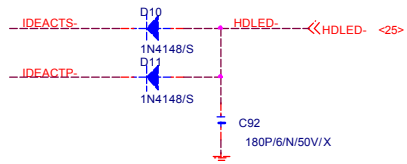
## CPU Voltage ID output



## Case Open Circuits



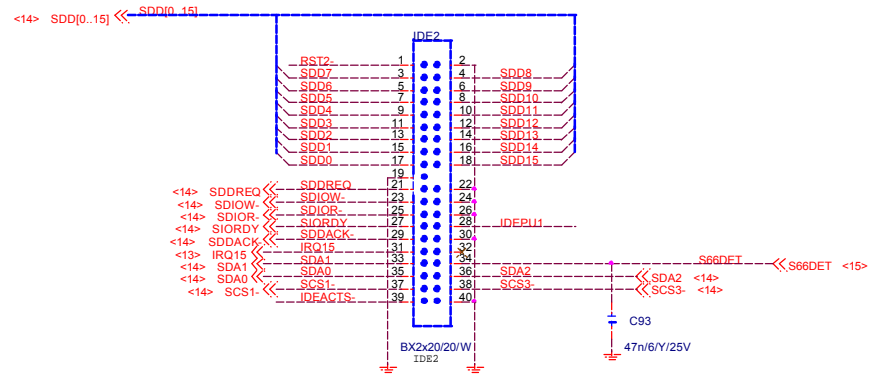
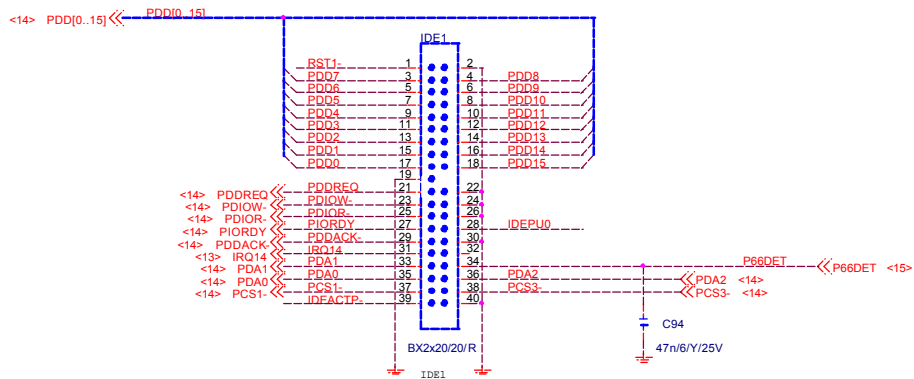
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<b>HAREWARE MONITOR</b>		
Size B	Document Number	Rev
	<b>GA-8IRM</b>	<b>2.2</b>
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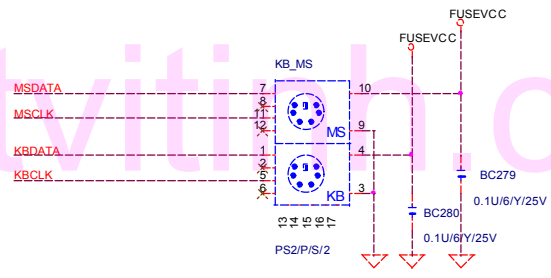
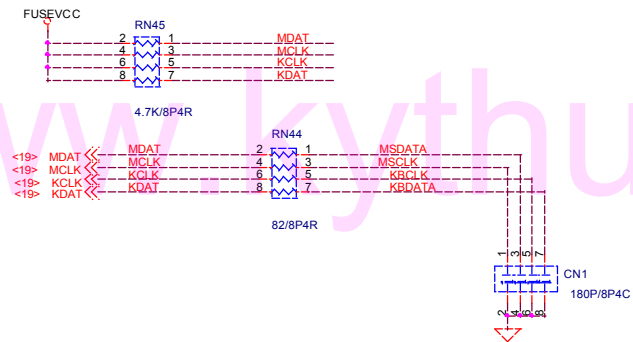
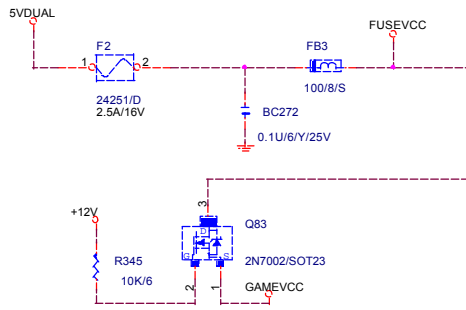
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PRIMARY IDE CONNECTOR

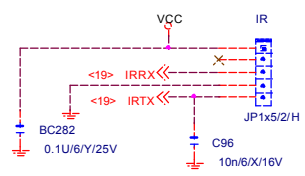
SECONDARY IDE CONNECTOR



GIGABYTE CORP.			
Title			
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Size	Document Number	GA-8IRM	Rev
B			<b>2.2</b>
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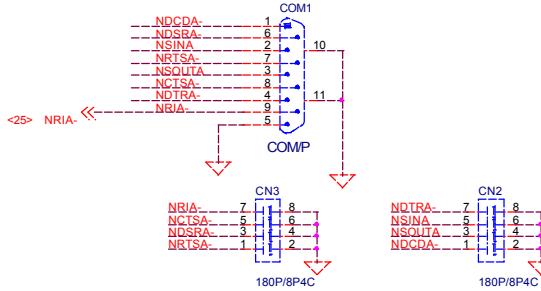
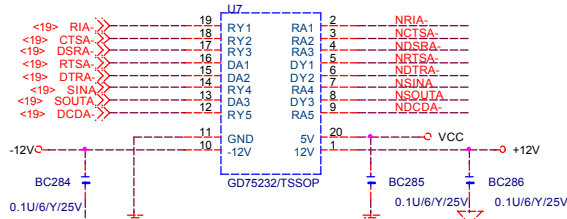


IR CONNECTOR

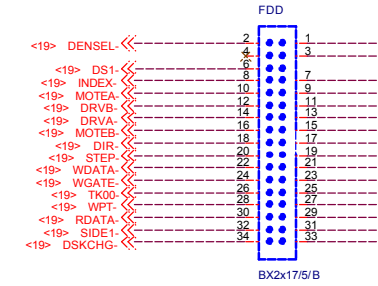


GIGABYTE CORP.			
Title			
<b>KB &amp; PS2 MOUSE &amp; IR</b>			
Size	Document Number	GA-8IRM	Rev
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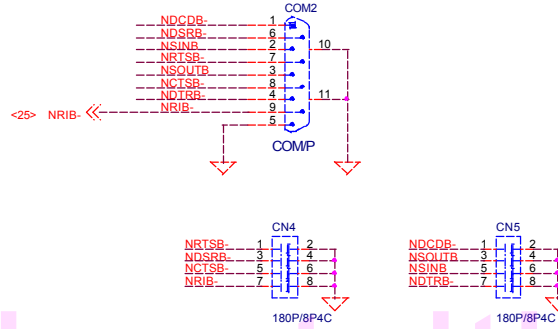
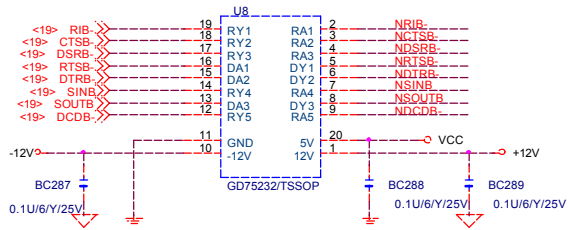
COMA



FLOPPY

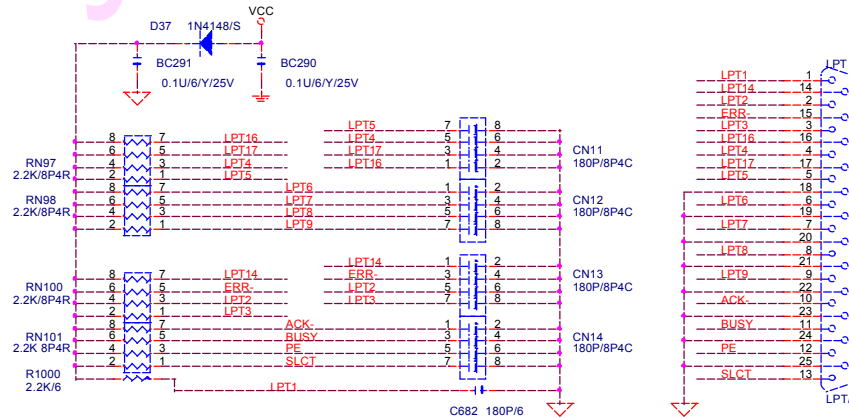
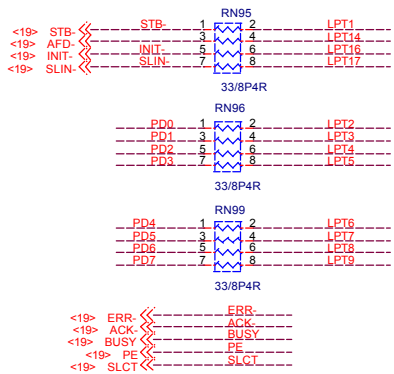


COMB



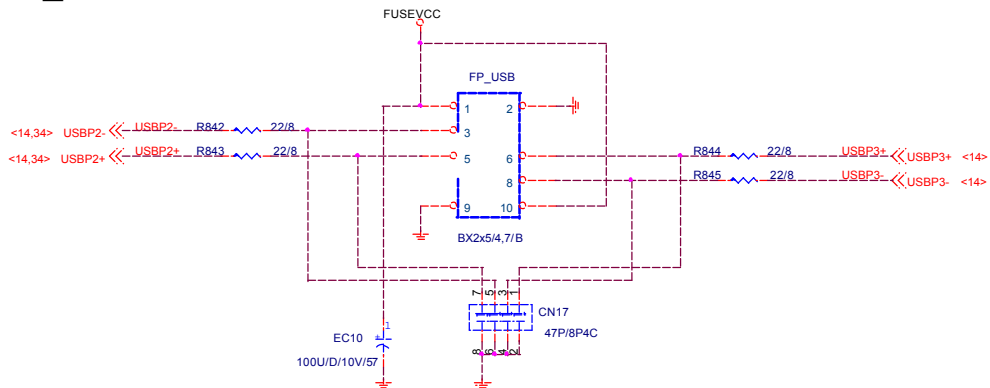
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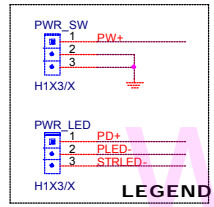
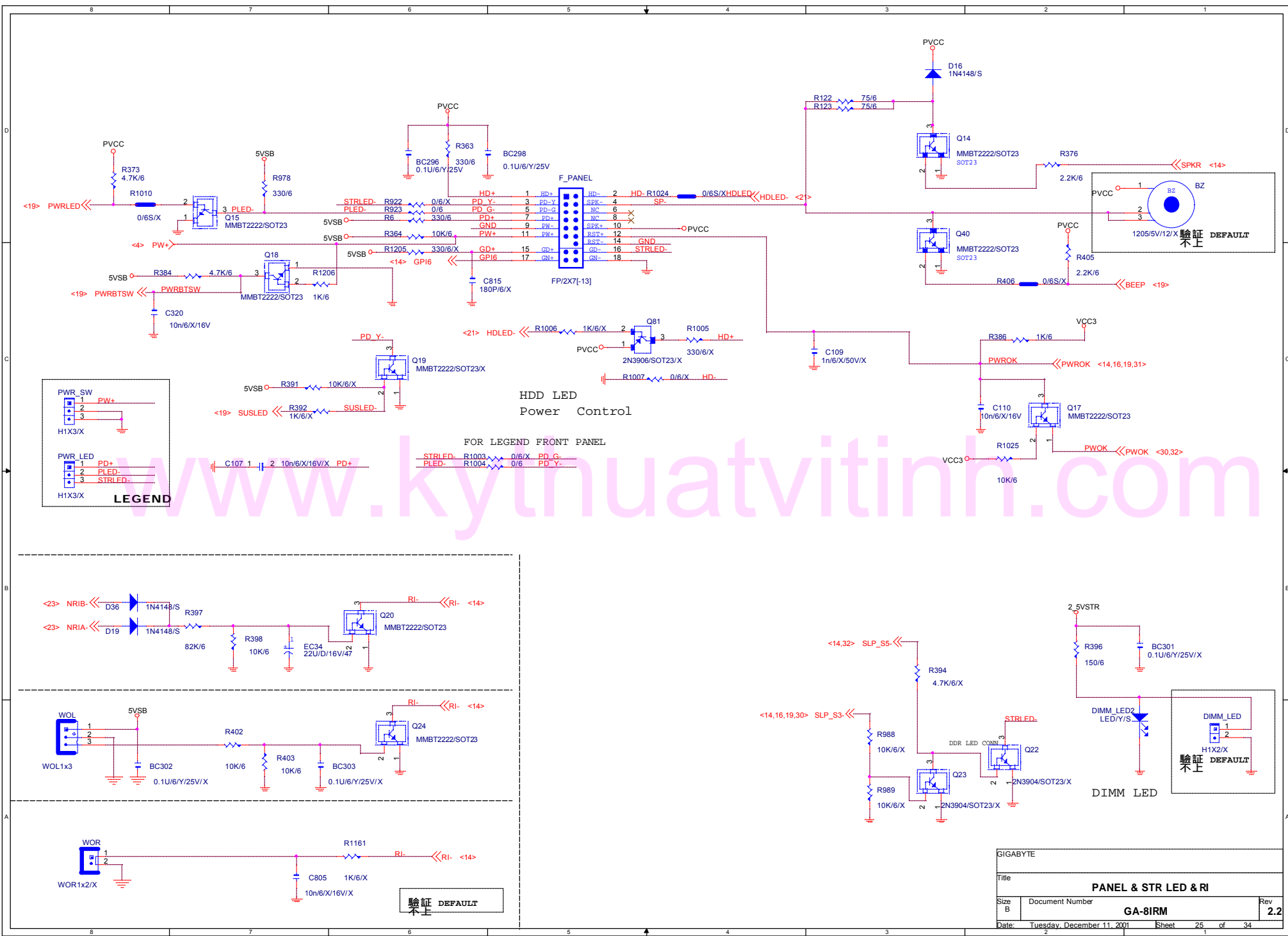


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### F\_USB

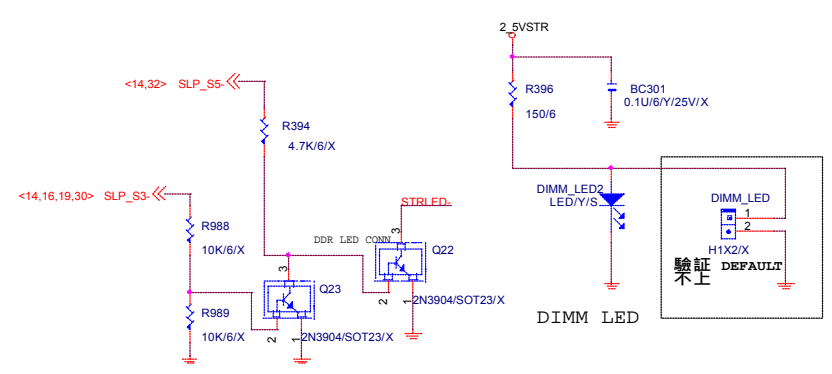
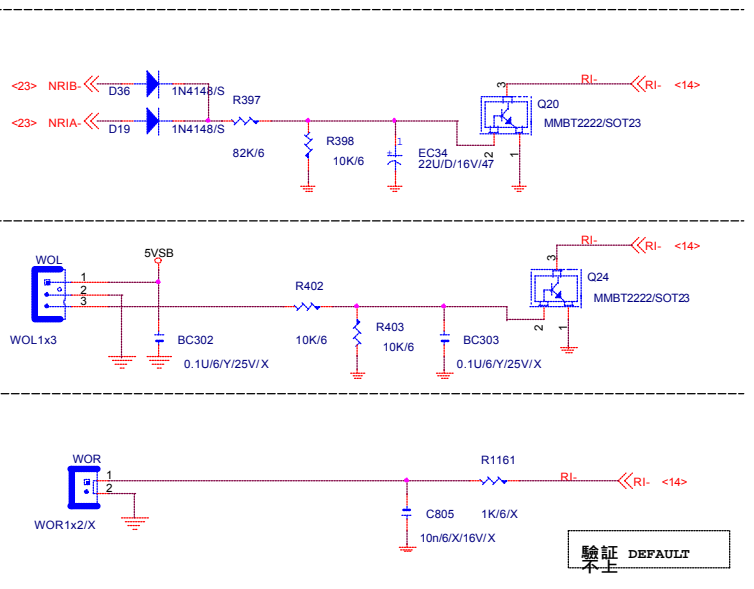


GIGABYTE CORP.			
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Size B	Document Number GA-8IRM	Rev 2.2	
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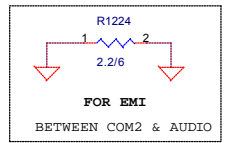
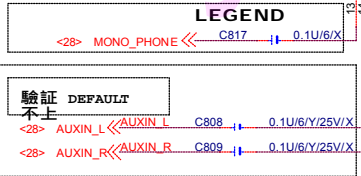
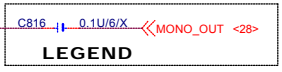
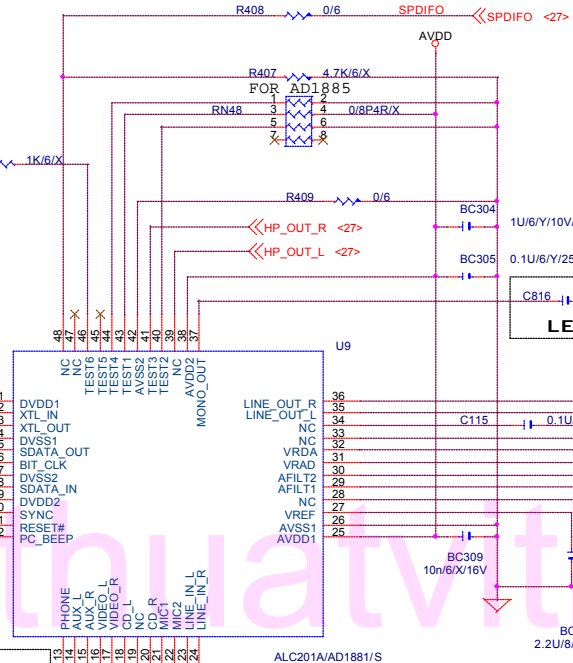
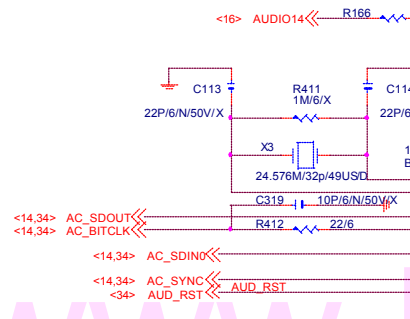
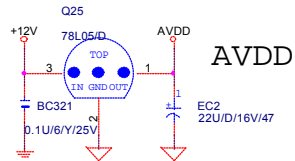


HDD LED  
Power Control

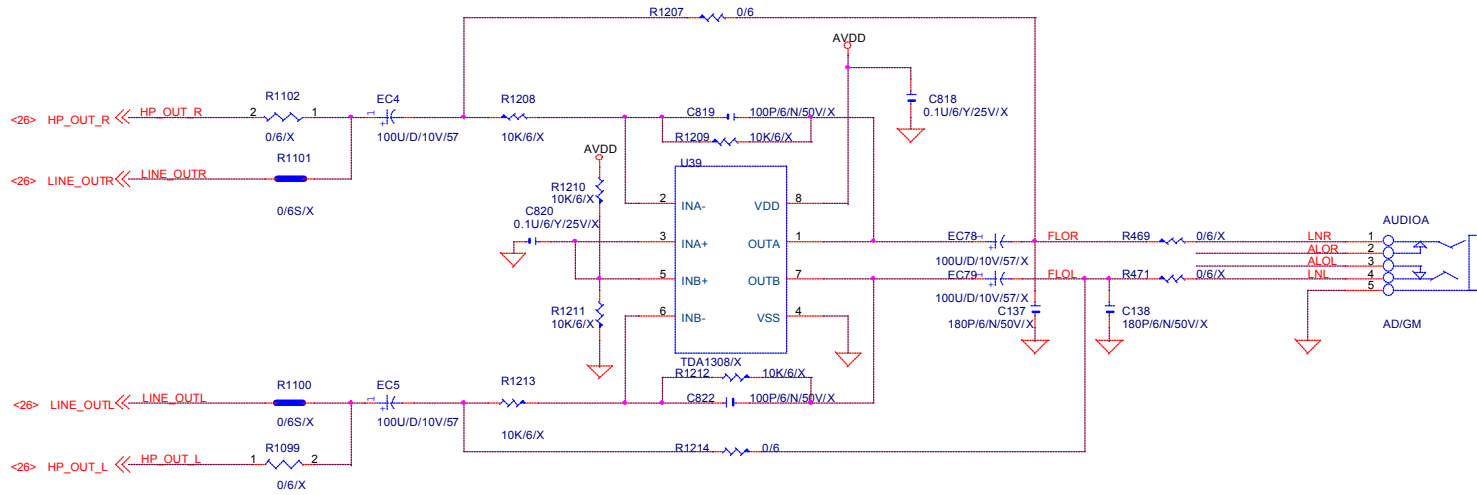
FOR LEGEND FRONT PANEL



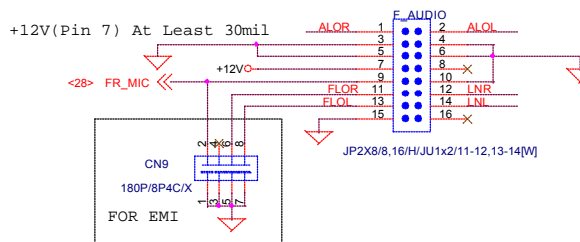
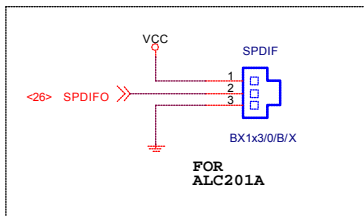
GIGABYTE			
Title			
<b>PANEL &amp; STR LED &amp; RI</b>			
Size B	Document Number	<b>GA-8IRM</b>	
Date:	Tuesday, December 11, 2001	Sheet	2.2
		25	34



GIGABYTE CORP.			
Title		AC97	
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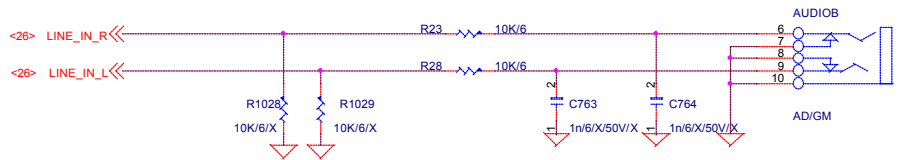


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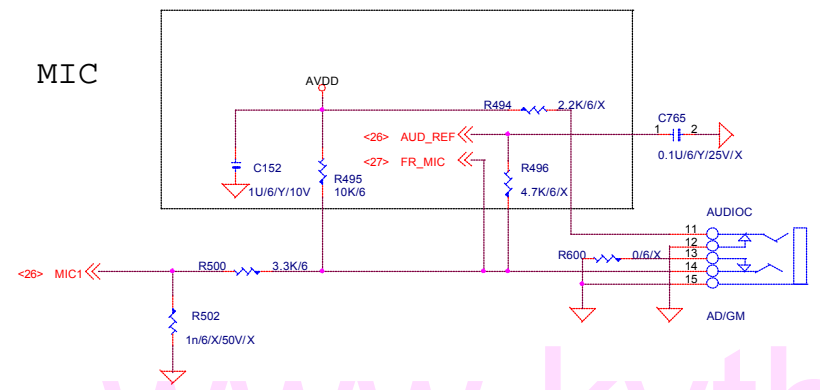


GIGABYTE CORP.			
Title			
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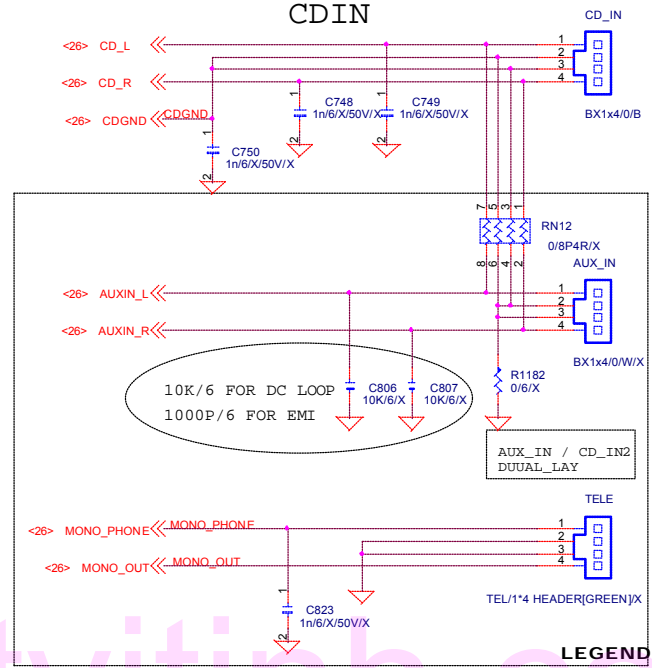
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# MIC



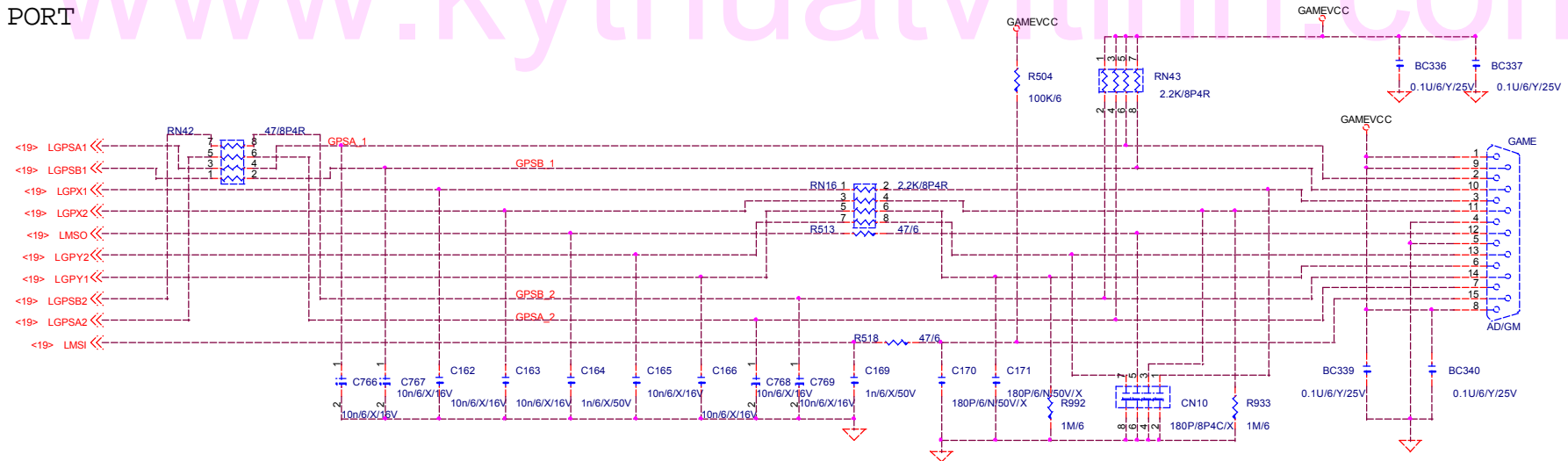
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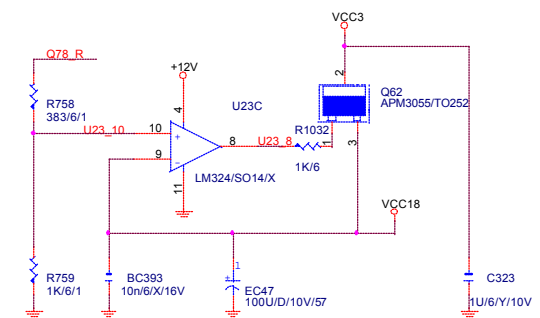
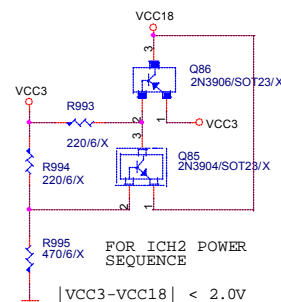
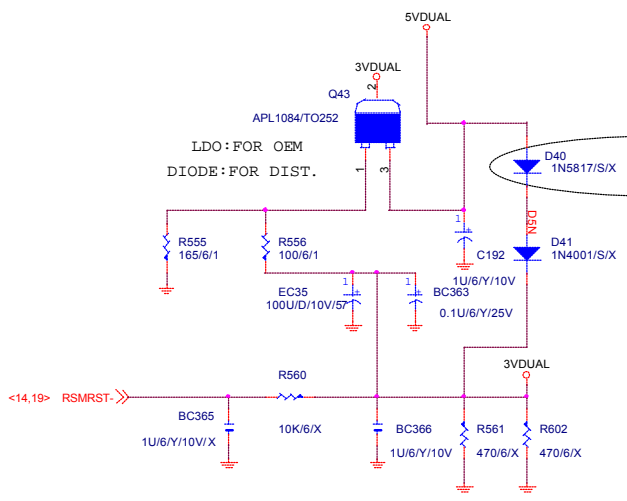
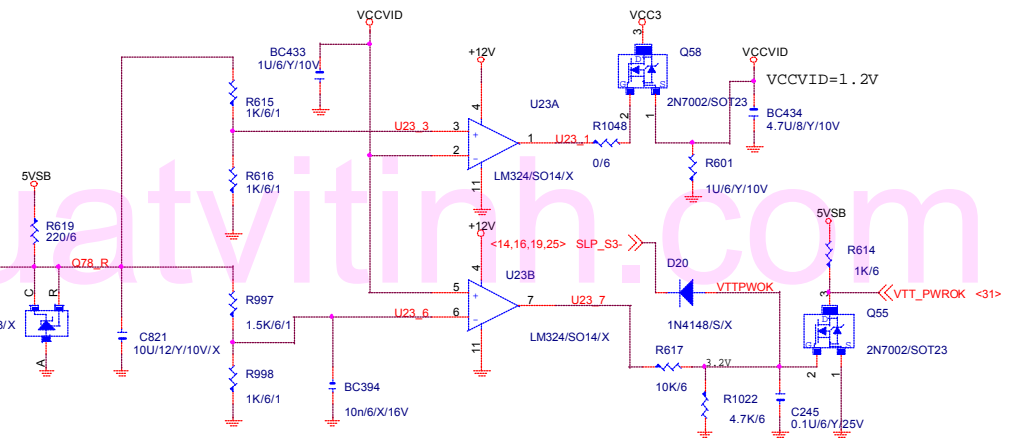
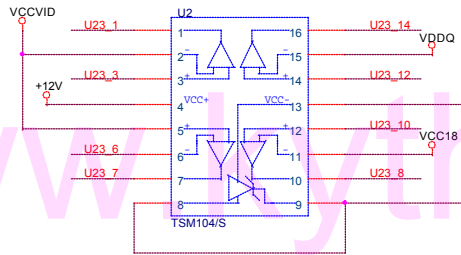
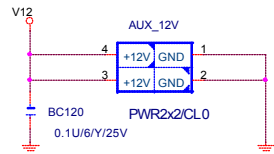
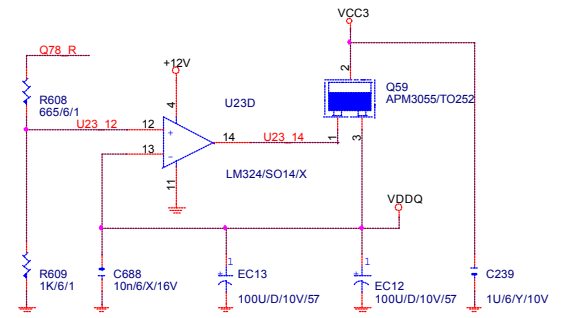
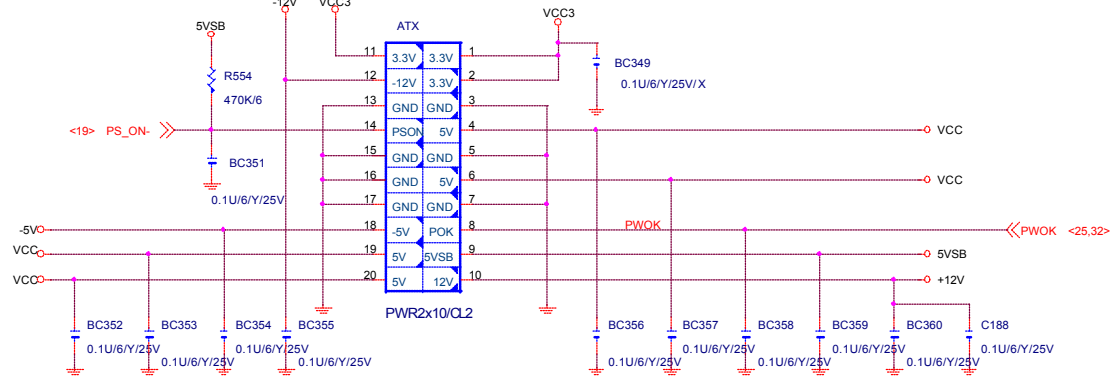
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Title			
<b>PHONE JACK, GAME PORT</b>			
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GAME PORT

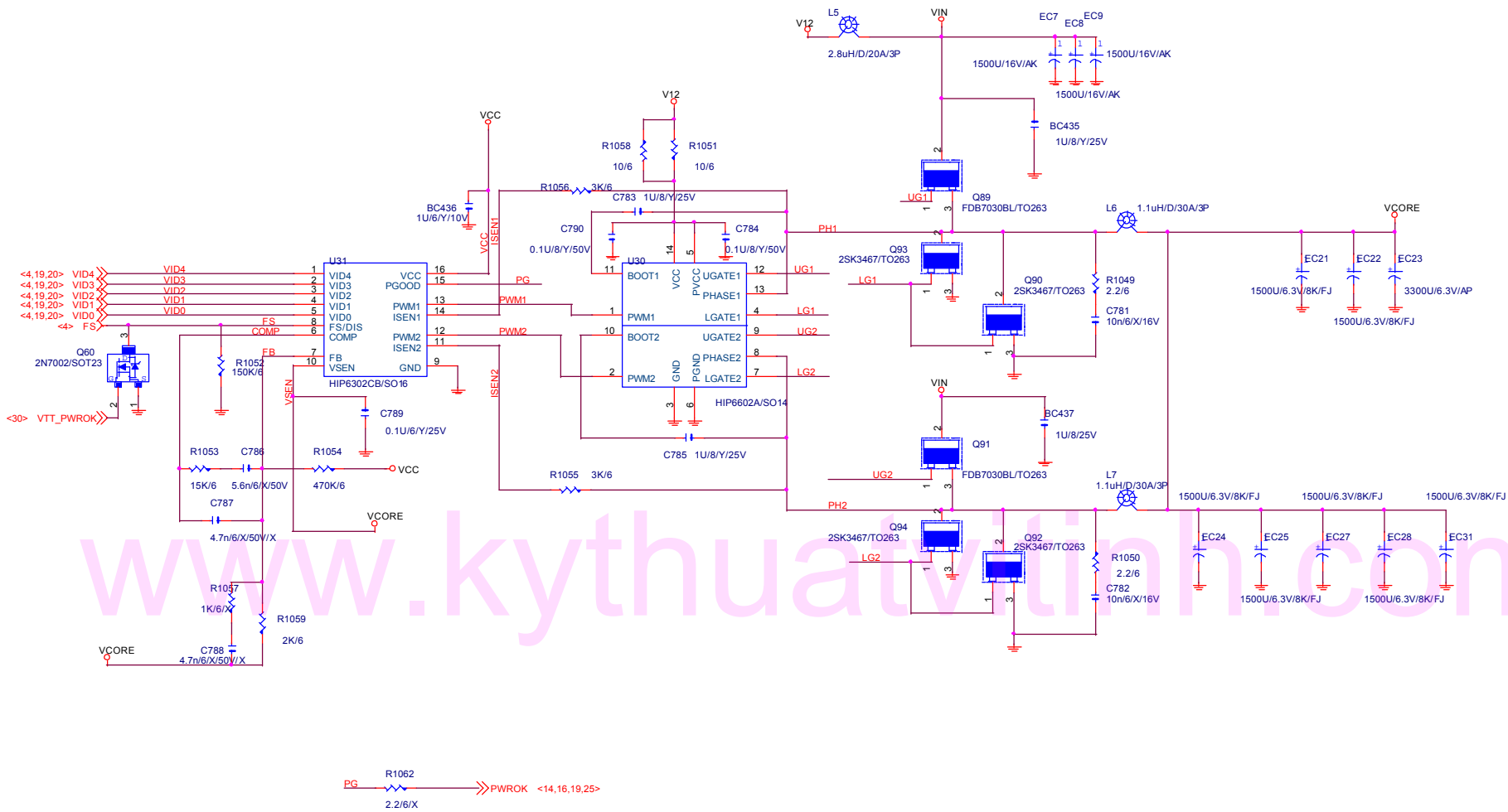


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Title <b>PHONE JACK, GAME PORT</b>			
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ATX POWER CONNECTOR

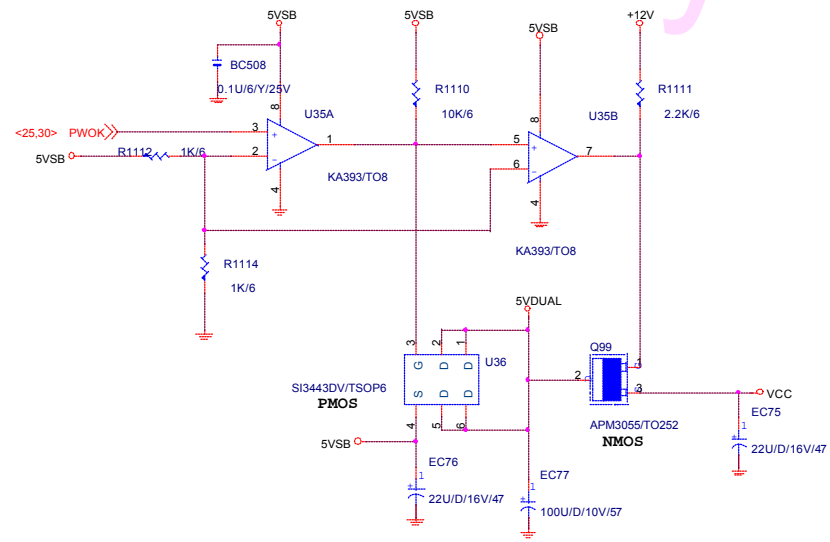
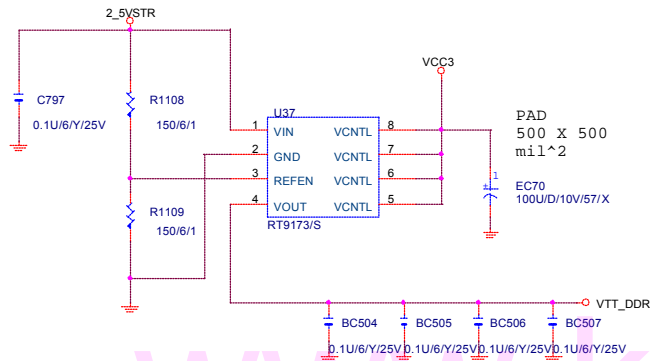


GIGABYTE CORP.			
Title			
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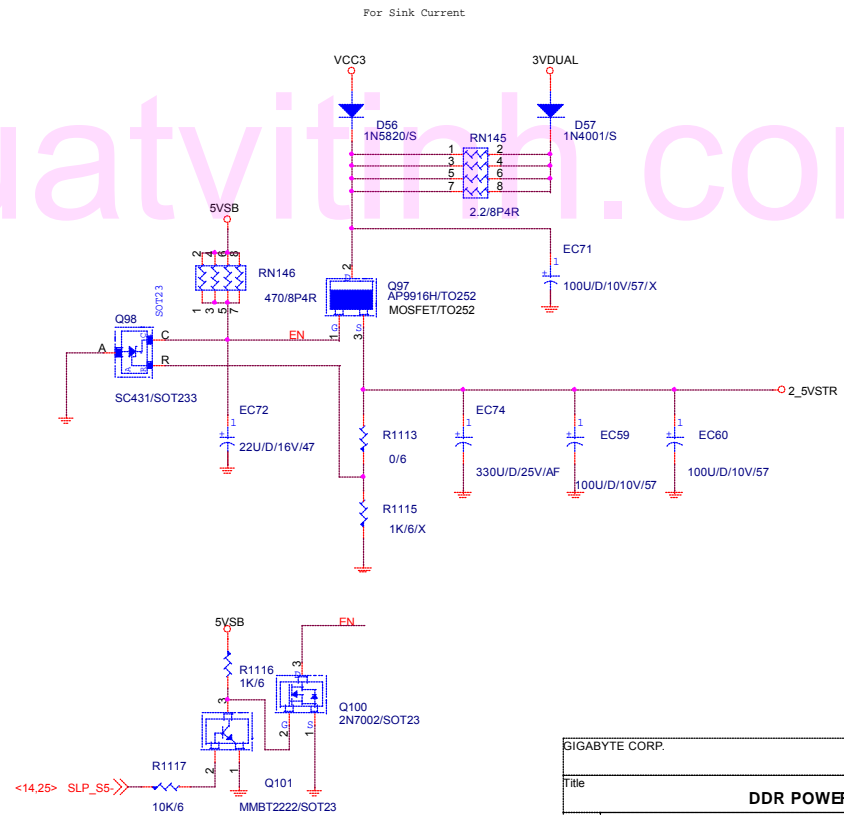


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1.25V VTT\_DDR LINEAR SOLUTION

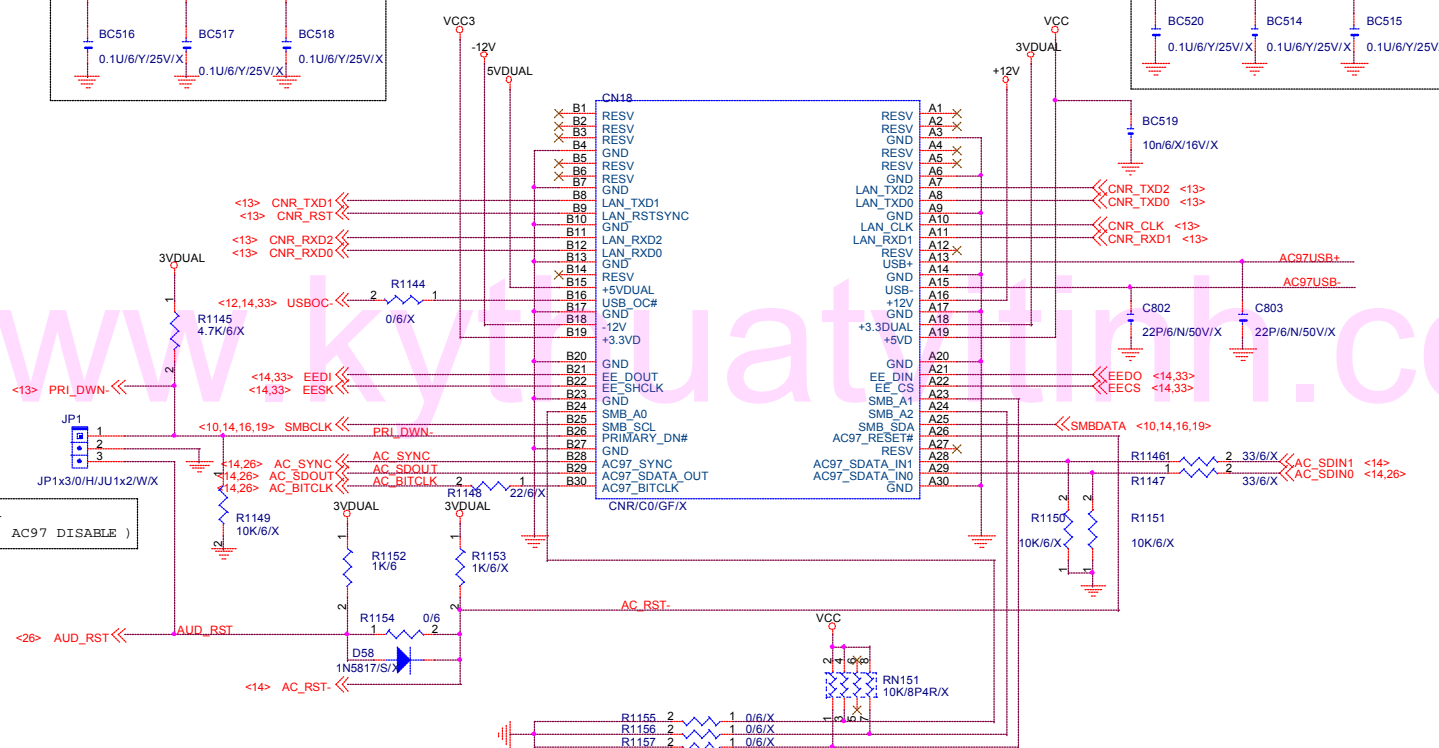
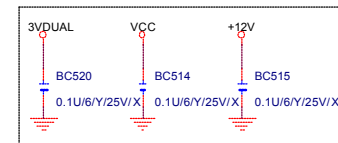
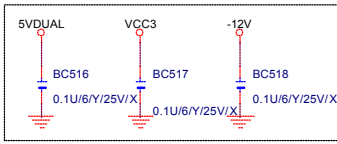


5VDUAL CIRCUIT

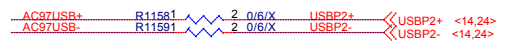


GIGABYTE CORP.	
Title	
<b>DDR POWER</b>	
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JP1:  
 1-2 CNR SECONDARY  
 2-3 CNR PRIMARY ( AC97 DISABLE )



<b>GIGABYTE CORP.</b>		
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