

GIGABYTE GA-8IG1000-G Schematics

Revision 3.0

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	P4_478A
05	P4_478B
06	P4_478C
07	SPRINGDALE HOST
08	SPRINGDALE DDR
09	SPRINGDALE AGP, HUB, CSA, VGA
10	SPRINGDALE PWR
11	DDR1,2 CHANNEL A
12	DDR3,4 CHANNEL B
13	DDR TERMINATION
14	AGP 8X SLOT
15	ICH5 PCI, USB, HUB, LAN
16	ICH5 IDE, GPIO, SATA, CTRL
17	ICH5 VCC, GND
18	DUAL FWH
19	ICS952635 CLOCK GENERATOR
20	PCI SLOT1/SLOT2
21	PCI SLOT3/SLOT4/RESET BUFFER
22	PCI SLOT5/SLOT6

SHEET	TITLE
23	AC '97 CODEC
24	AUDIO JACK, L_OUT, F_AUDIO
25	ITE 8712/IR_CIR/SCR/S_IRQ
26	COMA/VGA_COMB/LPT
27	IDE1/IDE2/FDD
28	FAN/HW MONITOR
29	KB_MS/GAME/FUSEVCC
30	FRONT PANEL
31	R_USB/F_USB1/F_USB2
32	DDR/VDDQ/5VDUAL/VCCVID POWER
33	VCORE POWER
34	ATX/ATX_12V/FAN1655M
35	MARVELL 88E8001
36	TI TSB43AB23(1394)
37	PCI ROUNTIONG
38	GPIO PIN LIST

PROCESS:C

COMPONENT SIDE (0.5 oz. Copper)
VCC SIDE (1 oz. Copper)
GND SIDE (1 oz. Copper)
SOLDER SIDE (0.5 oz. Copper)

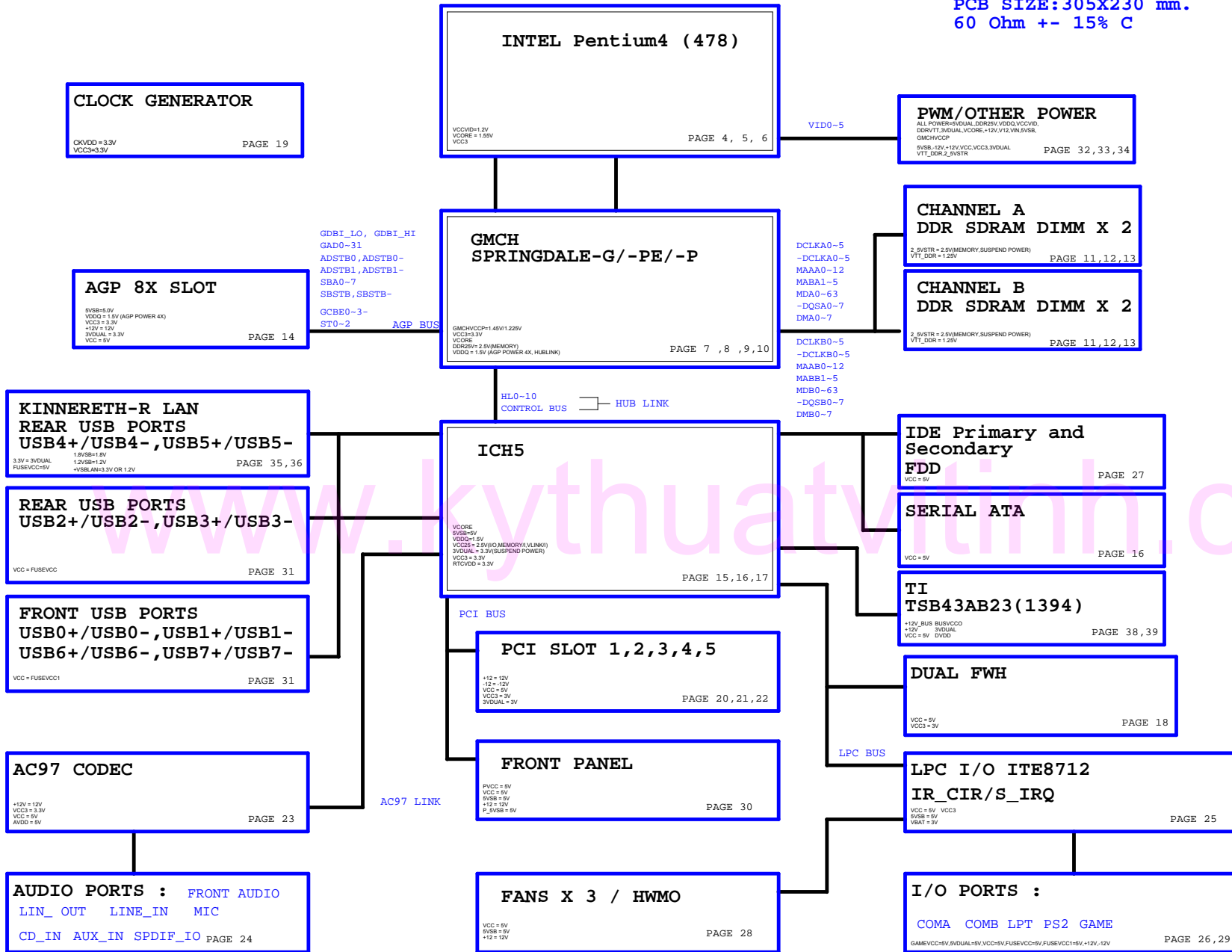
GIGABYTE CORP.

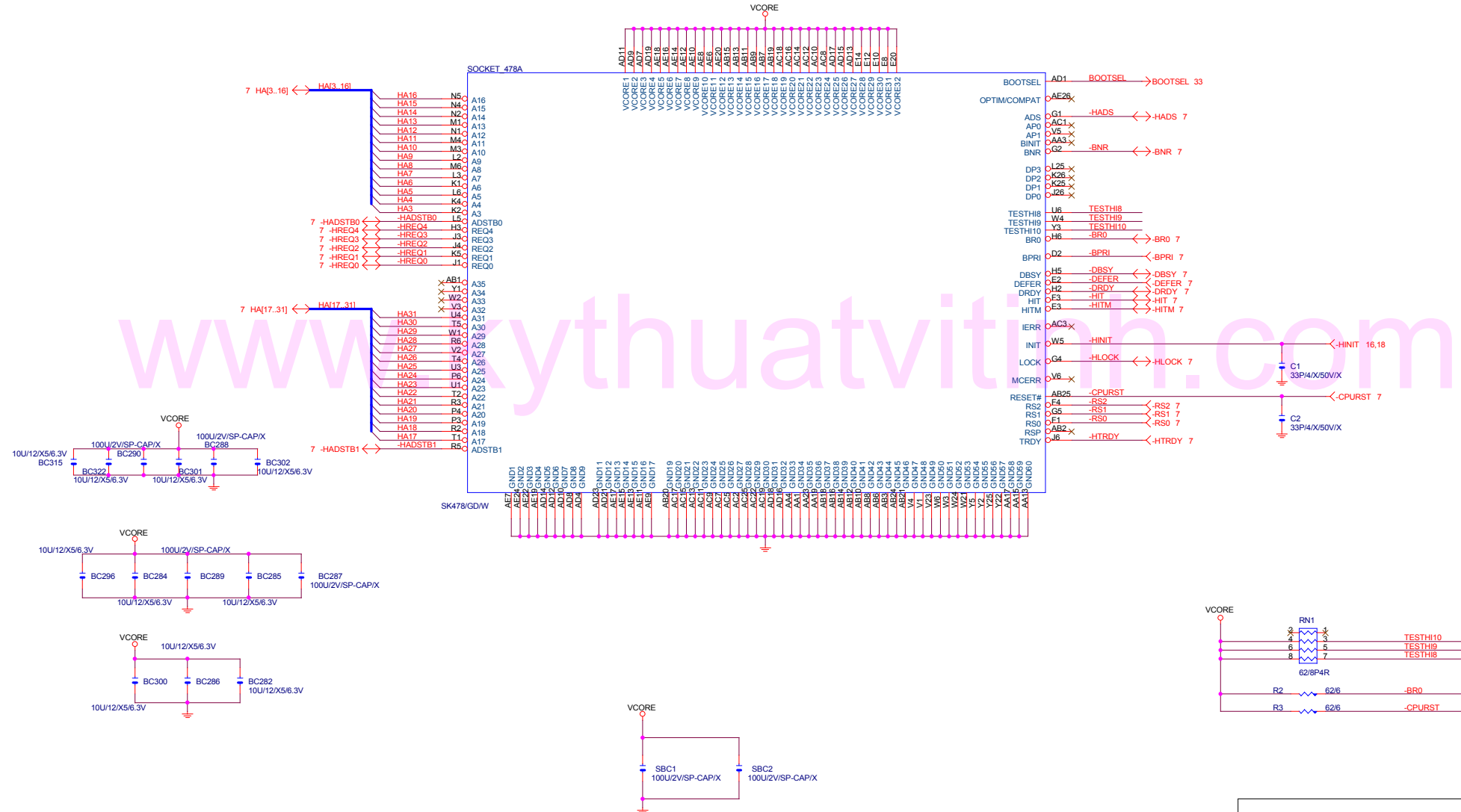
Title: COVER SHEET

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BLOCK DIAGRAM

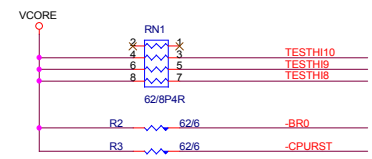
PCB SIZE: 305X230 mm.
60 Ohm +/- 15% C



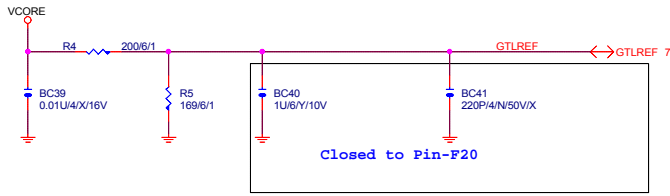


SP-CAP X 4 PCS
CPU SCKET

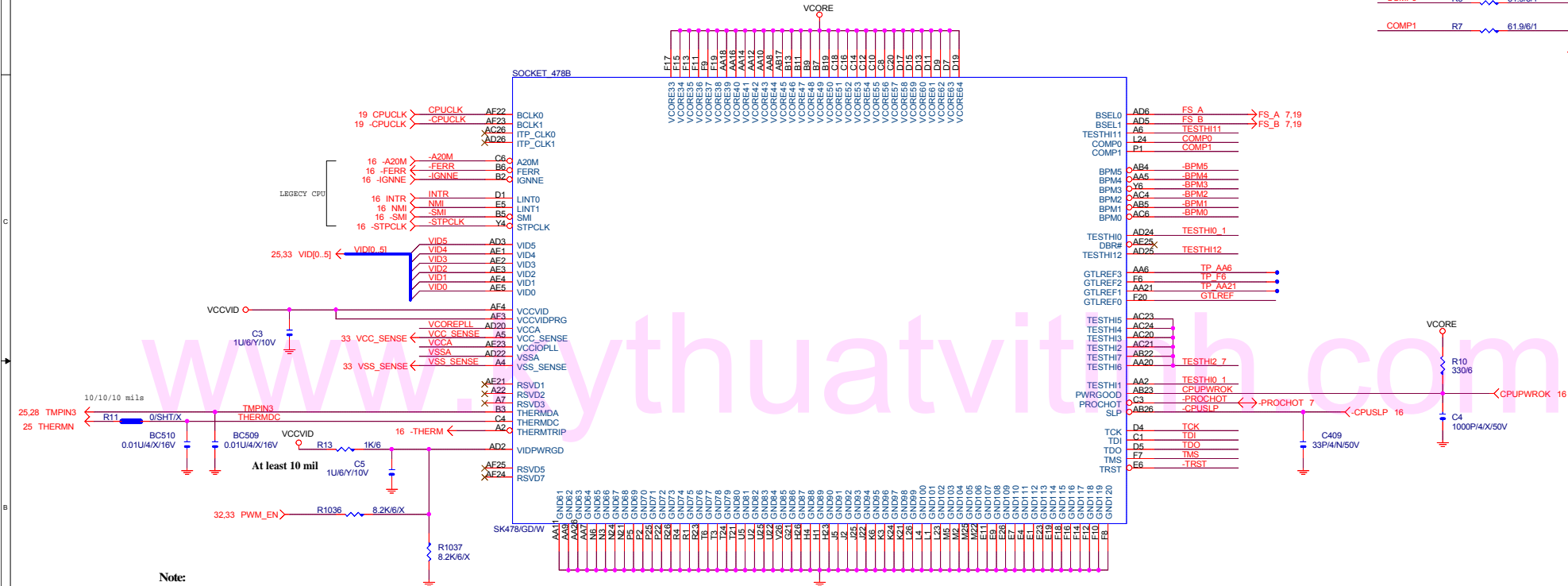
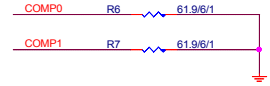
CAP place at (D14,D15) solder side



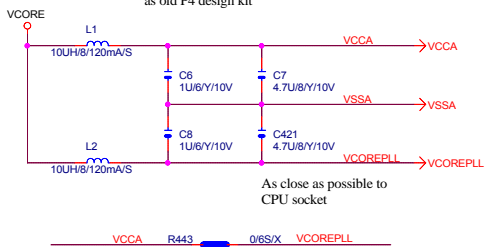
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P4 478A		
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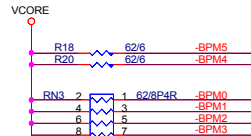
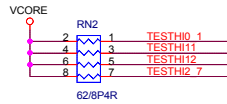
Place outside of CPU socket



Note:
VCCA & VCOREPLL define doesn't same as old P4 design kit

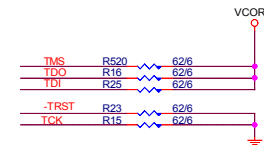


Trace width doesn't less than 12 Mil



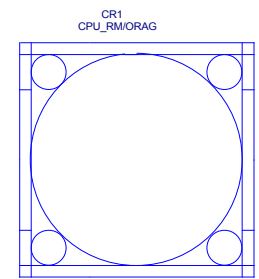
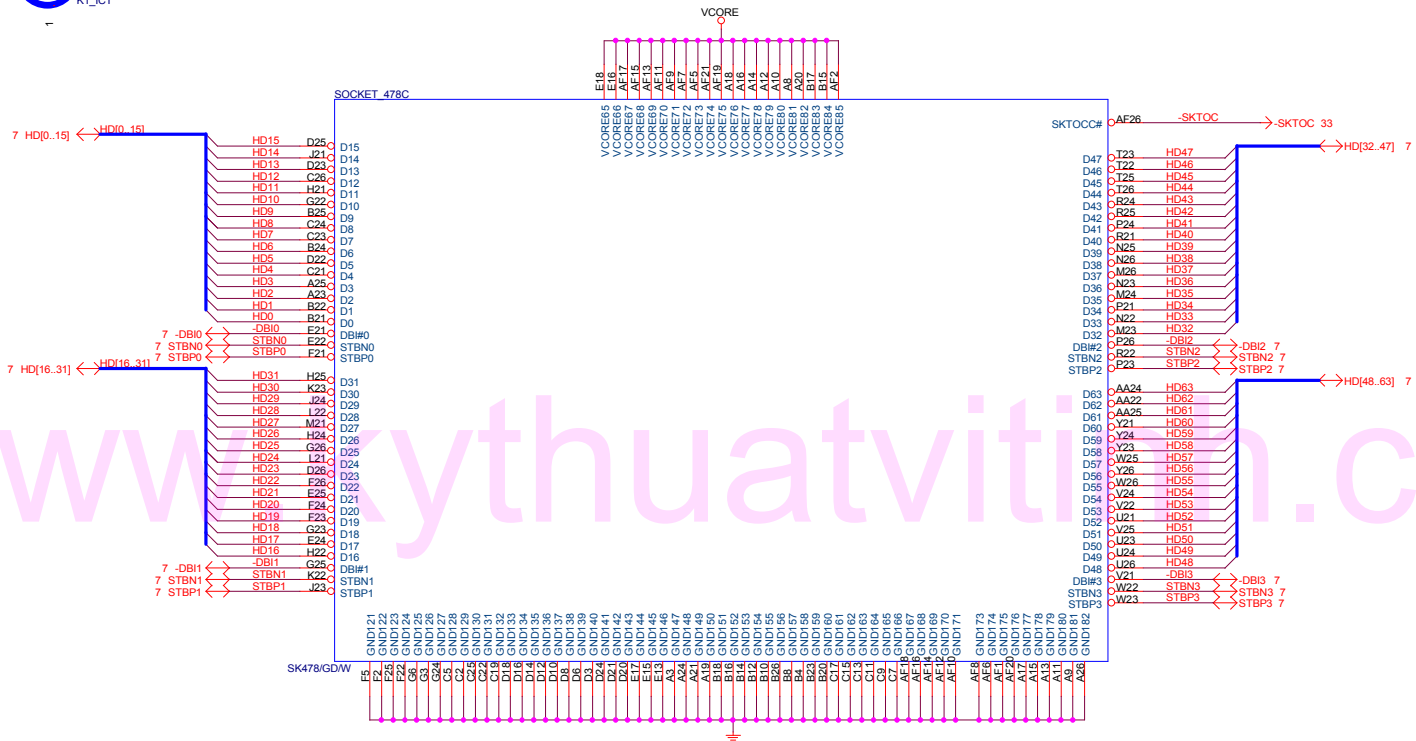
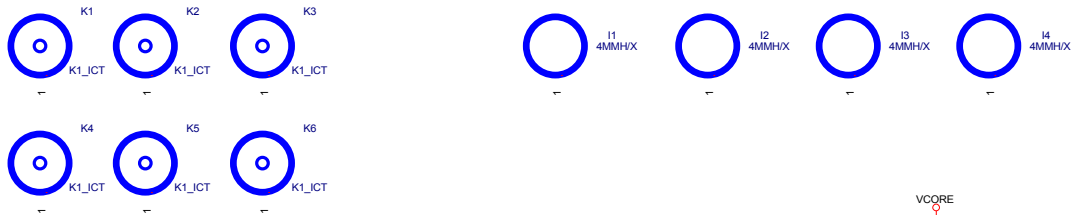
Close to CPU

Close to CPU

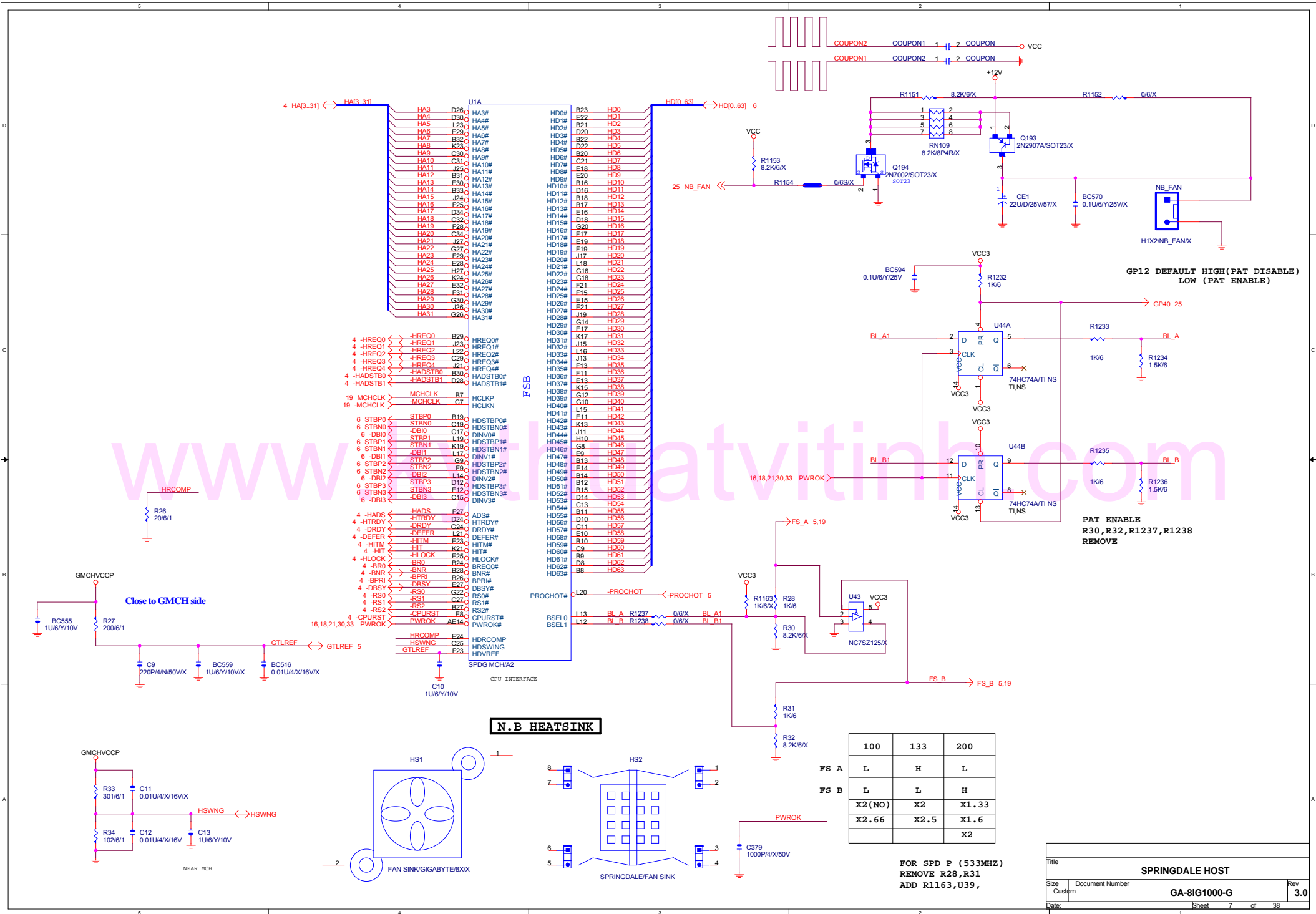


Pull up must place end of route

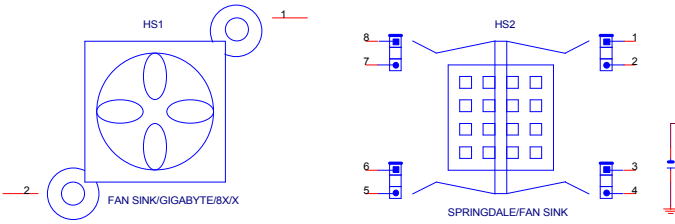
Title		
P4 478B		
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Title			P4 478C		
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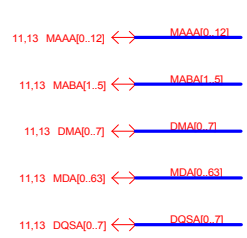
N.B HEATSINK



	100	133	200
FS_A	L	H	L
FS_B	L	L	H
X2 (NO)	X2	X2	X1.33
	X2.66	X2.5	X1.6
			X2

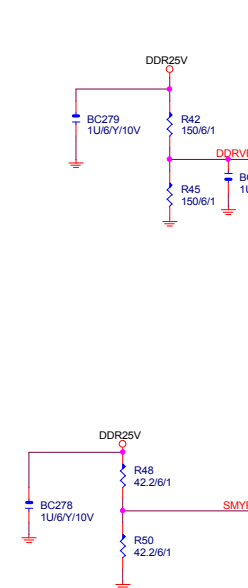
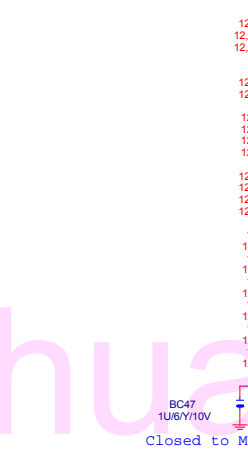
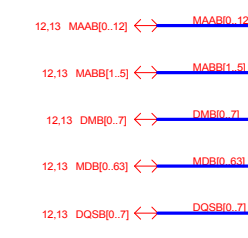
FOR SPD P (533MHZ)
REMOVE R28, R31
ADD R1163, U39,

DDR 400 1-2-3-4 CPC DISABLE
 DDR 333 SIGNAL 1 CPC ENABLE
 2 CPC DISABLE
 DUAL 2 CPC ENABLE
 DUAL 4 CPC DISABLE



U1B		U1C	
MAAA0 A134	SMAA_A0	MAAB0 AG31	SMAA_B0
MAAA1 A133	SMAA_A1	MAAB1 AJ31	SMAA_B1
MAAA2 AK29	SMAA_A2	MAAB2 AD27	SMAA_B2
MAAA3 AN31	SMAA_A3	MAAB3 AE24	SMAA_B3
MAAA4 AL30	SMAA_A4	MAAB4 AK27	SMAA_B4
MAAA5 AL26	SMAA_A5	MAAB5 AG25	SMAA_B5
MAAA6 AL28	SMAA_A6	MAAB6 AL25	SMAA_B6
MAAA7 AN25	SMAA_A7	MAAB7 AF21	SMAA_B7
MAAA8 AP26	SMAA_A8	MAAB8 AL23	SMAA_B8
MAAA9 AP24	SMAA_A9	MAAB9 AJ22	SMAA_B9
MAAA10 AJ33	SMAA_A10	MAAB10 AE26	SMAA_B10
MAAA11 AN23	SMAA_A11	MAAB11 AL21	SMAA_B11
MAAA12 AN21	SMAA_A12	MAAB12 AJ20	SMAA_B12
MABA1 AL34	SMAB_A1	MABB1 AE27	SMAB_B1
MABA2 AM34	SMAB_A2	MABB2 AD26	SMAB_B2
MABA3 AP32	SMAB_A3	MABB3 AL29	SMAB_B3
MABA4 AP31	SMAB_A4	MABB4 AL27	SMAB_B4
MABA5 AM26	SMAB_A5	MABB5 AE23	SMAB_B5
SWEA# AB34	SWE_A#	SWEB# W27C	SWE_B#
SCASA Y34	SCAS_A#	SCASB W31	SCAS_B#
SRASA AC33	SRA5_A#	SRASB W26C	SRA5_B#
SBA0 AE33	SBA_A0	SBAB0 Y25	SBA_B0
SBA1 AH34	SBA_A1	SBAB1 AA25	SBA_B1
CSA0 AA34	SCS_A0#	CSB0 U26	SCS_B0#
CSA1 Y32	SCS_A1#	CSB1 T28	SCS_B1#
CSA2 Y32	SCS_A2#	CSB2 V25	SCS_B2#
CSA3 W34	SCS_A3#	CSB3 W25	SCS_B3#
CKEA0 AL20	SCKE_A0	CKEB0 AK19	SCKE_B0
CKEA1 AN19	SCKE_A1	CKEB1 AF19	SCKE_B1
CKEA2 AM20	SCKE_A2	CKEB2 AG19	SCKE_B2
CKEA3 AP20	SCKE_A3	CKEB3 AE18	SCKE_B3
DCLKA0 AK32	SCMDCLK_A0	DCLKB0 AG29	SCMDCLK_B0
DCLKA1 AK31	SCMDCLK_A0#	DCLKB1 AG30	SCMDCLK_B0#
DCLKA2 AP17	SCMDCLK_A1	DCLKB2 N27	SCMDCLK_B1
DCLKA3 AN17	SCMDCLK_A1#	DCLKB3 N26	SCMDCLK_B1#
DCLKA4 N34	SCMDCLK_A2	DCLKB4 AK15	SCMDCLK_B2
DCLKA5 N34	SCMDCLK_A2#	DCLKB5 N11	SCMDCLK_B2#
DCLKA6 AK33	SCMDCLK_A3	DCLKB6 N30	SCMDCLK_B3
DCLKA7 AK34	SCMDCLK_A3#		
DCLKA8 AM16	SCMDCLK_A4		
DCLKA9 AL16	SCMDCLK_A4#		
DCLKA10 P31	SCMDCLK_A5		
DCLKA11 P32	SCMDCLK_A5#		
DDRREFA E34	SMVREF_A	DDRREFB AP9	SMVREF_B
SMXRCOMP AK9	SMXRCOMP	SMYRCOMP AA33	SMYRCOMP
SMXRCOMPVOH AN9	SMXRCOMPVOH	SMYRCOMPVOH R34	SMYRCOMPVOH
SMXRCOMPVOL AL9	SMXRCOMPVOL	SMYRCOMPVOL R33	SMYRCOMPVOL
V34 DQSA5	SDQS_A5		
W33 DMA5	SDM_A5		
AC34 MDA40	SDQ_A40		
AB31 MDA41	SDQ_A41		
V32 MDA42	SDQ_A42		
V31 MDA43	SDQ_A43		
AD31 MDA44	SDQ_A44		
AB32 MDA45	SDQ_A45		
U34 MDA46	SDQ_A46		
U33 MDA47	SDQ_A47		
M32 DQSA6	SDQS_A6		
M34 DMA6	SDM_A6		
T34 MDA48	SDQ_A48		
T32 MDA49	SDQ_A49		
K34 MDA50	SDQ_A50		
K32 MDA51	SDQ_A51		
T31 MDA52	SDQ_A52		
P34 MDA53	SDQ_A53		
L34 MDA54	SDQ_A54		
L33 MDA55	SDQ_A55		
H31 DQSA7	SDQS_A7		
H32 DMA7	SDM_A7		
J33 MDA56	SDQ_A56		
H34 MDA57	SDQ_A57		
E33 MDA58	SDQ_A58		
F33 MDA59	SDQ_A59		
K31 MDA60	SDQ_A60		
J34 MDA61	SDQ_A61		
G34 MDA62	SDQ_A62		
F34 MDA63	SDQ_A63		

DDR Channel A		DDR Channel B	
SDQS_A0 AN11 DQSA0	SDM_A0	SDQS_B0 AE15 DQSB0	SDM_B0
SDM_A0 AP10 MDA0	SDQ_A0	SDM_B0 AE10 MDB0	SDQ_B0
SDQ_A0 AP11 MDA1	SDQ_A1	SDQ_B0 AE15 MDB1	SDQ_B1
SDQ_A1 AN13 MDA3	SDQ_A2	SDQ_B1 AE16 MDB2	SDQ_B1
SDQ_A2 AM10 MDA4	SDQ_A3	SDQ_B2 AE18 MDB4	SDQ_B2
SDQ_A3 AL10 MDA5	SDQ_A4	SDQ_B4 AE12 MDB5	SDQ_B4
SDQ_A4 AP13 MDA7	SDQ_A5	SDQ_B5 AK11 MDB6	SDQ_B5
SDQ_A5 AP13 MDA7	SDQ_A6	SDQ_B6 AG12 MDB7	SDQ_B6
SDQS_A1 AP15 DQSA1	SDM_A1	SDQS_B1 AG13 DQSB1	SDM_B1
SDM_A1 AP16 DMA1	SDQ_A8	SDQ_B1 AG15 MDB1	SDQ_B1
SDQ_A8 AP14 MDA8	SDQ_A9	SDQ_B8 AE17 MDB8	SDQ_B8
SDQ_A9 AL18 MDA10	SDQ_A10	SDQ_B9 AL13 MDB9	SDQ_B9
SDQ_A10 AL18 MDA11	SDQ_A11	SDQ_B10 AK17 MDB10	SDQ_B10
SDQ_A11 AL14 MDA12	SDQ_A12	SDQ_B11 AL17 MDB10	SDQ_B11
SDQ_A12 AN15 MDA13	SDQ_A13	SDQ_B12 AK13 MDB12	SDQ_B12
SDQ_A13 AM18 MDA14	SDQ_A14	SDQ_B13 AJ14 MDB13	SDQ_B13
SDQ_A14 AM18 MDA15	SDQ_A15	SDQ_B14 AJ16 MDB14	SDQ_B14
SDQS_A2 AP23 DQSA2	SDM_A2	SDQ_B15 AJ18 MDB15	SDQ_B15
SDM_A2 AP24 DMA2	SDQ_A16	SDQS_B2 AG21 DQSB2	SDM_B2
SDQ_A16 AP22 MDA16	SDQ_A17	SDQ_B2 AE21 MDB2	SDQ_B2
SDQ_A17 AM22 MDA17	SDQ_A18	SDQ_B16 AE19 MDB16	SDQ_B16
SDQ_A18 AL24 MDA18	SDQ_A19	SDQ_B17 AE20 MDB17	SDQ_B17
SDQ_A19 AN27 MDA19	SDQ_A20	SDQ_B18 AG23 MDB18	SDQ_B18
SDQ_A20 AP21 MDA20	SDQ_A21	SDQ_B19 AK23 MDB19	SDQ_B19
SDQ_A21 AL22 MDA21	SDQ_A22	SDQ_B20 AL19 MDB20	SDQ_B20
SDQ_A22 AP25 MDA22	SDQ_A23	SDQ_B21 AK21 MDB21	SDQ_B21
SDQ_A23 AP27 MDA23	SDQS_A3 AM30 DQSA3	SDQ_B22 AJ24 MDB22	SDQ_B22
SDM_A3 AP30 DMA3	SDM_A3	SDQ_B23 AE22 MDB23	SDQ_B23
SDQ_A24 AP28 MDA24	SDQ_A25	SDQS_B3 AH27 DQSB3	SDM_B3
SDQ_A25 AP29 MDA25	SDQ_A26	SDQ_B3 AJ28 MDB3	SDQ_B3
SDQ_A26 AP33 MDA26	SDQ_A27	SDQ_B24 AK25 MDB24	SDQ_B24
SDQ_A27 AM33 MDA27	SDQ_A28	SDQ_B25 AH26 MDB25	SDQ_B25
SDQ_A28 AM28 MDA28	SDQ_A29	SDQ_B26 AG27 MDB26	SDQ_B26
SDQ_A29 AM29 MDA29	SDQ_A30	SDQ_B27 AE27 MDB27	SDQ_B27
SDQ_A30 AM31 MDA30	SDQ_A31	SDQ_B28 AJ26 MDB28	SDQ_B28
SDQ_A31 AN34 MDA31	SDQS_A4 AF34 DQSA4	SDQ_B29 AD25 MDB30	SDQ_B29
SDM_A4 AF31 DMA4	SDM_A4	SDQ_B30 AF28 MDB31	SDQ_B30
SDQ_A32 AH32 MDA32	SDQ_A33	SDQS_B4 AD29 DQSB4	SDM_B4
SDQ_A33 AS34 MDA33	SDQ_A34	SDM_B4 AC31 DMB4	SDQ_B4
SDQ_A34 AF32 MDA34	SDQ_A35	SDQ_B4 AE30 MDB32	SDQ_B4
SDQ_A35 AD32 MDA35	SDQ_A36	SDQ_B33 AC27 MDB33	SDQ_B33
SDQ_A36 AH31 MDA36	SDQ_A37	SDQ_B34 AC30 MDB34	SDQ_B34
SDQ_A37 AC33 MDA37	SDQ_A38	SDQ_B35 Y29 MDB35	SDQ_B35
SDQ_A38 AD34 MDA38	SDQ_A39	SDQ_B36 AE31 MDB36	SDQ_B36
SDQ_A39 AD34 MDA39	SDQS_A5 V34 DQSA5	SDQ_B37 AB29 MDB37	SDQ_B37
SDM_A5 W33 DMA5	SDM_A5	SDQ_B38 AA26 MDB38	SDQ_B38
SDQ_A40 AC34 MDA40	SDQ_A41	SDQ_B39 AA27 MDB39	SDQ_B39
SDQ_A41 AB31 MDA41	SDQ_A42	SDQS_B5 U30 DQSB5	SDM_B5
SDQ_A42 V32 MDA42	SDQ_A43	SDM_B5 U31 DMB5	SDQ_B5
SDQ_A43 V31 MDA43	SDQ_A44	SDQ_B40 W30 MDB41	SDQ_B40
SDQ_A44 AD31 MDA44	SDQ_A45	SDQ_B41 U27 MDB42	SDQ_B41
SDQ_A45 U34 MDA46	SDQ_A46	SDQ_B42 T26 MDB43	SDQ_B42
SDQ_A47 U33 MDA47	SDQS_A6 M32 DQSA6	SDQ_B43 AA31 MDB44	SDQ_B43
SDM_A6 M34 DMA6	SDM_A6	SDQ_B44 Y29 MDB45	SDQ_B44
SDQ_A48 T34 MDA48	SDQ_A49	SDQ_B45 U25 MDB46	SDQ_B45
SDQ_A49 K34 MDA50	SDQ_A50	SDQ_B46 R27 MDB47	SDQ_B46
SDQ_A50 K32 MDA51	SDQ_A51	SDQS_B6 L27 DQSB6	SDM_B6
SDQ_A51 T31 MDA52	SDQ_A52	SDM_B6 M29 DMB6	SDQ_B6
SDQ_A52 P34 MDA53	SDQ_A53	SDQ_B48 P29 MDB48	SDQ_B48
SDQ_A53 L34 MDA54	SDQ_A54	SDQ_B49 R30 MDB49	SDQ_B49
SDQ_A55 L33 MDA55	SDQS_A7 H31 DQSA7	SDQ_B50 K28 MDB50	SDQ_B50
SDM_A7 H32 DMA7	SDM_A7	SDQ_B51 L30 MDB51	SDQ_B51
SDQ_A56 J33 MDA56	SDQ_A57	SDQ_B52 R26 MDB53	SDQ_B52
SDQ_A57 H34 MDA57	SDQ_A58	SDQ_B53 P25 MDB54	SDQ_B53
SDQ_A58 E33 MDA58	SDQ_A59	SDQ_B54 L32 MDB55	SDQ_B54
SDQ_A59 F33 MDA59	SDQ_A60	SDQS_B7 J30 DQSB7	SDM_B7
SDQ_A60 K31 MDA60	SDQ_A61	SDM_B7 J31 DMB7	SDQ_B7
SDQ_A61 J34 MDA61	SDQ_A62	SDQ_B56 K30 MDB56	SDQ_B56
SDQ_A62 G34 MDA62	SDQ_A63	SDQ_B57 H29 MDB57	SDQ_B57
SDQ_A63 F34 MDA63		SDQ_B58 C33 MDB58	SDQ_B58
		SDQ_B59 N25 MDB60	SDQ_B59
		SDQ_B60 M25 MDB61	SDQ_B60
		SDQ_B61 J28 MDB62	SDQ_B61
		SDQ_B62 G32 MDB63	SDQ_B62
			SDQ_B63



DDR Channel A		DDR Channel B	
SDQS_B0 AE15 DQSB0	SDM_B0	SDQS_B1 AG13 DQSB1	SDM_B1
SDM_B0 AE10 MDB0	SDQ_B0	SDQ_B1 AG15 MDB1	SDQ_B1
SDQ_B0 AE15 MDB1	SDQ_B1	SDQ_B8 AE17 MDB8	SDQ_B8
SDQ_B1 AE16 MDB2	SDQ_B2	SDQ_B9 AL13 MDB9	SDQ_B9
SDQ_B2 AE18 MDB4	SDQ_B3	SDQ_B10 AK17 MDB10	SDQ_B10
SDQ_B4 AE12 MDB5	SDQ_B4	SDQ_B11 AL17 MDB10	SDQ_B11
SDQ_B5 AK11 MDB6	SDQ_B5	SDQ_B12 AK13 MDB12	SDQ_B12
SDQ_B6 AG12 MDB7	SDQS_B1 AG13 DQSB1	SDQ_B13 AJ14 MDB13	SDQ_B13
SDQS_B1 AG15 MDB1	SDM_B1	SDQ_B14 AJ16 MDB14	SDQ_B14
SDQ_B8 AE17 MDB8	SDQ_B9	SDQ_B15 AJ18 MDB15	SDQ_B15
SDQ_B9 AL13 MDB9	SDQ_B10	SDQS_B2 AG21 DQSB2	SDM_B2
SDQ_B10 AK17 MDB10	SDQ_B11	SDQ_B2 AE21 MDB2	SDQ_B2
SDQ_B11 AL17 MDB10	SDQ_B12	SDQ_B16 AE19 MDB16	SDQ_B16
SDQ_B12 AK13 MDB12	SDQ_B13	SDQ_B17 AE20 MDB17	SDQ_B17
SDQ_B13 AJ14 MDB13	SDQ_B14	SDQ_B18 AG23 MDB18	SDQ_B18
SDQ_B14 AJ16 MDB14	SDQ_B15	SDQ_B19 AK23 MDB19	SDQ_B19
SDQ_B15 AJ18 MDB15	SDQS_B2 AG21 DQSB2	SDQ_B20 AL19 MDB20	SDQ_B20
SDQS_B2 AG21 DQSB2	SDM_B2	SDQ_B21 AK21 MDB21	SDQ_B21
SDQ_B2 AE21 MDB2	SDQ_B26	SDQ_B22 AJ24 MDB22	SDQ_B22
SDQ_B16 AE19 MDB16	SDQ_B27	SDQ_B23 AE22 MDB23	SDQ_B23
SDQ_B17 AE20 MDB17	SDQ_B28	SDQS_B3 AH27 DQSB3	SDM_B3
SDQ_B18 AG23 MDB18	SDQ_B29	SDQ_B3 AJ28 MDB3	SDQ_B3
SDQ_B19 AK23 MDB19	SDQ_B30	SDQ_B24 AK25 MDB24	SDQ_B24
SDQ_B20 AL19 MDB20	SDQ_B31	SDQ_B25 AH26 MDB25	SDQ_B25
SDQ_B21 AK21 MDB21	SDQS_B4 AD29 DQSB4	SDQ_B26 AG27 MDB26	SDQ_B26
SDQ_B22 AJ24 MDB22	SDM_B4	SDQ_B27 AE27 MDB27	SDQ_B27
SDQ_B23 AE22 MDB23	SDQ_B40	SDQ_B28 AJ26 MDB28	SDQ_B28
SDQS_B3 AH27 DQSB3	SDQ_B41	SDQ_B29 AD25 MDB30	SDQ_B29
SDM_B3 AJ28 MDB3	SDQS_B4 AD29 DQSB4	SDQ_B30 AF28 MDB31	SDQ_B30
SDQ_B4 AK25 MDB24	SDM_B4	SDQ_B31 AE28 MDB31	SDQ_B31
SDQ_B25 AH26 MDB25	SDQ_B42	SDQS_B5 U30 DQSB5	SDM_B5
SDQ_B26 AG27 MDB26	SDQ_B43	SDM_B5 U31 DMB5	SDQ_B5
SDQ_B27 AE27 MDB27	SDQ_B44	SDQ_B48 P29 MDB48	SDQ_B48
SDQ_B28 AJ26 MDB28	SDQ_B45	SDQ_B49 R30 MDB49	SDQ_B49
SDQ_B29 AD25 MDB30	SDQ_B46	SDQ_B50 K28 MDB50	SDQ_B50
SDQ_B30 AF28 MDB31	SDQS_B5 U30 DQSB5	SDQ_B51 L30 MDB51	SDQ_B51
SDQ_B31 AE28 MDB31	SDM_B5	SDQ_B52 R26 MDB53	SDQ_B52
SDQS_B4 AD29 DQSB4	SDQ_B53	SDQ_B53 P25 MDB54	SDQ_B53
SDM_B4 AC31 DMB4	SDQ_B54	SDQ_B54 L32 MDB55	SDQ_B54
SDQ_B4 AE30 MDB32	SDQS_B6 L27 DQSB6	SDQS_B7 J30 DQSB7	SDM_B7
SDQ_B33 AC27 MDB33	SDM_B6	SDM_B7 J31 DMB7	SDQ_B7
SDQ_B34 AC30 MDB34	SDQ_B48	SDQ_B56 K30 MDB56	SDQ_B56
SDQ_B35 Y29 MDB35	SDQ_B49	SDQ_B57 H29 MDB57	SDQ_B57
SDQ_B36 AE31 MDB36	SDQ_B50	SDQ_B58 C33 MDB58	SDQ_B58
SDQ_B37 AB29 MDB37	SDQ_B51	SDQ_B59 N25 MDB60	SDQ_B59
SDQ_B38 AA26 MDB38	SDQ_B52	SDQ_B60 M25 MDB61	SDQ_B60
SDQ_B39 AA27 MDB39	SDQ_B53	SDQ_B61 J28 MDB62	SDQ_B61
SDQS_B5 U30 DQSB5	SDQ_B54	SDQ_B62 G32 MDB63	SDQ_B62

14 GAD[0..31] <-> GAD[0..31]
 14 SBA[0..7] <-> SBA[0..7]
 15 HL[0..10] <-> HL[0..10]

AGP

14 -GCBE0 <-> -GCBE0 Y7
 14 -GCBE1 <-> -GCBE1 W5
 14 -GCBE2 <-> -GCBE2 AA3
 14 -GCBE3 <-> -GCBE3 U2

14 -GFRAME <-> -GFRAME U6
 19 GMCH3V66 <-> GMCH3V66 M4
 14 -GDEVSEL <-> -GDEVSEL AB4
 14 -GIRDY <-> -GIRDY V11
 14 -GTRDY <-> -GTRDY AB5
 14 -GSTOP <-> -GSTOP W11
 14 -GPAR <-> -GPAR AB2
 14 -GREQ <-> -GREQ N6
 14 -GGNT <-> -GGNT M7

14 GSWING <-> GSWING AC3
 14 MCH_AGPREF <-> MCH_AGPREF AD2

14 -RBF <-> -RBF R10
 14 -WBF <-> -WBF R9
 14 -PIPE <-> -PIPE M4
 14 GDBI_LO <-> GDBI_LO M5

14 ST0 <-> ST0 N3
 14 ST1 <-> ST1 N2
 14 ST2 <-> ST2 M5

HUB

HL0 <-> AF5 H10
 HL1 <-> AC3 H11
 HL2 <-> AK2 H12
 HL3 <-> AG5 H13
 HL4 <-> AK5 H14
 HL5 <-> AL3 H15
 HL6 <-> AL2 H16
 HL7 <-> AL4 H17
 HL8 <-> AJ2 H18
 HL9 <-> AJ2 H19
 HL10 <-> AJ3 H10
 HL11 <-> AJ4 H11
 HL12 <-> AJ5 H12

15 HLSTBF <-> HLSTBF AH5
 15 HLSTBS <-> HLSTBS AH4

CSA

>AK7 C10
 >AH7 C11
 >AD11 C12
 >AF7 C13
 >AD7 C14
 >AC10 C15
 >AF9 C16
 >AG7 C17
 >AE9 C18
 >AH9 C19
 >AC6 C10
 >C1STRF C1STRF
 >AJ5 C1STRS

10 CLSWING_SPG <-> CLSWING_SPG AF2
 10 CLVREF_SPG <-> CLVREF_SPG AF4

19 DOTCLK <-> DOTCLK G4
 TP GMCH AP8 <-> TP GMCH AP8 AP8
 16 -JCHSYNC <-> -JCHSYNC AJ8
 15,18,21,25,35,36 -PCIRST <-> -PCIRST AK4

TP GMCH AG10 <-> TP GMCH AG10 AG10
 TP GMCH AG9 <-> TP GMCH AG9 AG9

VGA

RED# E4
 REDP# E4
 GREEN# G5
 BLUE# H7
 BLUE# G6

REFSET D2

NC_1 A3
 NC_2 A33
 NC_3 A35
 NC_4 AF13
 NC_5 AF23
 NC_6 AJ12
 NC_7 AN1
 NC_8 AP2
 NC_9 AR33
 NC_10 AR35
 NC_11 B2
 NC_12 B25
 NC_13 B34
 NC_14 C1
 NC_15 C1
 NC_16 C23
 NC_17 C35
 NC_18 E26
 NC_19 M31
 NC_20 R25

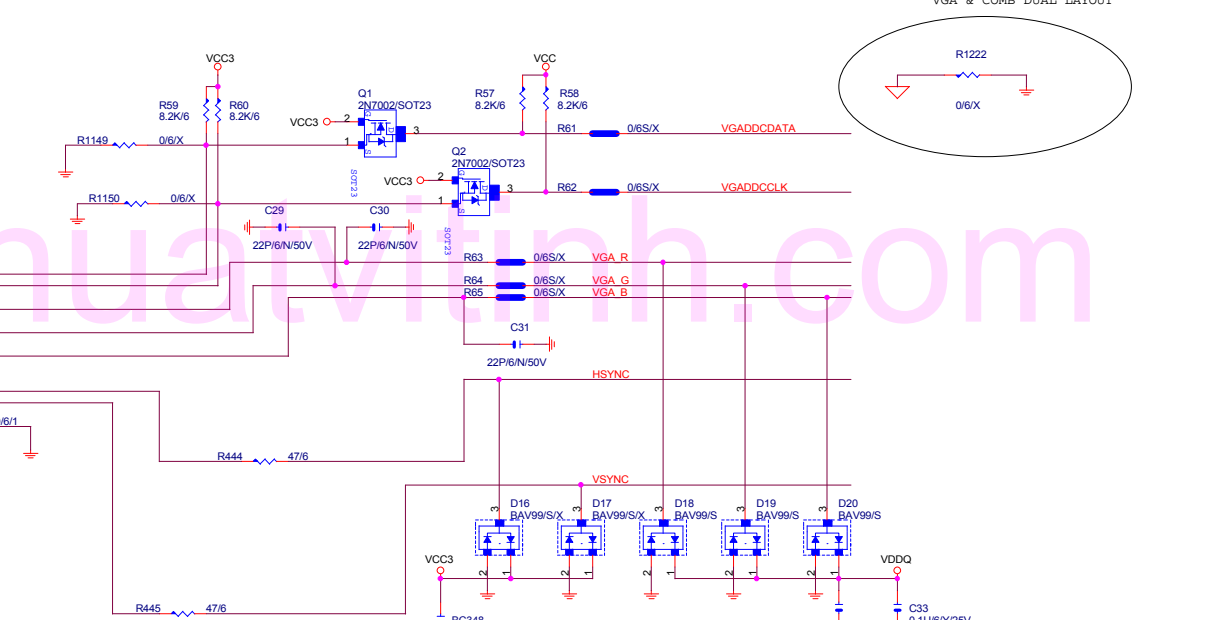
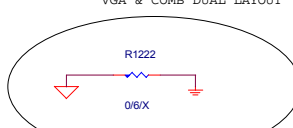
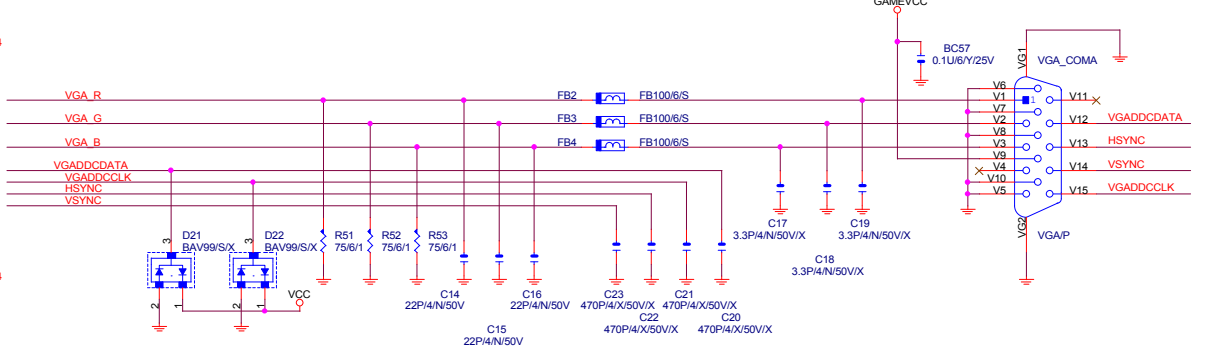
DDCA_DATA

DDCA_CLK DDCA_CLK

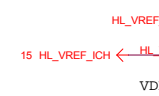
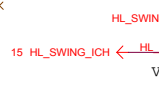
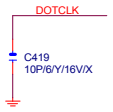
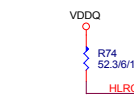
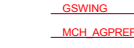
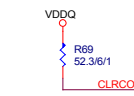
RESERVED_1 RESERVED_1
 RESERVED_2 RESERVED_2
 RESERVED_3 RESERVED_3
 RESERVED_4 RESERVED_4
 RESERVED_5 RESERVED_5

SPDG MCH/A2

SPDG MCH/A2



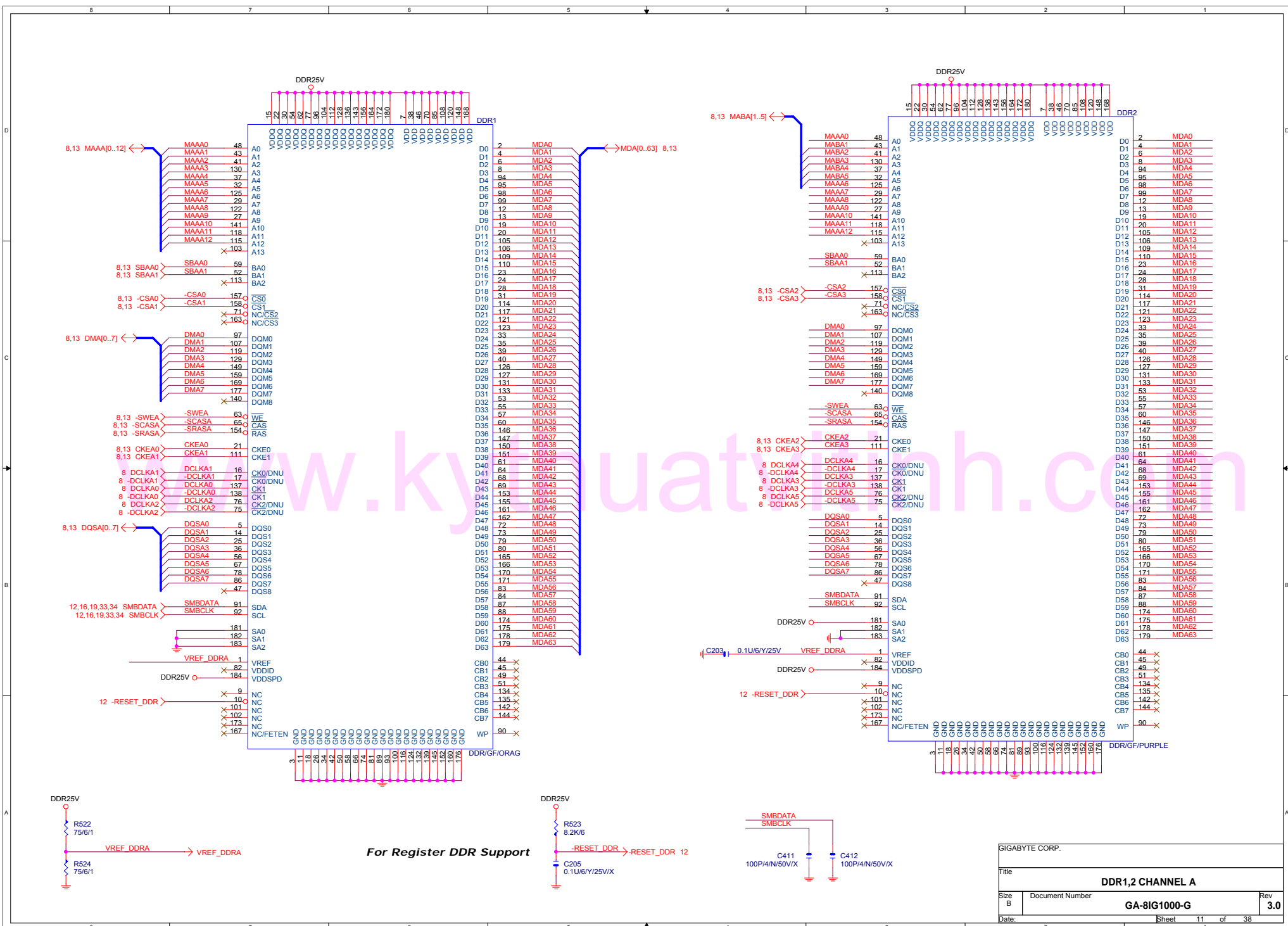
Close to MCH



Place mid of bus trace

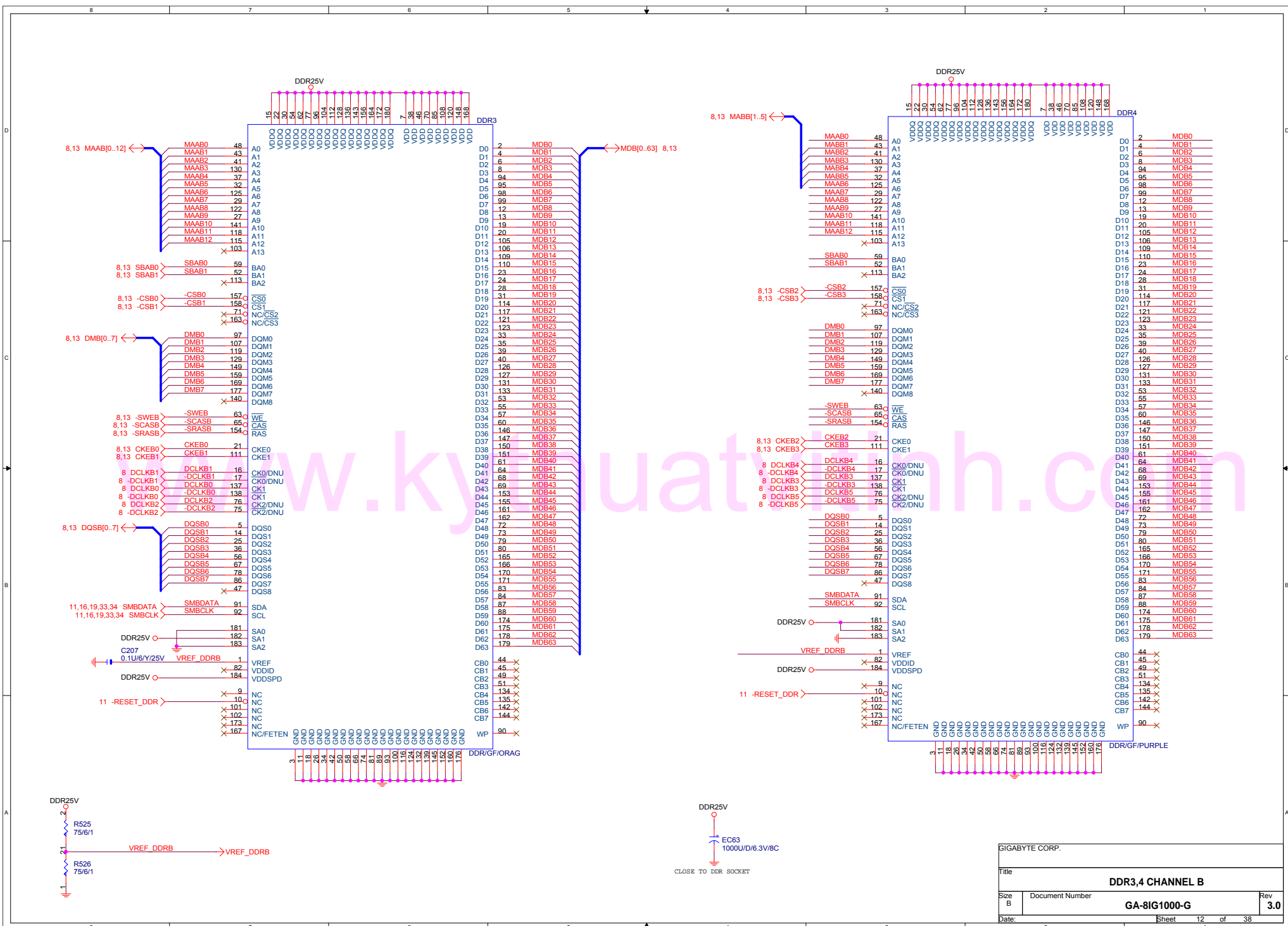
10 mil trace with 7 mil space

Title		
SPRINGDALE AGP,HUB,CSA,VGA		
Size	Document Number	Rev
Custom	GA-8IG1000-G	3.0
Date:	Sheet	9 of 38



For Register DDR Support

SIGABYTE CORP.		
Title		
DDR1,2 CHANNEL A		
Size	Document Number	Rev
B	GA-8IG1000-G	3.0
Date:	Sheet 11 of 38	



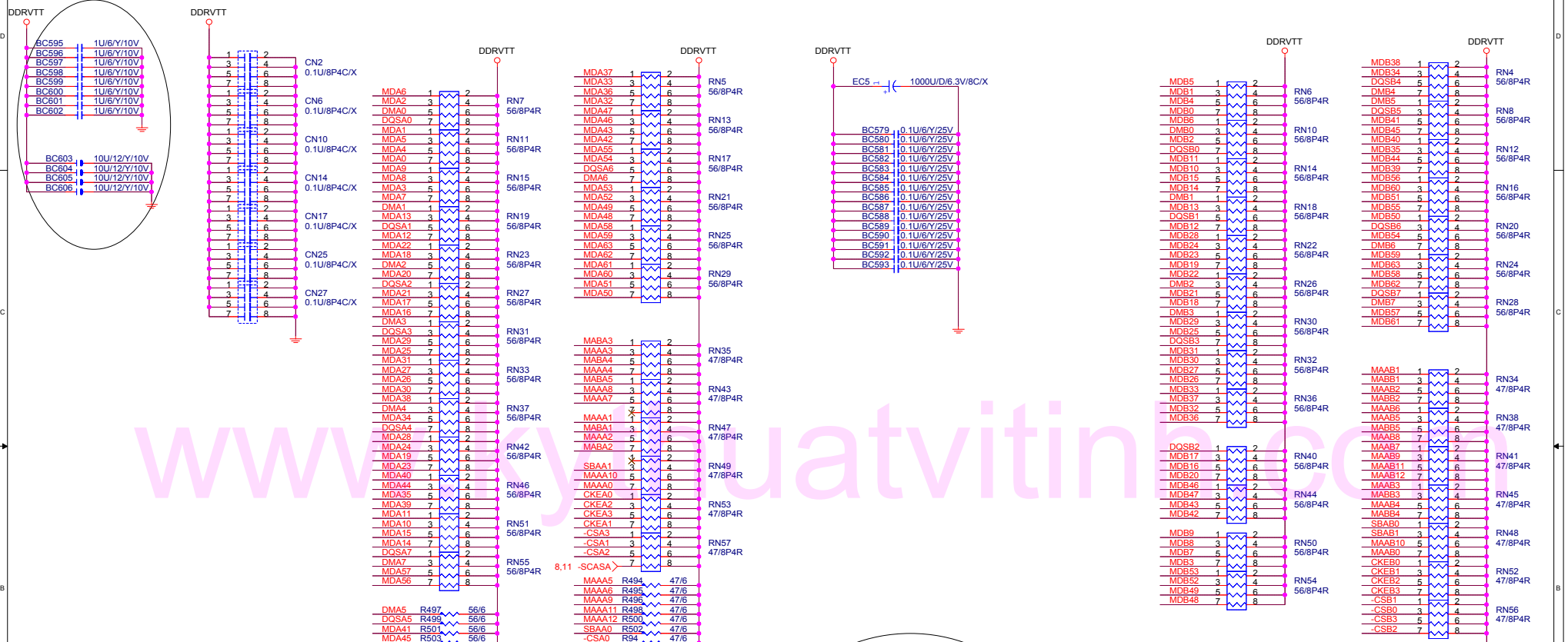
SIGABYTE CORP.		
Title		
DDR3,4 CHANNEL B		
Size	Document Number	Rev
B	GA-8IG1000-G	3.0
Date:	Sheet 12	of 38

DDRVTT Decouple

DDR TERMINATION CHANNEL A

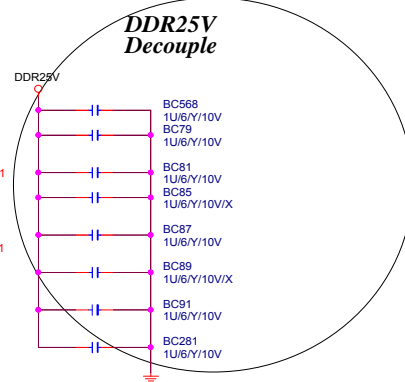
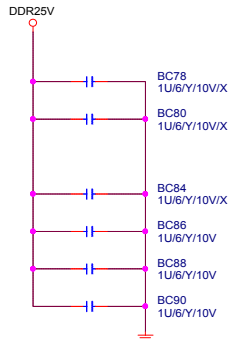
DDRVTT Decouple

CHANNEL B



DDR25V Decouple

DDR25V Decouple



- 47 Ohms**
- SBA0[0..11] <- SBA[0..1] 8,11
 - CSA[0..31] <- -CSA[0..3] 8,11
 - CKEA[0..31] <- CKEA[0..3] 8,11
 - MABA[1..51] <- MABA[1..5] 8,11
 - MAAA[0..121] <- MAAA[0..12] 8,11

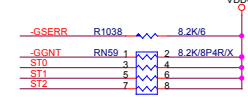
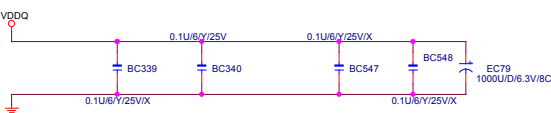
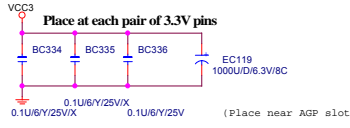
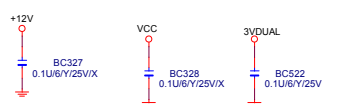
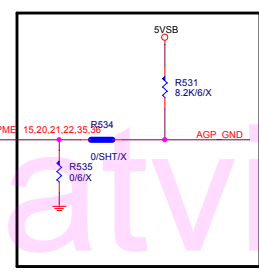
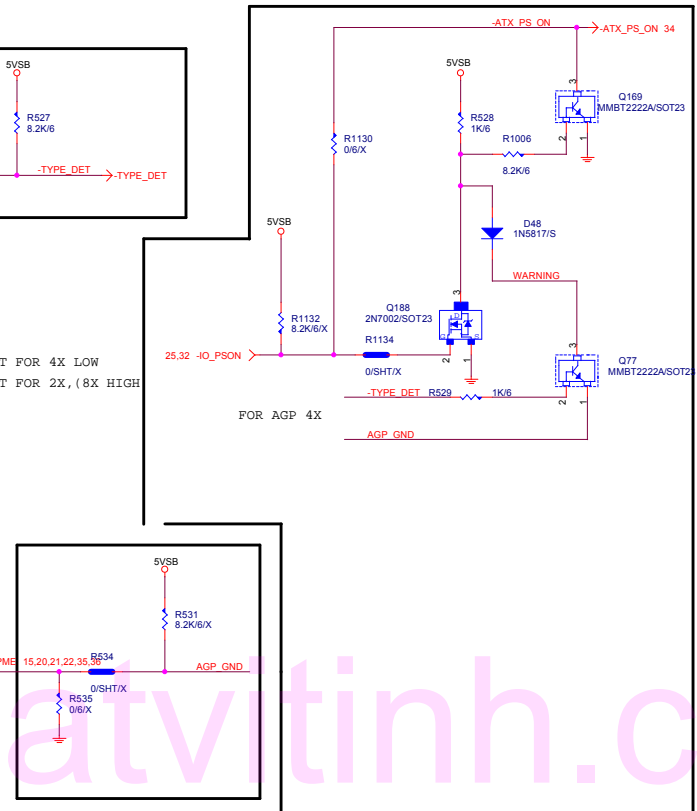
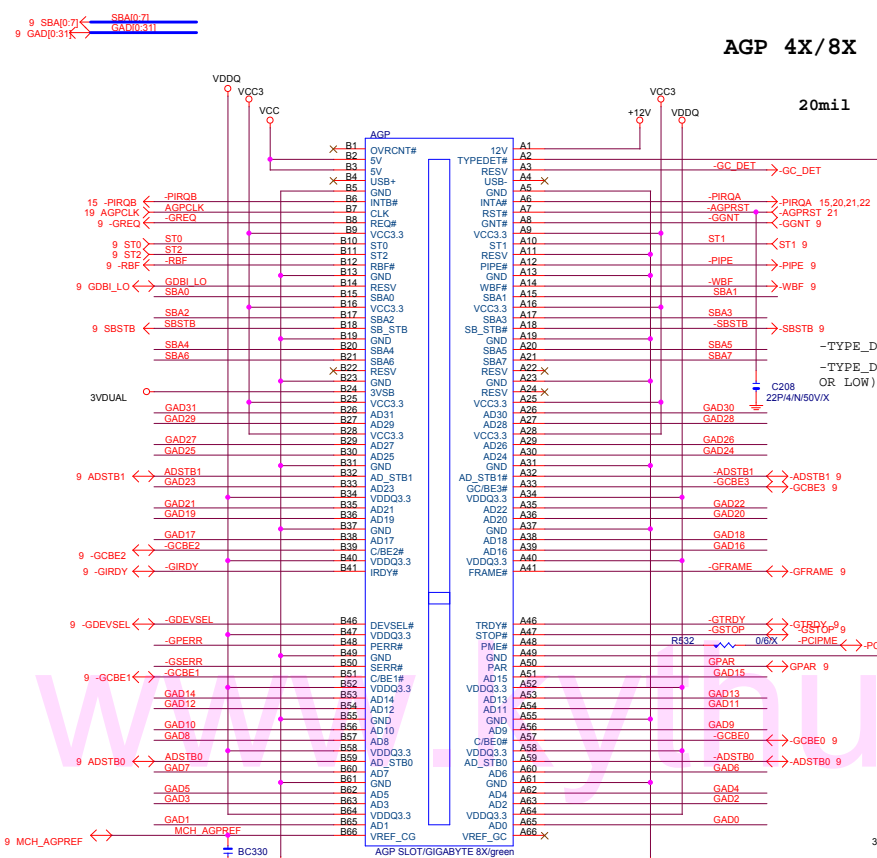
- 56 Ohms**
- DQSA[0..71] <- DQSA[0..7] 8,11
 - DMA[0..71] <- DMA[0..7] 8,11
 - MDA[0..631] <- MDA[0..63] 8,11

- 47 Ohms**
- SBAB[0..11] <- SBAB[0..1] 8,12
 - CSB[0..31] <- -CSB[0..3] 8,12
 - CKEB[0..31] <- CKEB[0..3] 8,12
 - MABB[1..51] <- MABB[1..5] 8,12
 - MAAB[0..121] <- MAAB[0..12] 8,12
- 56 Ohms**
- DQSB[0..71] <- DQSB[0..7] 8,12
 - DMB[0..71] <- DMB[0..7] 8,12
 - MDB[0..631] <- MDB[0..63] 8,12

SIGABYTE CORP.		
Title		
DDR TERMINATION		
Size B	Document Number	Rev
	GA-8IG1000-G	3.0
Date:	Sheet 13 of 38	

AGP 4X/8X

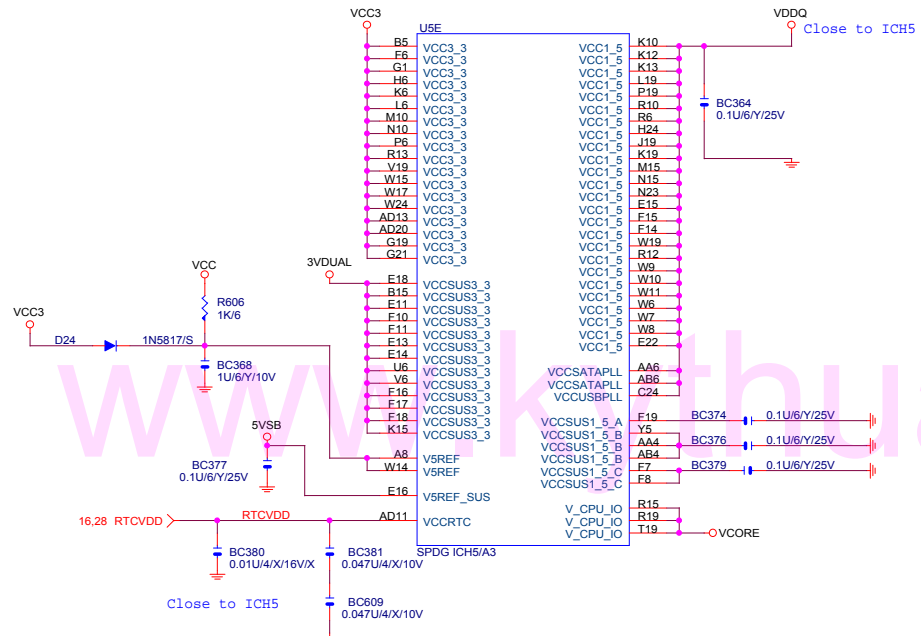
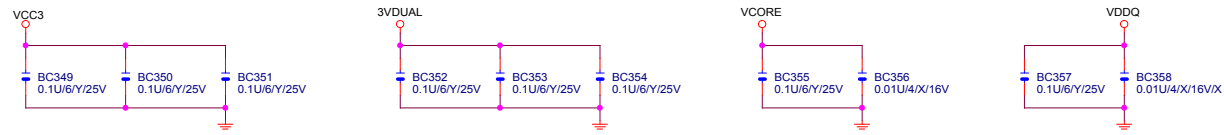
20mil

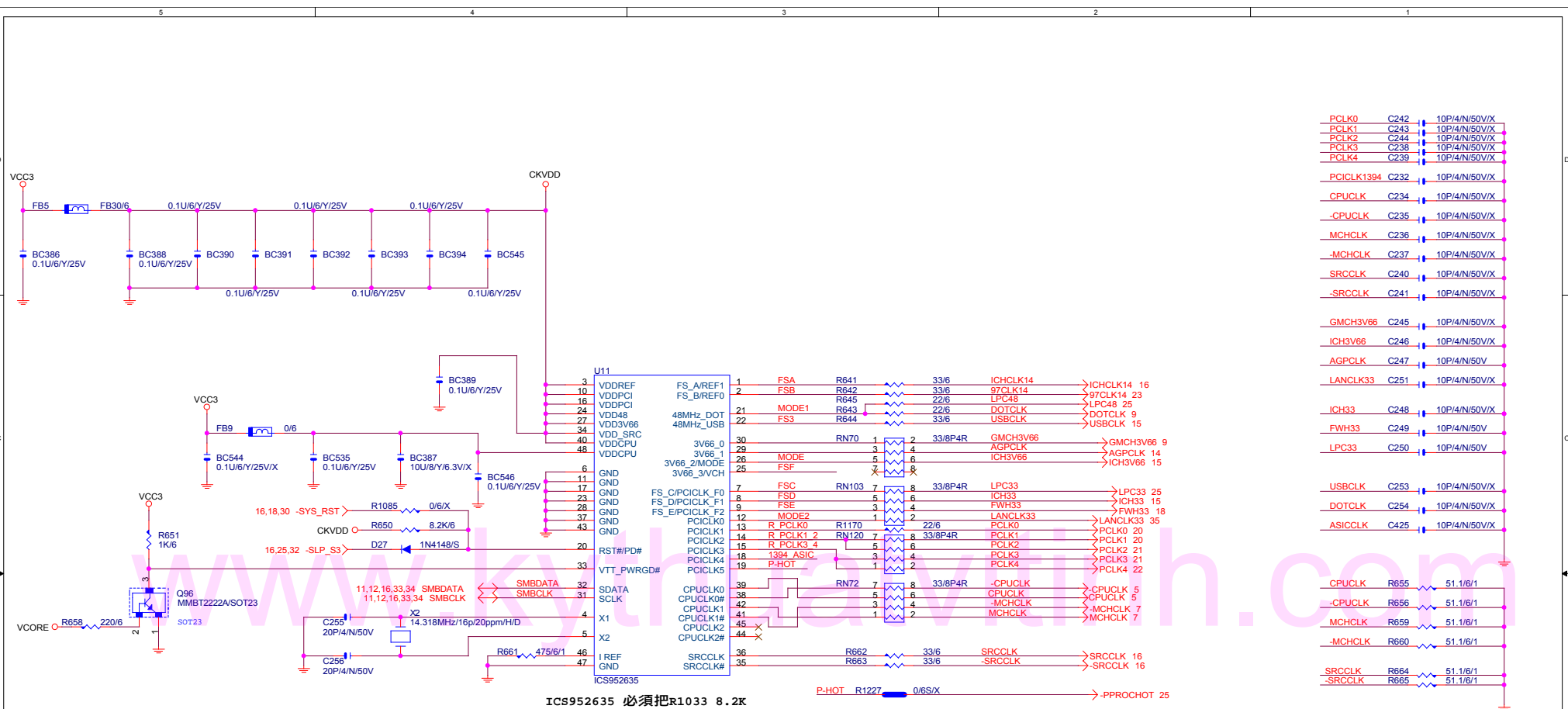


Place 1 at each pair of VDDQ pins
Place an additional for spread from A14 - A33

GIGABYTE CORP.

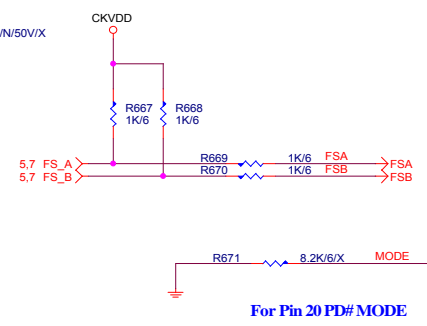
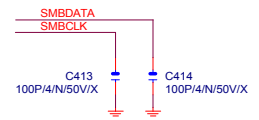
Title			AGP SLOT
Size	Document Number	Rev	3.0
Customer	GA-8IG1000-G		
Date	星期二, 二月 05, 2004	Sheet	14 of 38





- PCLK0 C242 10P/4/N/50V/X
- PCLK1 C243 10P/4/N/50V/X
- PCLK2 C244 10P/4/N/50V/X
- PCLK3 C238 10P/4/N/50V/X
- PCLK4 C239 10P/4/N/50V/X
- PCICLK1394 C232 10P/4/N/50V/X
- CPUCCLK C234 10P/4/N/50V/X
- CPUCCLK C235 10P/4/N/50V/X
- MCHCLK C236 10P/4/N/50V/X
- MCHCLK C237 10P/4/N/50V/X
- SRRCCLK C240 10P/4/N/50V/X
- SRRCCLK C241 10P/4/N/50V/X
- GMCH3V66 C245 10P/4/N/50V/X
- ICH3V66 C246 10P/4/N/50V/X
- AGPCLK C247 10P/4/N/50V
- LANCLK33 C251 10P/4/N/50V/X
- ICH33 C248 10P/4/N/50V/X
- FWH33 C249 10P/4/N/50V
- LPC33 C250 10P/4/N/50V
- USBCLK C253 10P/4/N/50V/X
- DOTCLK C254 10P/4/N/50V/X
- ASICCLK C425 10P/4/N/50V/X
- CPUCCLK R655 51.1/6/1
- CPUCCLK R656 51.1/6/1
- MCHCLK R659 51.1/6/1
- MCHCLK R660 51.1/6/1
- SRRCCLK R664 51.1/6/1
- SRRCCLK R665 51.1/6/1

ICS952635 必須把R1033 8.2K REMOVE



For Pin 20 PD# MODE

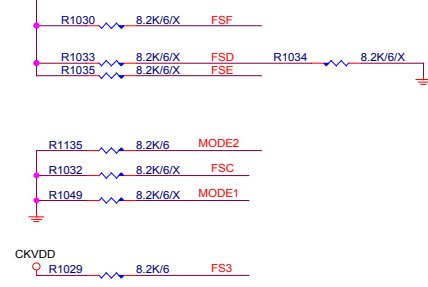
CYPRESS CY28405

FS_E	FS_D	FS_C	FS_B	FS_A	Clock
1	1	0	0	0	100.9MHz
1	1	0	0	1	133.9MHz
1	1	0	1	1	166.9MHz
1	1	0	1	0	200.9MHz

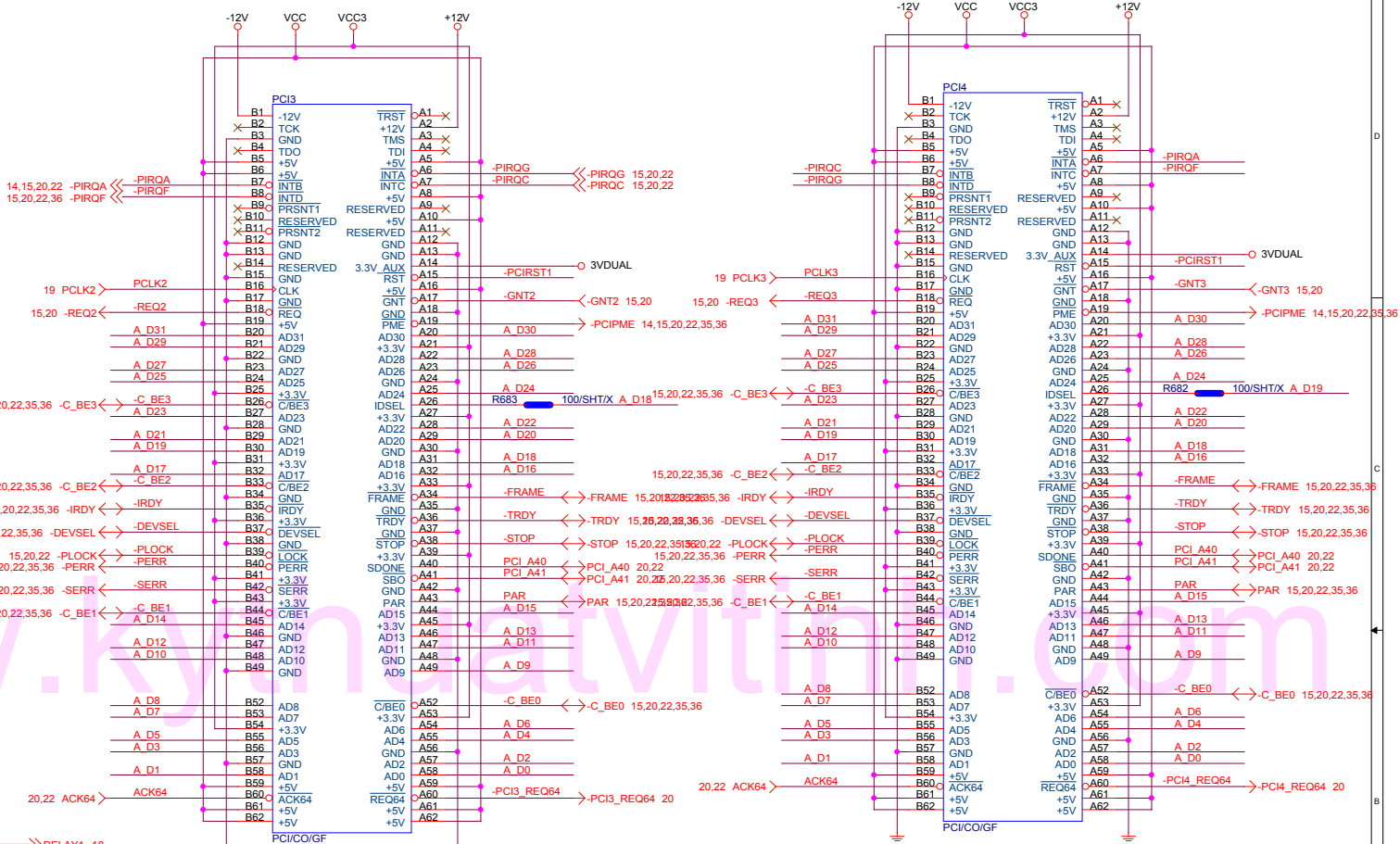
ICS952616

FS_E	FS_3	FS_C	FS_B	FS_A	Clock
1	0	0	0	0	100MHz
1	0	0	0	1	133MHz
1	0	0	1	1	166MHz
1	0	0	1	0	200MHz

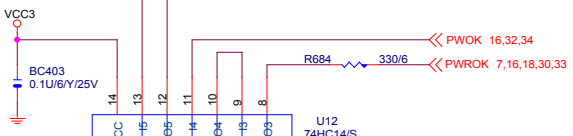
ICS + CYPRESS CO-LAYOUT



15,20,22,35,36 A_D[0..31] << A_D[0..31]



18 DUAL_BIOS_DELAY << DELAY1 18



9,15,18,25,35,36 -PCIRST << PCIRST- FROM ICH5

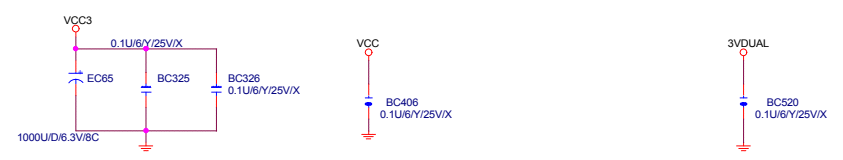
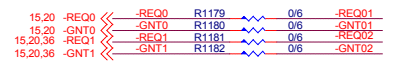
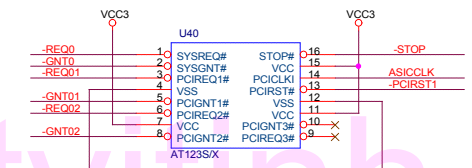
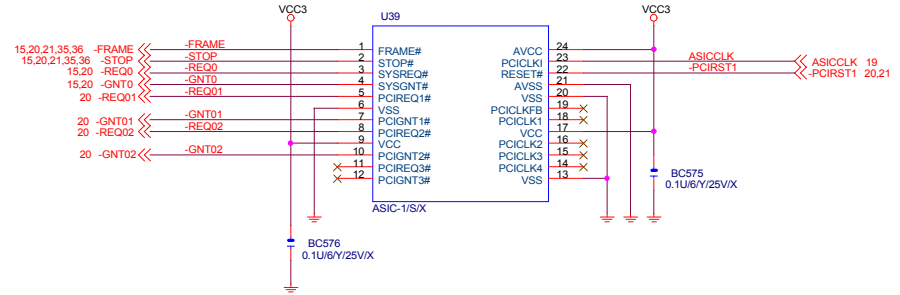
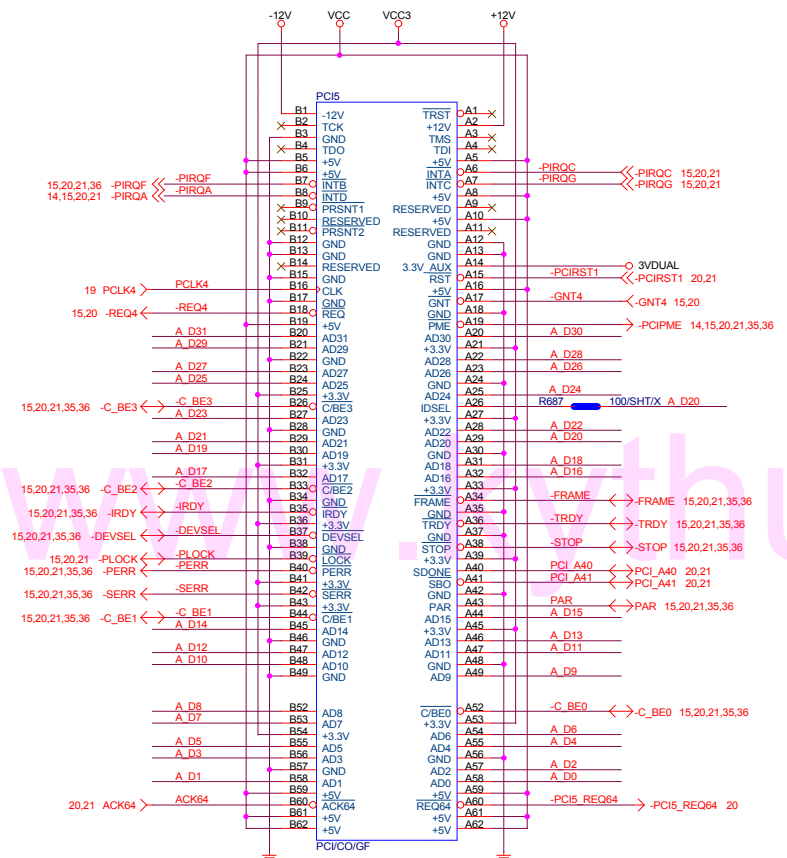
<< -AGPRST 14 TO AGP SLOT

<< -PCIRST1 20,22 TO PCI SLOT

<< -IDERST 27 TO IDE SLOT

GIGABYTE CORP.		
Title		
PCI SLOT 3/4		
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	GA-8IG1000-G	3.0
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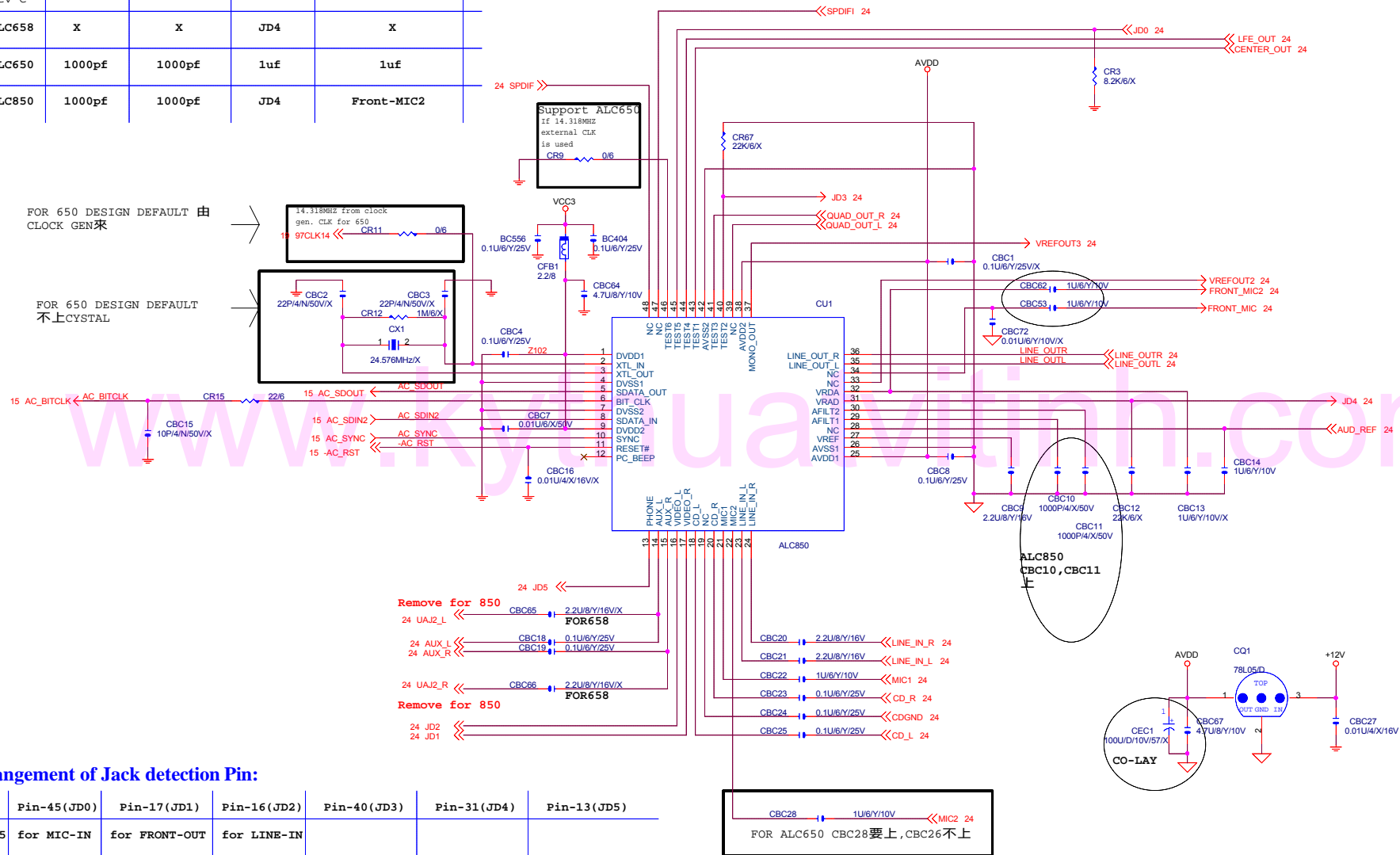
15,20,21,35,36_A_D[0..31] << A_D[0..31]



GIGABYTE CORP.			
Title			
PCI SLOT 5			
Size	Document Number	Rev	
Custom	GA-8IG1000-G	3.0	
Date:	日期: 二月 05, 2004	Sheet	22 of 38

Filter Cap design:

	Pin-29	Pin-30	Pin-31	Pin-32
ALC655 Rev D	1000pf	1000pf	1uf	Front-MIC2
ALC655 Rev C	1000pf	1000pf	1uf	X
ALC658	X	X	JD4	X
ALC650	1000pf	1000pf	1uf	1uf
ALC850	1000pf	1000pf	JD4	Front-MIC2



FOR 650 DESIGN DEFAULT 由 CLOCK GEN来

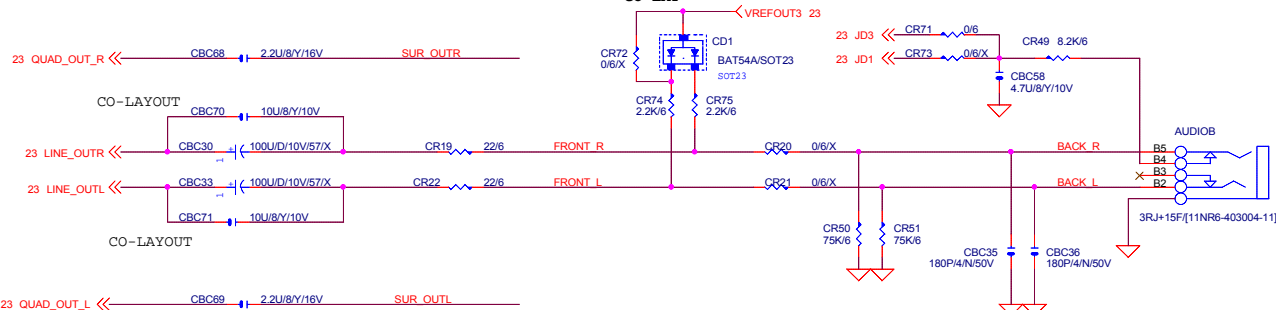
FOR 650 DESIGN DEFAULT 不上CRYSTAL

Arrangement of Jack detection Pin:

	Pin-45(JD0)	Pin-17(JD1)	Pin-16(JD2)	Pin-40(JD3)	Pin-31(JD4)	Pin-13(JD5)
ALC655	for MIC-IN	for FRONT-OUT	for LINE-IN			
ALC658	for MIC-IN	for UAJ1	for UAJ2	for FRONT-OUT External pull high is needed	for LINE-IN External pull high is needed	
ALC850	for MIC-IN	for Front Pannel OUT	for Front Pannel IN	for FRONT-OUT	for LINE-IN	for SurrBack Out

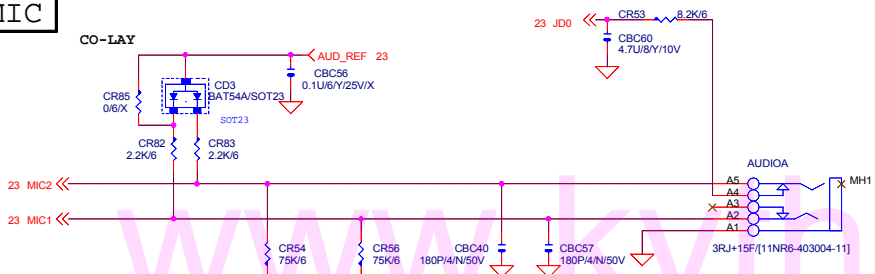
LINE OUT

JDO,JD2,GPIO0 為偵測DEVICE INPUT 時由LOW TO HIGH Edge trigger(pop manual) 1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ以上AC 信號全部衰減 TO 0V 不會造成JDO 誤動作(無device 時play wav)



LINE OUT SENSING
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

MIC



MICROPHONE IN SENSING(當INPUT)(利用vref 偏壓 與CR43,CR32 並聯求出阻抗)
7.1k ohm>R>2.3k ohm==>microphone in
R<2.3k ohm or R>7.1k ohm==>unknown device

MICROPHONE IN SENSING(當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

2x5 header for 850

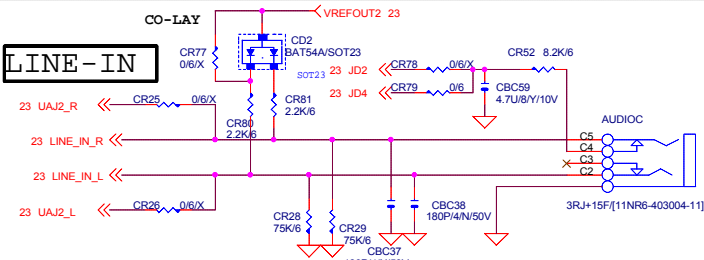
For 850 if JD5 = low AUX-In is configured as input
For 850 if JD5 = high AUX-In is configured as output, Surr-Back out

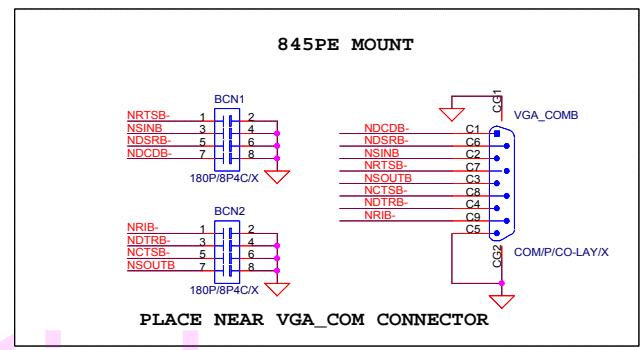
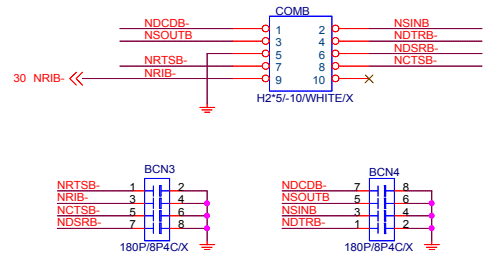
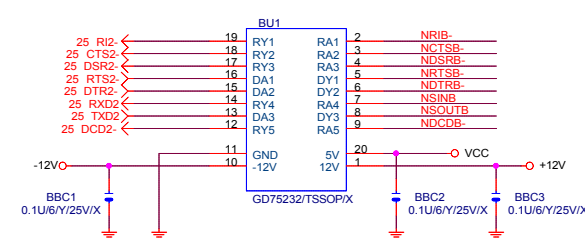
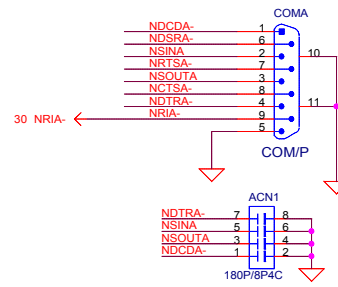
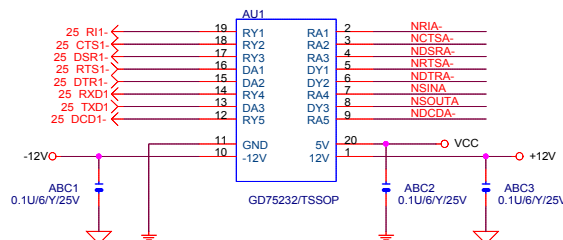
For 850 AUX-In is shared to Surr-Back out

LINE IN SENSING(當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE

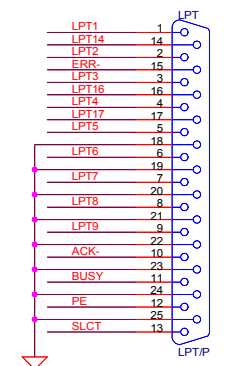
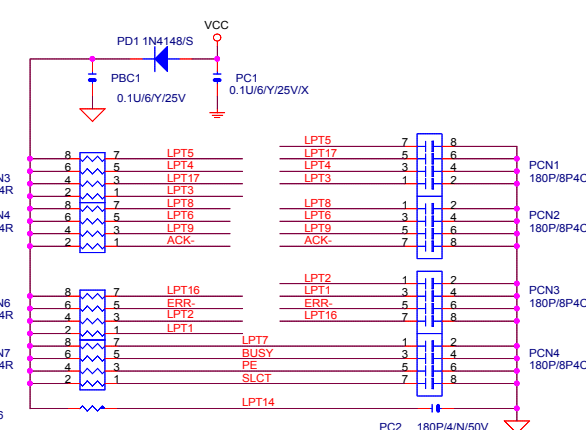
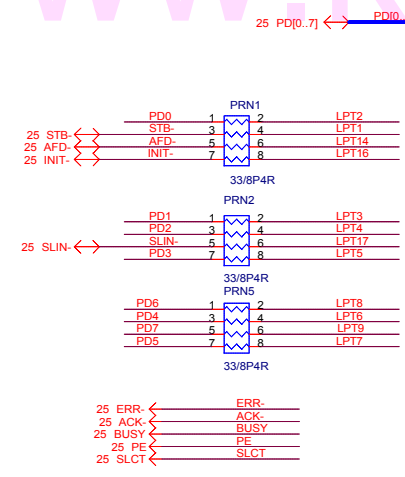
LINE IN SENSING(當INPUT)
swing of input signal>-40dbv(10mv)==>line in device active
swing of input signal<-40dbv(10mv)==>unknown line in device

LINE-IN

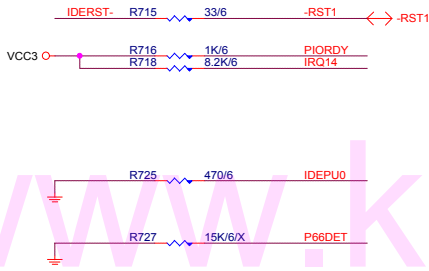
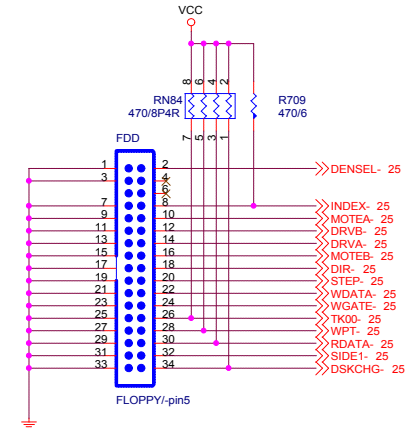
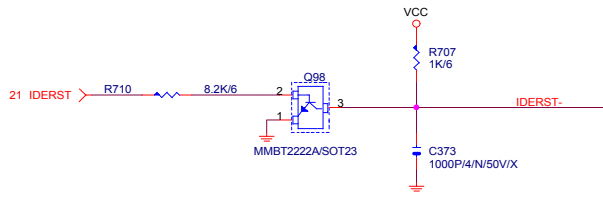




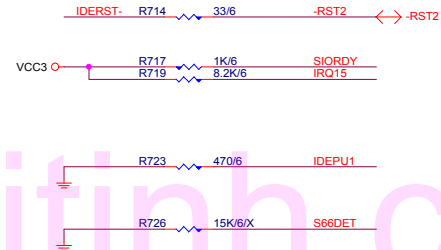
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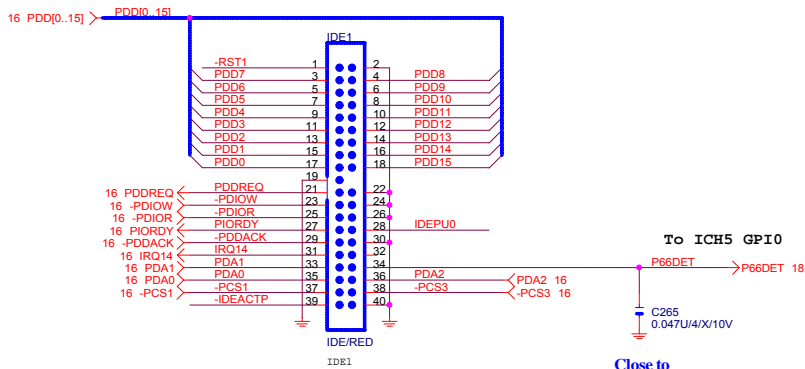
SIGABYTE CORP.		
Title		
COM & IR & LPT PORT & FLOOPY		
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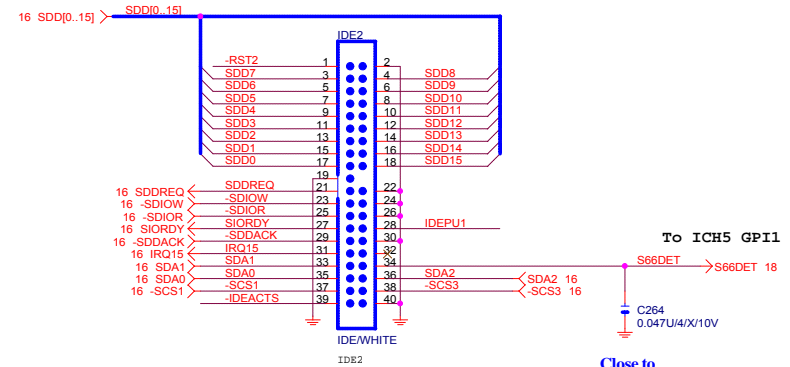
PRIMARY IDE CONNECTOR



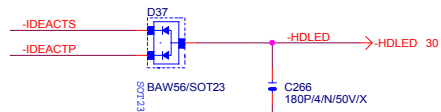
SECONDARY IDE CONNECTOR



Close to connector

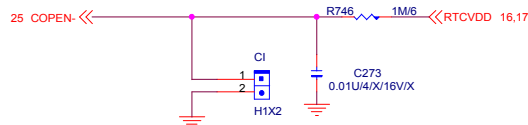
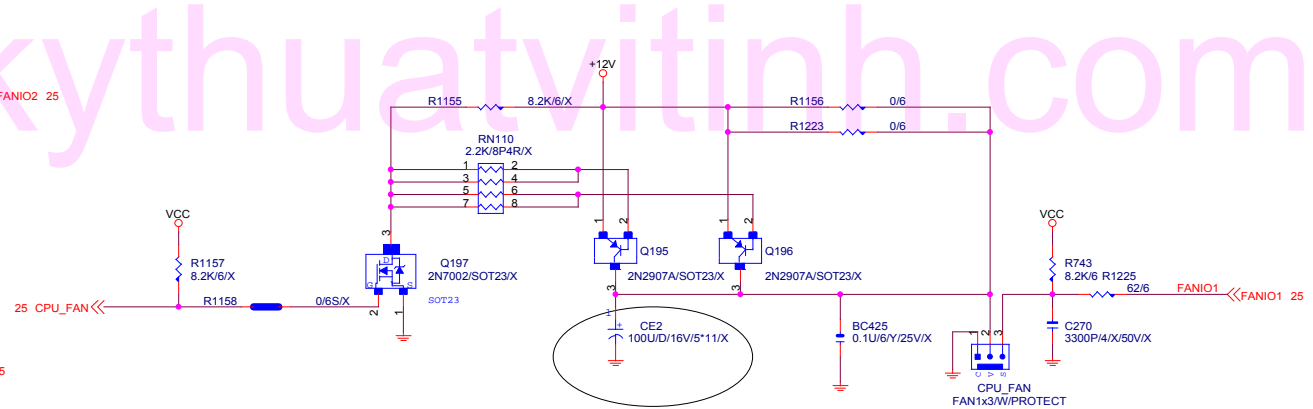
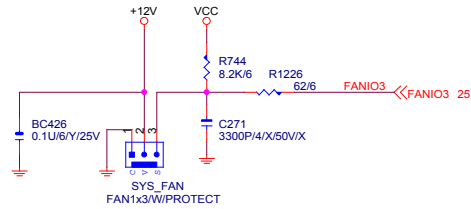
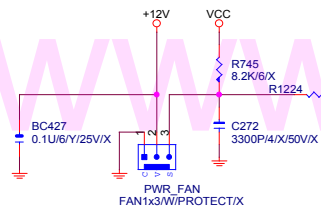
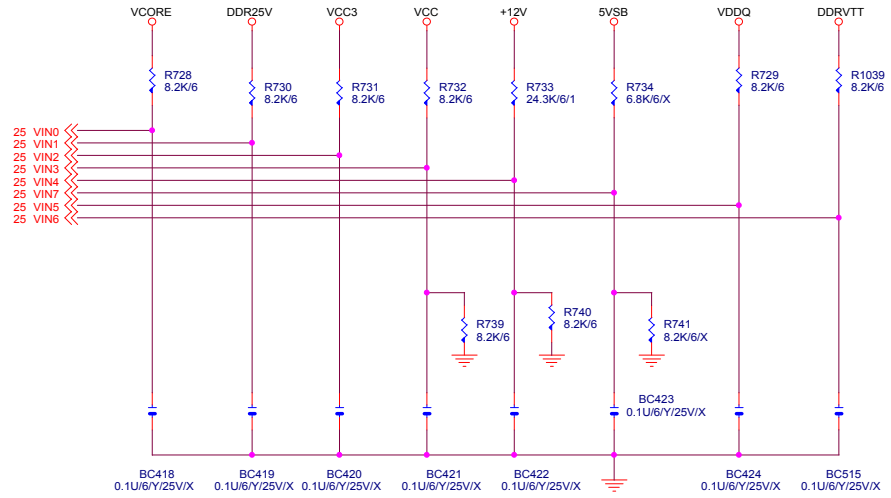
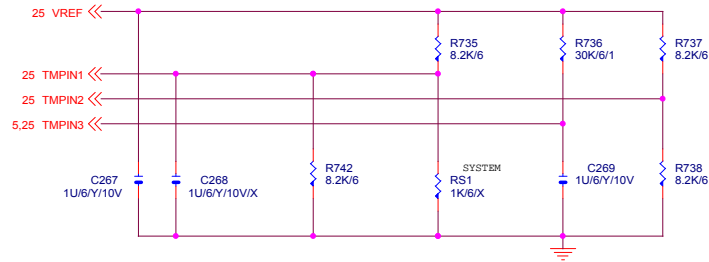


Close to connector

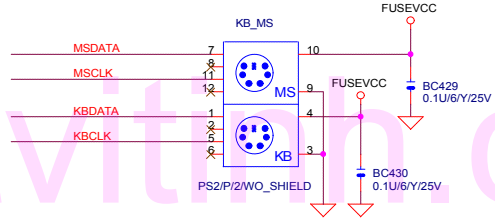
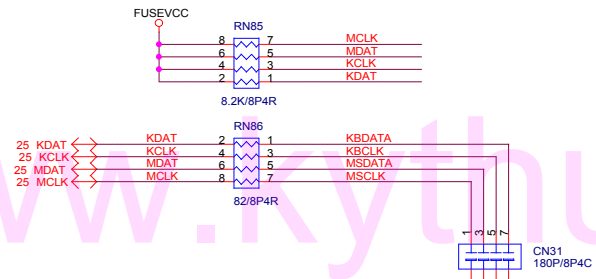
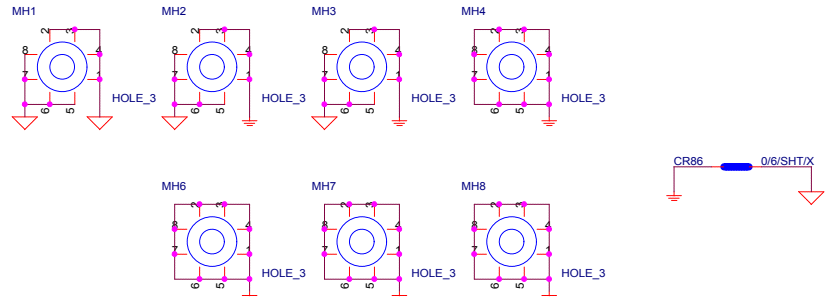
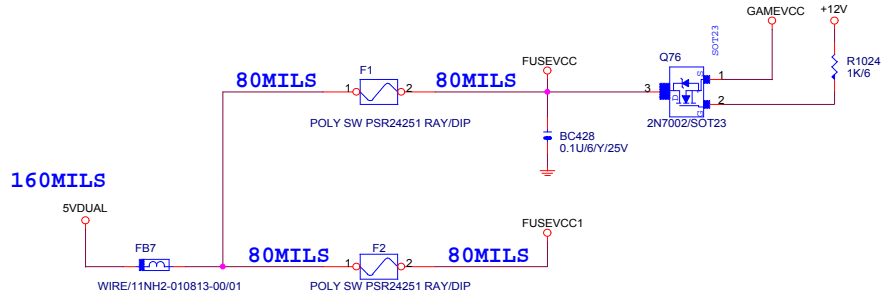


GIGABYTE CORP.		
Title		
IDE CONNECTOR		
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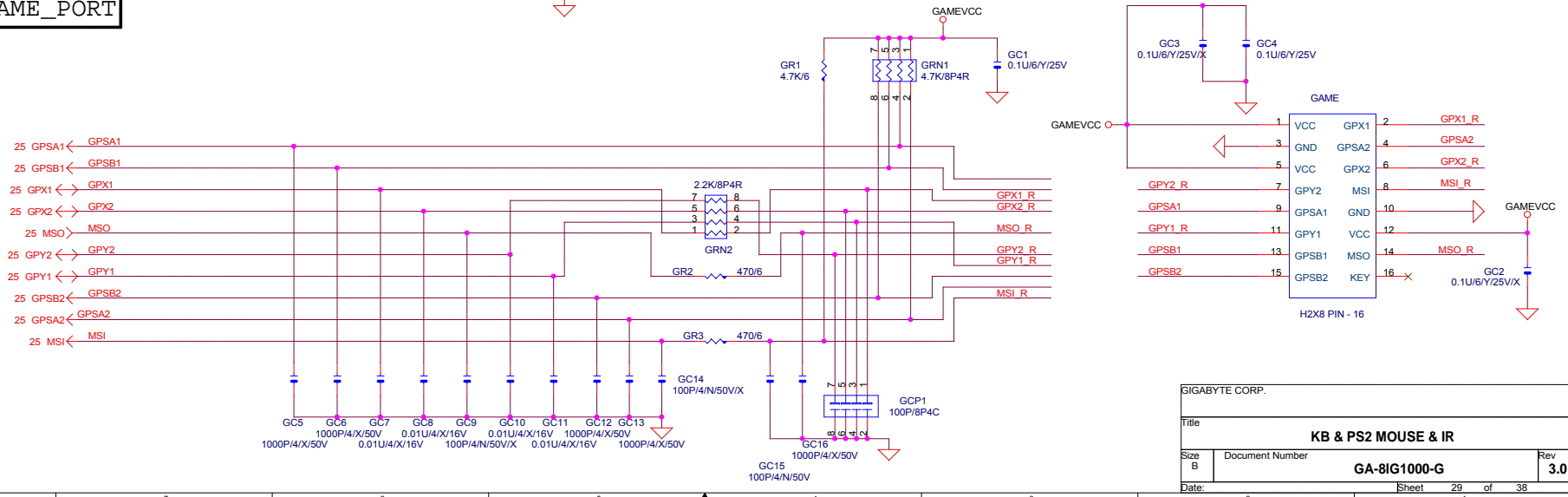
Hardware Monitor circuits



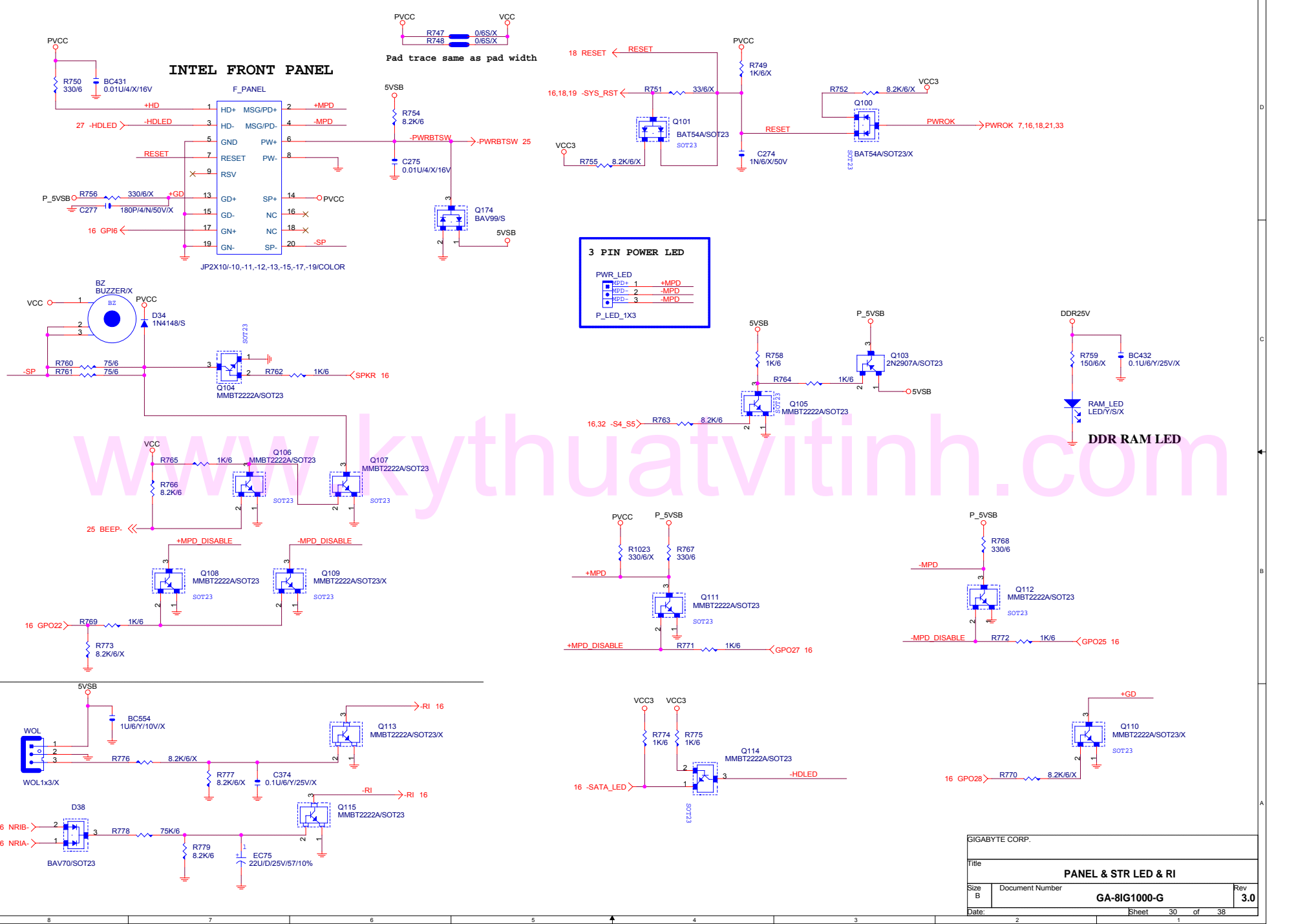
SIGABYTE CORP.		
Title		
FAN/HWMO		
Size B	Document Number	Rev
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GAME_PORT



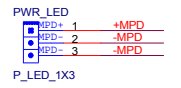
SIGABYTE CORP.		
Title		
KB & PS2 MOUSE & IR		
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INTEL FRONT PANEL

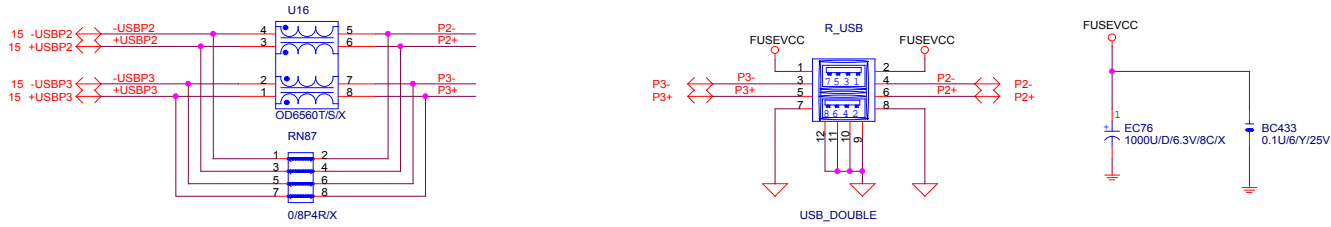
Pad trace same as pad width

3 PIN POWER LED

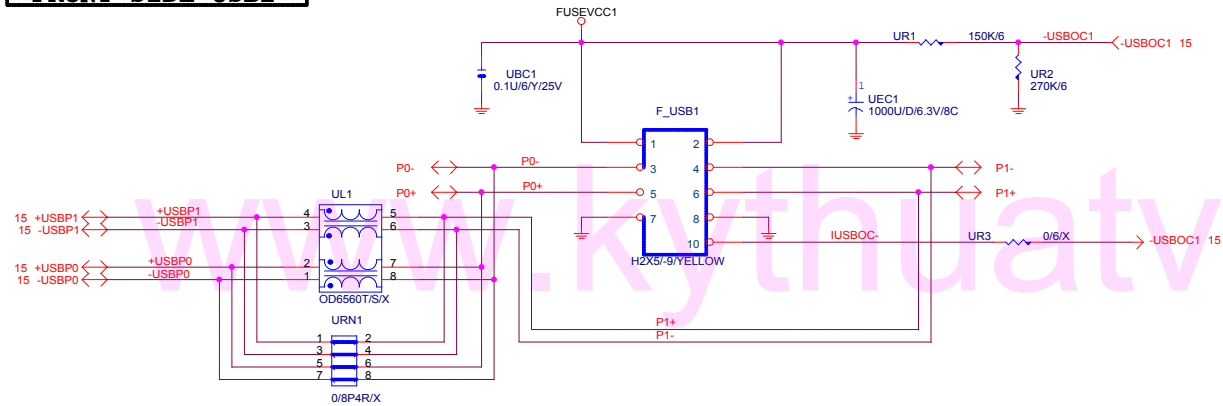


GIGABYTE CORP.		
Title		
PANEL & STR LED & RI		
Size B	Document Number	Rev
	GA-8IG1000-G	3.0
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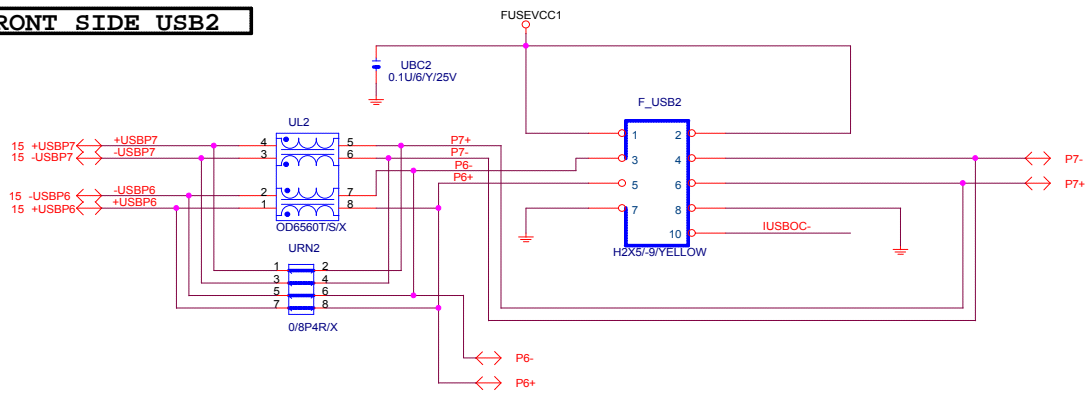
REAR USB



FRONT SIDE USB1

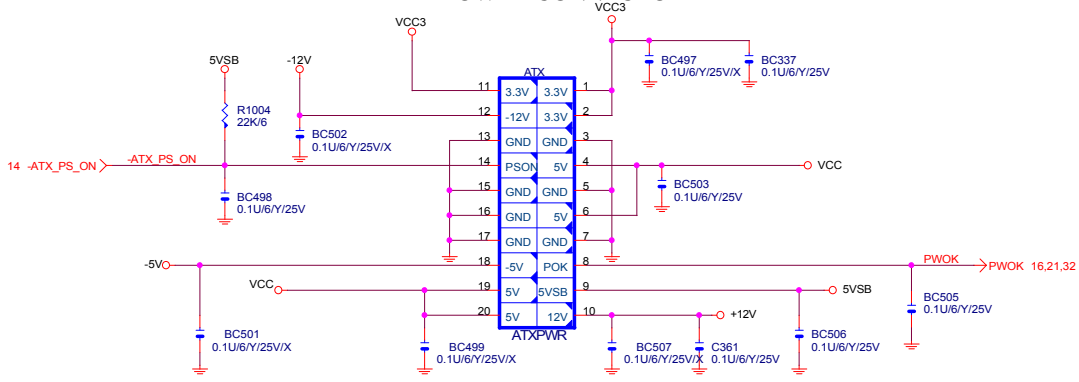


FRONT SIDE USB2

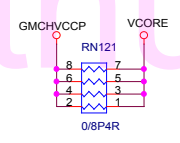
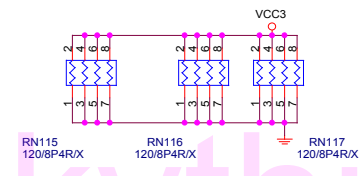
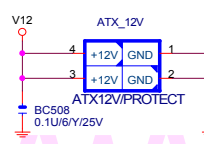
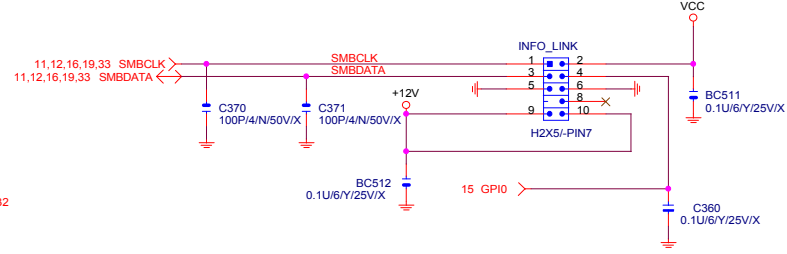


GIGABYTE CORP.		
Title ICH USB PORT		
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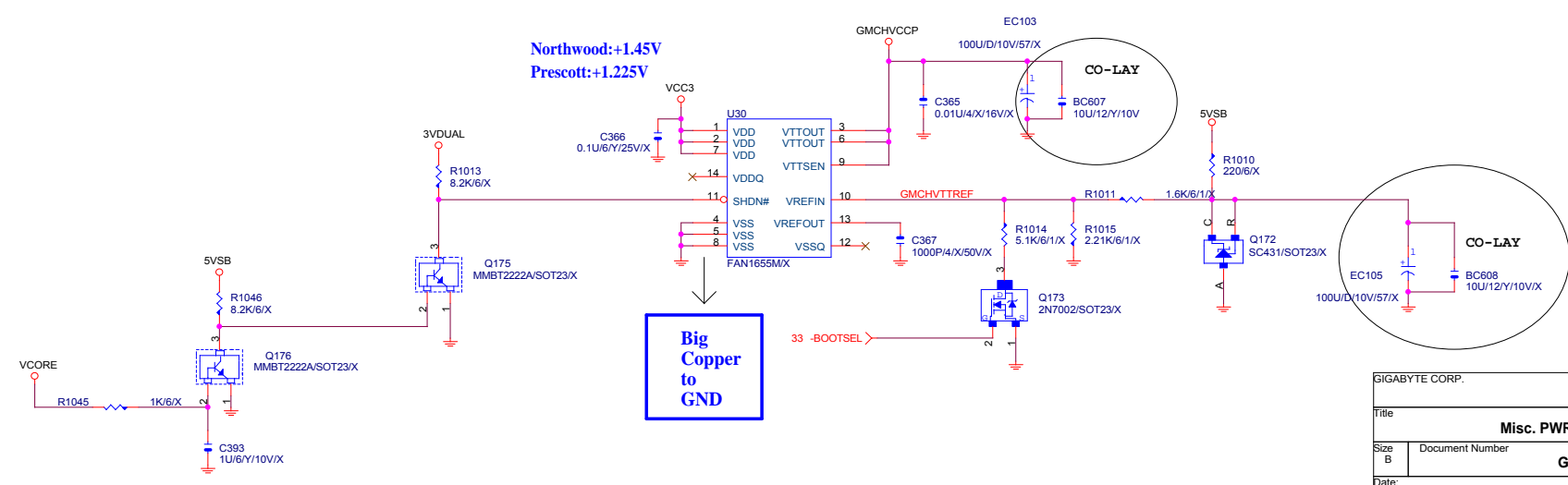
ATX POWER CONNECTOR



SMBUS CONN.



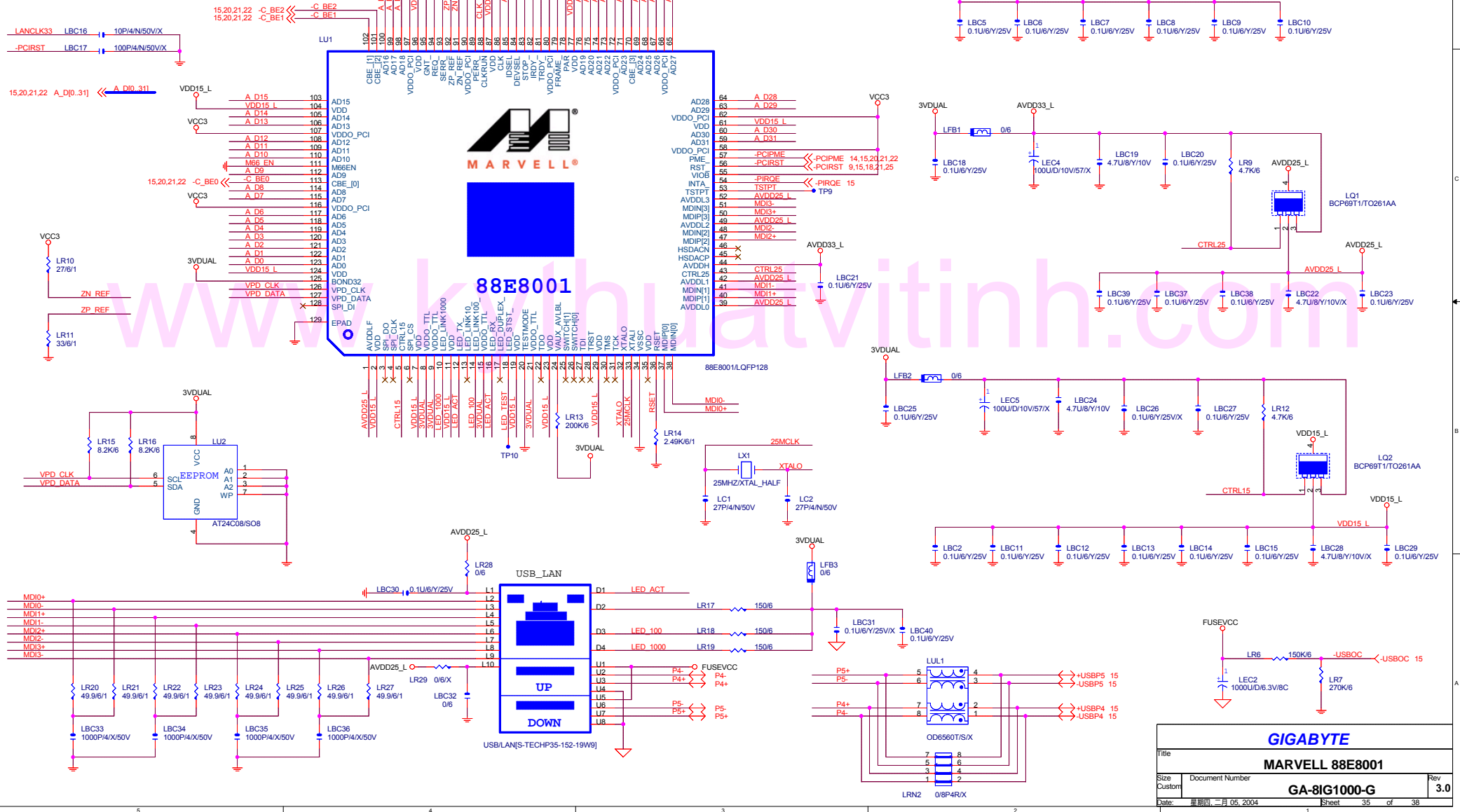
Northwood:+1.45V
Prescott:+1.225V



GIGABYTE CORP.		
Title		
Misc. PWR & ATX CONN.		
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- # Layout Check 注意事項
1. LU1 PIN129 需下內層GND, 至少打 22 VIA
 2. 3VDUAL, VCC3, VDD15_L, AVDD25_L 至少走20mil寬, 並且電容擺設每兩pin至少放一顆Bypass Cap.
 3. X'TAL 25MHz 兩訊號線, TRACE 愈短愈好, 線寬12mil
 4. MDI正負0-3, TRACE 8:7:8, 每對之間保持 40mil

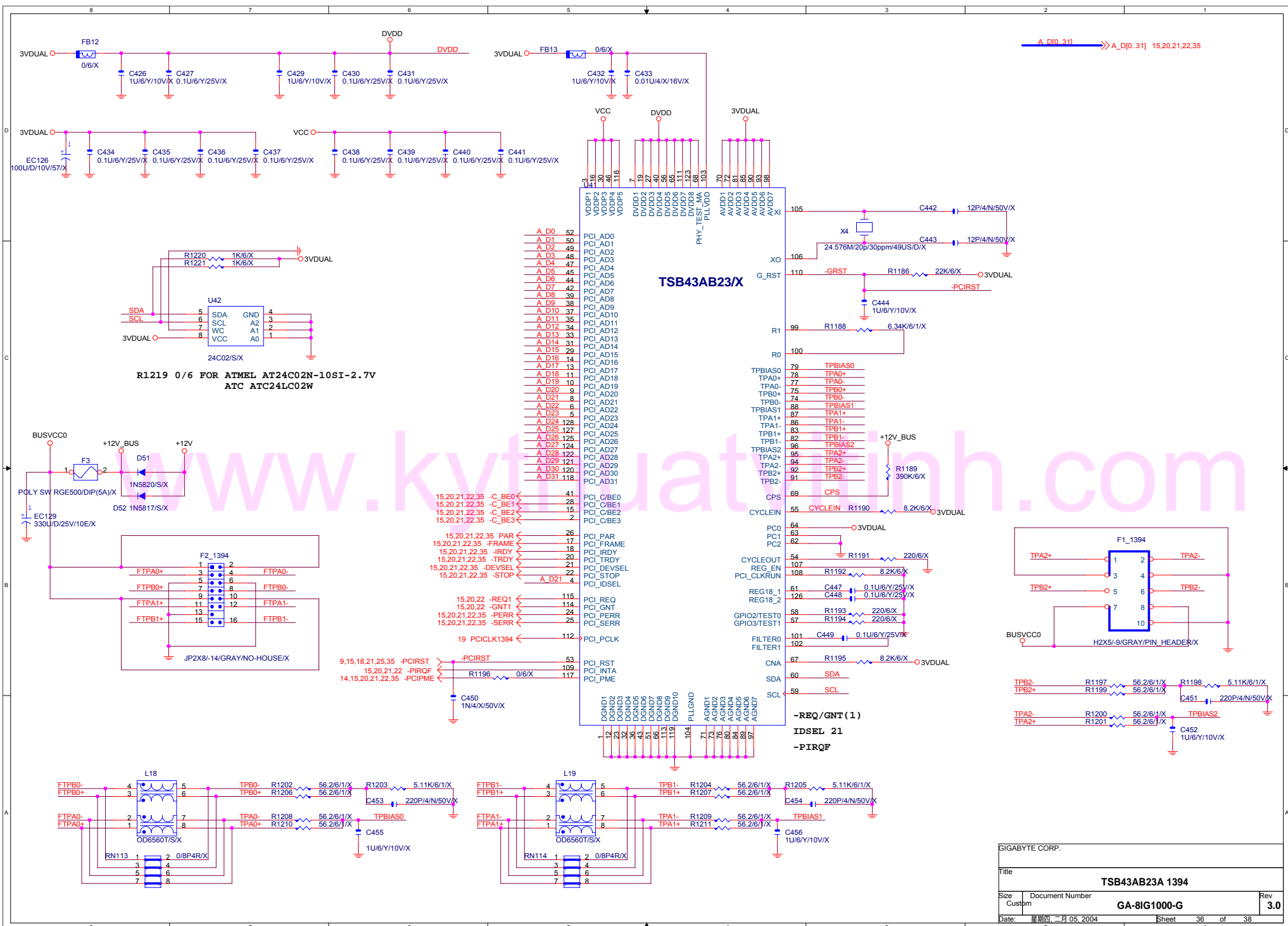


POWER DECOUPLING CAP.



88E8001

Title		
MARVELL 88E8001		
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A D0	52	PCI_AD0
A D1	50	PCI_AD1
A D2	49	PCI_AD2
A D3	48	PCI_AD3
A D4	47	PCI_AD4
A D5	45	PCI_AD5
A D6	44	PCI_AD6
A D7	42	PCI_AD7
A D8	39	PCI_AD8
A D9	38	PCI_AD9
A D10	37	PCI_AD10
A D11	35	PCI_AD11
A D12	34	PCI_AD12
A D13	33	PCI_AD13
A D14	31	PCI_AD14
A D15	29	PCI_AD15
A D16	14	PCI_AD16
A D17	13	PCI_AD17
A D18	11	PCI_AD18
A D19	10	PCI_AD19
A D20	9	PCI_AD20
A D21	8	PCI_AD21
A D22	6	PCI_AD22
A D23	5	PCI_AD23
A D24	128	PCI_AD24
A D25	127	PCI_AD25
A D26	125	PCI_AD26
A D27	124	PCI_AD27
A D28	122	PCI_AD28
A D29	121	PCI_AD29
A D30	120	PCI_AD30
A D31	118	PCI_AD31
15, 20, 21, 22, 35	-C_BE0	41
15, 20, 21, 22, 35	-C_BE1	28
15, 20, 21, 22, 35	-C_BE2	15
15, 20, 21, 22, 35	-C_BE3	2
15, 20, 21, 22, 35	PAR	26
15, 20, 21, 22, 35	-FRAME	17
15, 20, 21, 22, 35	-IRDY	18
15, 20, 21, 22, 35	-TRDY	20
15, 20, 21, 22, 35	PCI_DEVSEL	21
15, 20, 21, 22, 35	-STOP	22
15, 20, 21, 22, 35	A_D21_4	22
15, 20, 22	-REQ1	115
15, 20, 22	-GNT1	114
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15, 20, 21, 22, 35	-SERR	25
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15, 20, 21, 22	-PIRQF	109
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CGND1	104	PULLGND
CGND2	71	AGND1
CGND3	73	AGND2
CGND4	76	AGND3
CGND5	80	AGND4
CGND6	83	AGND5
CGND7	87	AGND6
CGND8	97	AGND7
CGND9	104	PULLGND
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CGND332	438	PULLGND
CGND33		

GIGABYTE GA-8IG1000-G PCI ROUNTING LIST

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT
PCI SLOT1	16	C,F,G,A	PCLK0	-REQ01	-GNT01
PCI SLOT2	17	F,G,A,C	PCLK1	-REQ02	-GNT02
PCI SLOT3	18	G,A,C,F	PCLK2	-REQ2	-GNT2
PCI SLOT4	19	A,C,F,G	PCLK3	-REQ3	-GNT3
PCI SLOT5	20	C,F,G,A	PCLK4	-REQ4	-GNT4
TI 1394	21	F	PCICLK1394	-REQ1	-GNT1
LAN (Marvell)	25	E	LANCLK33	-REQ5 (REQB#)	-GNT5 (GNTB#)

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Title PCI ROUNT LIST			
Size	Document Number	Rev	
Custom	GA-8IG1000-G	3.0	
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