GA-8GE800 Schematics

Revision 1.1

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	P4_478A
05	P4_478B
06	P4_478C
07	GMCH-BROOKDALE-G_A
08	GMCH-BROOKDALE-G_B
09	GMCH-BROOKDALE-G_C
10	DDR SERIAL TERMINATION
11	DDR1 & DDR2
12	DDR3 & DDR TERM.
13	AGP SLOT
14	ICH4_1
15	ICH4_2
16	FWH
17	ICS950211AF CLOCK GEN
18	PCI1 & PCI2 SLOT
19	PCI3 & PCI4 SLOT
20	PCI5 SLOT
21	LPC IO ITE 8702 & FLOPPY & FAN
22	FRONT USB2.0
23	FRONT PANEL

SHEET	TITLE
24	IDE CONNECTOR
25	PS/2 K/B & M/S
26	COM & PRT PORT
27	AC97 CODEC
28	LINE OUT/IN/MIC/FRONT AUDIO & GAME
29	VGA CONNECTOR
30	MISC. PWR & ATX POWER
31	VCORE PWM(FAN5093)
32	DDR POWER
33	REALTEK8100BL + Transformer
34	PCI ROUNTING
35	GPIO PIN LIST

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Title	COVER SHEET	
Size: Custom	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 1 of 35	

Model Name: GA-8GE800

Version:1.1

29.3cm X 21.0cm 60+-15% C Type

Component value change history

DATE	Change Item	Reason
2003/01/27 1.1A	New BOM Release	
2003/02/10 1.1B	Change Q127 2N2222 to 2N7002	
	Change R825 8.2K to 1K	
	Change R826 8.2K to 22K	
2003/03/03 1.1C	ADD EC21 1000uF/6.3/8E.	
2003/03/05 1.1D	刪除 BC278 10000P 電容.	

Circuit or PCB layout change for next version

DATE	Change Item	Reason

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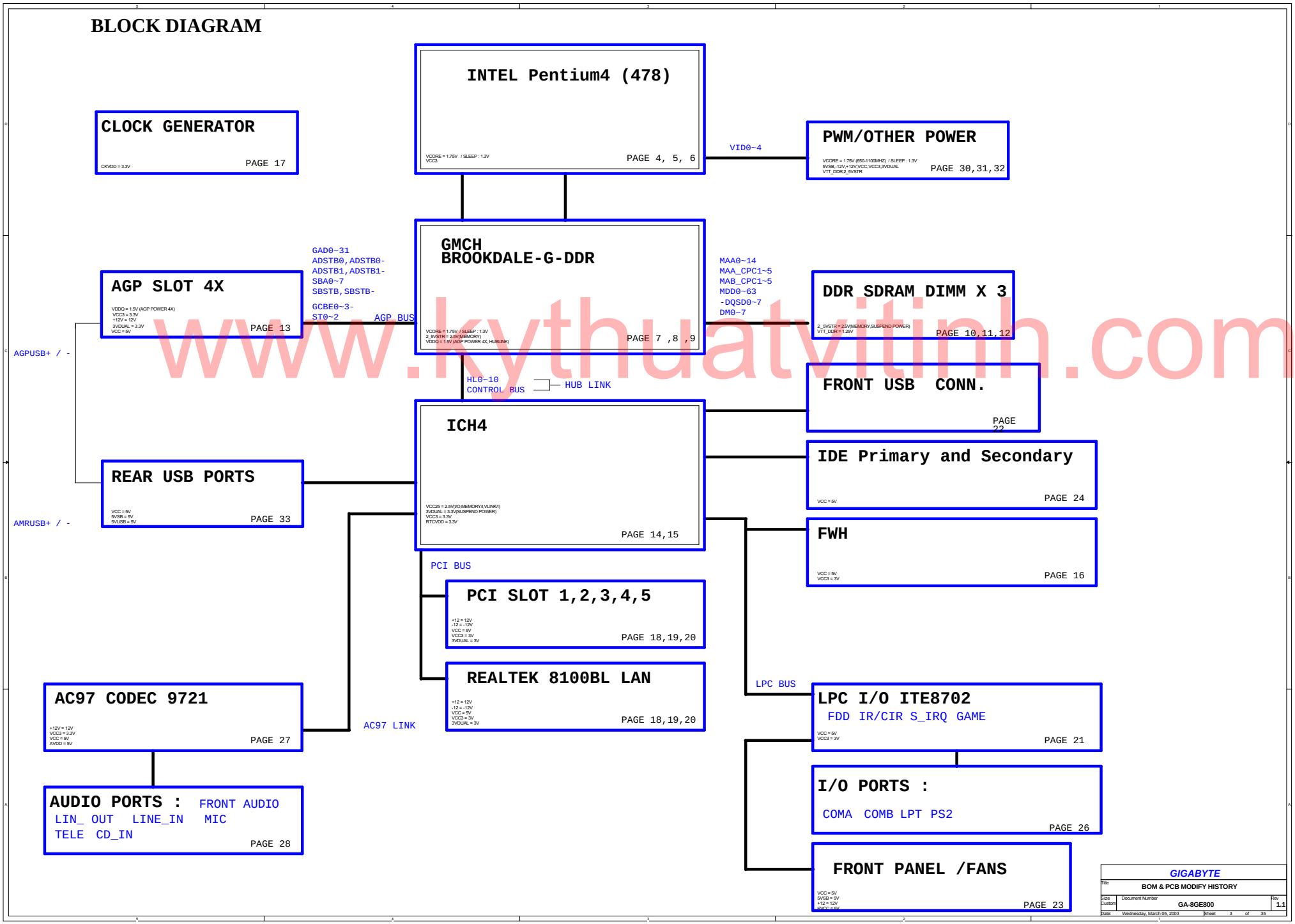
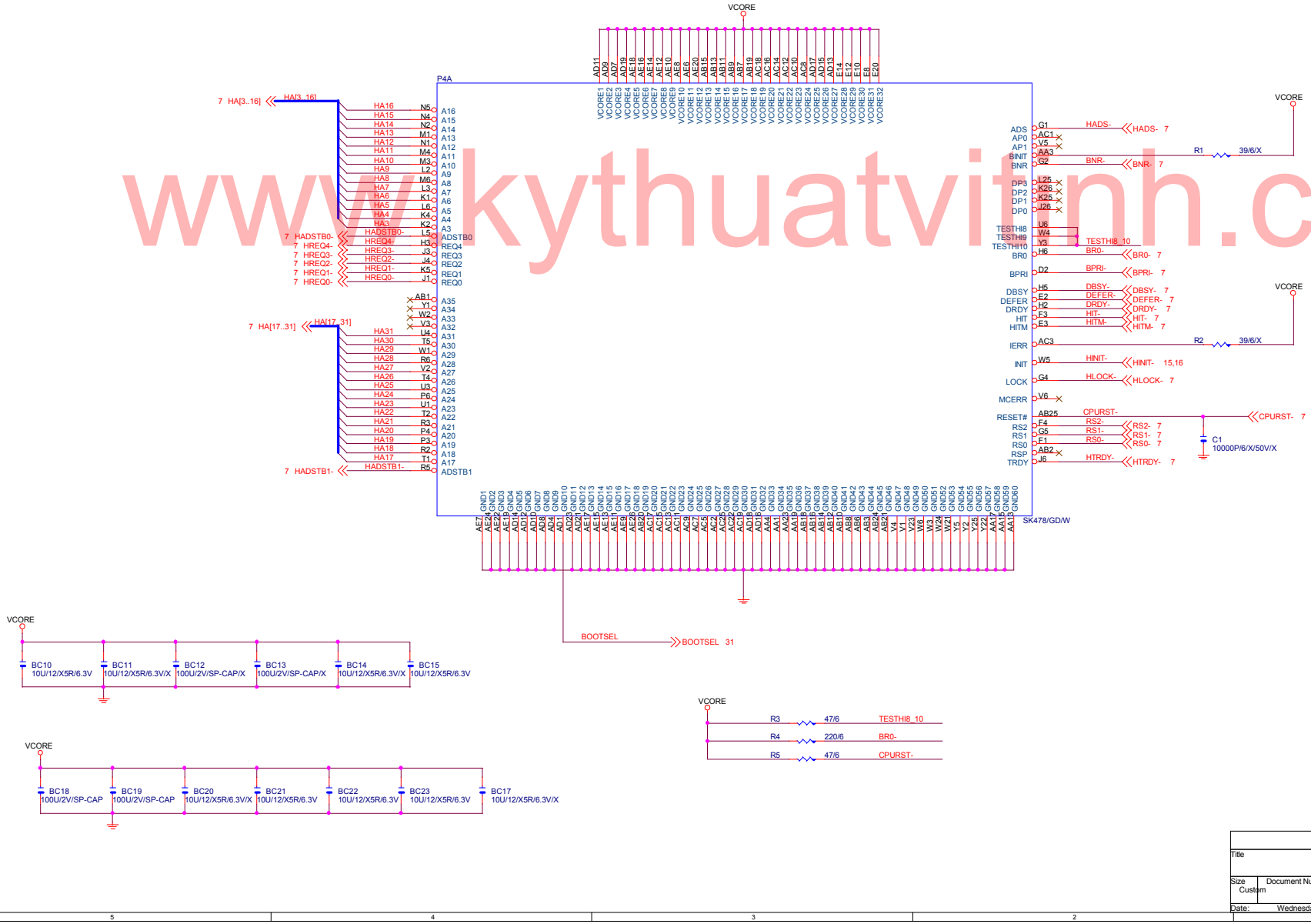
Title: BOM & PCB MODIFY HISTORY

Size: Custom | Document Number: GA-8GE800 | Rev: 1.1

Date: Wednesday, March 05, 2003 | Sheet 2 of 35

BLOCK DIAGRAM





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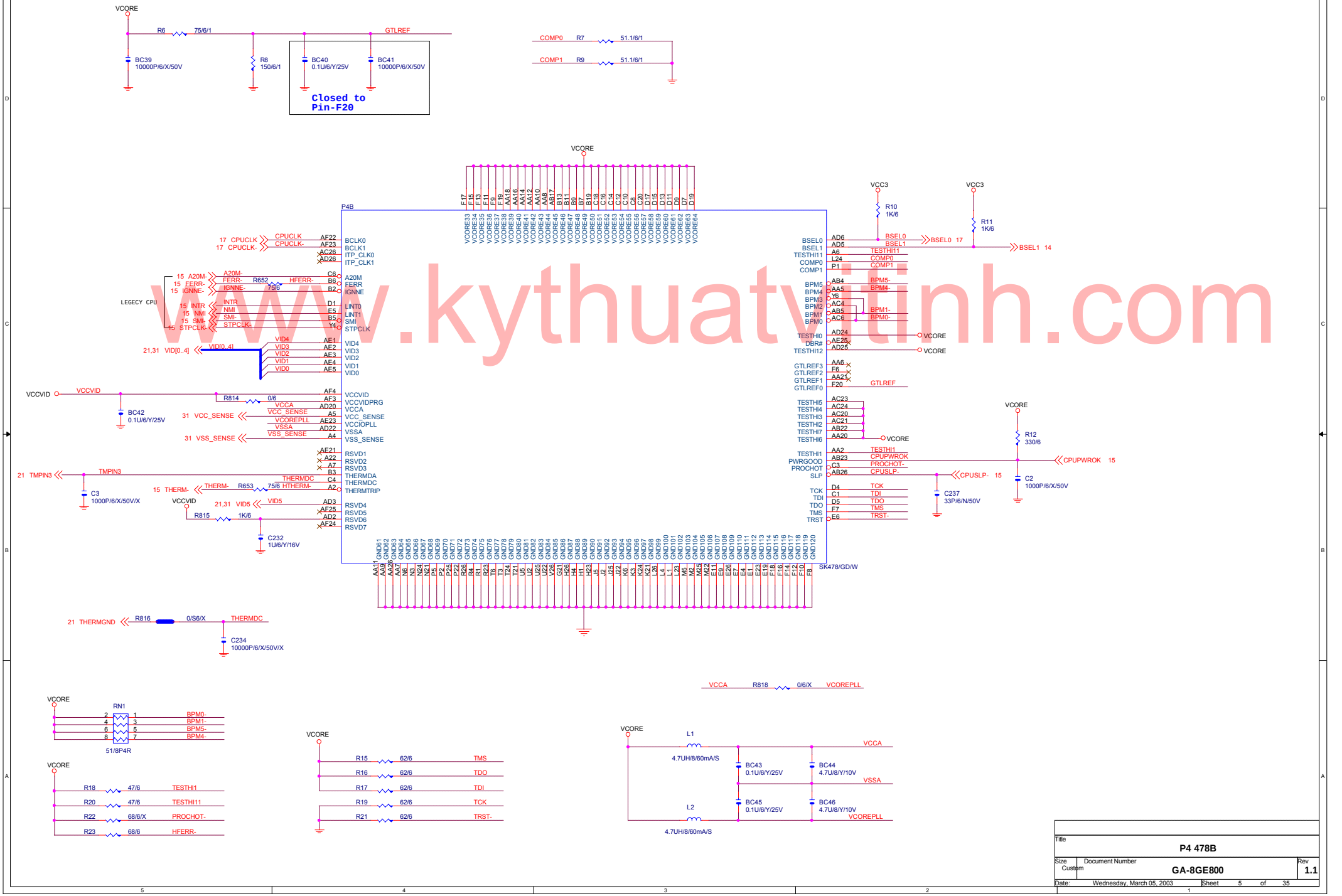
Size: Custom	Document Number: GA-8GE800		Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 4 of 35		



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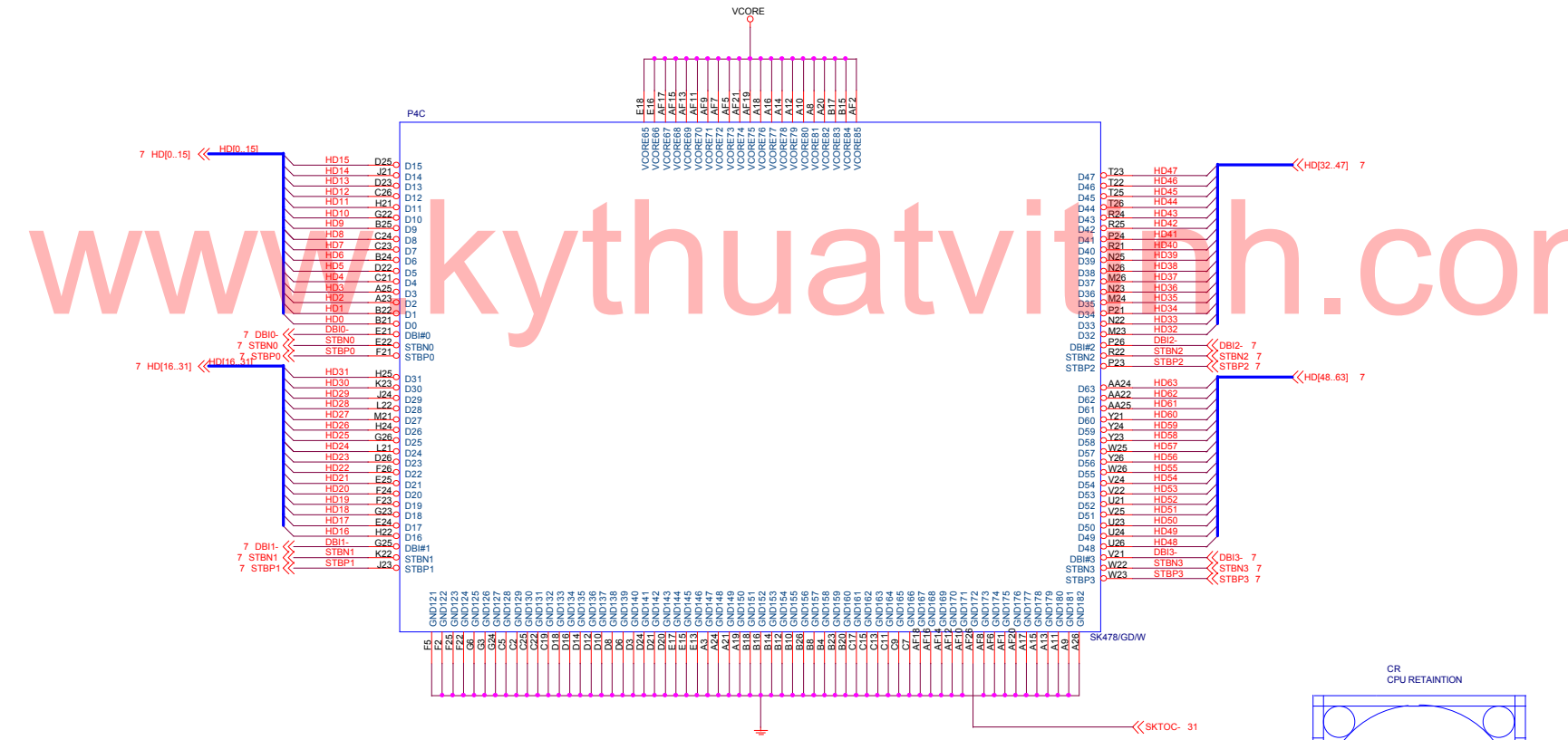
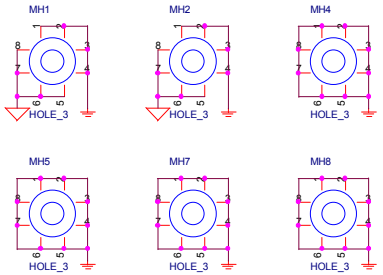
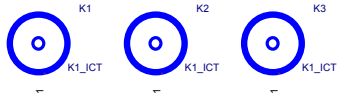
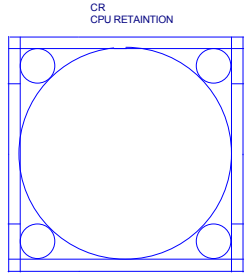
Size: Custom	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 5 of 35	









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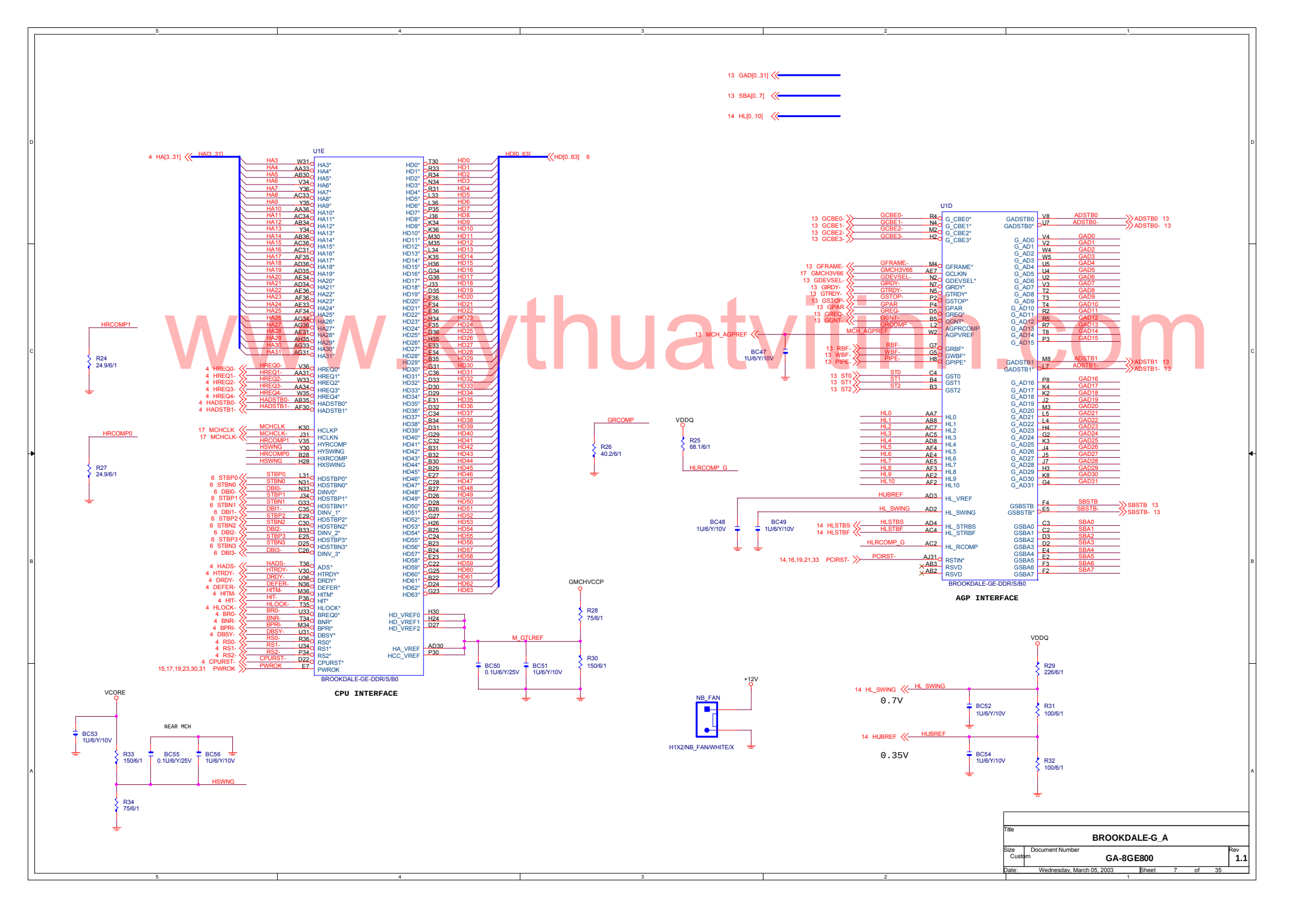
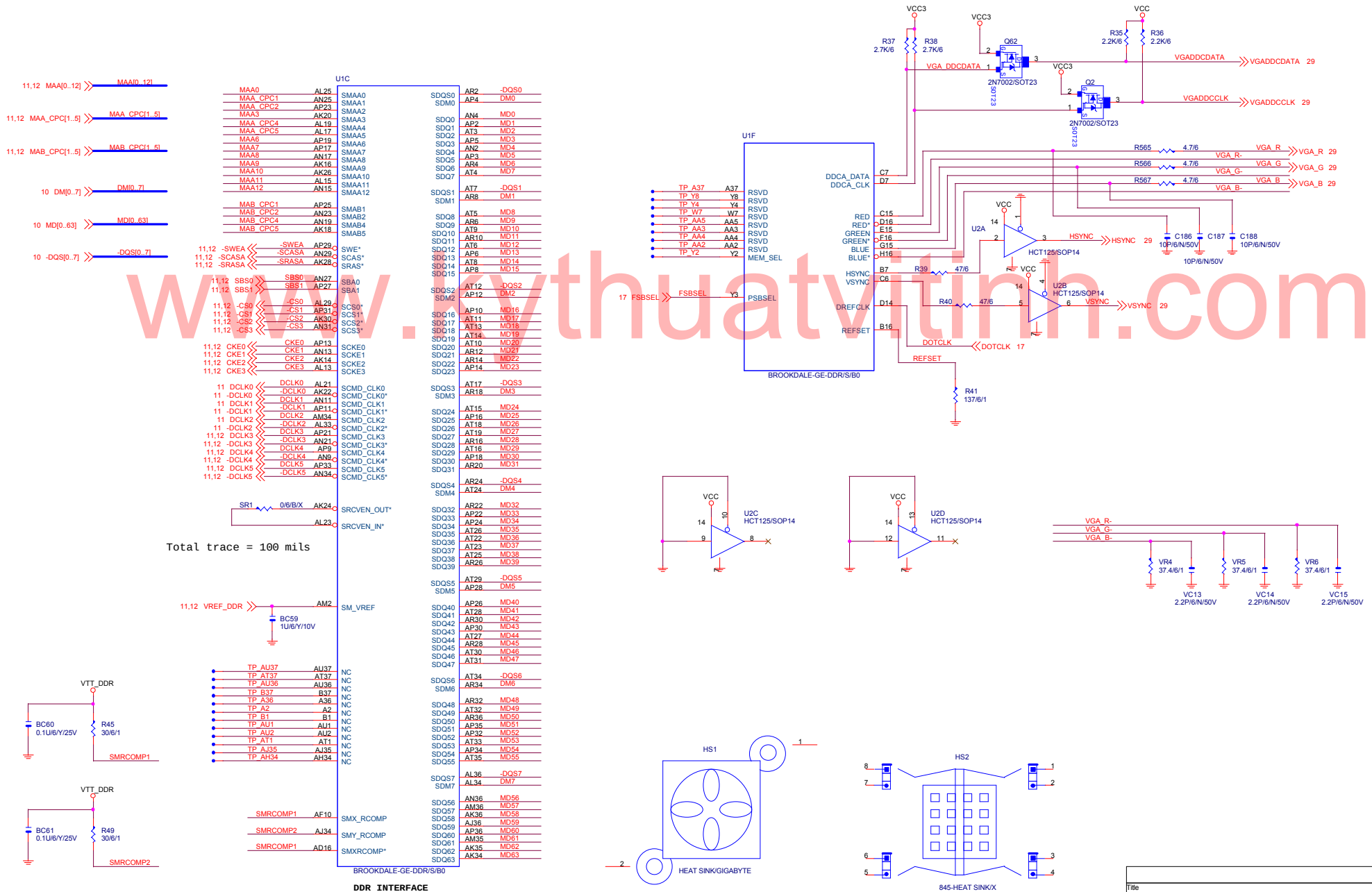
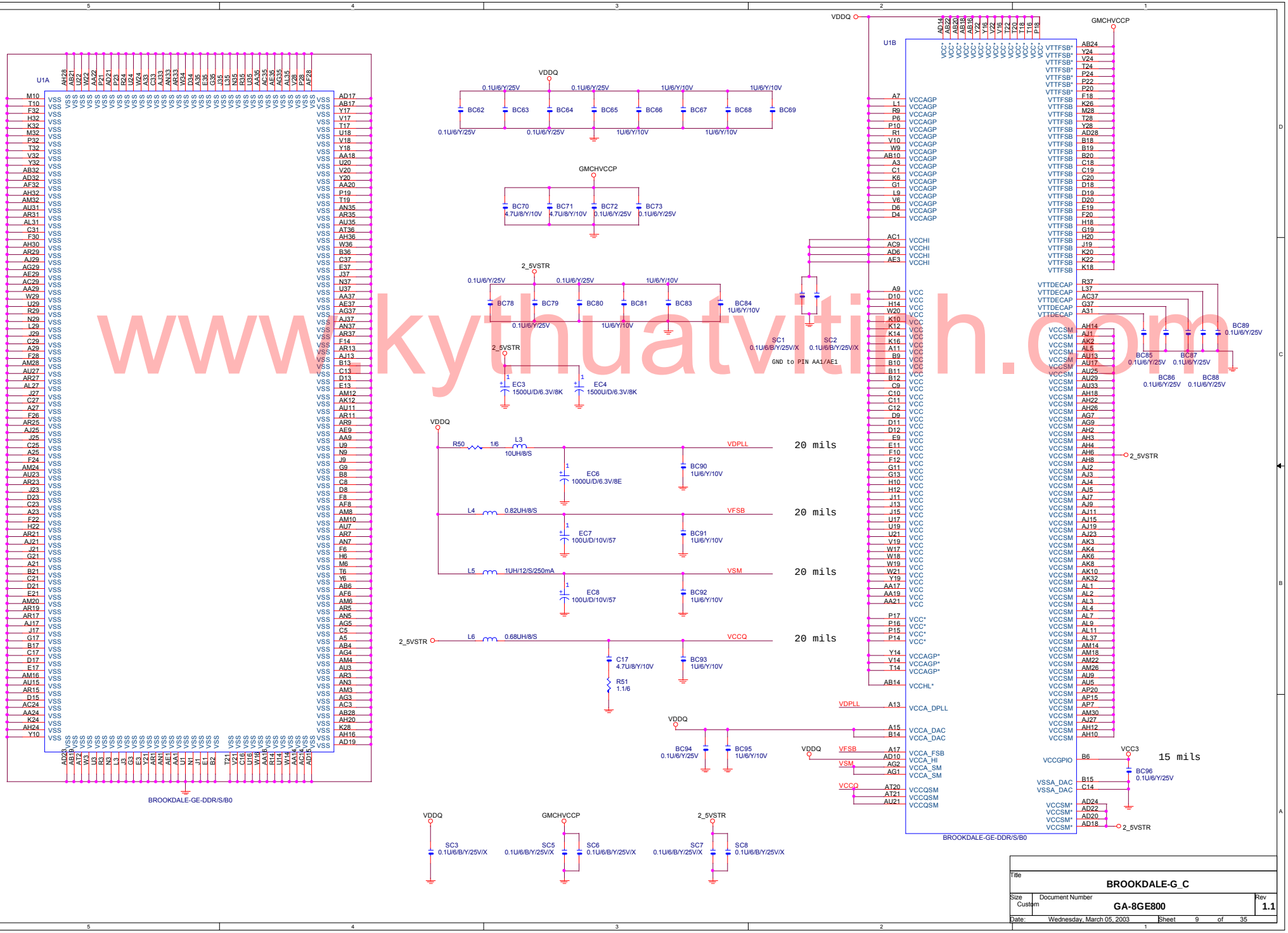
Size: Custom	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 6 of 35	

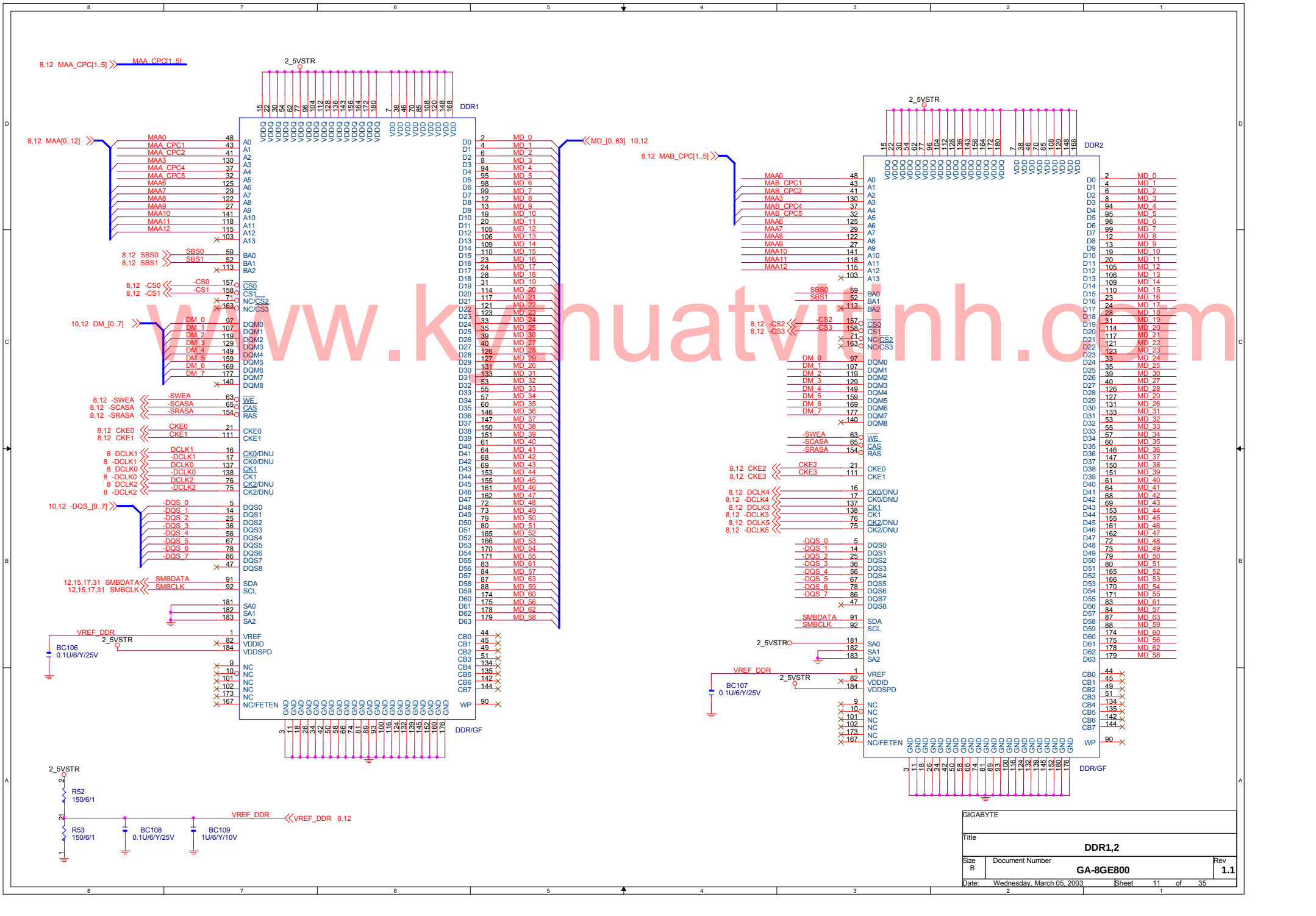


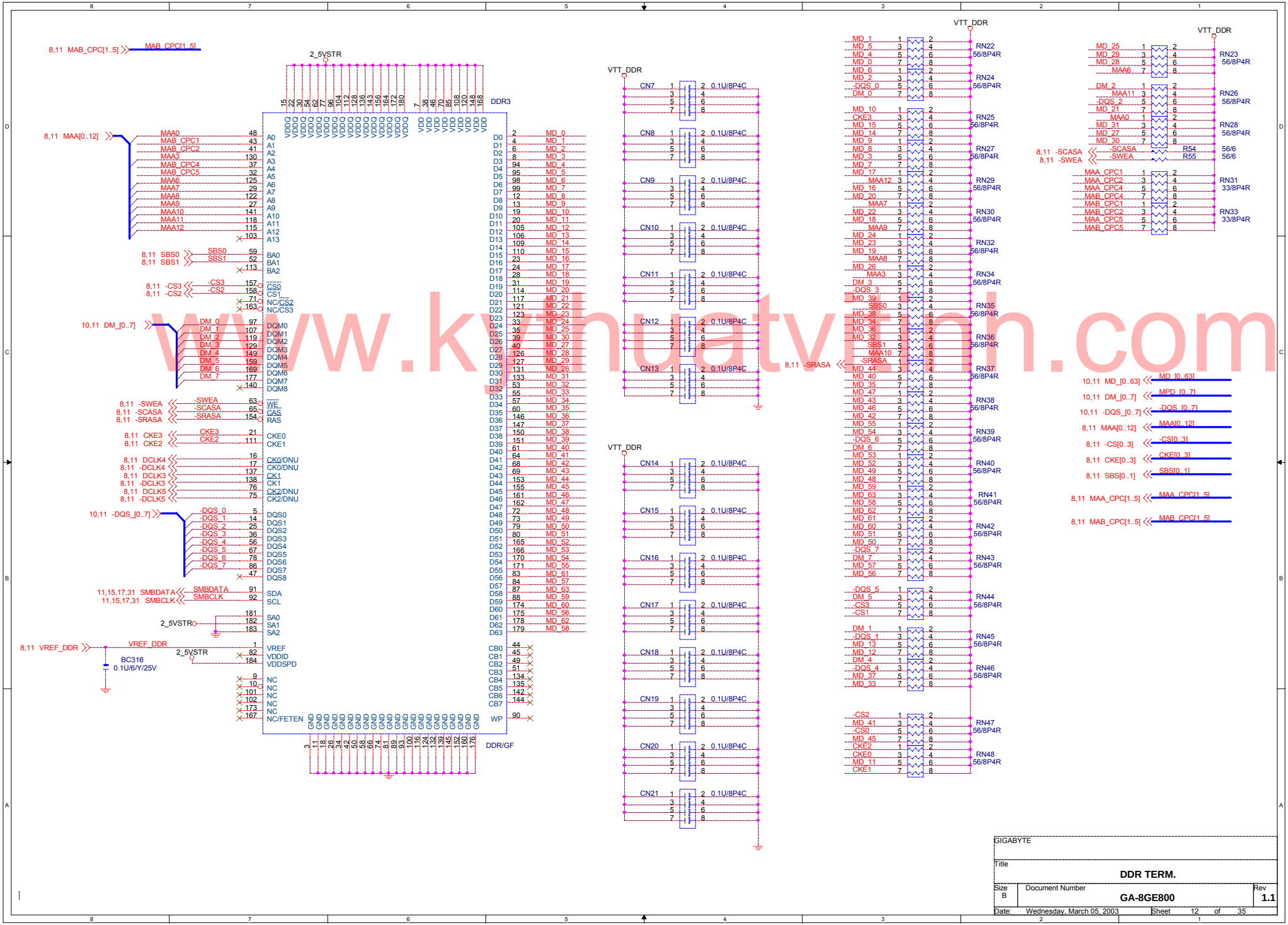


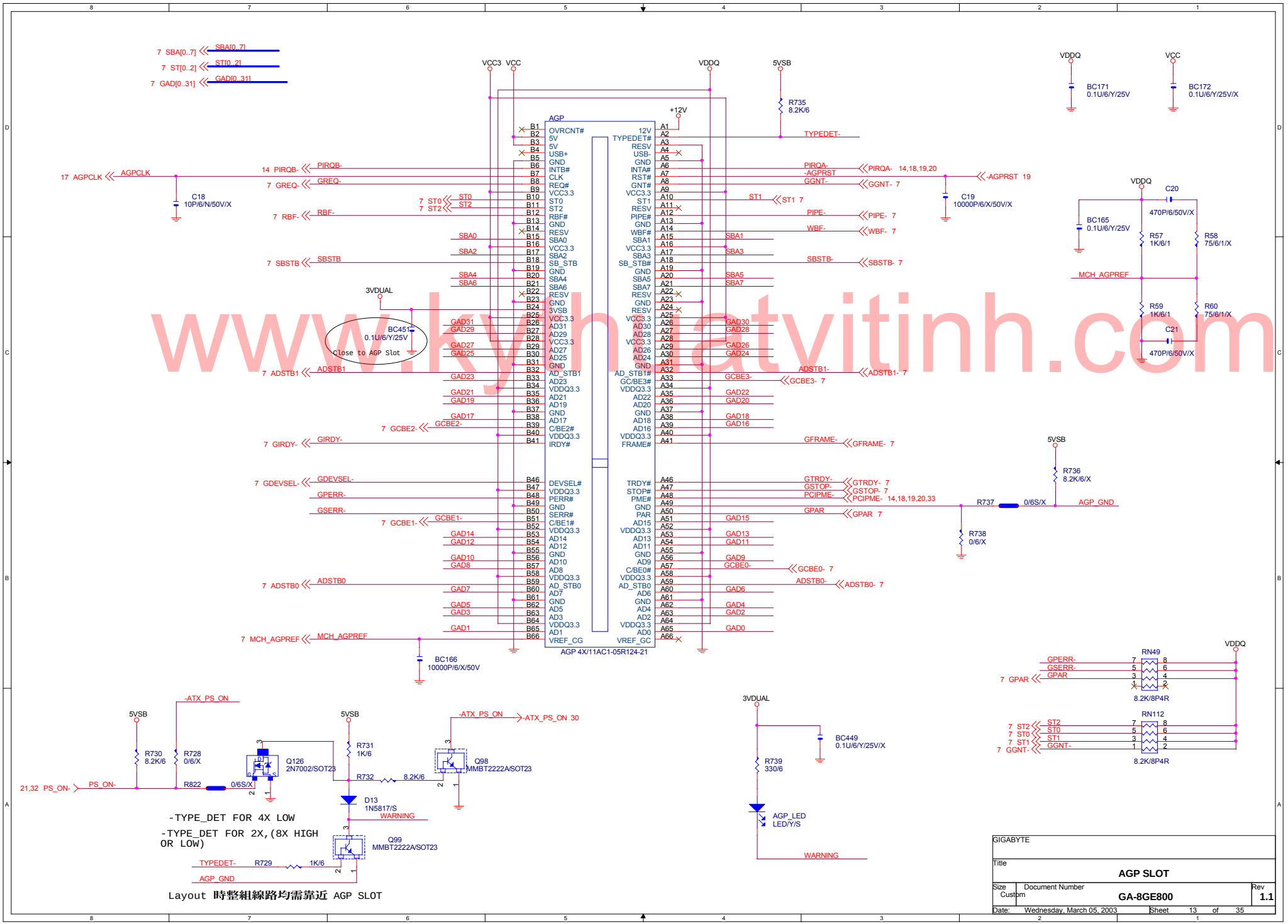


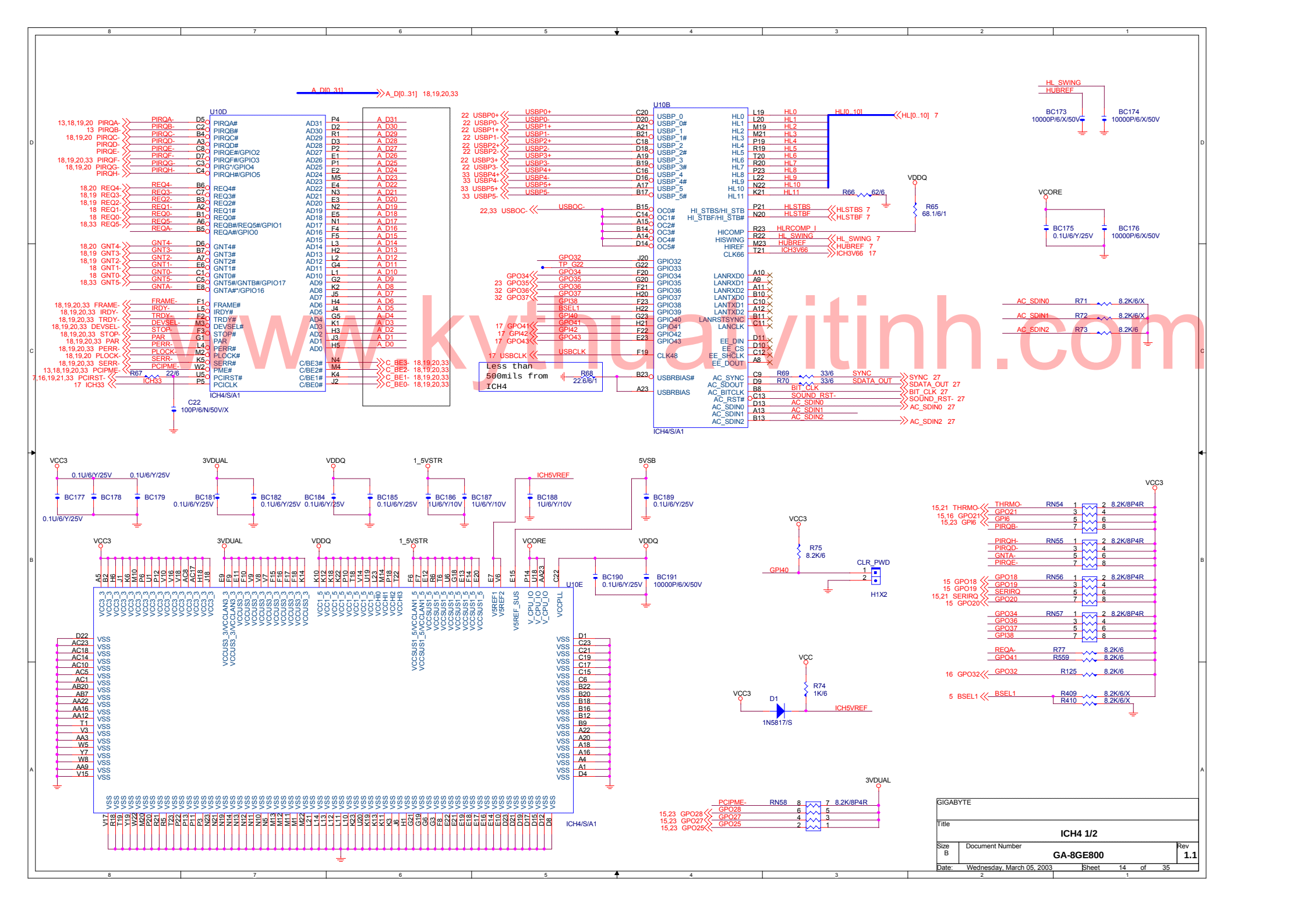
Title	BROOKDALE-G_C	
Size: Custom	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 9 of 35	

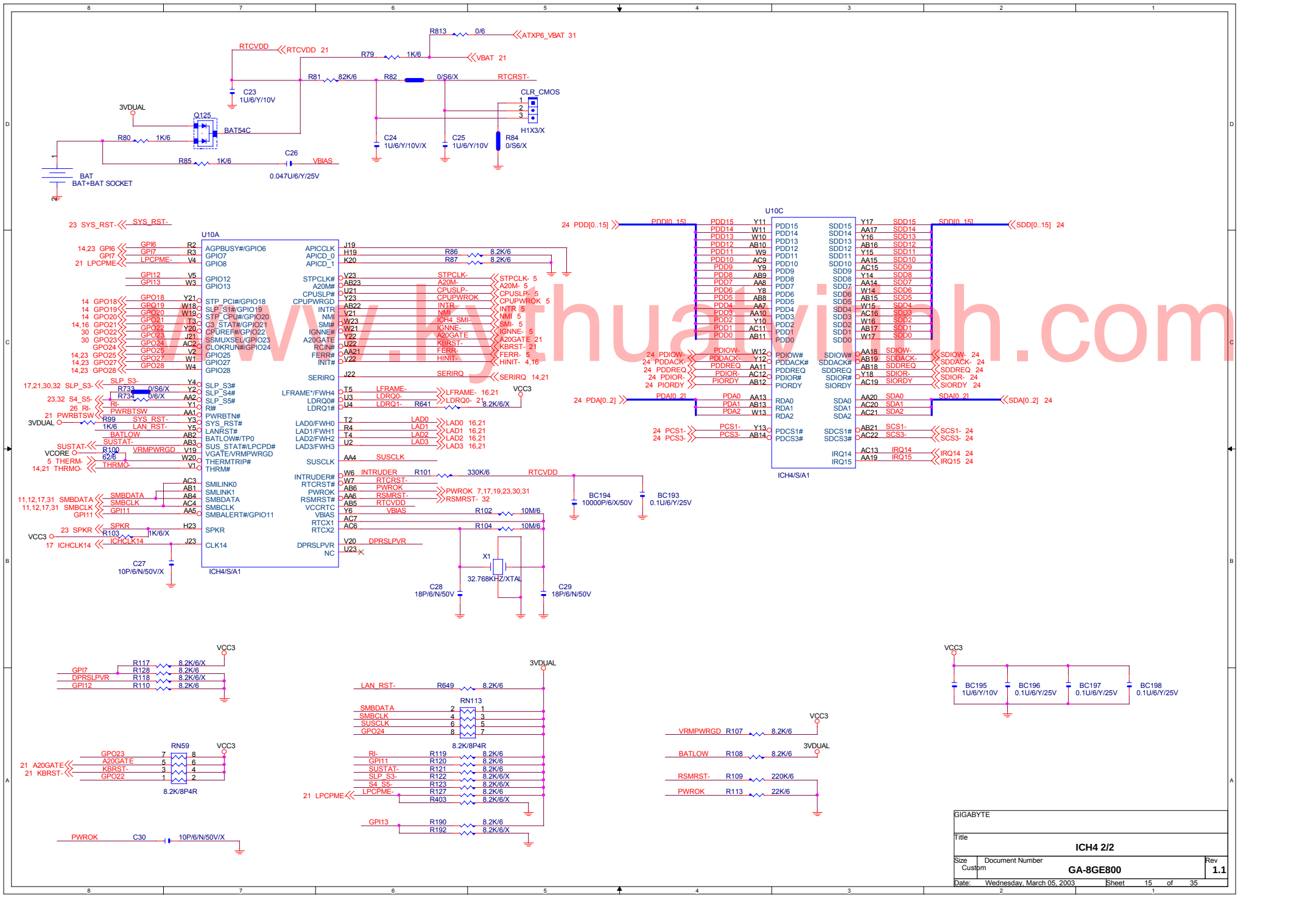
GIGABYTE		
Title	DDR SERIAL TERM.	
Size B	Document Number GA-8GE800	Rev 1.1
Date: Wednesday, March 05, 2003	Sheet 10 of 35	

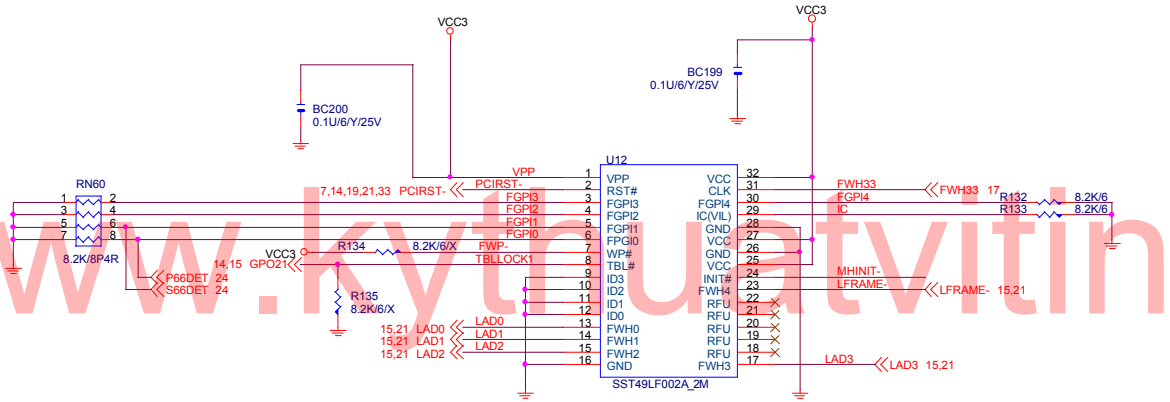


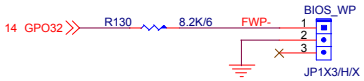


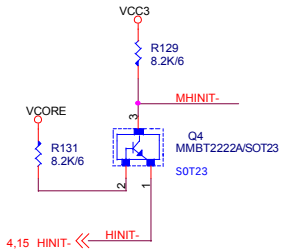


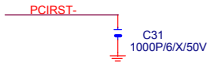












GIGABYTE		
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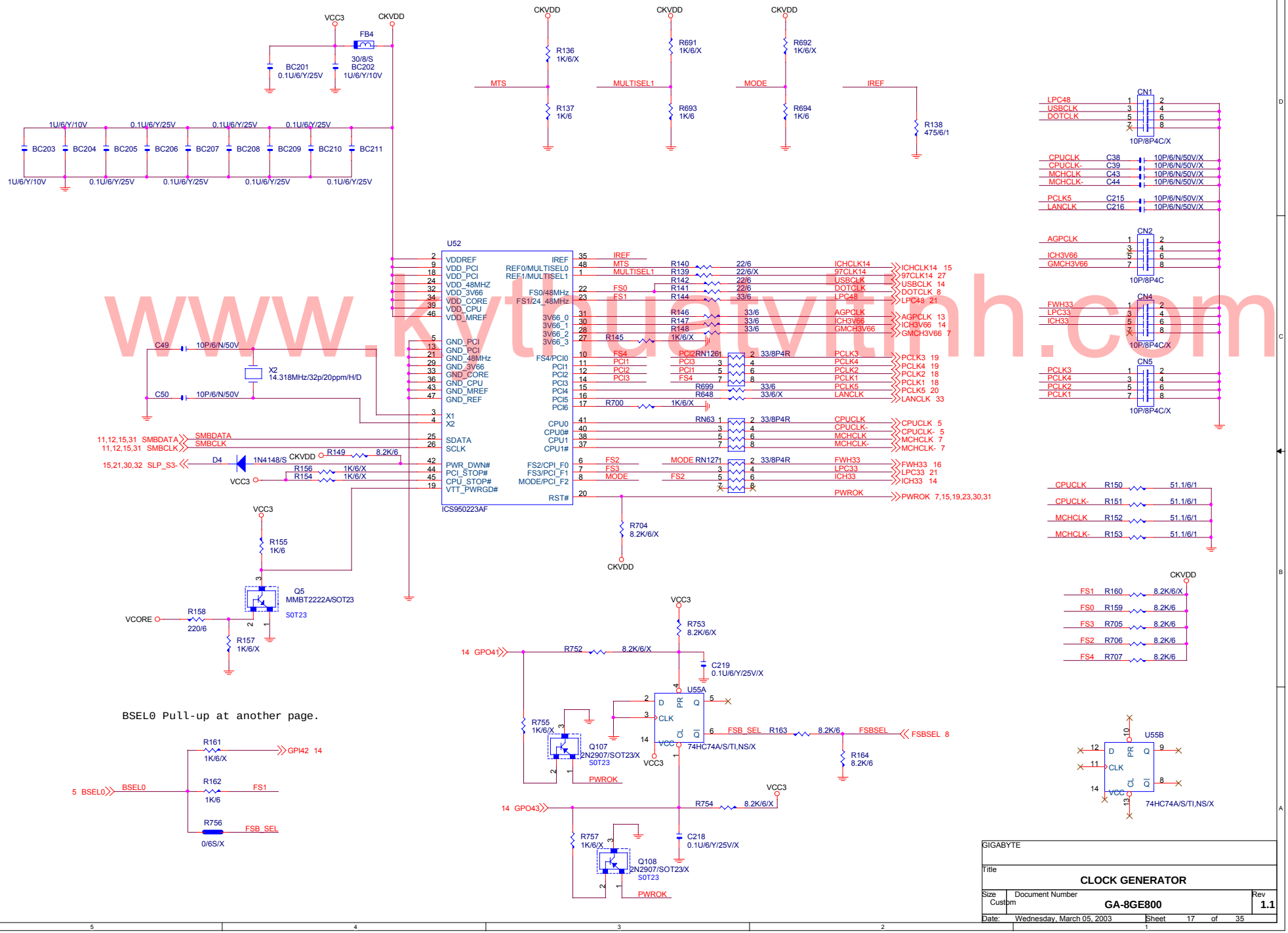
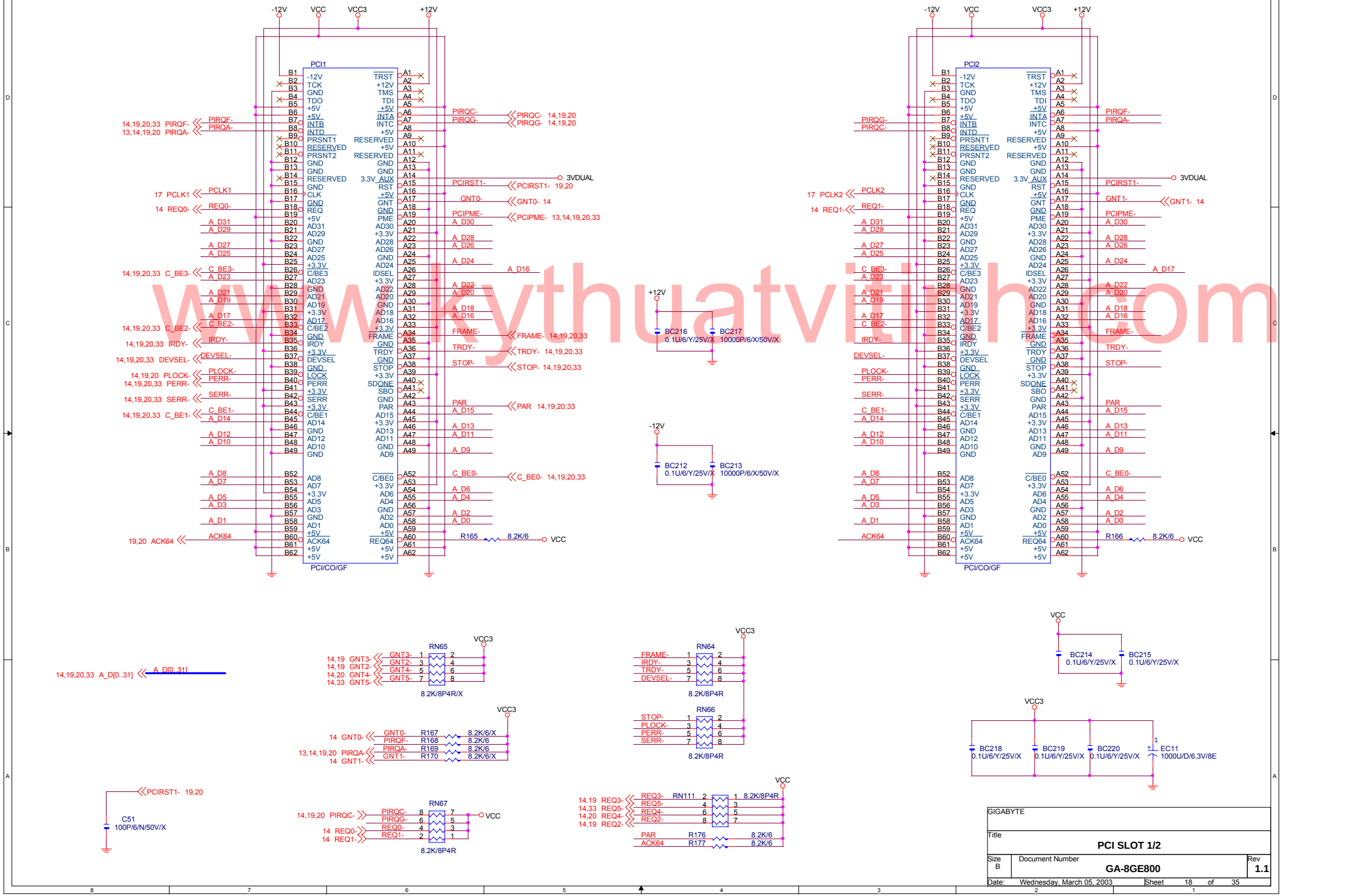
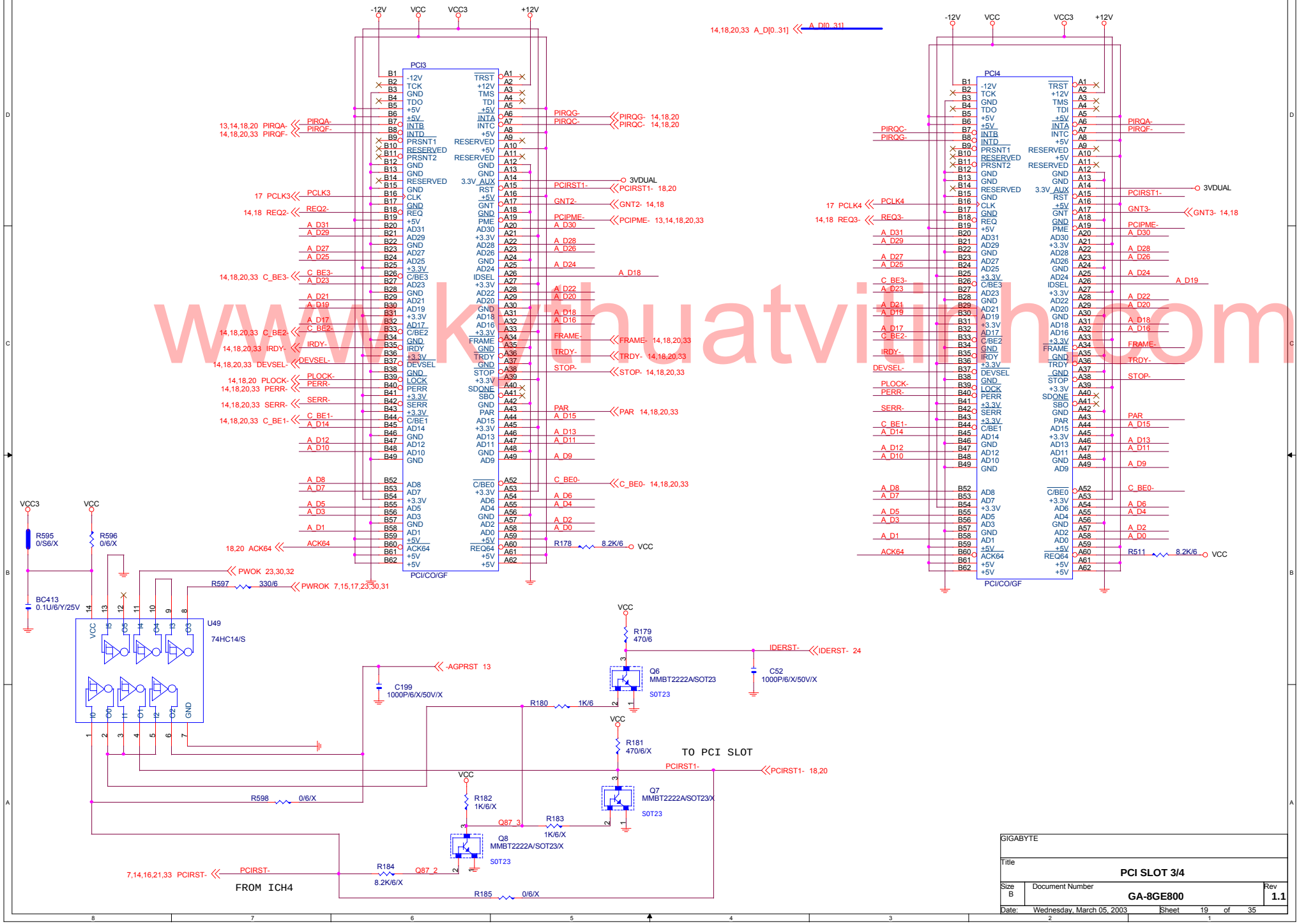
Size B	Document Number GA-8GE800	Rev 1.1
Date: Wednesday, March 05, 2003	Sheet 16 of 35	







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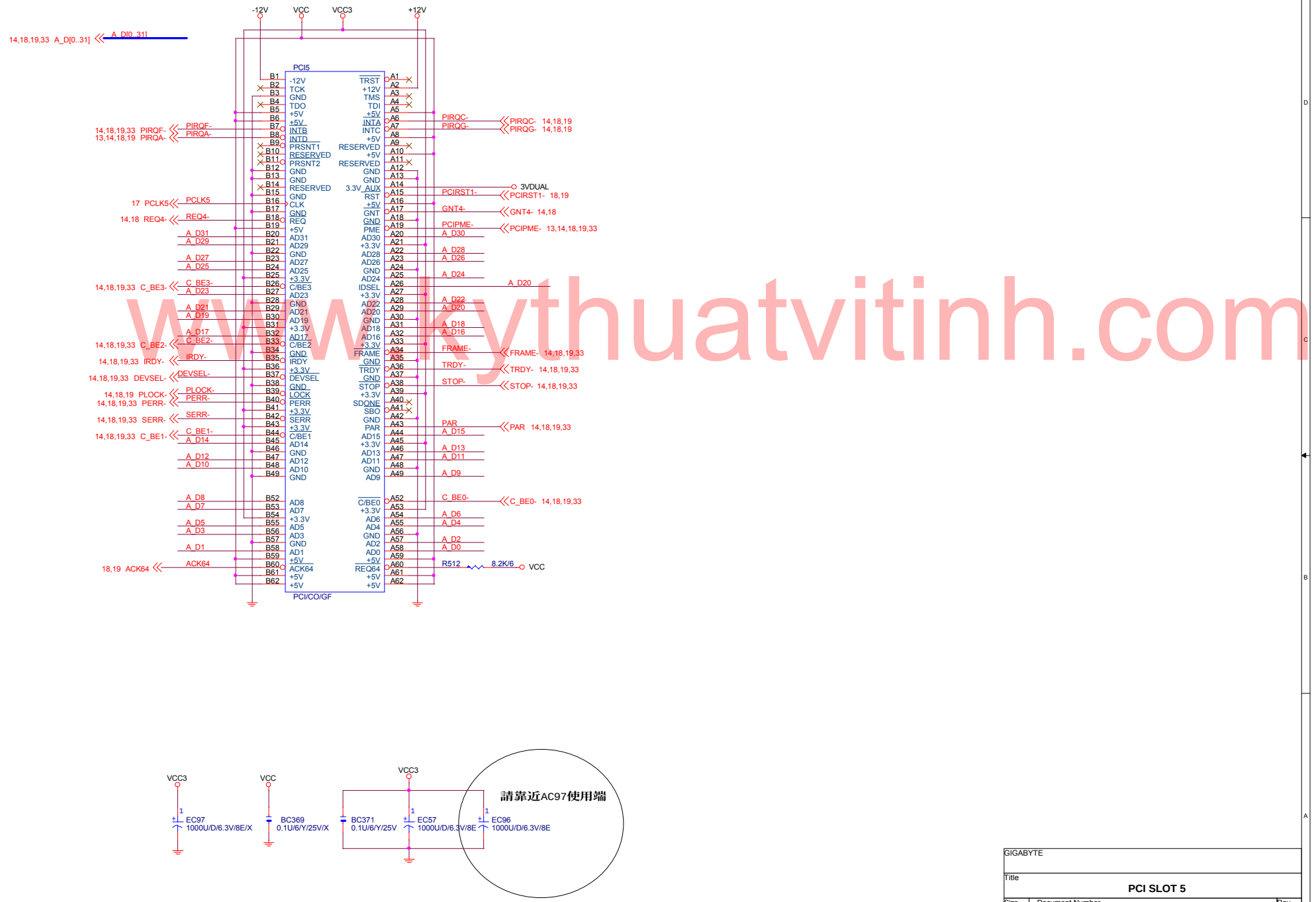
Size B	Document Number GA-8GE800	Rev 1.1
Date: Wednesday, March 05, 2003	Sheet 19 of 35	



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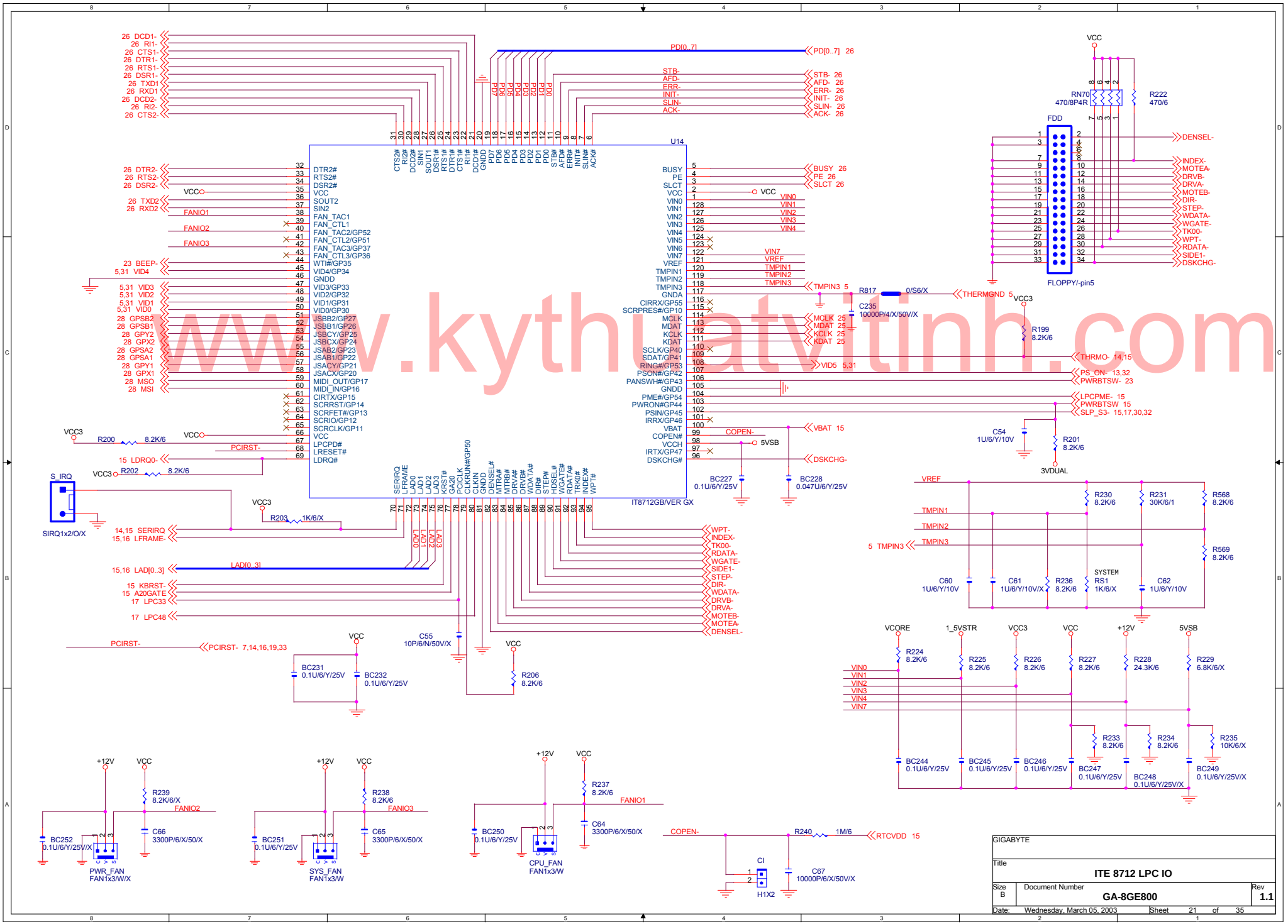
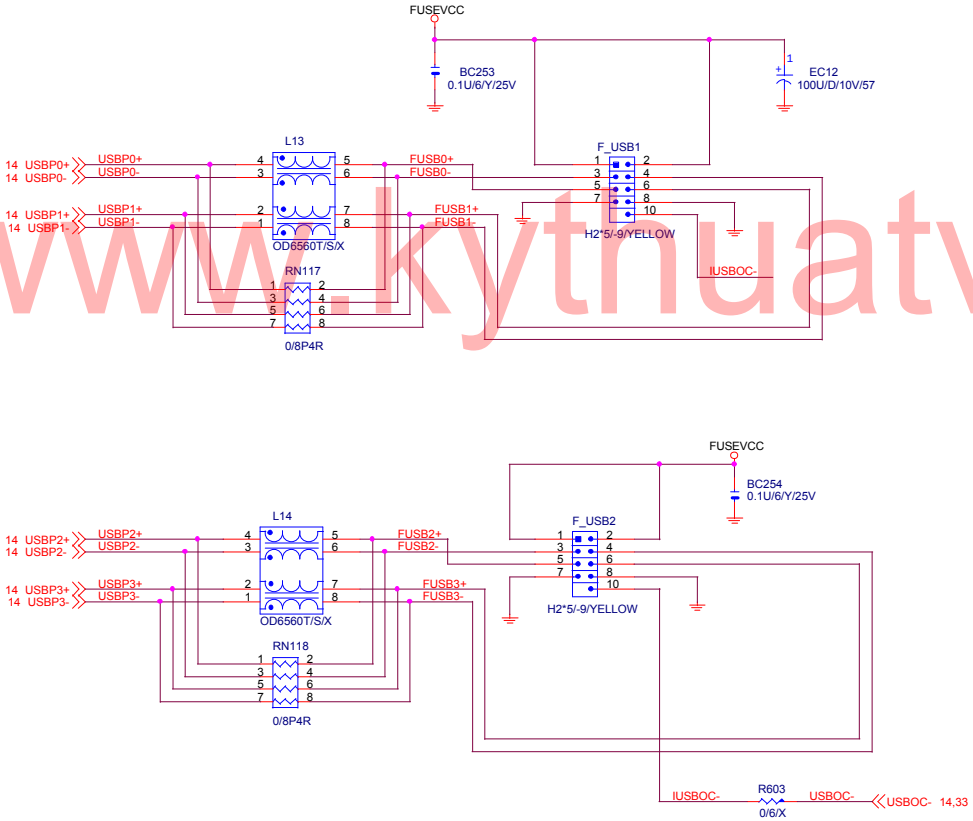
Size B	Document Number GA-8GE800	Rev 1.1
Date: Wednesday, March 05, 2003	Sheet 20 of 35	



ICH4 USB2.0



GIGABYTE		
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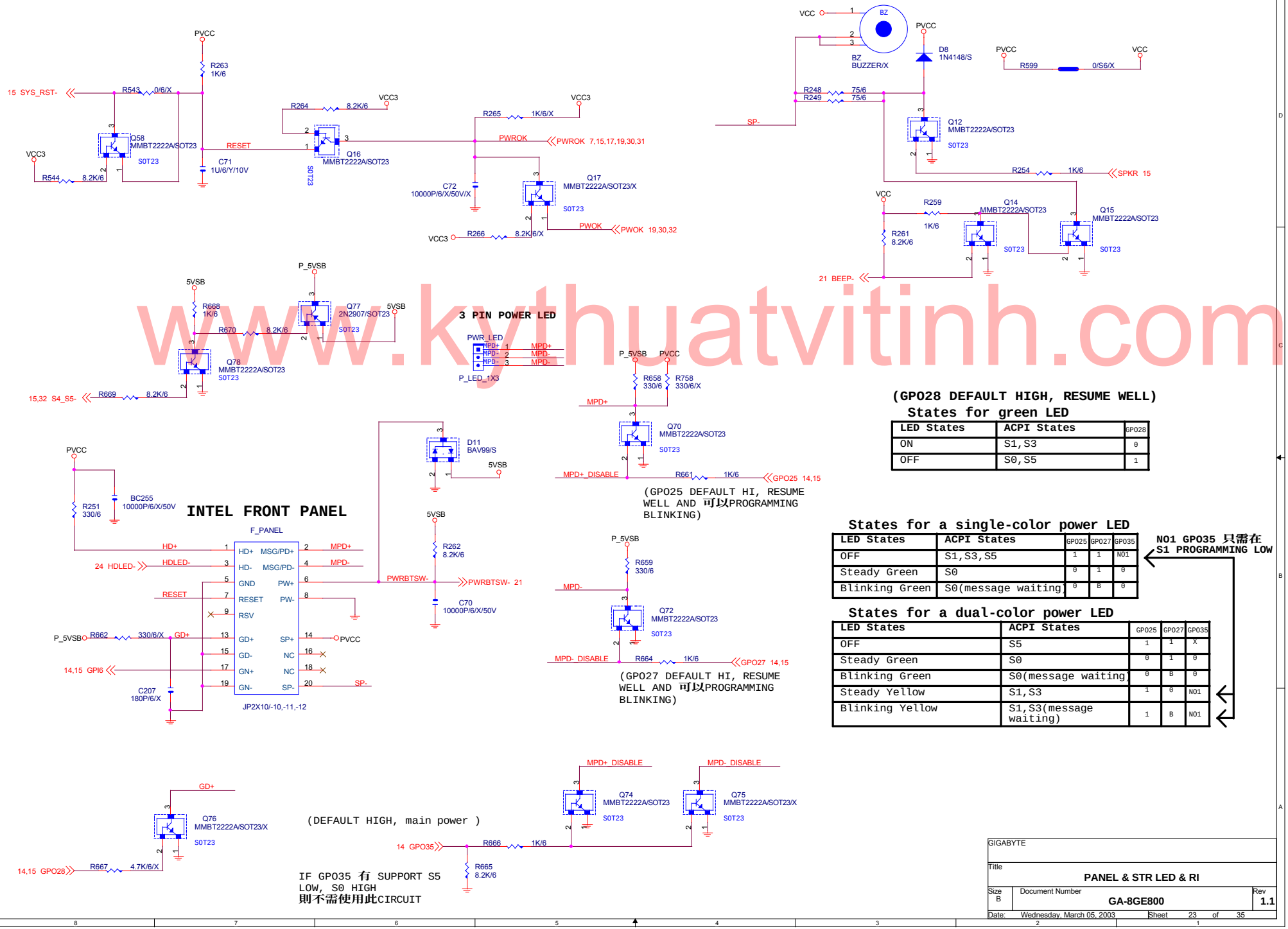
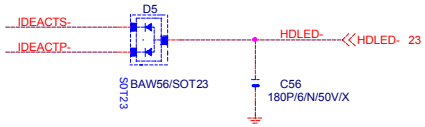
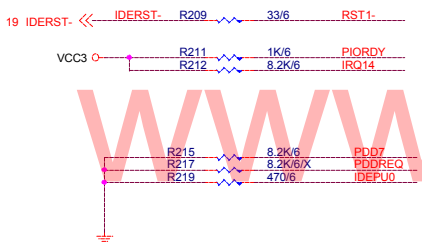
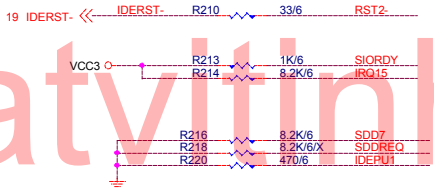
Size: B	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 22 of 35	

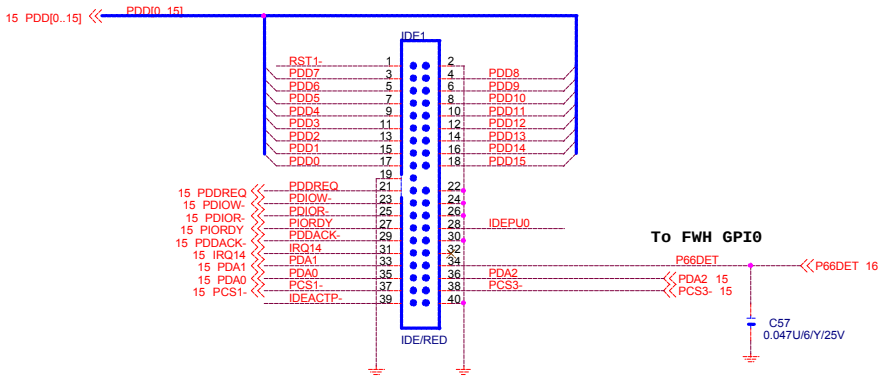




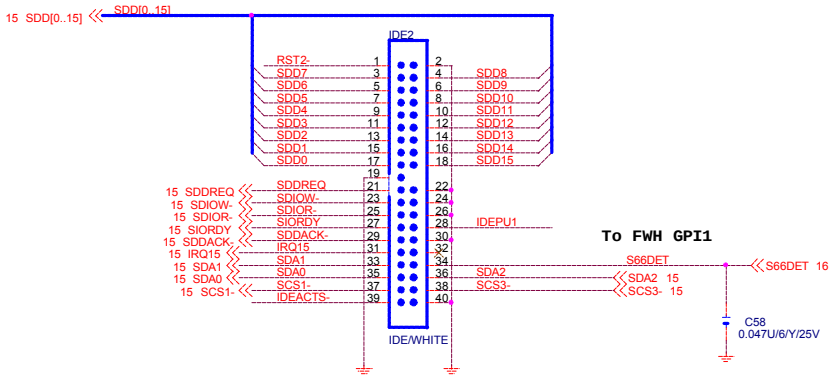




PRIMARY IDE CONNECTOR



SECONDARY IDE CONNECTOR



GIGABYTE		
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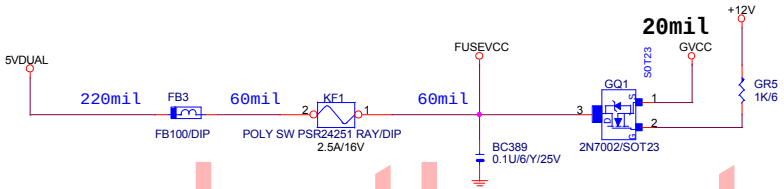
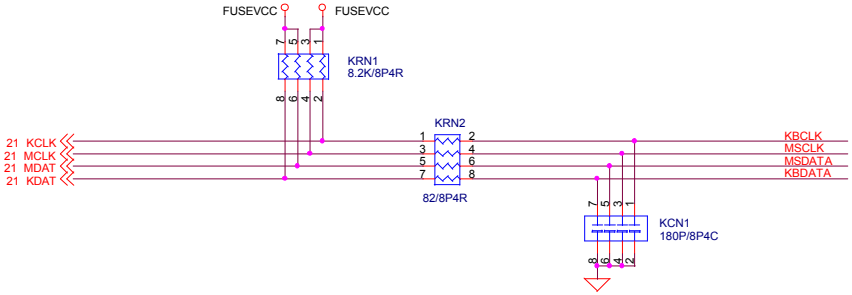
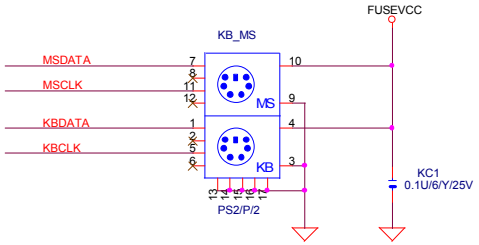
Size B	Document Number GA-8GE800	Rev 1.1
Date: Wednesday, March 05, 2003	Sheet 24 of 35	







模組化線路

GIGABYTE CORP.		
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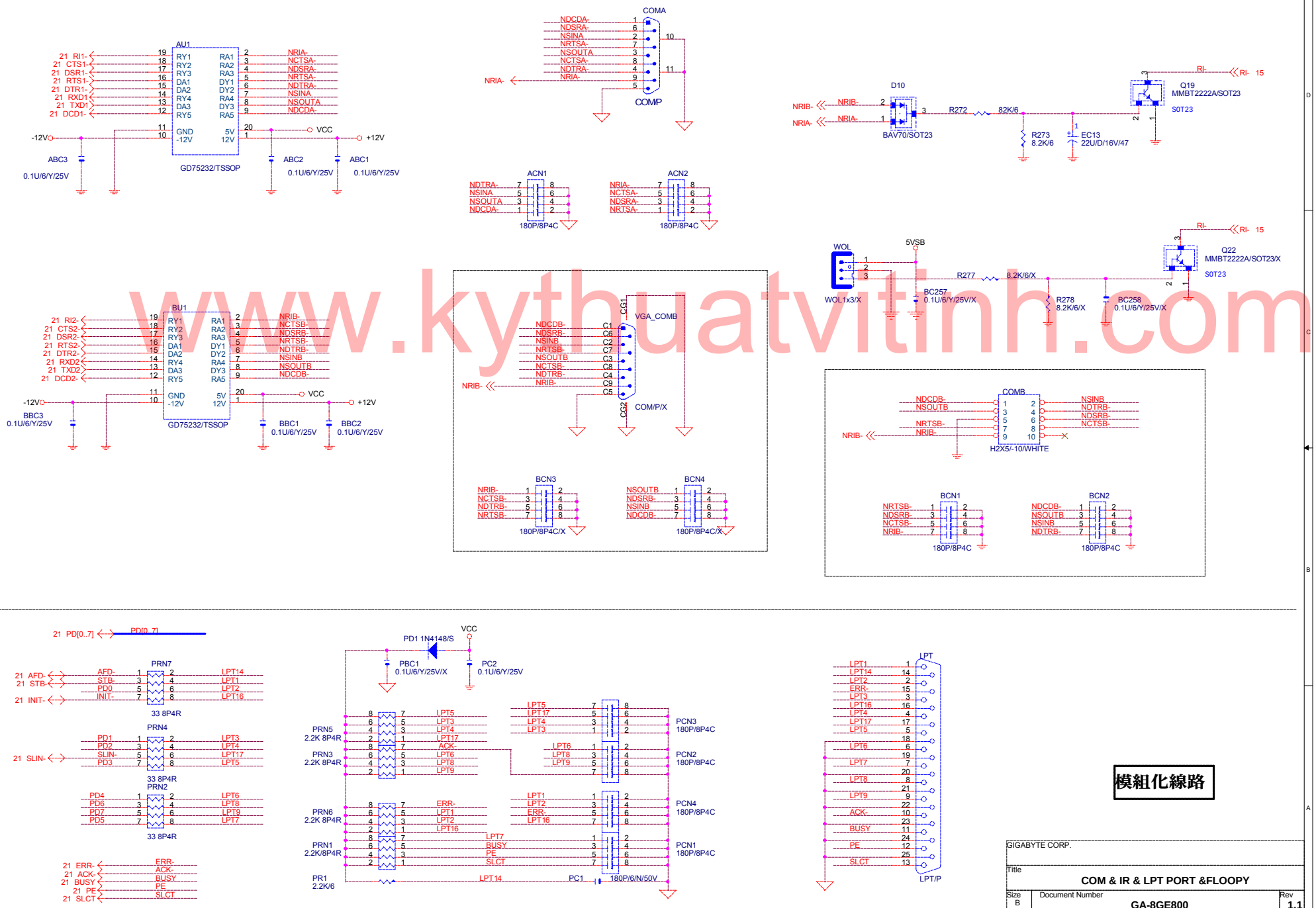
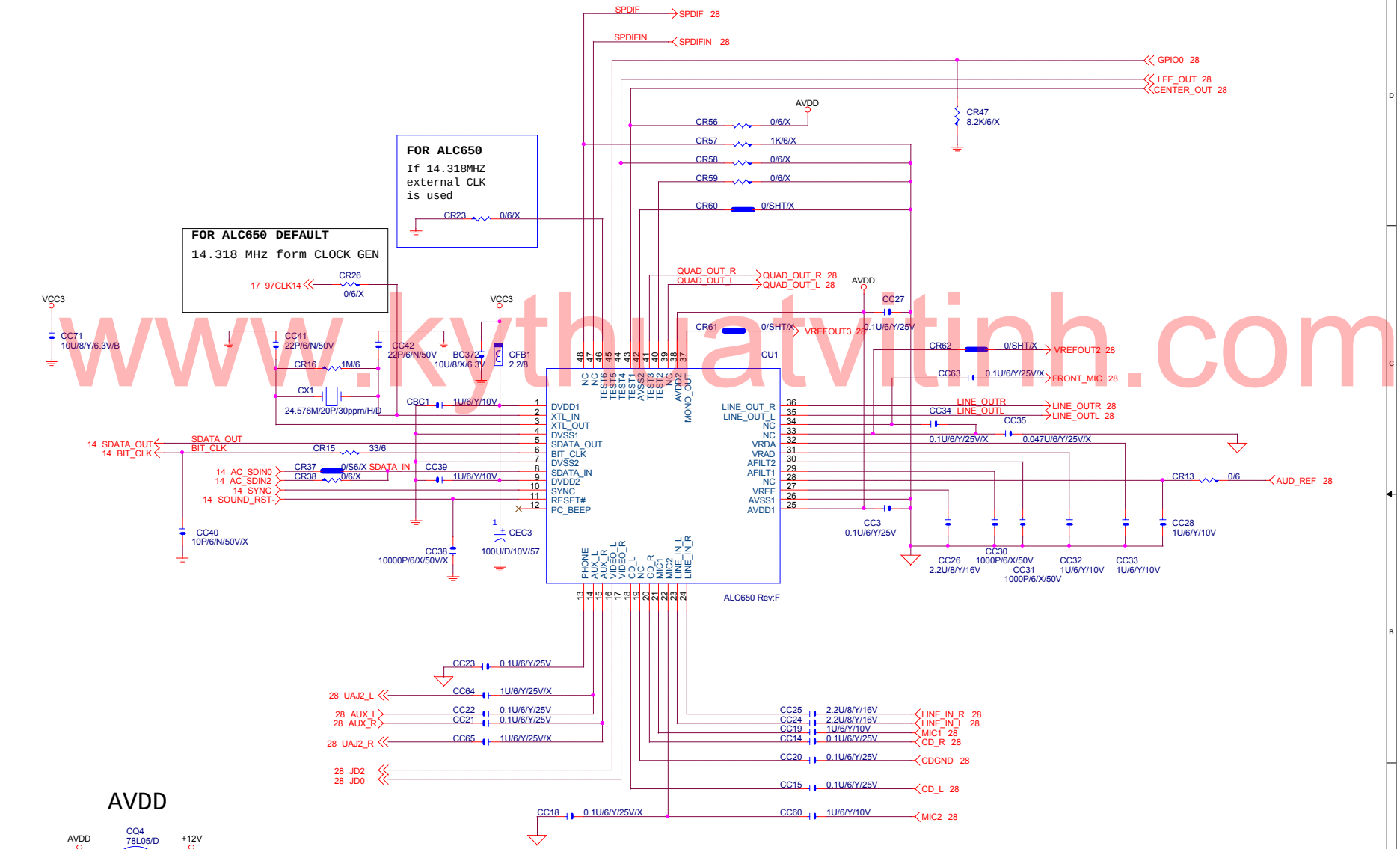
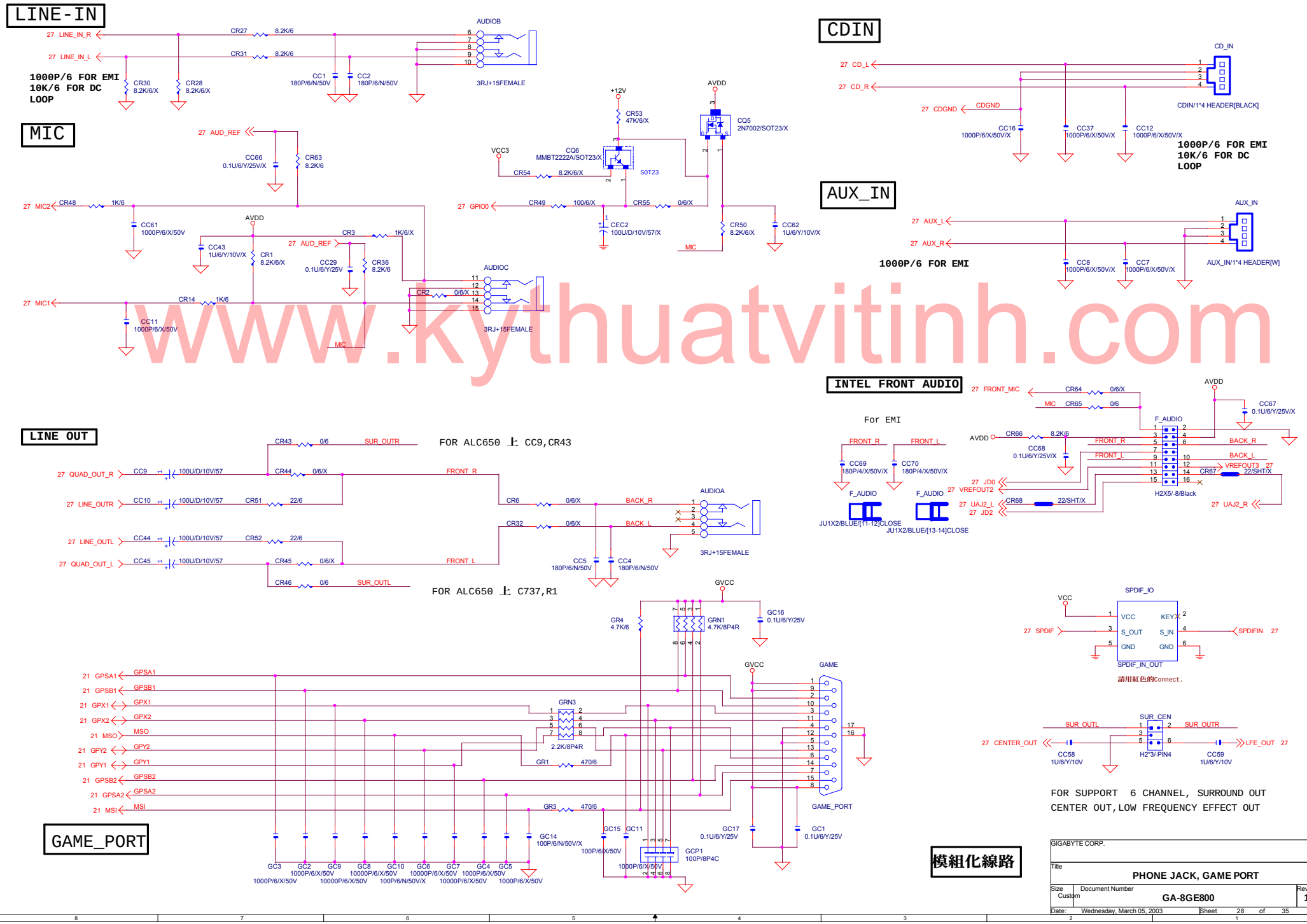
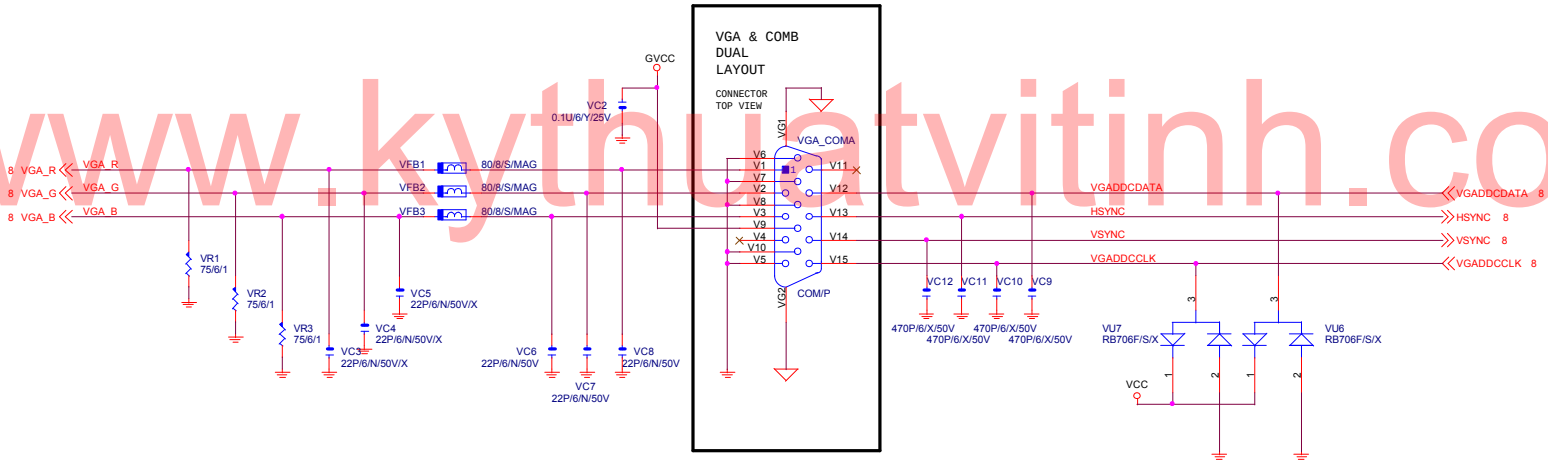
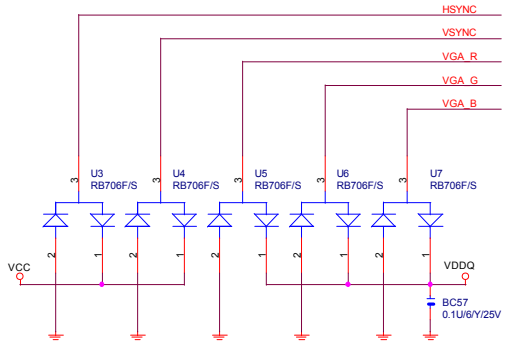
Size: B	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 25 of 35	











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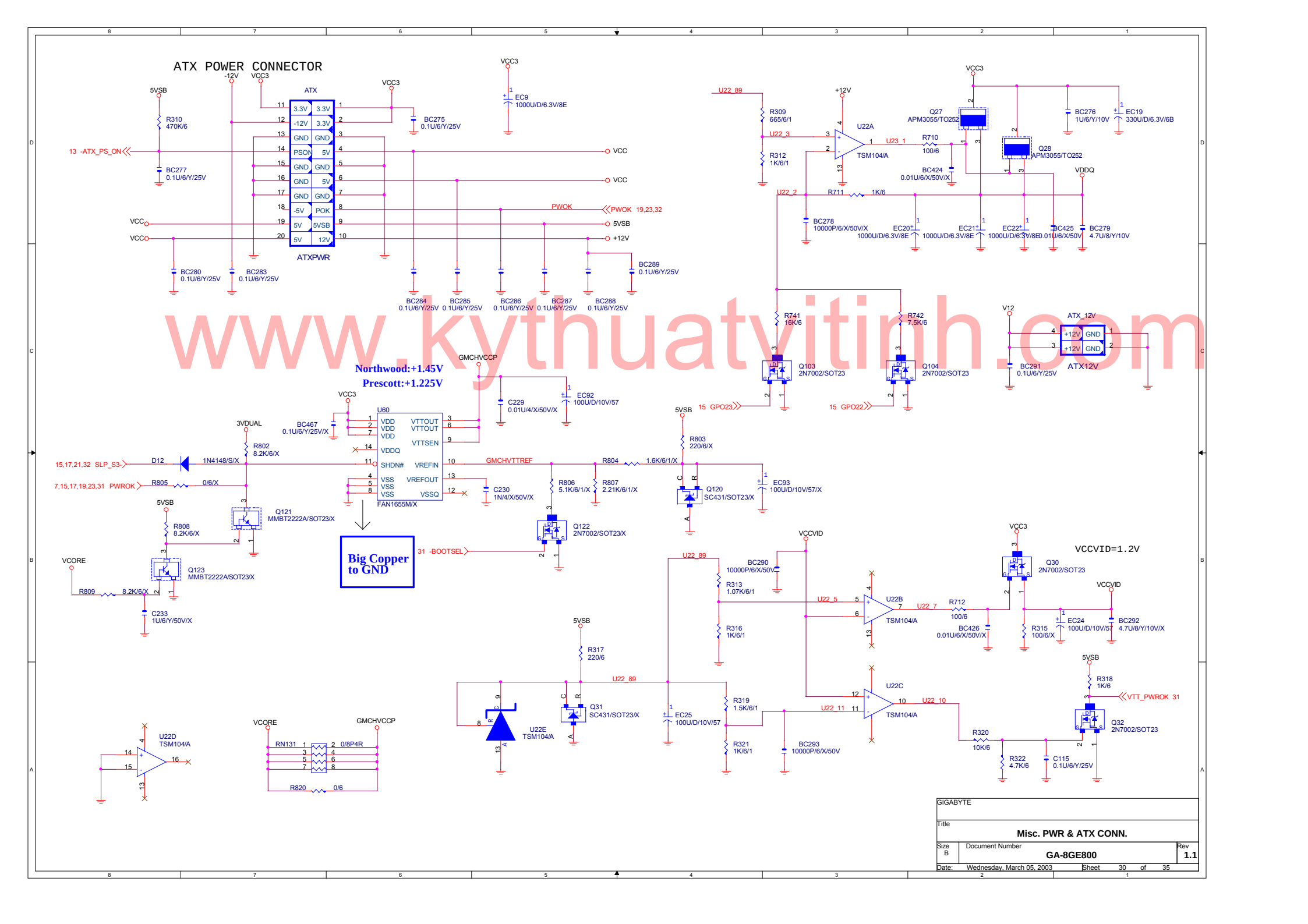
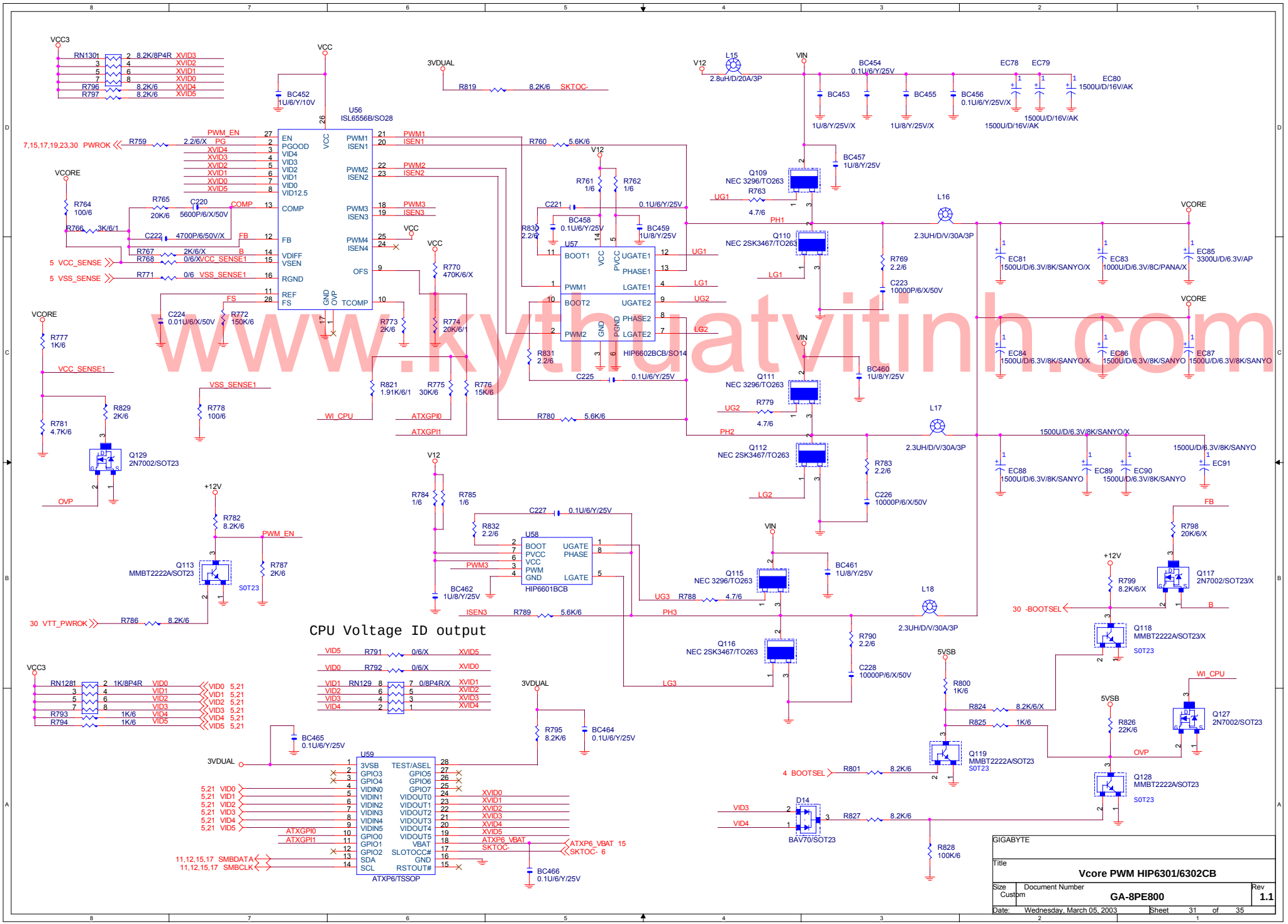
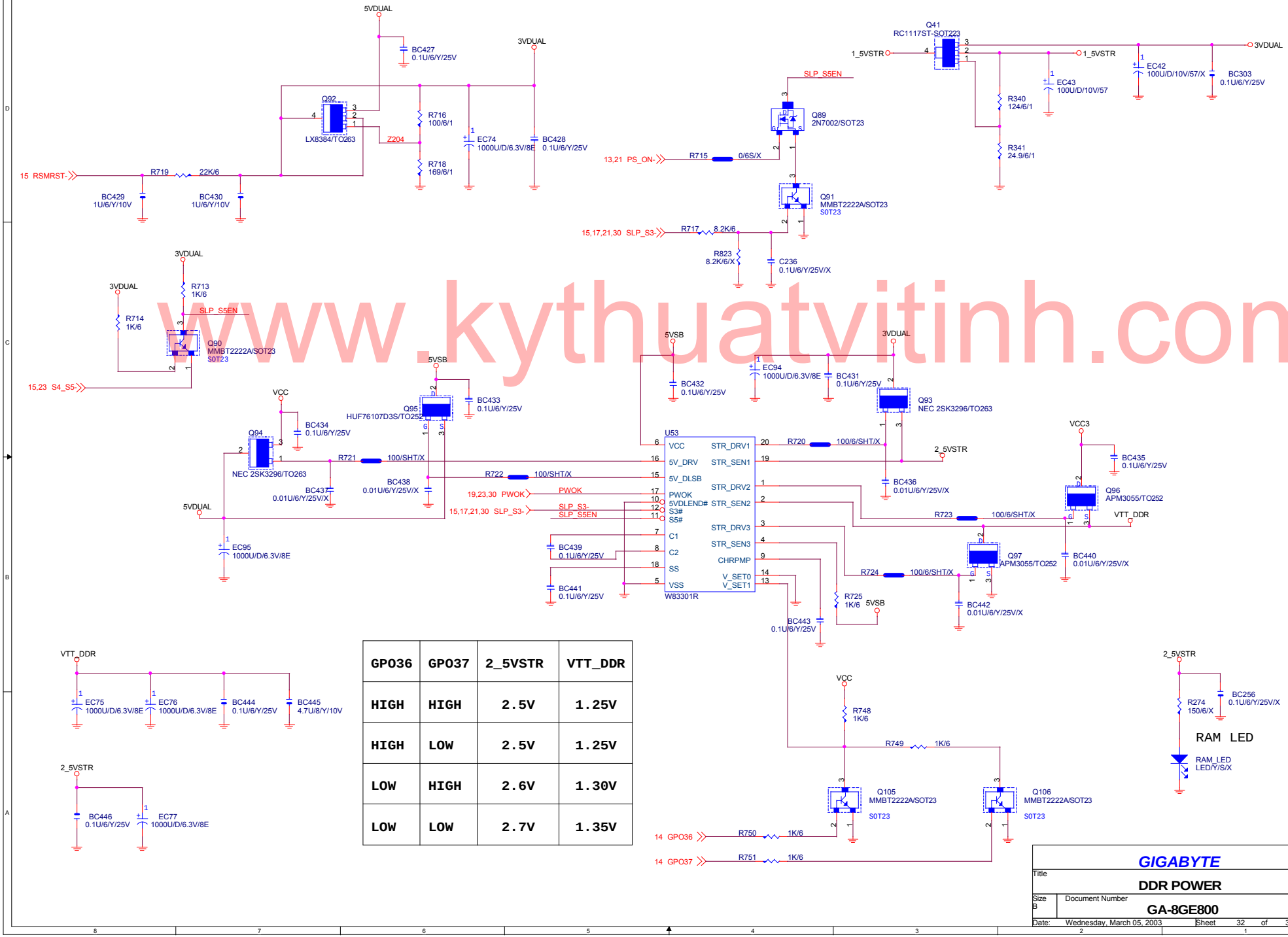
Title	VGA CONNECTOR	
Size: Custom	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 29 of 35	







GPO36	GPO37	2_5VSTR	VTT_DDR
HIGH	HIGH	2.5V	1.25V
HIGH	LOW	2.5V	1.25V
LOW	HIGH	2.6V	1.30V
LOW	LOW	2.7V	1.35V

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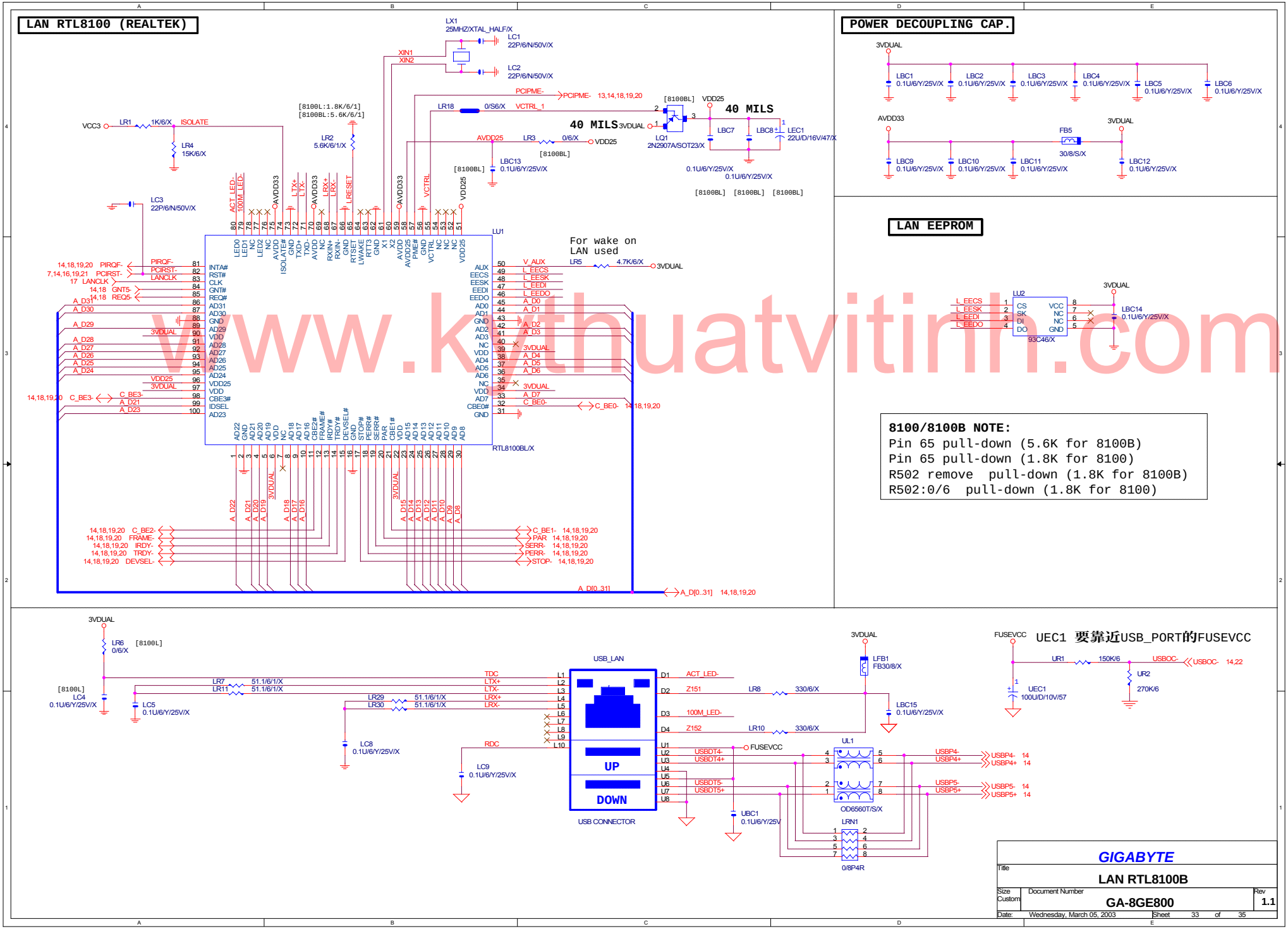
Title: DDR POWER

Size: B | Document Number: GA-8GE800 | Rev: 1.1

Date: Wednesday, March 05, 2003 | Sheet 32 of 35



GIGABYTE GA-8PE667 PCI ROUNTING LIST *Revision : 0.1*

PCI DEVICE	IDSEL	INT	CLOCK	REQ	GNT	
PCI SLOT1	16	C,F,G,A	PCLK1	REQ0-	GNT0-	
PCI SLOT2	17	F,G,A,C	PCLK2	REQ1-	GNT1-	
PCI SLOT3	18	G,A,C,F	PCLK3	REQ2-	GNT2-	
PCI SLOT4	19	A,C,F,G	PCLK4	REQ3-	GNT3-	
PCI SLOT5	20	C,F,G,A	PCLK5	REQ4-	GNT4-	
LAN	21	F	LANCLK	REQ5-	GNT5-	

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Title: PCI ROUNT LIST

Size: Custom | Document Number: GA-8GE800 | Rev: 1.1

Date: Wednesday, March 05, 2003 | Sheet 34 of 35

GIGABYTE GA-8PE667 GPIO LIST

Revision 0.1

SHEET	TITLE

GPI		
GPI0/REQA-		PULL DOWN 15K, detect IDE1 connector type.
GPI1/REQ5-		PULL DOWN 15K, detect IDE2 connector type.
GPI2/PIRQE-		PULL 8.2K TO VCC3
GPI3/PIRQF-		PULL 8.2K TO VCC3
GPI4/PIRQG-		PULL 8.2K TO VCC3
GPI5/PIRQH-		PULL 8.2K TO VCC3
GPI6		PULL 8.2K TO VCC3 (GREEN_BUTTON)
GPI7		NOT IMPLEMENTED
GPI8		PULL 8.2K TO 3VDUAL, LPC PME.
GPI9	NA	NOT IMPLEMENTED
GPI10	NA	NOT IMPLEMENTED
GPI11		PULL 4.7K TO 3VDUAL (SMBALERT)
GPI12		PULL DOWN 10K.
GPI13		PULL DOWN 10K, CNR_PRIMARY
GPI14	NA	NOT IMPLEMENTED
GPI15	NA	NOT IMPLEMENTED

SHEET	TITLE

GPO		
GPO16		PULL 8.2K TO VCC3
GPO17		PULL 8.2K TO VCC3 (GNT5-)
GPO18		PULL 8.2K TO VCC3
GPO19		PULL 8.2K TO VCC3
GPO20		PULL 8.2K TO VCC3
GPO21		PULL 8.2K TO VCC3
GPO22		PULL 8.2K TO VCC3
GPO23		PULL 8.2K TO VCC3
GPO24		PULL 1K TO 3VDUAL (TOP BLOCK)
GPO25		PULL 4.7K TO 3VDUAL, POWER LED CONTROL.
GPO26		NOT IMPLEMENTED
GPO27		PULL 8.2K TO 3VDUAL, POWER LED CONTROL.
GPO28		PULL 8.2K TO 3VDUAL,GREEN LED.
GPO32		PULL 8.2K TO 3VDUAL,BIOS WRITE PROTECT.
GPO35		PULL DOWN 10K, POWER LED CONTROL.

GIGABYTE		
Title: GPIO LIST		
Size: Custom	Document Number: GA-8GE800	Rev: 1.1
Date: Wednesday, March 05, 2003	Sheet 35 of 35	