

Model Name:GA-G31M-ES2C

Revision 1.12

SHEET

TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B,D
06	P4 LGA775 C
07	P4 LGA775 E,F,G,H
08	G31 HOST
09	G31 DDRII
10	G31 PCI E, DMI
11	G31 VGA
12	G31 GND
13	G31 PWR
14	PCI EXPRESS*16 SLOT
15	DDRII CHANNEL A
16	DDRII CHANNEL B
17	DDRII TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY
24	ITE 8718 GB/HX
25	COM LPT
26	CI,HWM,KB/MS,DUALBIOS
27	ALC883

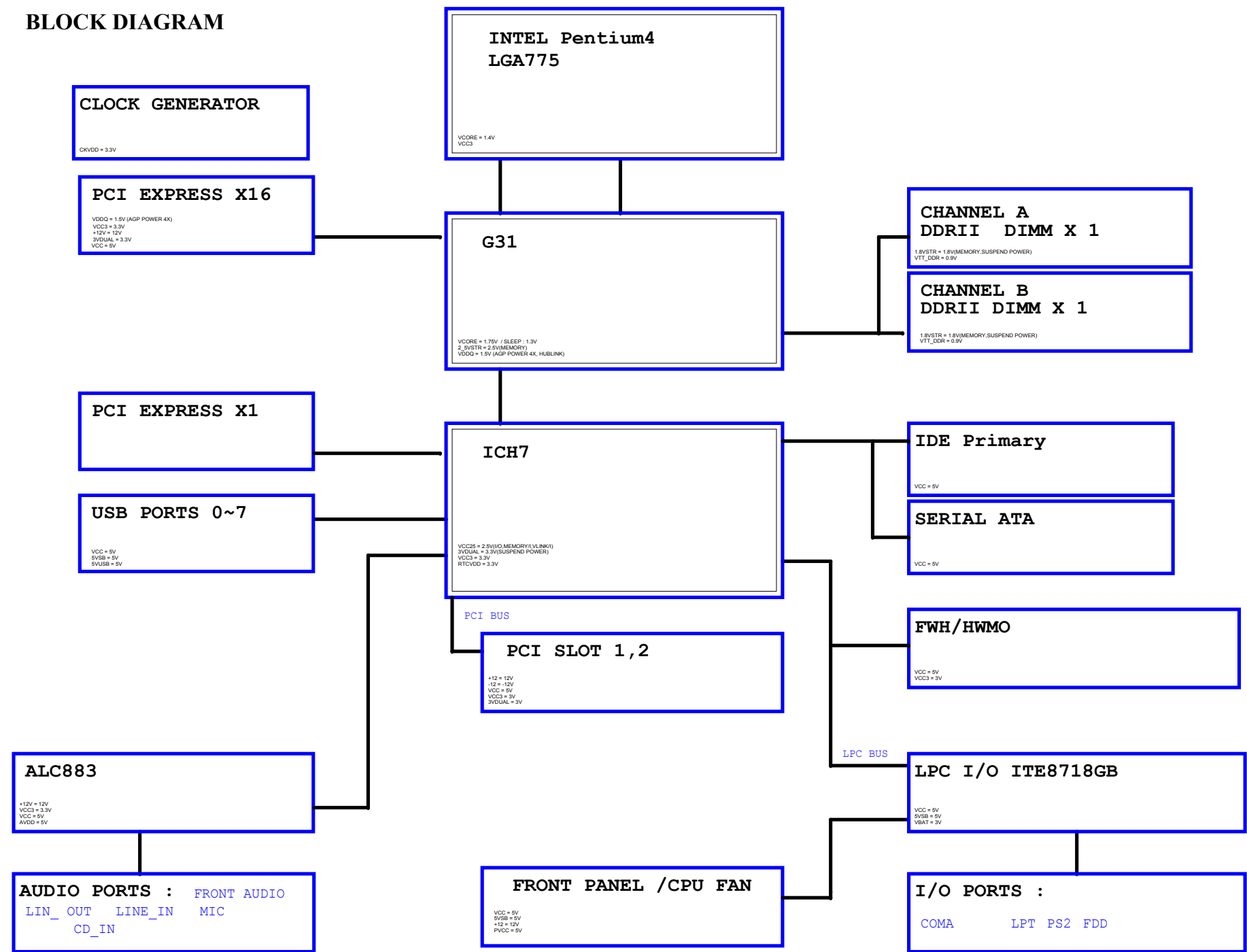
SHEET

TITLE

28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM RT9214
31	ATX, OTHERS POWER
32	FRONT PANEL
33	REALTEK RTL8111C/8102E

Gigabyte Technology			
Title			
Cover Sheet			
Size	Document Number	Rev	
Custom	GA-G31M-ES2C	1.12	
Date:	Friday, November 21, 2008	Sheet	1 of 33

BLOCK DIAGRAM

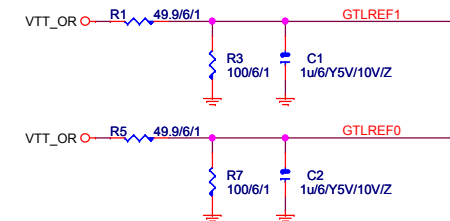
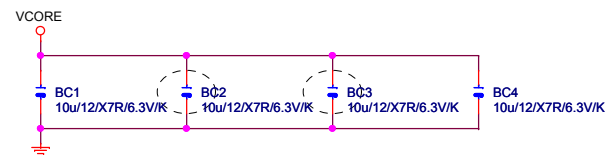
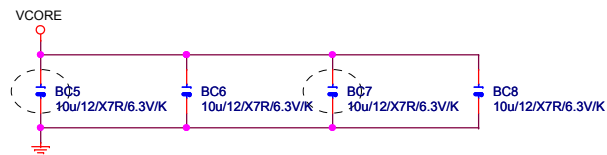


Version: 1.12

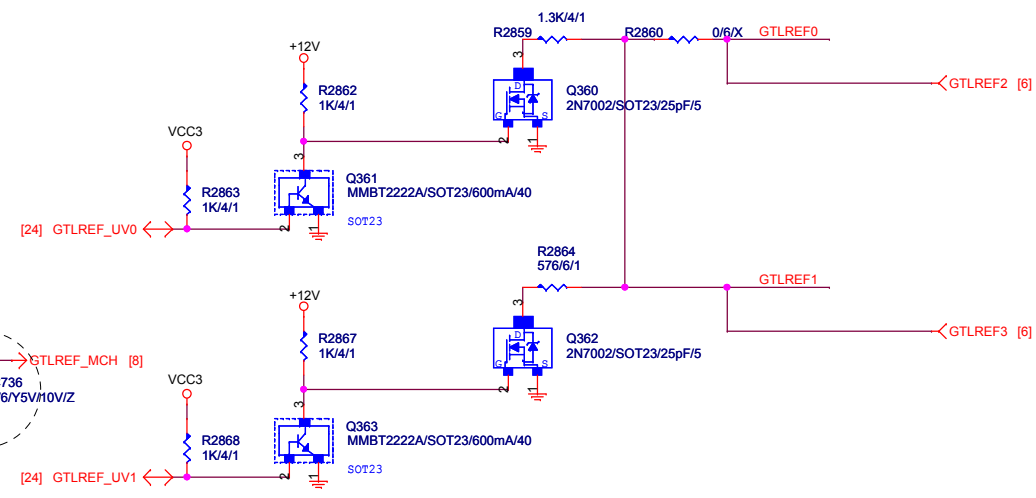
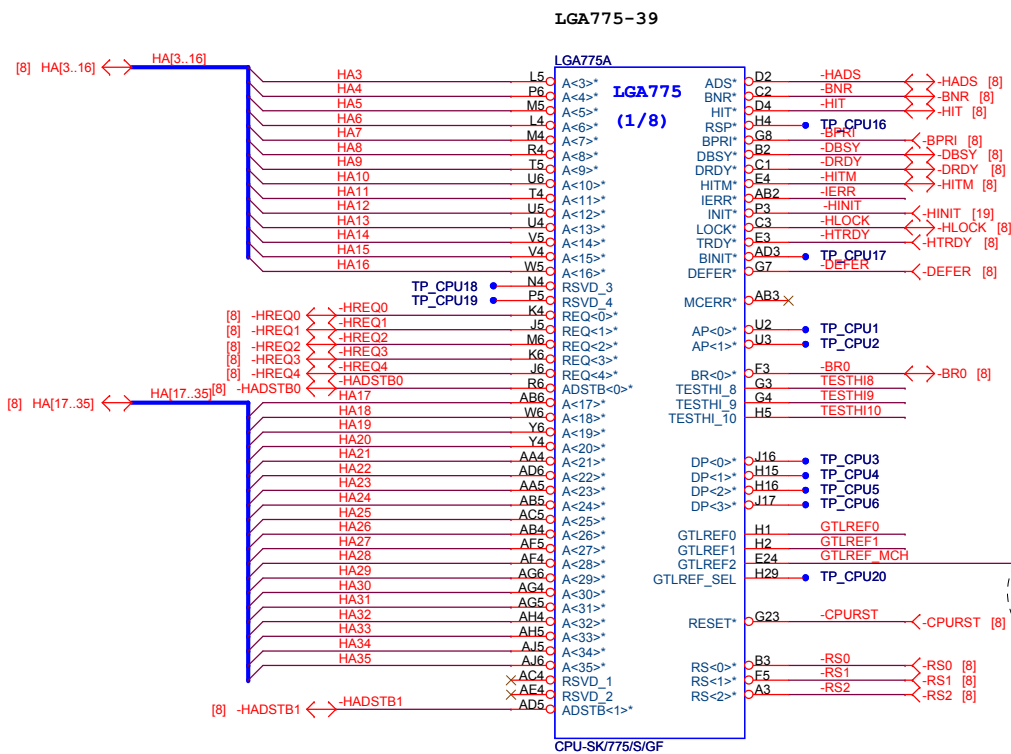
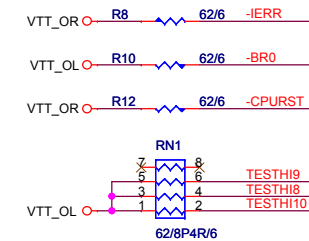
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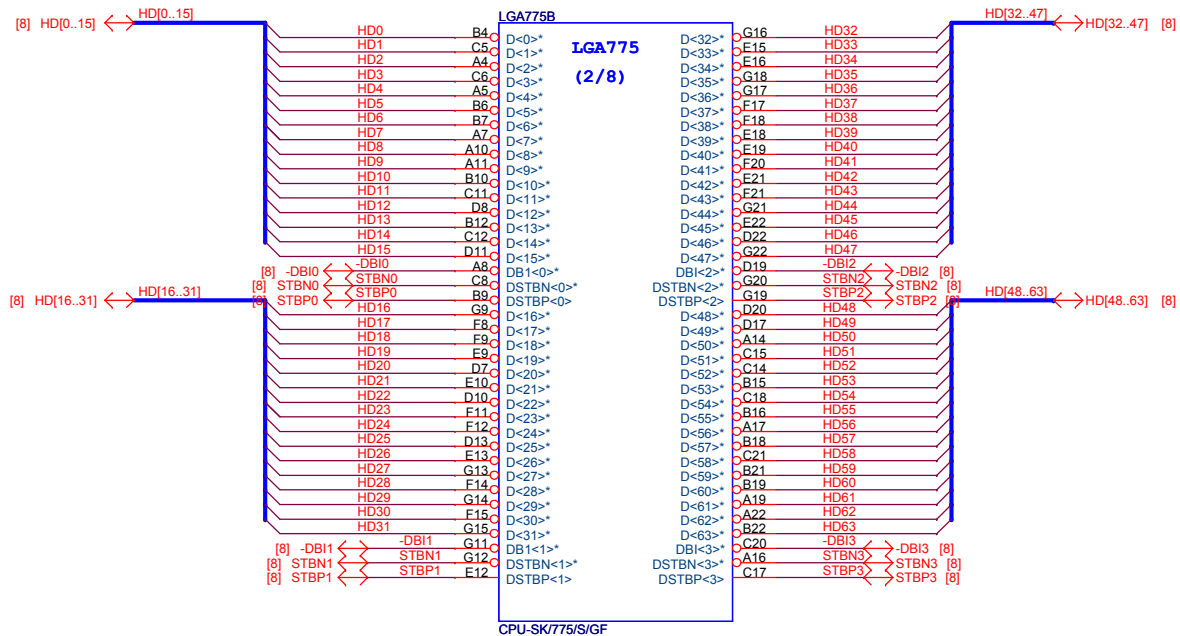
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<i>Gigabyte Technology</i>			
BOM & PCB MODIFY HISTORY			
Size Custom	Document Number	GA-G31M-ES2C	Rev 1.12
Date:	Tuesday, November 25, 2008	Sheet	3 of 33

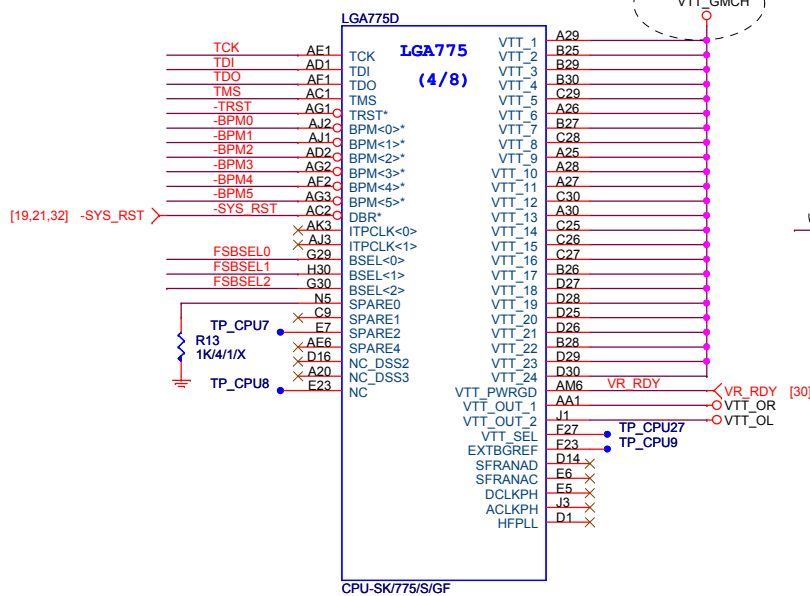


中間値0.9V

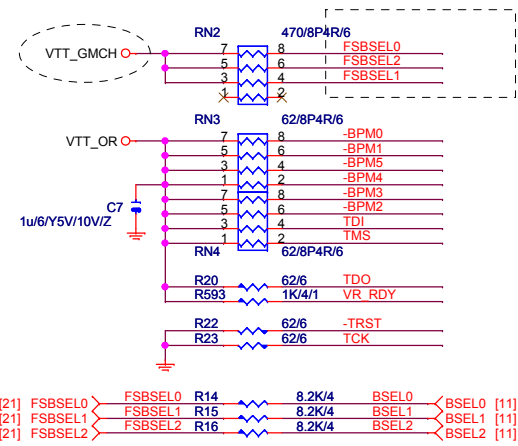
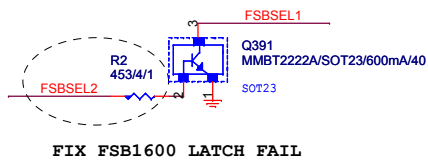




CPU-SK/775/S/GF



CPU-SK/775/S/GF



TO CLK GEN

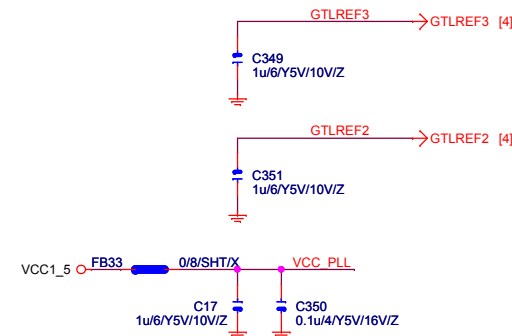
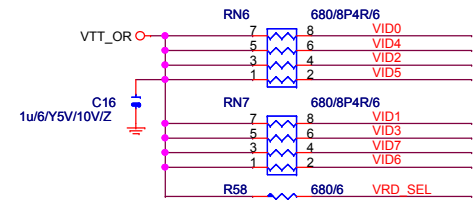
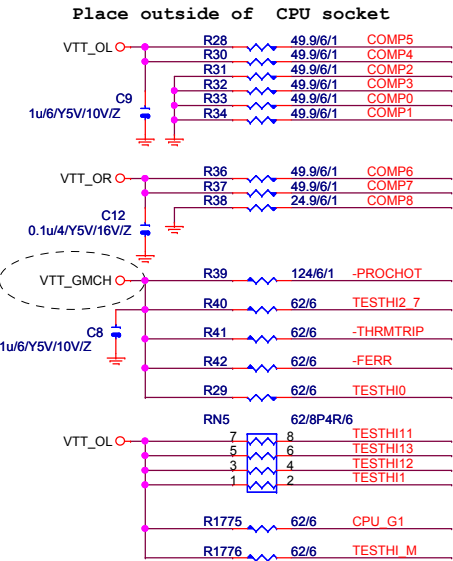
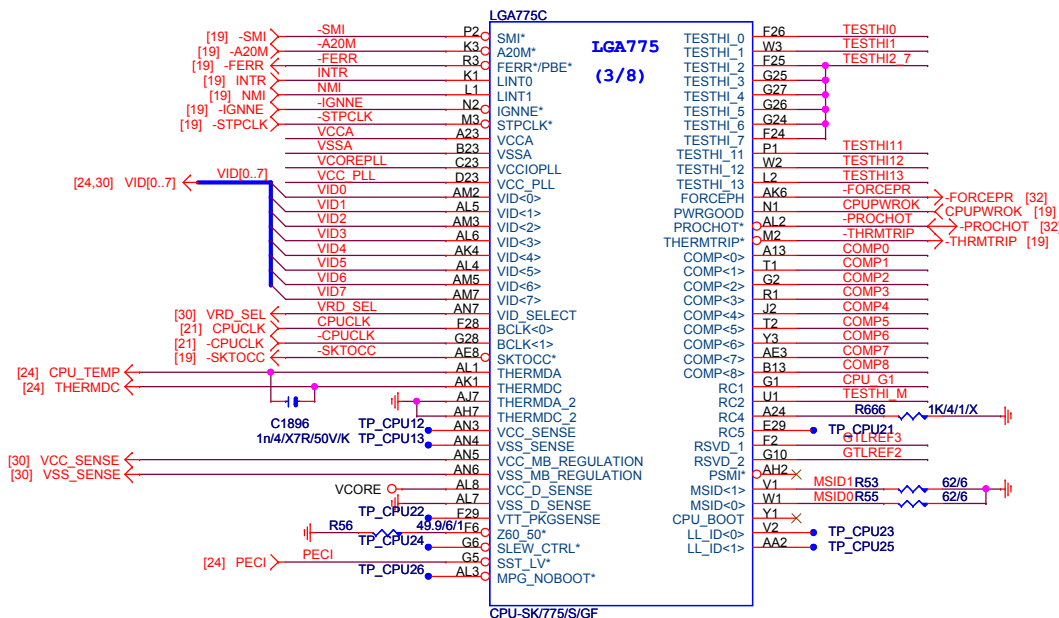
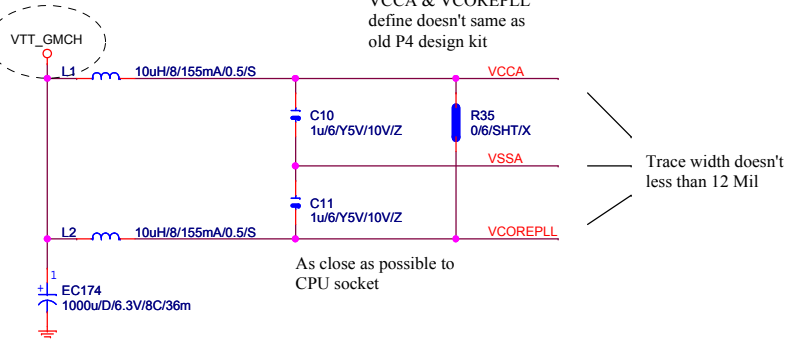
TO NB

CPU

NA	FSB	FSA	
FSBSEL3	FSBSEL1	FSBSEL0	Clock
1	0	1	100MHz
0	0	1	133MHz
0	1	1	166MHz
0	1	0	200MHz
0	0	0	266MHz

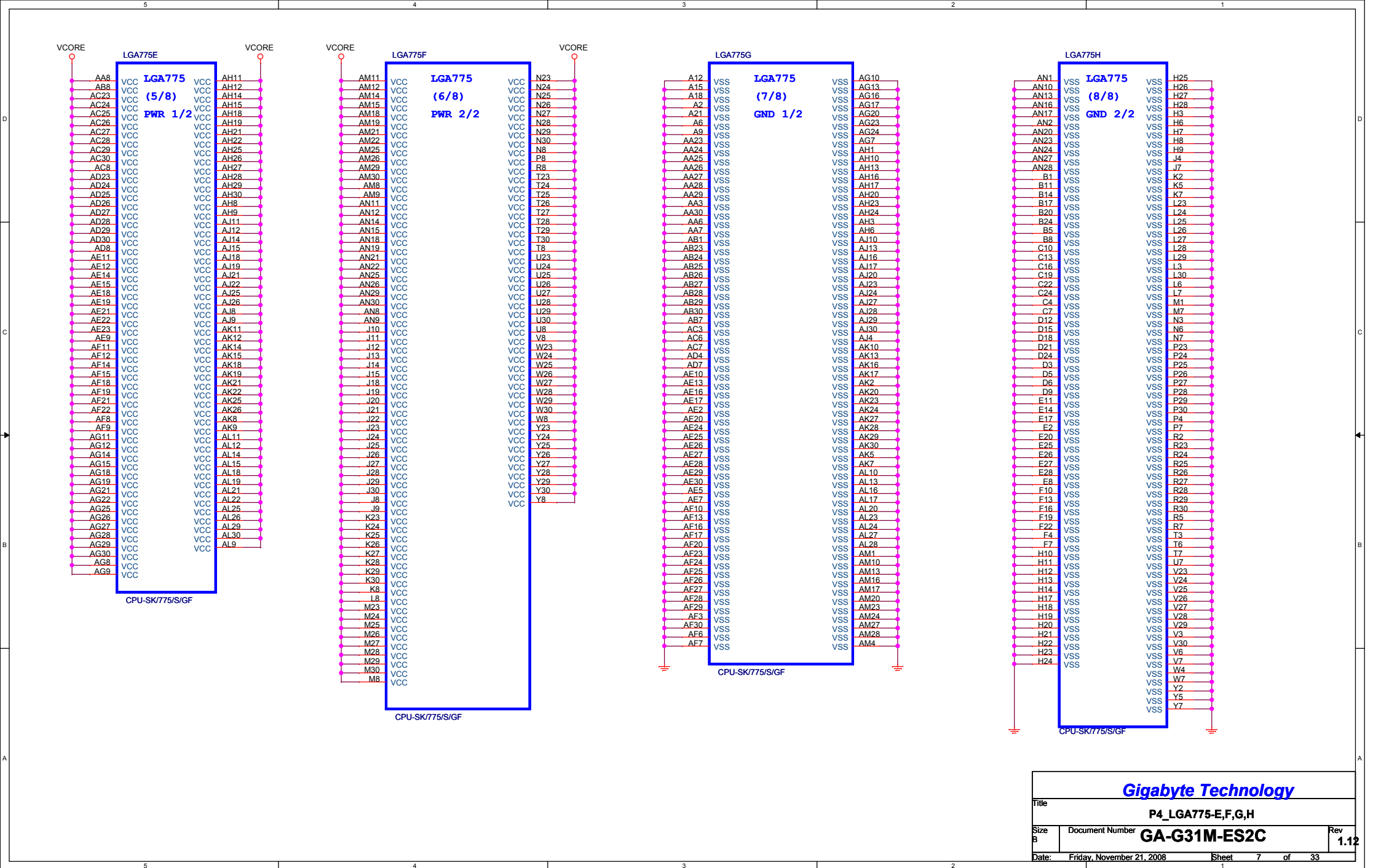
X

Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



Gigabyte Technology			
Title			
P4_LGA775-C			
Size	Document Number	GA-G31M-ES2C	
B		Rev	
		1.12	
Date:	Friday, November 21, 2008	Sheet	6 of 33

PECI:Platform Environment Control Interface





Title			
GMCH-HOST			
Size B	Document Number	GA-G31M-ES2C	Rev 1.12
Date:	Friday, November 21, 2008	Sheet 8 of 33	

U2C

BW
(3/9)

MAAA0 BA31 SMA_A0 SDQS_A0
 MAA01 BB25 SMA_A1 SDQS_A1#
 MAA02 BA26 SMA_A2 SDQS_A1#
 MAA03 BA25 SMA_A3 SDQS_A1#
 MAA04 AY25 SMA_A4 SDQS_A1#
 MAA05 BA23 SMA_A5 SDQS_A1#
 MAA06 AY24 SMA_A6 SDQS_A1#
 MAA07 AY23 SMA_A7 SDQS_A1#
 MAA08 BB23 SMA_A8 SDQS_A1#
 MAA09 BA22 SMA_A9 SDQS_A1#
 MAA10 AY23 SMA_A10 SDQS_A1#
 MAA11 BB22 SMA_A11 SDQS_A1#
 MAA12 AY23 SMA_A12 SDQS_A1#
 MAA13 AY38 SMA_A13 SDQS_A1#
 MAA14 BA21 SMA_A14 SDQS_A1#

[15.17] -SWEA ← BB3AC SWE_A#
 [15.17] -SCASA ← AY35C SCAS_A#
 [15.17] -SRASA ← BB33C SRAS_A#

[15.17] SBA00 ← BA33 SBA_A0
 [15.17] SBA01 ← BB32 SBA_A1
 [15.17] SBA02 ← BB32 SBA_A2

[15.17] CSA0 ← AW35C CSA_A0#
 [15.17] CSA1 ← BA35C CSA_A1#
 [17] CSA2 ← BA34C CSA_A2#
 [17] CSA3 ← BB38C CSA_A3#

[15.17] CKEA0 ← BC20 SCKE_A0
 [15.17] CKEA1 ← AY20 SCKE_A1
 [15.17] CKEA2 ← AY21 SCKE_A2
 [17] CKEA3 ← BA19 SCKE_A3

MODT_A0 AY37 MODT_A0
 MODT_A1 BA38 MODT_A1
 BB38 MODT_A1
 BA39 MODT_A1

[15] DCLKA0 ← AU31 SCLK_A0#
 [15] DCLKA0 ← AR31C SCLK_A0#
 [15] DCLKA1 ← AP27 SCLK_A1#
 [15] DCLKA1 ← AN27 SCLK_A1#
 [15] DCLKA2 ← AV33C SCLK_A2#
 [15] DCLKA2 ← AW33C SCLK_A2#

SDQS_A4 DQS44
 SDQS_A4# DQS44
 SDQS_A4 DQS44

SDQ_A32 AV42 MDA32
 SDQ_A33 AV42 MDA33
 SDQ_A34 AV42 MDA34
 SDQ_A35 AV42 MDA35
 SDQ_A36 AV42 MDA36
 SDQ_A37 AV42 MDA37
 SDQ_A38 AV42 MDA38
 SDQ_A39 AV42 MDA39

SDQS_A5 AL41 DQS45
 SDQS_A5# AL41 DQS45
 SDQS_A5 AL41 DQS45

SDQ_A40 AL40 MDA40
 SDQ_A41 AL40 MDA41
 SDQ_A42 AL40 MDA42
 SDQ_A43 AL40 MDA43
 SDQ_A44 AL40 MDA44
 SDQ_A45 AL40 MDA45
 SDQ_A46 AL40 MDA46
 SDQ_A47 AL40 MDA47

SDQS_A6 AG42 DQS46
 SDQS_A6# AG42 DQS46
 SDQS_A6 AG42 DQS46

SDQ_A48 AJ40 MDA48
 SDQ_A49 AH43 MDA49
 SDQ_A50 AF39 MDA50
 SDQ_A51 AE40 MDA51
 SDQ_A52 AJ42 MDA52
 SDQ_A53 AJ41 MDA53
 SDQ_A54 AF41 MDA54
 SDQ_A55 AF42 MDA55

SDQS_A7 AC42 DQS47
 SDQS_A7# AC42 DQS47
 SDQS_A7 AC42 DQS47

RESERVED

SDQ_A56 AD40 MDA56
 SDQ_A57 AD43 MDA57
 SDQ_A58 AB41 MDA58
 SDQ_A59 AA40 MDA59
 SDQ_A60 AE42 MDA60
 SDQ_A61 AE41 MDA61
 SDQ_A62 AC39 MDA62
 SDQ_A63 AB42 MDA63

DDR_0

AN21

LE82G31B0/S[10HB1-038231-30R]

CHANNEL A

U2D

BW
(4/9)

MAAB0 BB17 SMA_B0 SDQS_B0
 MAAB1 AY17 SMA_B1 SDQS_B0#
 MAAB2 BA17 SMA_B2 SDQS_B0#
 MAAB3 BC16 SMA_B3 SDQS_B0#
 MAAB4 AW15 SMA_B4 SDQS_B0#
 MAAB5 BA15 SMA_B5 SDQS_B0#
 MAAB6 BB15 SMA_B6 SDQS_B0#
 MAAB7 BA14 SMA_B7 SDQS_B0#
 MAAB8 AY15 SMA_B8 SDQS_B0#
 MAAB9 BA15 SMA_B9 SDQS_B0#
 MAAB10 AW15 SMA_B10 SDQS_B0#
 MAAB11 BB13 SMA_B11 SDQS_B0#
 MAAB12 BA13 SMA_B12 SDQS_B0#
 MAAB13 AY20 SMA_B13 SDQS_B0#
 MAAB14 AY13 SMA_B14 SDQS_B0#

[16.17] -SWEA ← BB27C SWE_B#
 [16.17] -SCASB ← AW29C SCAS_B#
 [16.17] -SRASB ← BB26C SRAS_B#

[16.17] SBA80 ← BA19 SBA_B0
 [16.17] SBA81 ← BA18 SBA_B1
 [16.17] SBA82 ← BC12 SBA_B2

[16.17] CSB0 ← BB27C SCBS_B0#
 [16.17] CSB1 ← BB30C SCBS_B1#
 [17] CSB2 ← AY27C SCBS_B2#
 [17] CSB3 ← AY31C SCBS_B3#

[16.17] CKEB0 ← AW12 SCKE_B0
 [16.17] CKEB1 ← AW12 SCKE_B1
 [17] CKEB2 ← BB11 SCKE_B2
 [17] CKEB3 ← BA11 SCKE_B3

MODT_B0 BA29 MODT_B0
 MODT_B1 BA30 MODT_B1
 BB29 MODT_B1
 BB31 MODT_B1

[16] DCLKB0 ← AV31 SCLK_B0
 [16] DCLKB0 ← AU31C SCLK_B0#
 [16] DCLKB1 ← DCLKB1 DCLKB1
 [16] DCLKB2 ← DCLKB2 DCLKB2
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SDQS_B4 AW39 DQS84
 SDQS_B4# AU39 DQS84
 SDQS_B4 AU39 DQS84

SDQ_B32 AW37 MDB32
 SDQ_B33 AU38 MDB33
 SDQ_B34 AU38 MDB34
 SDQ_B35 AN37 MDB35
 SDQ_B36 AU38 MDB36
 SDQ_B37 AR35 MDB37
 SDQ_B38 AN35 MDB38
 SDQ_B39 AR37 MDB39

SDQS_B5 AL35 DQS85
 SDQS_B5# AL34 DQS85
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SDQ_B40 AM35 MDB40
 SDQ_B41 AM38 MDB41
 SDQ_B42 AJ34 MDB42
 SDQ_B43 AL38 MDB43
 SDQ_B44 AR39 MDB44
 SDQ_B45 AM34 MDB45
 SDQ_B46 AL37 MDB46
 SDQ_B47 AL32 MDB47

SDQS_B6 AG35 DQS86
 SDQS_B6# AG36 DQS86
 SDQS_B6 AG39 DQS86

SDQ_B48 AG38 MDB48
 SDQ_B49 AJ38 MDB49
 SDQ_B50 AF38 MDB50
 SDQ_B51 AF33 MDB51
 SDQ_B52 AJ37 MDB52
 SDQ_B53 AJ35 MDB53
 SDQ_B54 AG33 MDB54
 SDQ_B55 AF34 MDB55

SDQS_B7 AC38 DQS87
 SDQS_B7# AC37 DQS87
 SDQS_B7 AD38 DQS87

SDQ_B56 AD38 MDB56
 SDQ_B57 AC33 MDB57
 SDQ_B58 AA34 MDB58
 SDQ_B59 AA36 MDB59
 SDQ_B60 AD34 MDB60
 SDQ_B61 AF38 MDB61
 SDQ_B62 AC34 MDB62
 SDQ_B63 AA33 MDB63

RESERVED_1
 RESERVED_2
 RESERVED_3
 RESERVED_4
 RESERVED_5
 RESERVED_6
 RESERVED_7
 RESERVED_8

MCH_VREF AM21
 SVREF AM6

SMRCOMP0 AN2
 SMRCOMP1 AN3
 SMRCOMP2 BA40
 SMRCOMP3 BA40
 SMRCOMPVOL AM8
 SMRCOMPVOL AM10
 SMRCOMPVOL BB19

DDR_1

RESERVED_7

SVREF

SDQS_B7#

SDM_B7

SDQS_B7

SDM_B7

SDQS_B7

SDM_B7

SDQS_B7

SDM_B7

SDQS_B7

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SDM_B7

SDQS_B7

SDM_B7

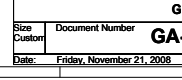
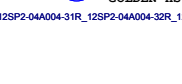
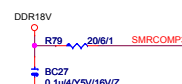
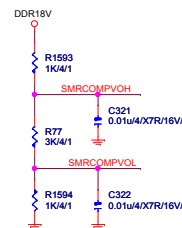
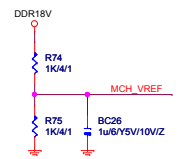
SDQS_B7

SDM_B7

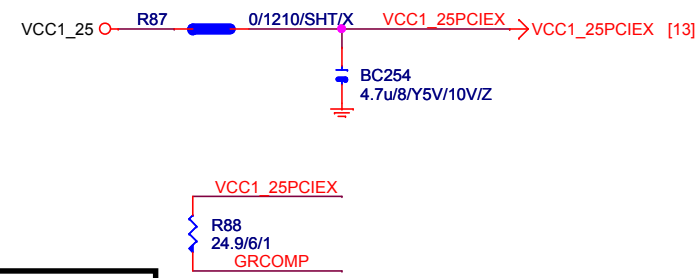
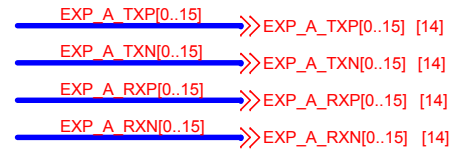
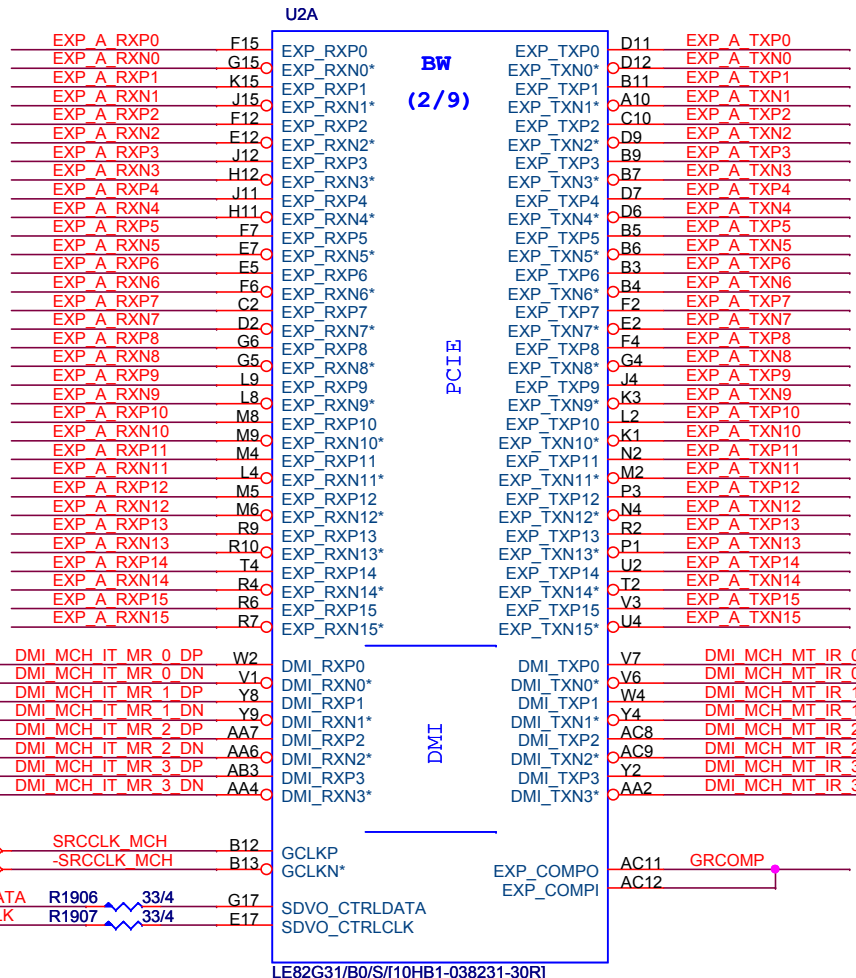
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SDM_B7

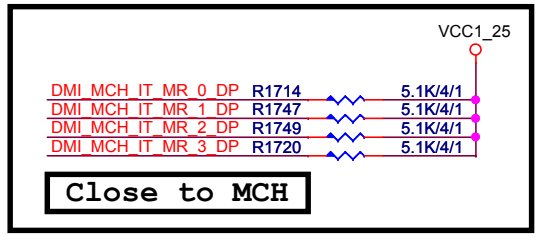
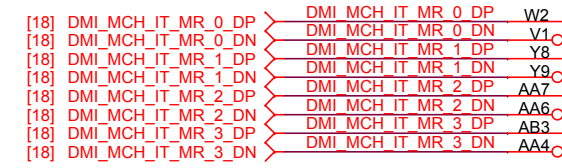
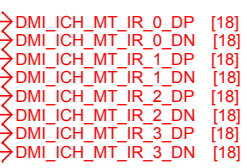
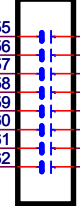
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 [16.17] MAAB[0..14] ← MAAB[0..14]
 [16] DMB[0..7] ← DMB[0..7]
 [16] DMB[0..63] ← DMB[0..63]
 [16] DQSB[0..7] ← DQSB[0..7]
 [15.17] MAA[0..14] ← MAA[0..14]
 [15] DMA[0..7] ← DMA[0..7]
 [15] MDA[0..63] ← MDA[0..63]
 [15] DQSA[0..7] ← DQSA[0..7]
 [15] -DQSA[0..7] ← -DQSA[0..7]



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Close to MCH

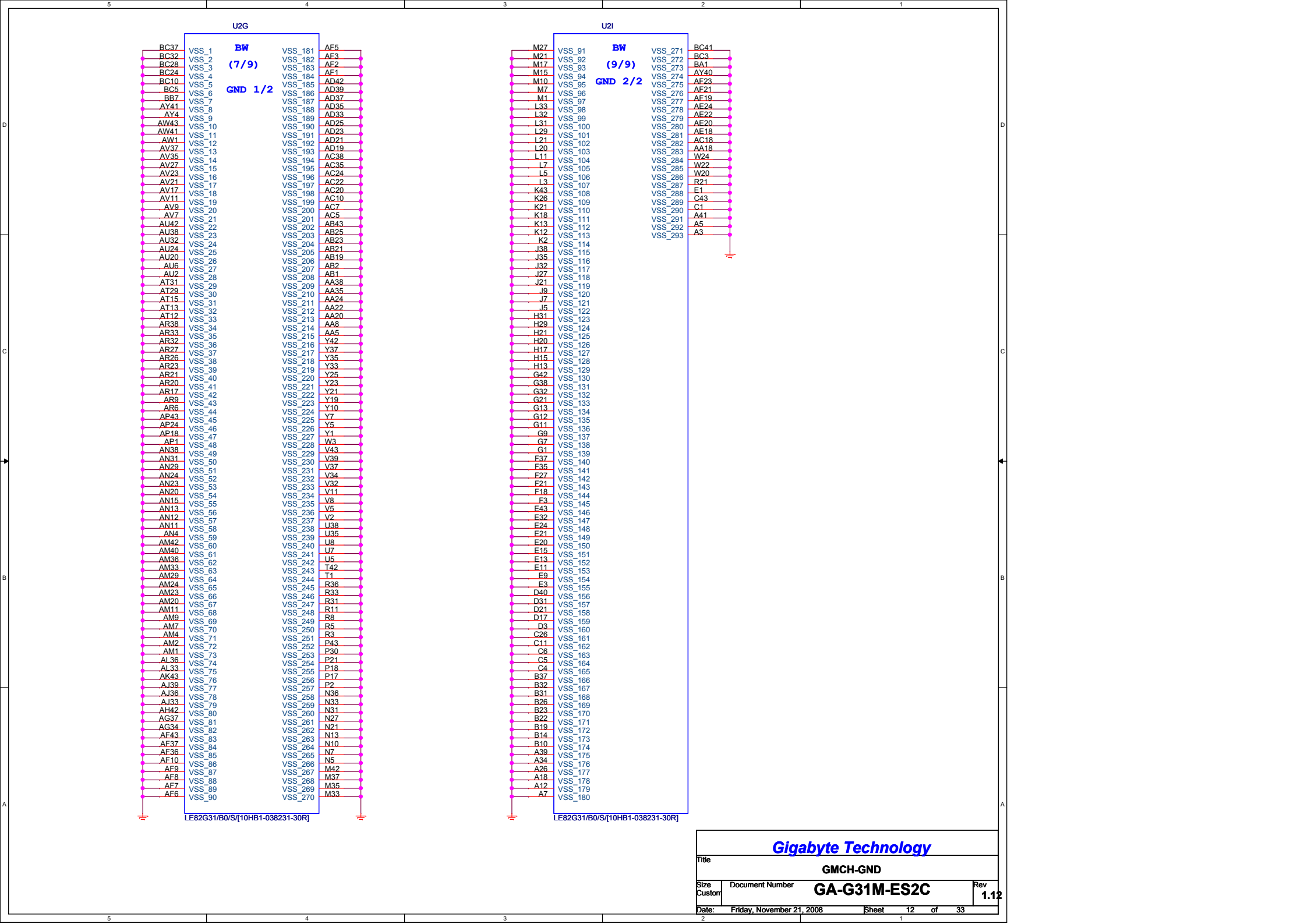


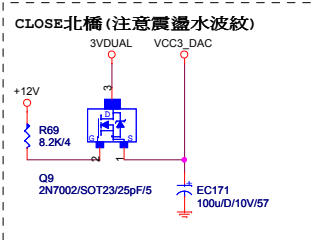
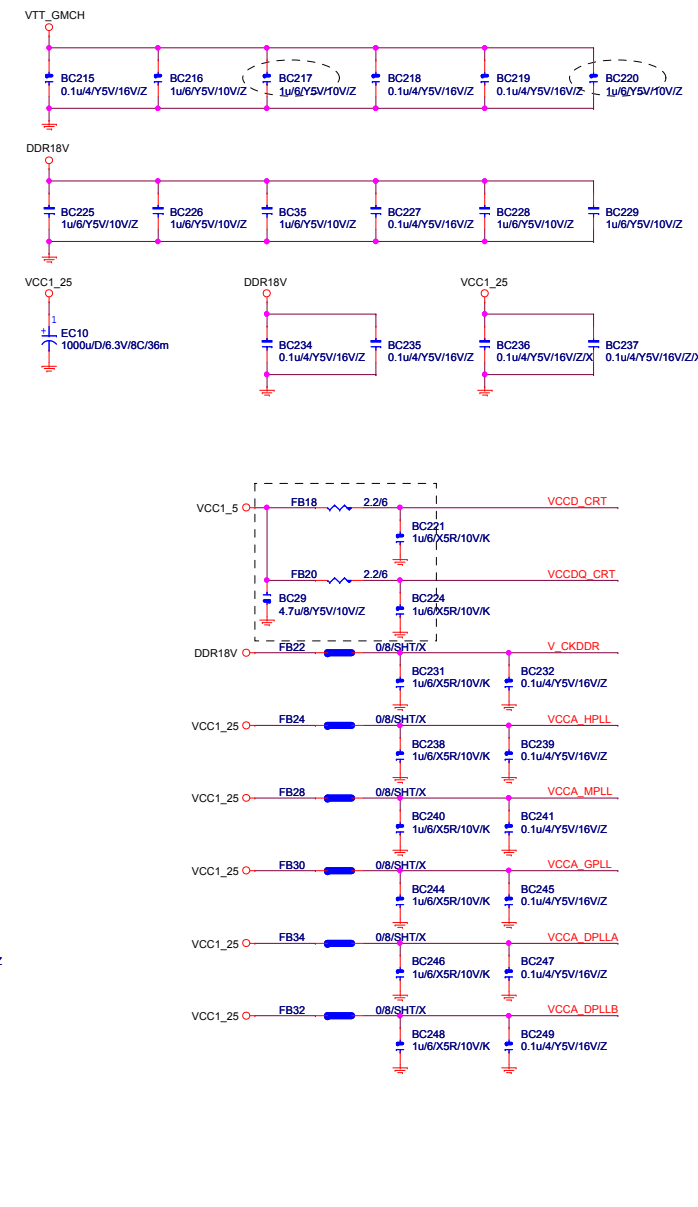
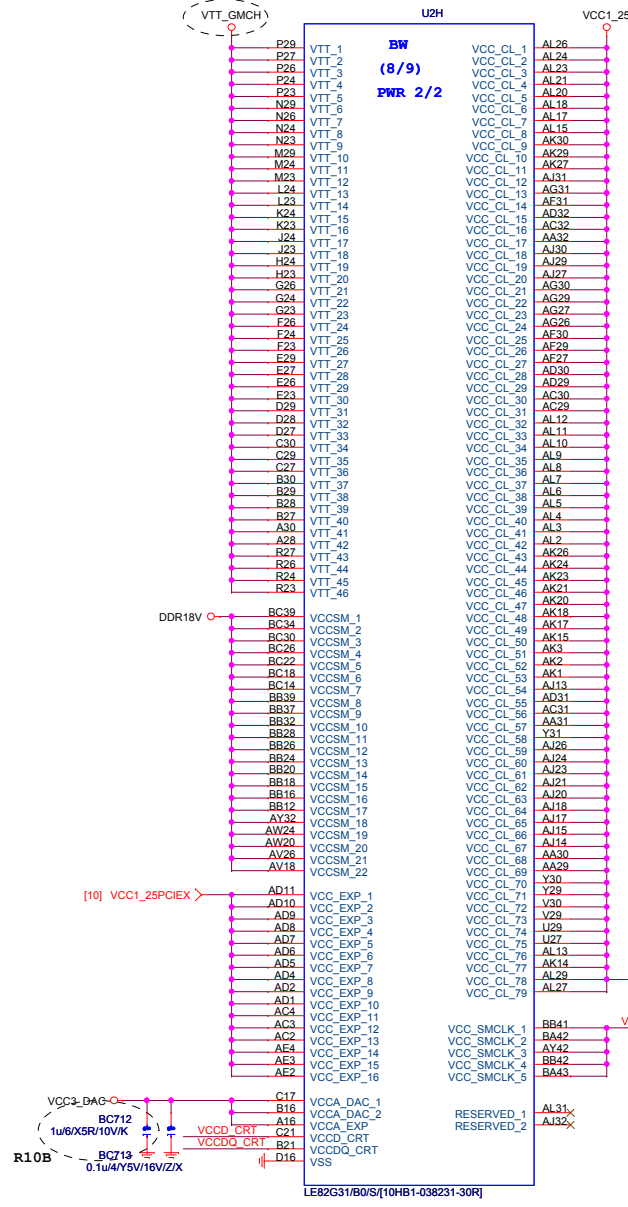
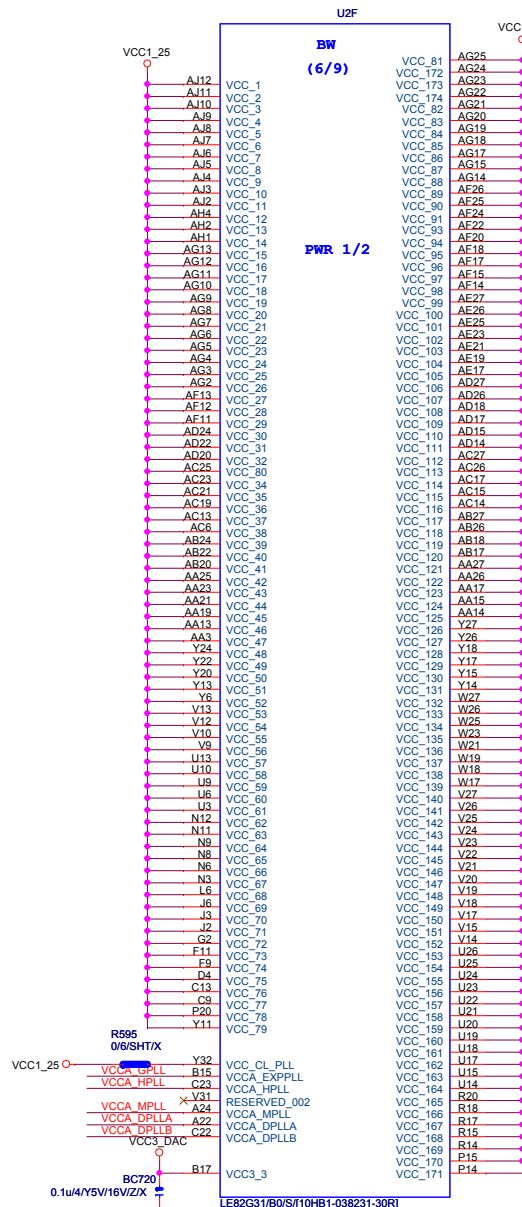
Gigabyte Technology

Title GMCH-PCI E & DMI

Size Custom Document Number GA-G31M-ES2C Rev 1.12

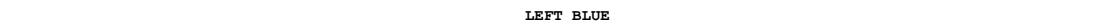
Date: Friday, November 21, 2008 Sheet 10 of 33





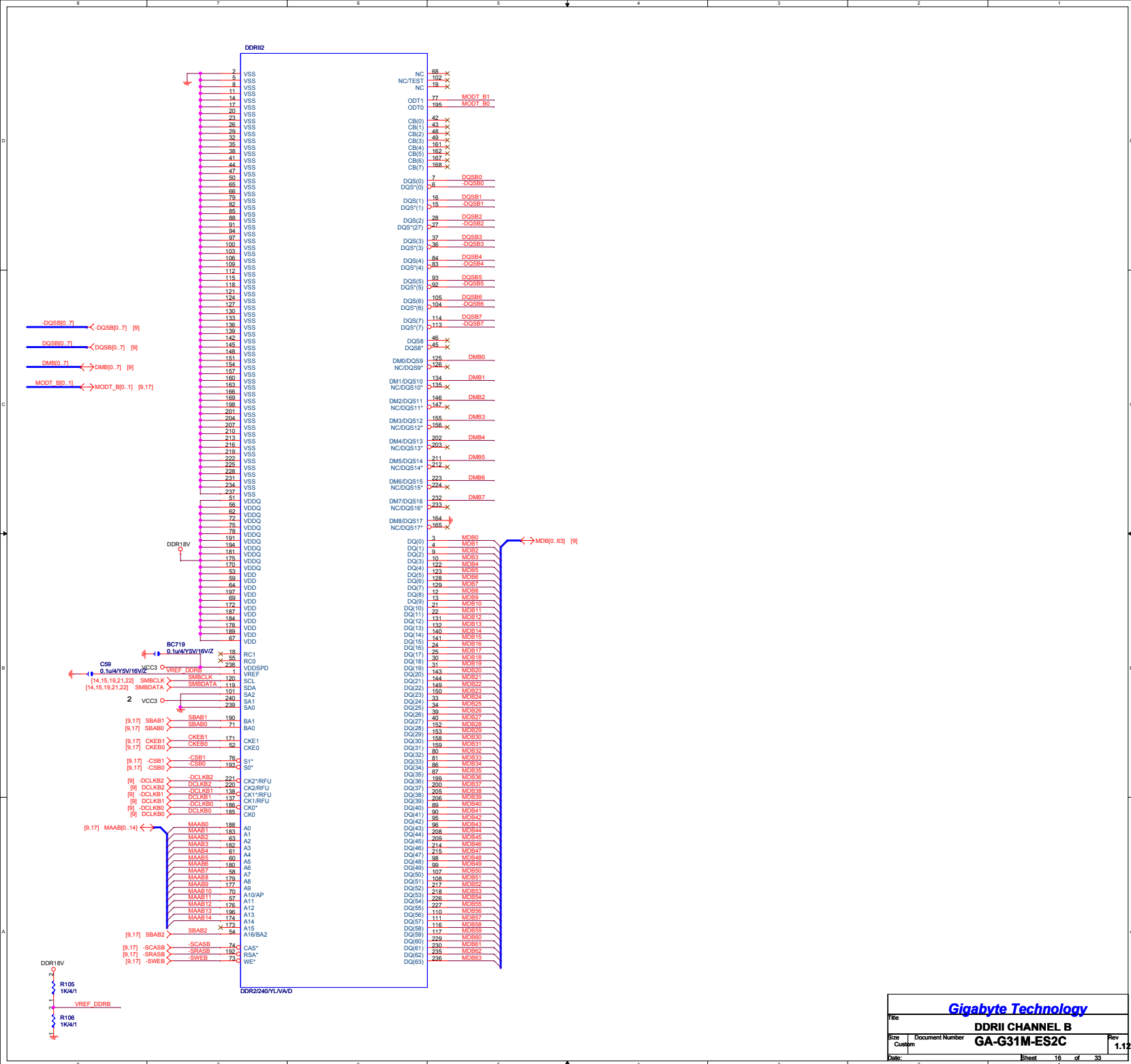


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EXP A TPX0	C93	1.01uW/XTR/16V/K	EXP A TPX0C
EXP A TPX1	C94	1.01uW/XTR/16V/K	EXP A TPX1C
EXP A TPX1	C95	1.01uW/XTR/16V/K	EXP A TPX1C
EXP A TPX2	C96	1.01uW/XTR/16V/K	EXP A TPX2C
EXP A TPX2	C97	1.01uW/XTR/16V/K	EXP A TPX2C
EXP A TPX3	C98	1.01uW/XTR/16V/K	EXP A TPX3C
EXP A TPX3	C99	1.01uW/XTR/16V/K	EXP A TPX3C
EXP A TPX4	C100	1.01uW/XTR/16V/K	EXP A TPX4C
EXP A TPX4	C101	1.01uW/XTR/16V/K	EXP A TPX4C
EXP A TPX5	C102	1.01uW/XTR/16V/K	EXP A TPX5C
EXP A TPX5	C103	1.01uW/XTR/16V/K	EXP A TPX5C
EXP A TPX6	C104	1.01uW/XTR/16V/K	EXP A TPX6C
EXP A TPX6	C105	1.01uW/XTR/16V/K	EXP A TPX6C
EXP A TPX7	C106	1.01uW/XTR/16V/K	EXP A TPX7C
EXP A TPX7	C107	1.01uW/XTR/16V/K	EXP A TPX7C
EXP A TPX8	C108	1.01uW/XTR/16V/K	EXP A TPX8C
EXP A TPX8	C109	1.01uW/XTR/16V/K	EXP A TPX8C
EXP A TPX9	C110	1.01uW/XTR/16V/K	EXP A TPX9C
EXP A TPX9	C111	1.01uW/XTR/16V/K	EXP A TPX9C
EXP A TPX10	C112	1.01uW/XTR/16V/K	EXP A TPX10C
EXP A TPX10	C113	1.01uW/XTR/16V/K	EXP A TPX10C
EXP A TPX11	C114	1.01uW/XTR/16V/K	EXP A TPX11C
EXP A TPX11	C115	1.01uW/XTR/16V/K	EXP A TPX11C
EXP A TPX12	C116	1.01uW/XTR/16V/K	EXP A TPX12C
EXP A TPX12	C117	1.01uW/XTR/16V/K	EXP A TPX12C
EXP A TPX13	C118	1.01uW/XTR/16V/K	EXP A TPX13C
EXP A TPX13	C119	1.01uW/XTR/16V/K	EXP A TPX13C
EXP A TPX14	C120	1.01uW/XTR/16V/K	EXP A TPX14C
EXP A TPX14	C121	1.01uW/XTR/16V/K	EXP A TPX14C
EXP A TPX15	C122	1.01uW/XTR/16V/K	EXP A TPX15C
EXP A TPX15	C123	1.01uW/XTR/16V/K	EXP A TPX15C



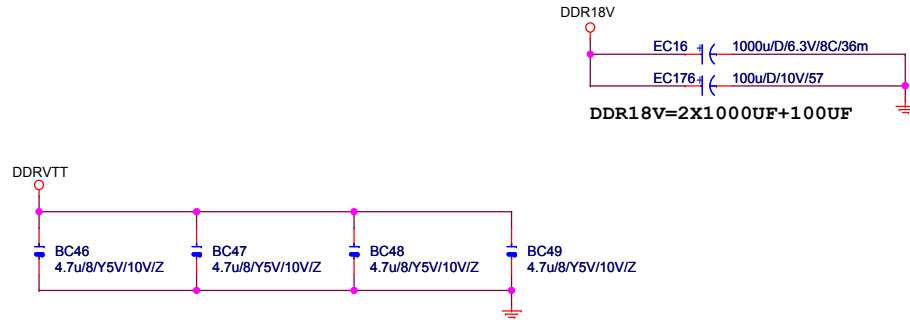
Gigabyte Technology

Title			
PCI EXPRESS * 16			
Size	Document Number		Rev
Custom	GA-G31M-ES2C		1.12
Date:	Friday, November 21, 2008	Sheet	14 of 33

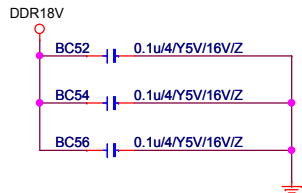


DDR TERMINATION CHANNEL A

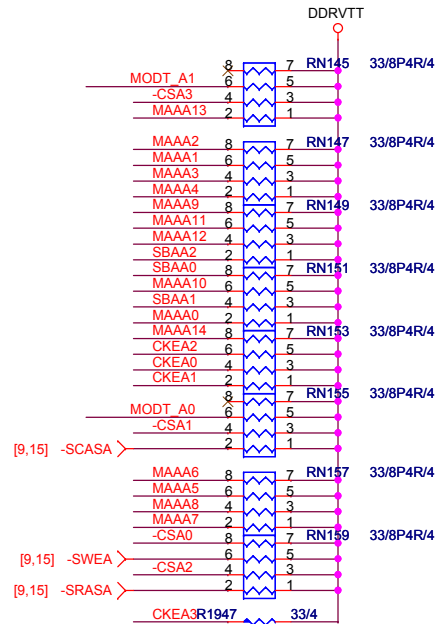
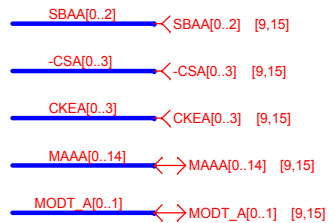
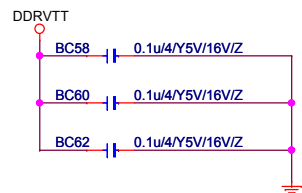
DDRVTT Decouple



DDR18V Decouple

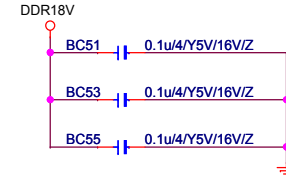


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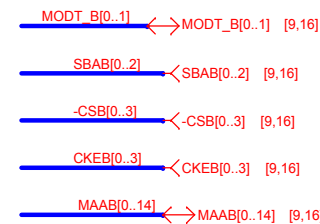
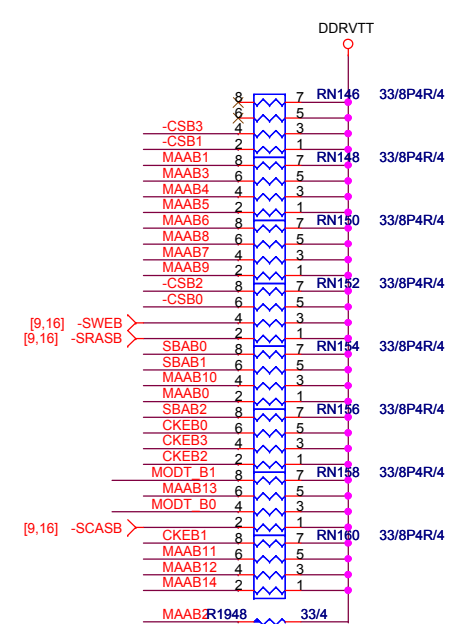
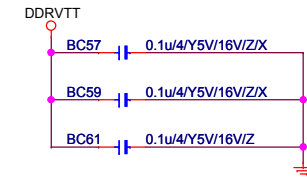


DDR TERMINATION CHANNEL B

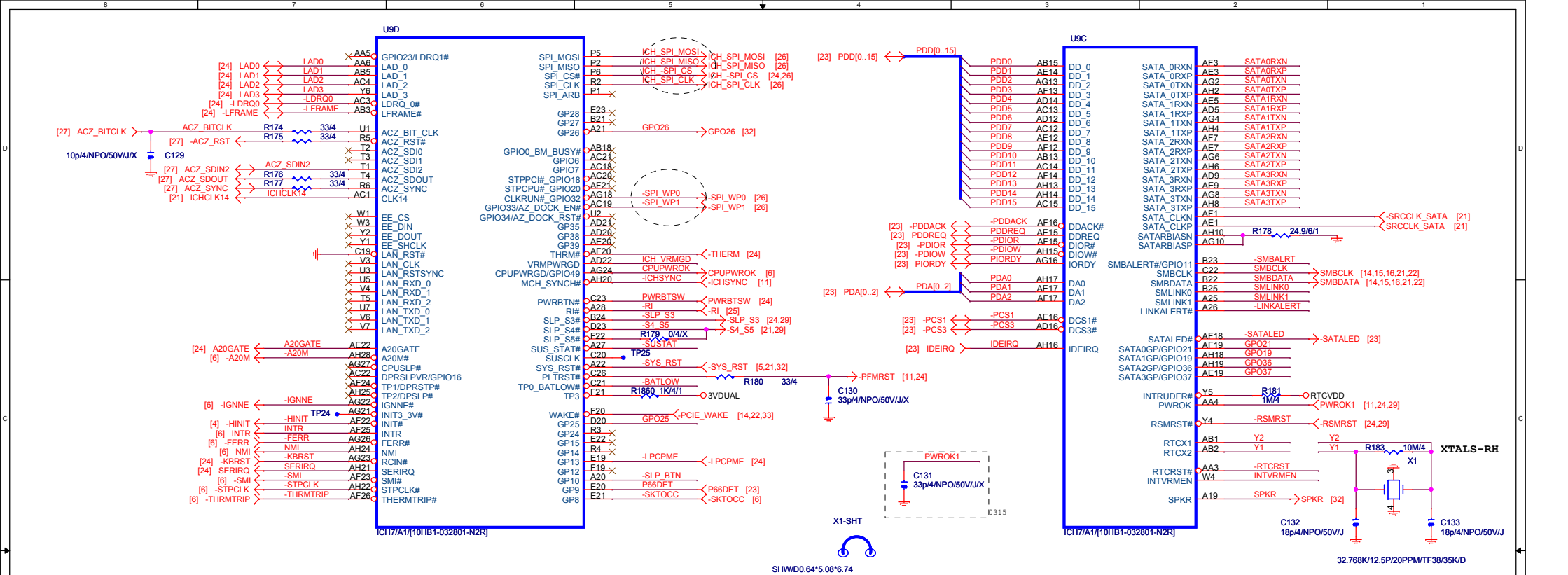
DDR18V Decouple



DDRVTT Decouple

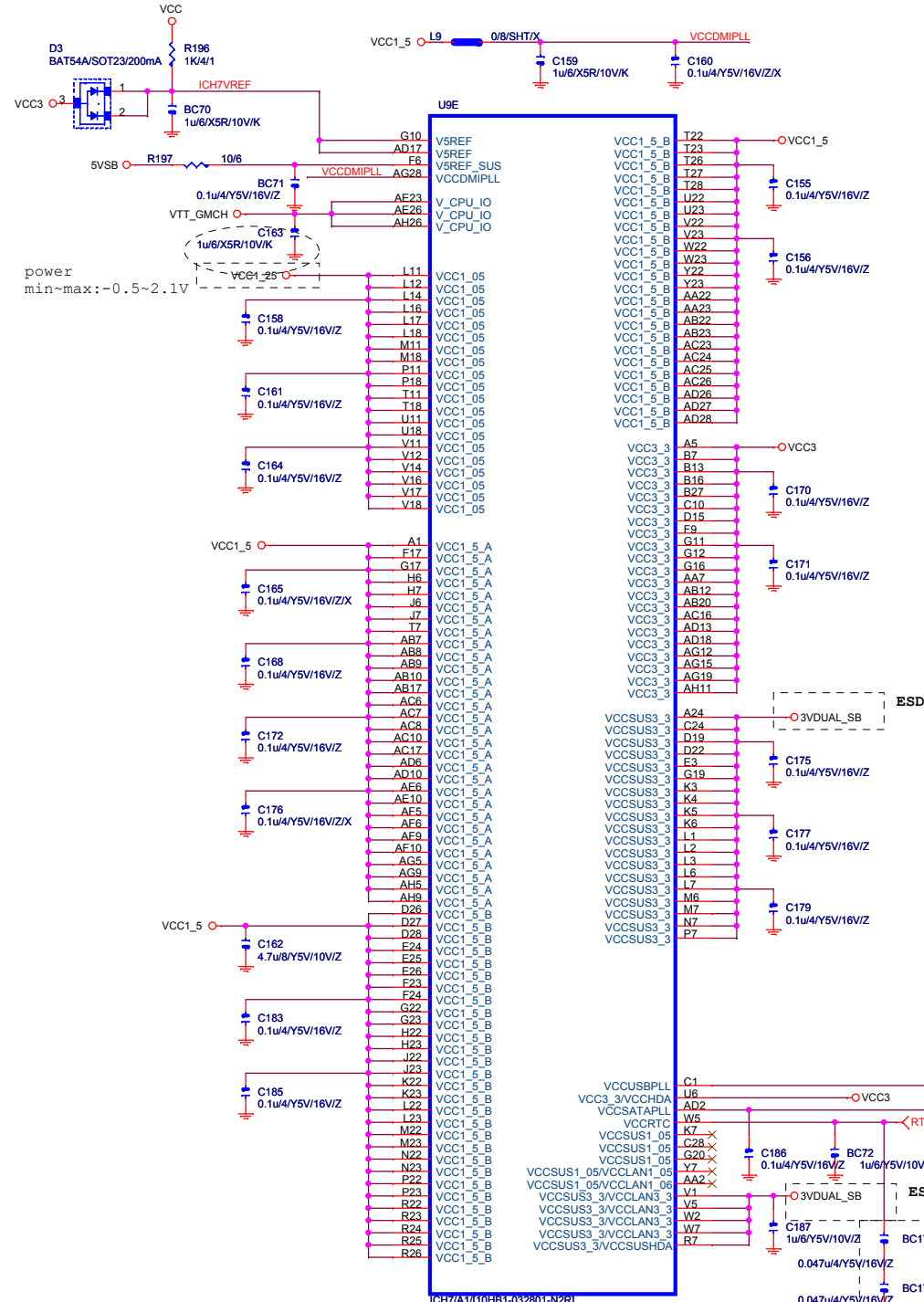


Gigabyte Technology



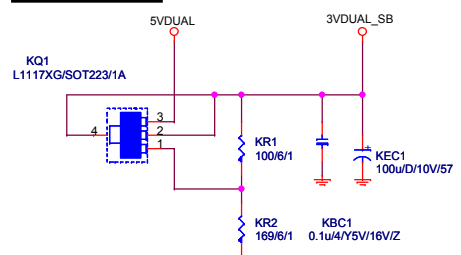
U9F			
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A23	VSS2	VSS102	R15
B1	VSS3	VSS103	R16
B8	VSS4	VSS104	R17
B11	VSS5	VSS105	R18
B14	VSS6	VSS106	T8
B17	VSS7	VSS107	T12
B20	VSS8	VSS108	T13
B26	VSS9	VSS109	T14
B28	VSS10	VSS110	T15
C2	VSS11	VSS111	T16
C6	VSS12	VSS112	T17
D10	VSS13	VSS113	U4
D13	VSS14	VSS114	U13
D18	VSS15	VSS115	U14
D21	VSS16	VSS116	U15
D24	VSS17	VSS117	U16
E1	VSS18	VSS118	U17
E2	VSS19	VSS119	U24
E8	VSS20	VSS120	U25
E15	VSS21	VSS121	U26
F3	VSS22	VSS122	V2
F4	VSS23	VSS123	V13
F5	VSS24	VSS124	V15
F12	VSS25	VSS125	V16
F27	VSS26	VSS126	V27
F28	VSS27	VSS127	V28
G1	VSS28	VSS128	W6
G2	VSS29	VSS129	W24
G5	VSS30	VSS130	W25
G6	VSS31	VSS131	W26
G9	VSS32	VSS132	Y3
G14	VSS33	VSS133	Y24
G18	VSS34	VSS134	Y27
G21	VSS35	VSS135	Y28
G24	VSS36	VSS136	AA1
G25	VSS37	VSS137	AA24
G26	VSS38	VSS138	AA25
H3	VSS39	VSS139	AA26
H4	VSS40	VSS140	AB4
H24	VSS41	VSS141	AB6
H27	VSS43	VSS143	AB11
H28	VSS44	VSS144	AB14
J1	VSS45	VSS145	AB16
J2	VSS46	VSS146	AB19
J5	VSS47	VSS147	AB21
J24	VSS48	VSS148	AB24
J25	VSS49	VSS149	AB27
J26	VSS50	VSS150	AB28
K24	VSS51	VSS151	AC2
K27	VSS52	VSS152	AC5
K28	VSS53	VSS153	AC9
L13	VSS54	VSS154	AC11
L15	VSS55	VSS155	AD1
L24	VSS56	VSS156	AD3
L25	VSS57	VSS157	AD4
L26	VSS58	VSS158	AD7
M3	VSS59	VSS159	AD8
M4	VSS60	VSS160	AD11
M5	VSS61	VSS161	AD15
M12	VSS62	VSS162	AD19
M13	VSS63	VSS163	AD23
M14	VSS64	VSS164	AE2
M15	VSS65	VSS165	AE4
M16	VSS66	VSS166	AE8
M17	VSS67	VSS167	AE11
M24	VSS68	VSS168	AE13
M27	VSS69	VSS169	AE18
M28	VSS70	VSS170	AE21
N1	VSS71	VSS171	AE24
N2	VSS72	VSS172	AE2
N5	VSS73	VSS173	AF4
N6	VSS74	VSS174	AF8
N11	VSS75	VSS175	AF11
N12	VSS76	VSS176	AF27
N13	VSS77	VSS177	AF28
N14	VSS78	VSS178	AG1
N15	VSS79	VSS179	AG3
N16	VSS80	VSS180	AG7
N17	VSS81	VSS181	AG14
N18	VSS82	VSS182	AG17
N24	VSS83	VSS183	AG20
N25	VSS84	VSS184	AG26
N26	VSS85	VSS185	AH1
P3	VSS86	VSS186	AH3
P4	VSS87	VSS187	AH7
P12	VSS88	VSS188	AH12
P13	VSS89	VSS189	AH23
P14	VSS90	VSS190	AH27
P15	VSS91	VSS191	C27
P16	VSS92	VSS192	E4
P17	VSS93	VSS193	E4
P24	VSS94	VSS194	EH11
P27	VSS95		
P28	VSS96		
R1	VSS97		
R11	VSS98		
R12	VSS99		
R13	VSS100		

ICH7/A1/[10HB1-032801-N2R]



ICH7/A1/[10HB1-032801-N2R]

3VDUAL_SB



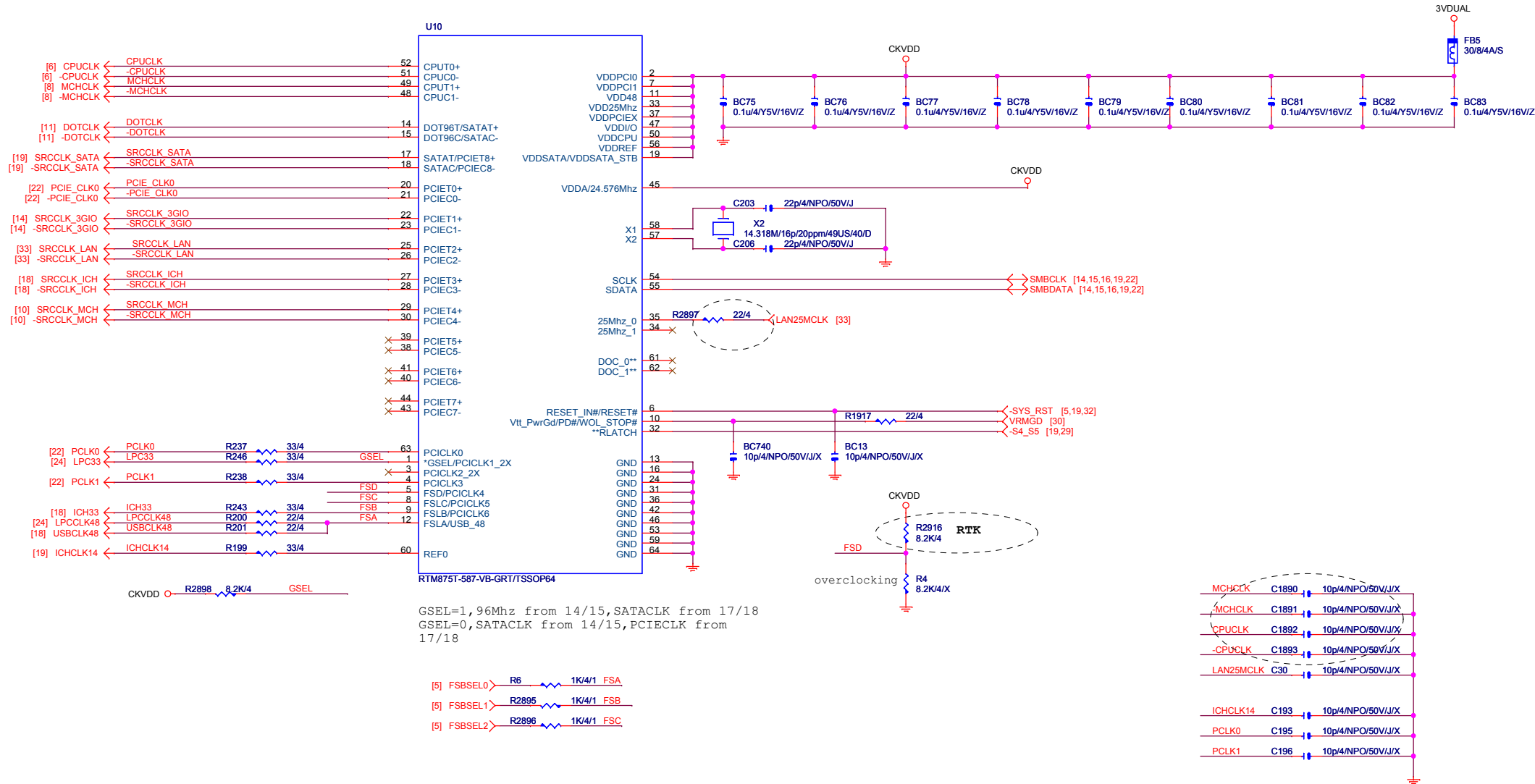
ESD

ESD

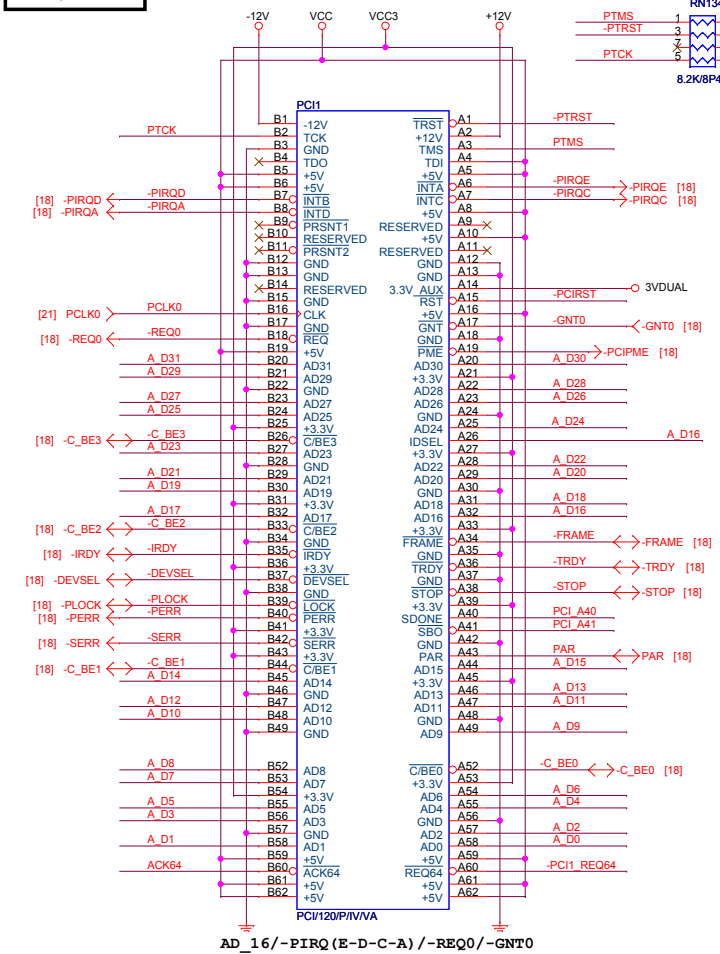
FOR

漏電

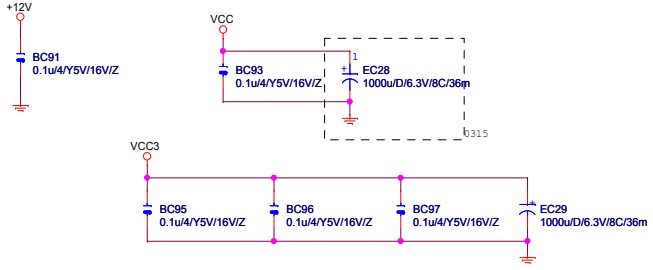
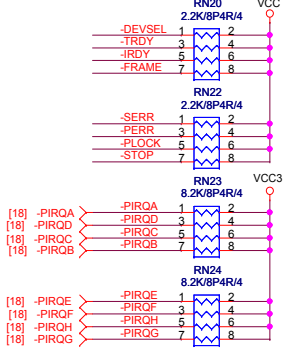
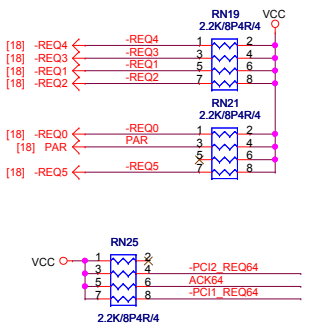
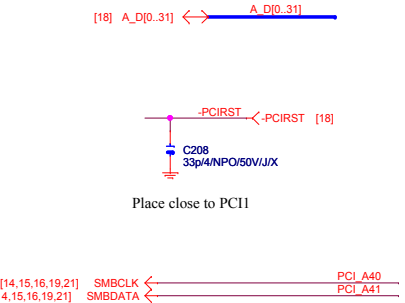
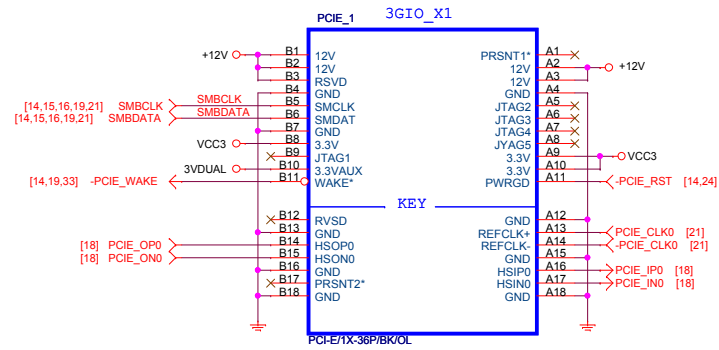
Gigabyte Technology			
Title ICH7-PWR & GND			
Size B	Document Number	GA-G31M-ES2C	
Date	Friday, November 21, 2008	Sheet	20 of 33
Rev 1.12			



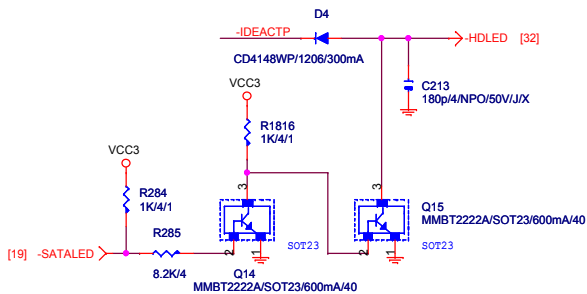
PCI1, 2 SLOT



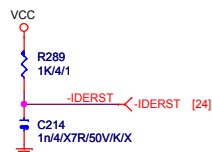
PCI-E*1



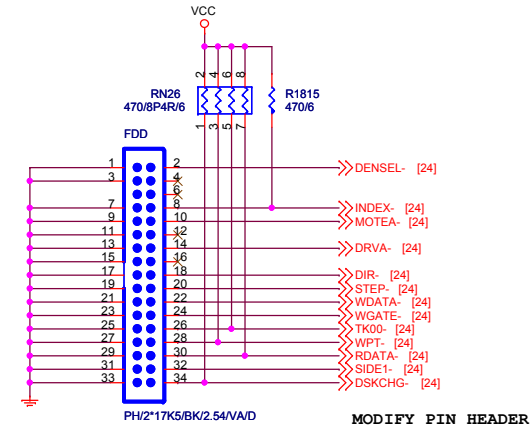
IDE/SATA LED



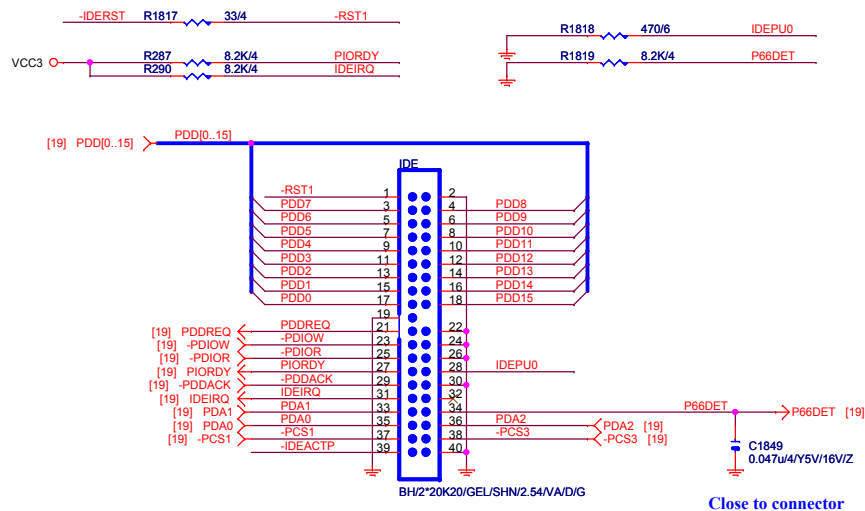
IDE RESET



FLOPPY

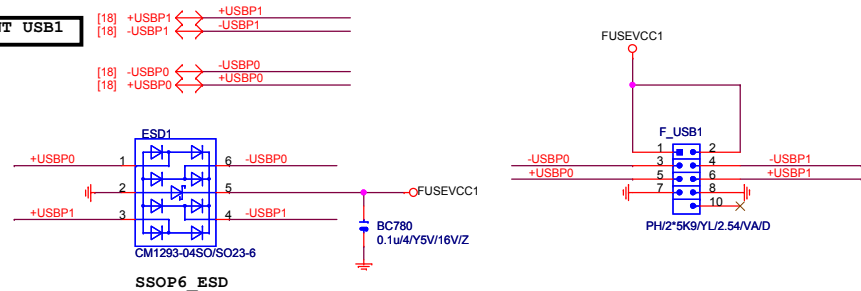


IDE

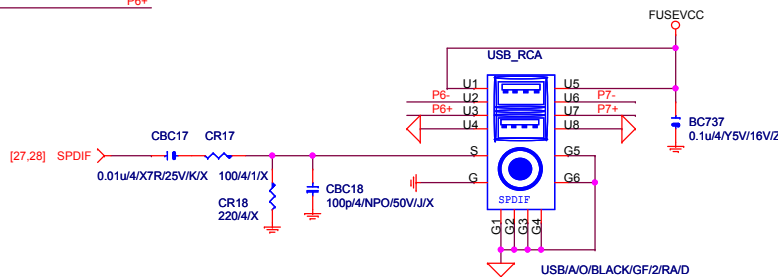
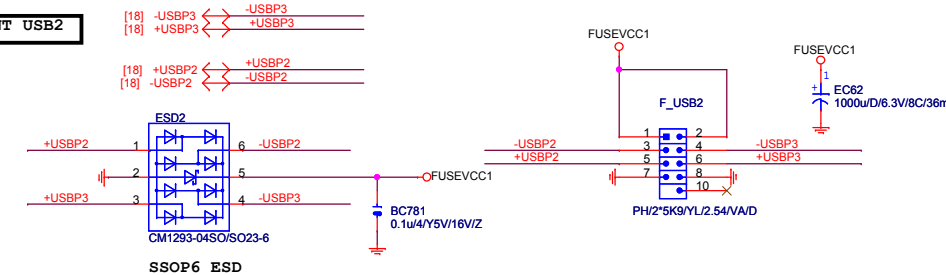


Close to connector

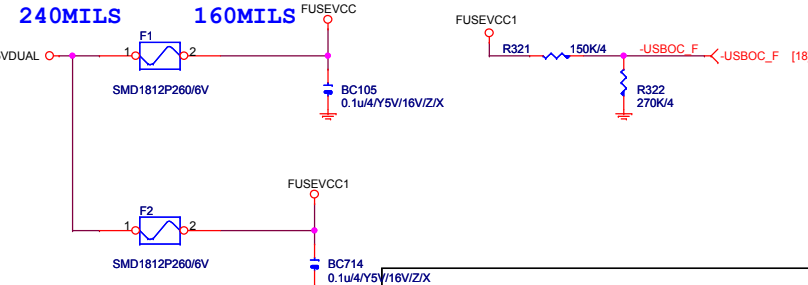
FRONT USB1



FRONT USB2



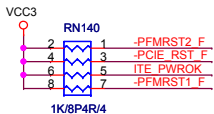
POP USBX2



Gigabyte Technology

Title		
IDE,FDD,F USB,USB RCA		
Size	Document Number	Rev
B	GA-G31M-ES2C	1.12
Date:	Friday, November 21, 2008	Sheet 23 of 33

DEFAULT 50%



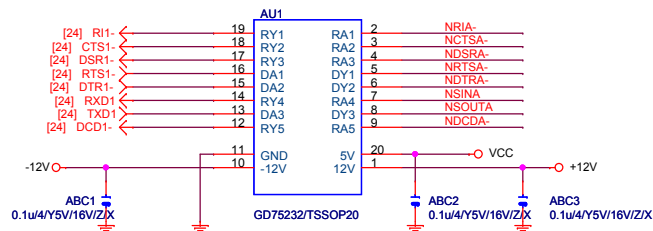
1.2V or 3.3V tolerance select.
1.2V OUTPUT 接 VTT_GMCH
3.3V OUTPUT 接3.3V
LPCPD#=VIDVCC

[26] -SIO_SPI_CS1 << 0/4 R412 CEB_N

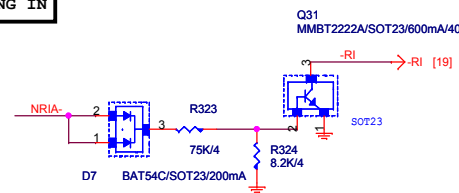
[26] -SIO_SPI_CS0 << 0/4 R404 -RST_BTN

Title			
ITE 8712/18 LPC IO			
Size B	Document Number		Rev
	GA-G31M-ES2C		1.12
Date:	Friday, November 21, 2008	Sheet	24 of 33

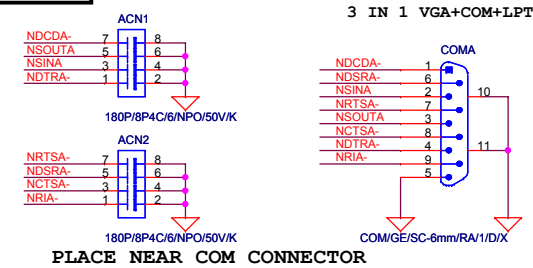
COMA



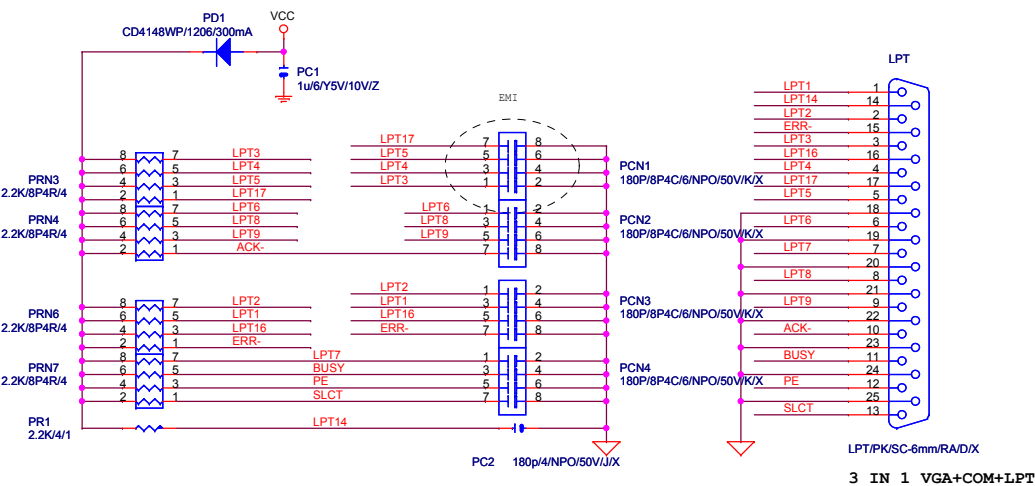
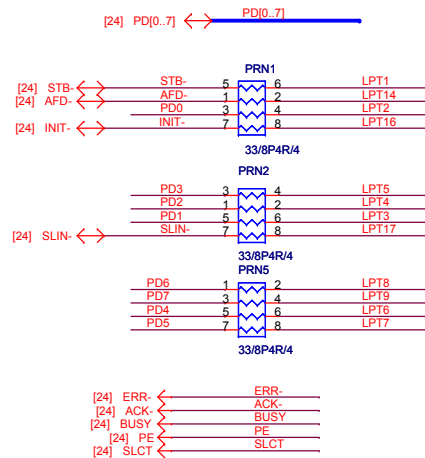
RING IN



EXTERNAL COMA



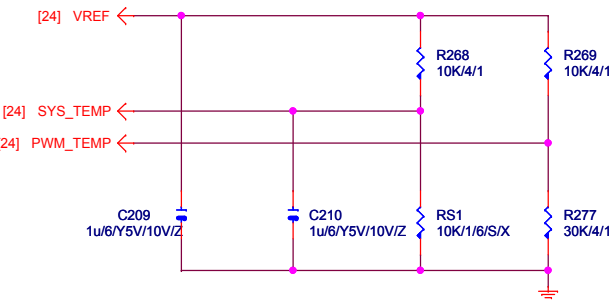
LPT PORT



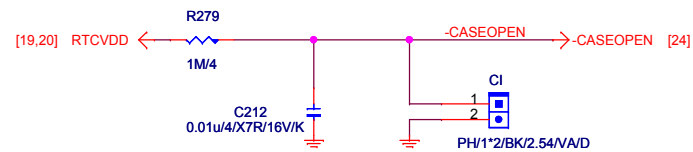
Gigabyte Technology

Title		COM & LPT PORT	
Size	Document Number	GA-G31M-ES2C	Rev
B			1.12
Date:	Friday, November 21, 2008	Sheet	25 of 33

TEMP H/W MONITOR

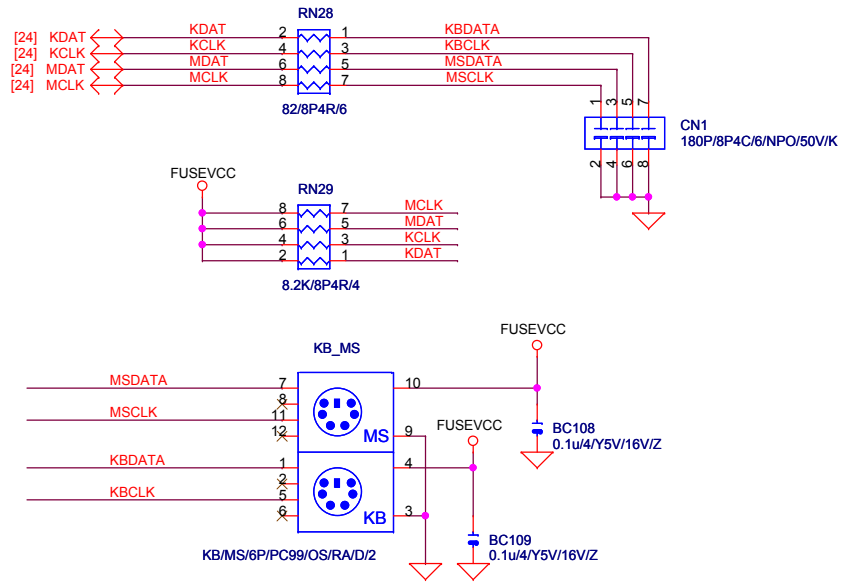


CASE OPEN

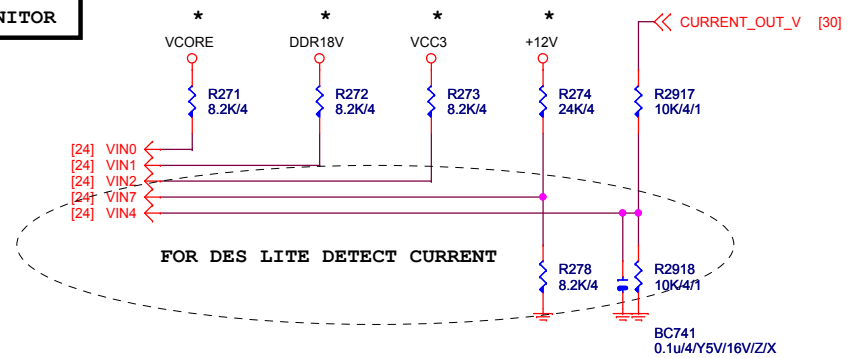


Case Open Circuits

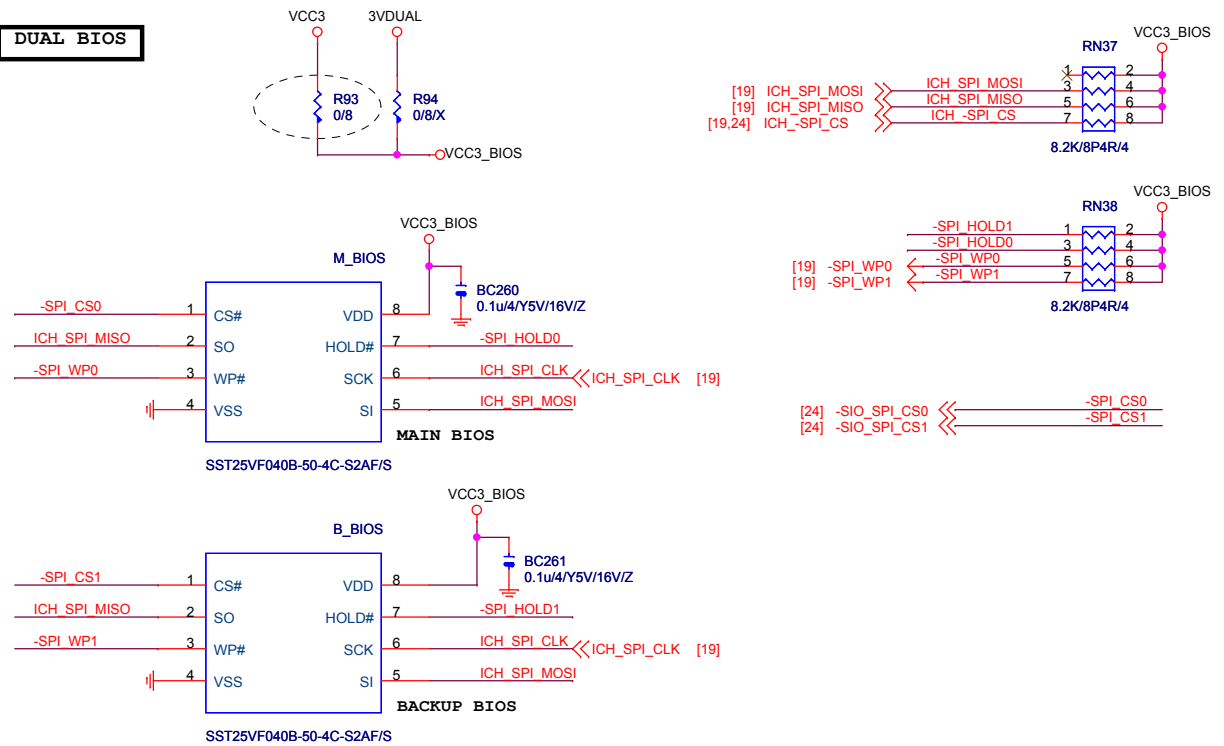
KB/MS

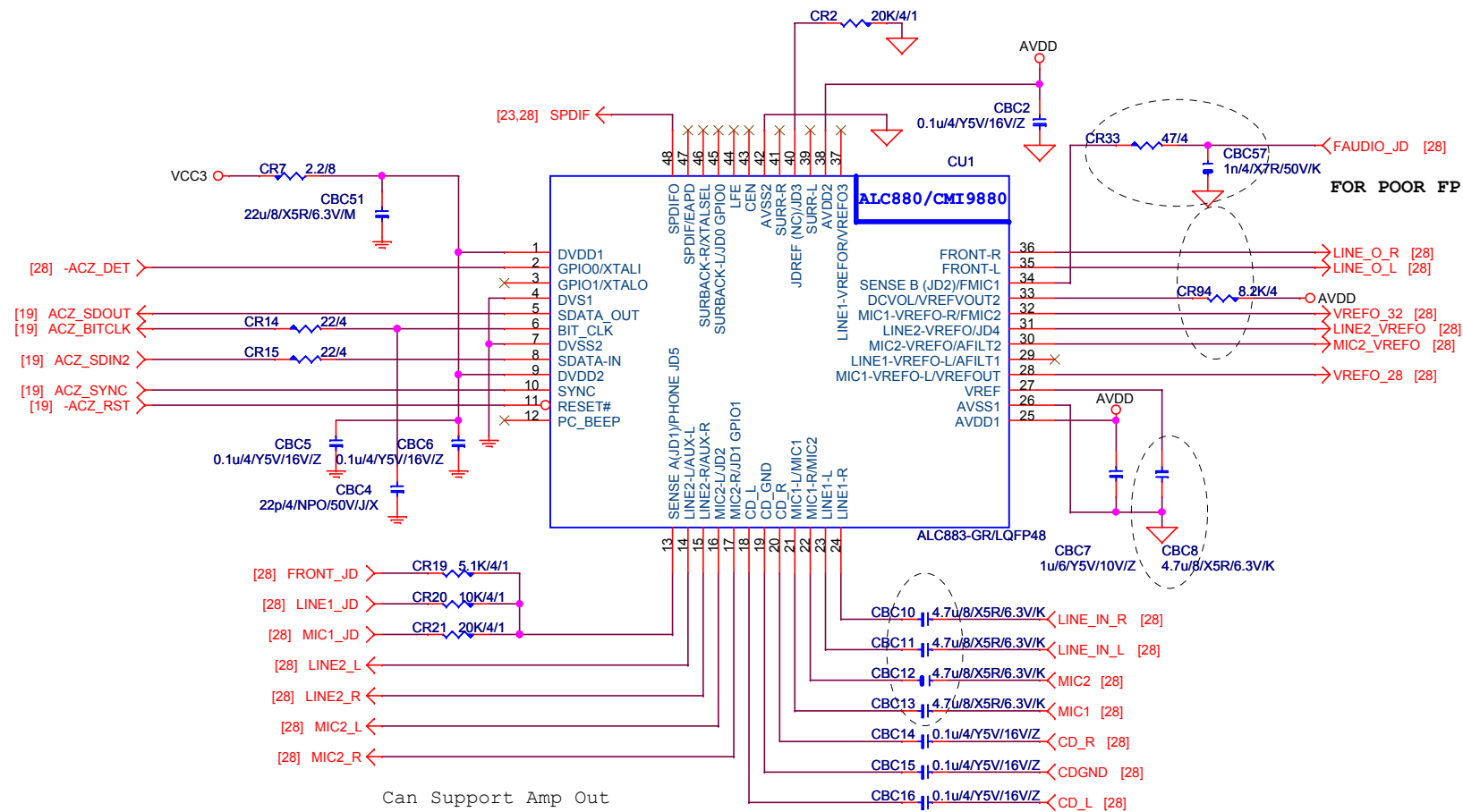


VOLTAGE-- H/W MONITOR



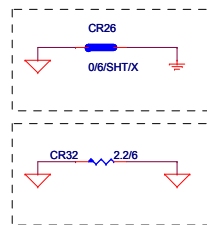
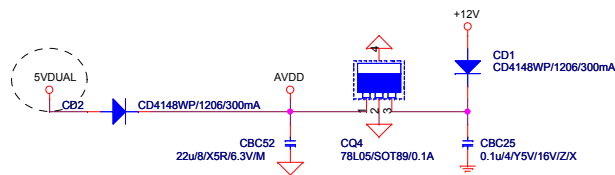
DUAL BIOS



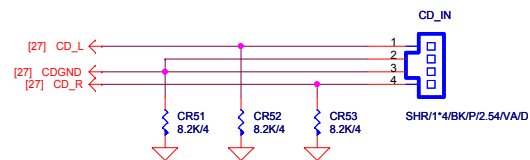
**Gigabyte Technology**

Title			AZALIA ALC883	
Size Custom			GA-G31M-ES2C	
Date: Friday, November 21, 2008			Sheet 27 of 33	Rev 1.12

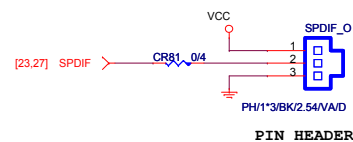
CODEC POWER/EMI PAD



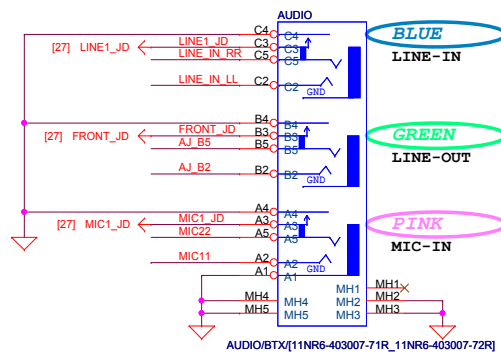
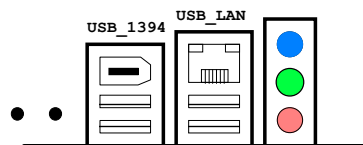
CD	IN
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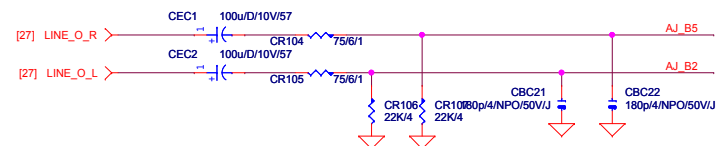
SPDIF IN



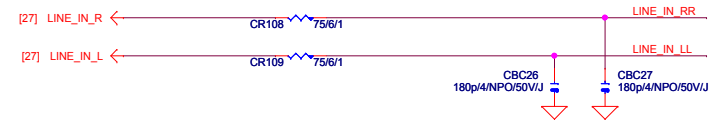
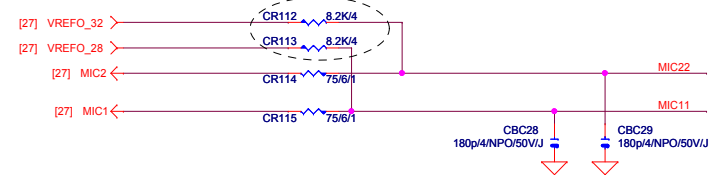
AZALIA JACK



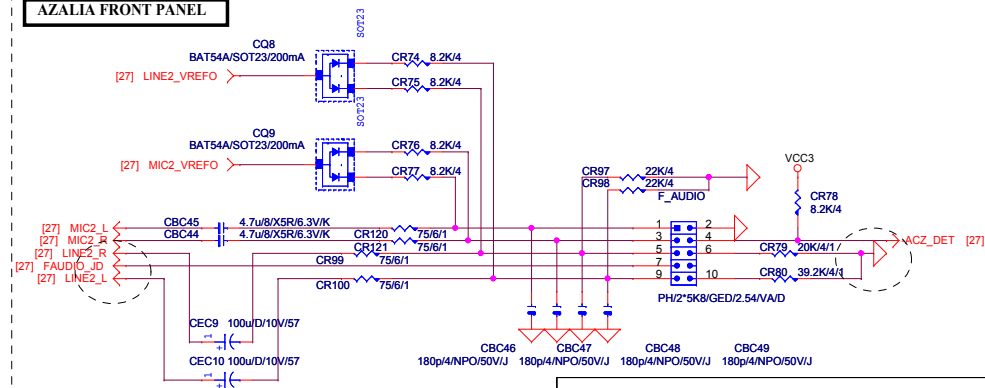
LINE-OUT

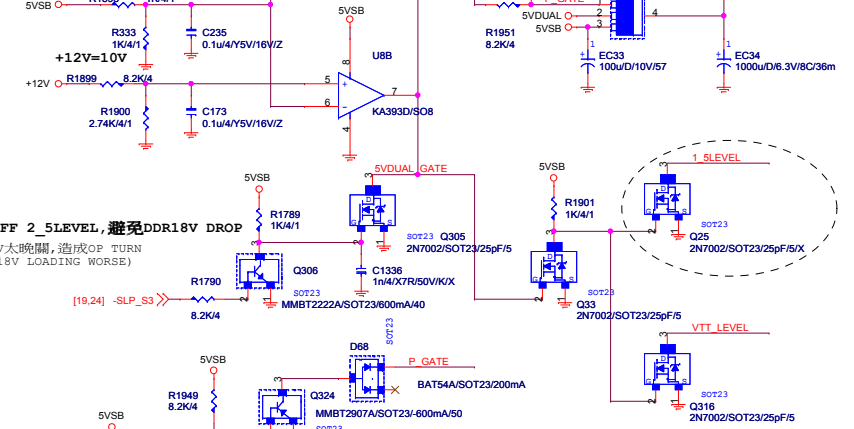
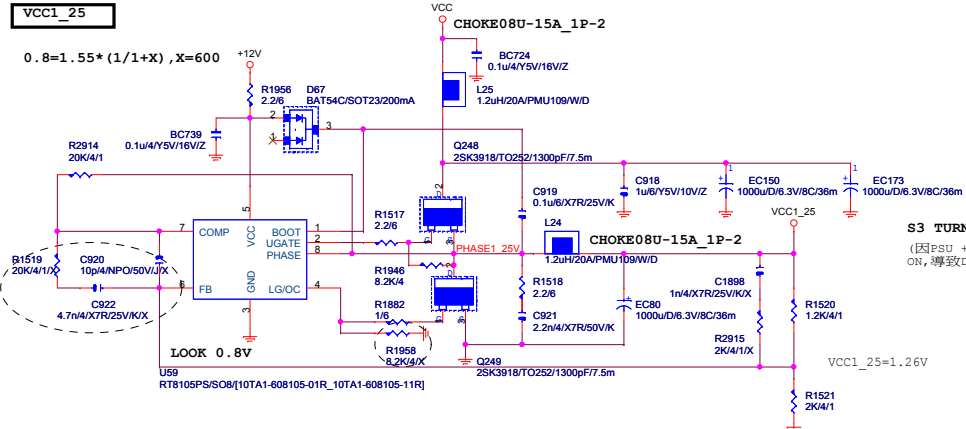
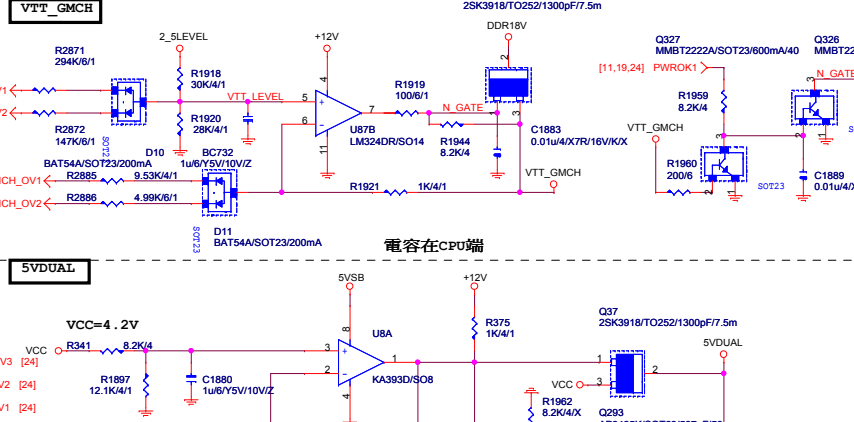
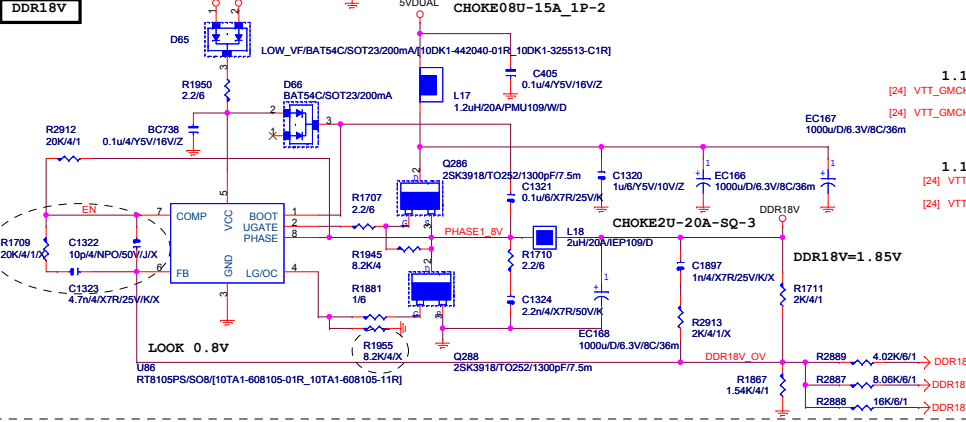
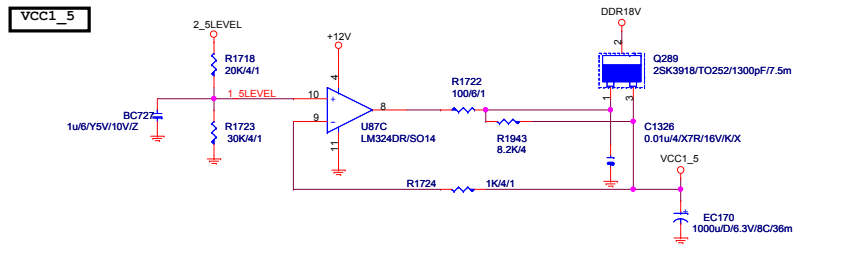
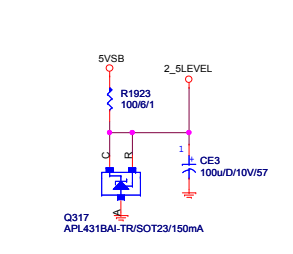
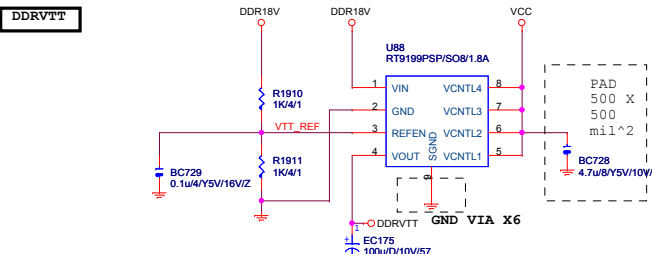
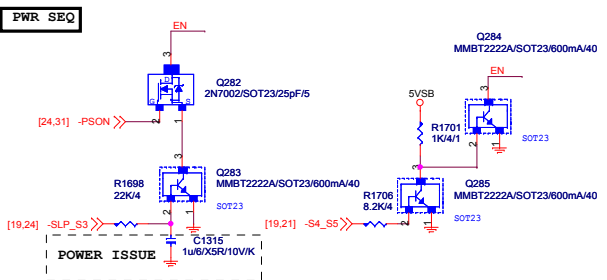
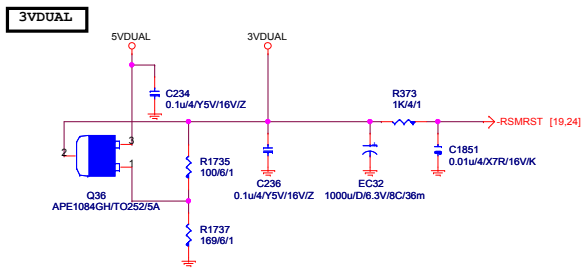
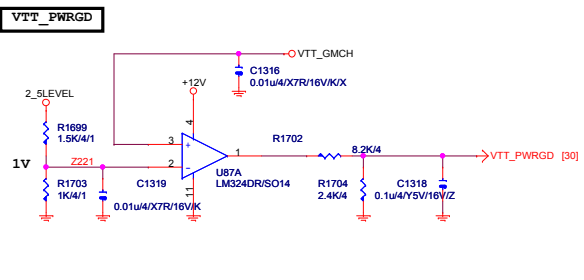


LINE-IN

**MIC-IN**

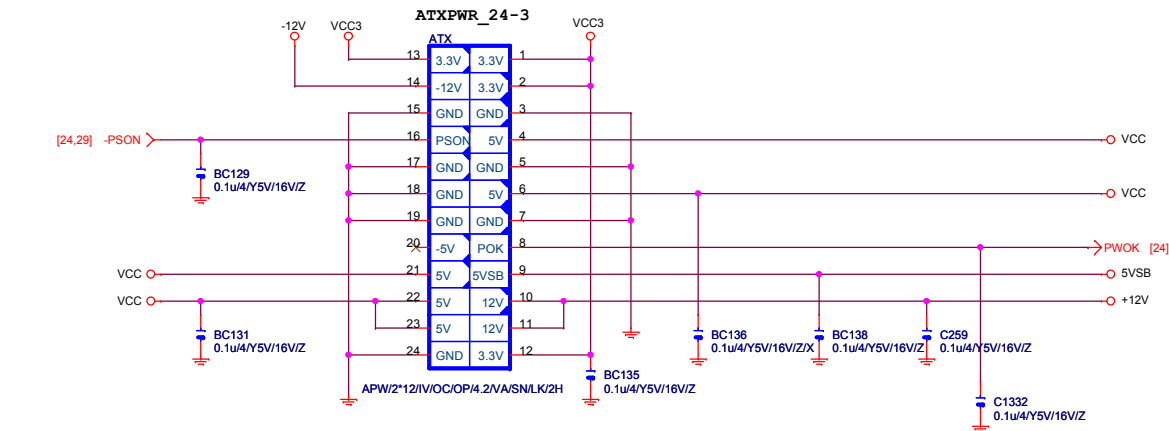
AZALIA FRONT PANEL



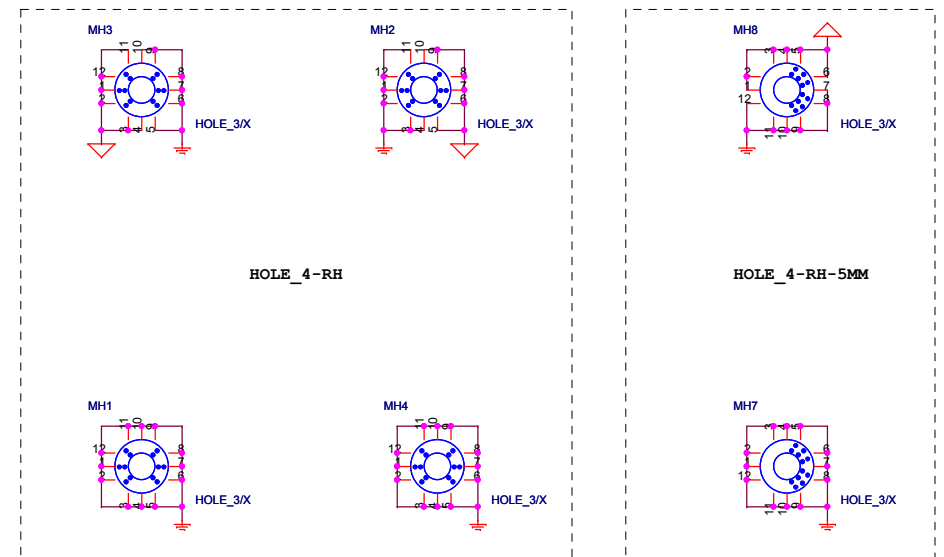
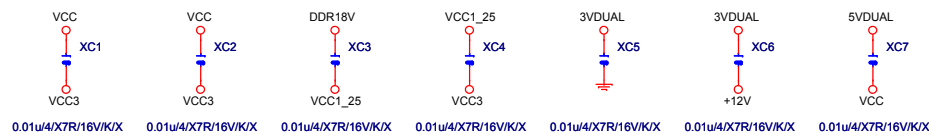
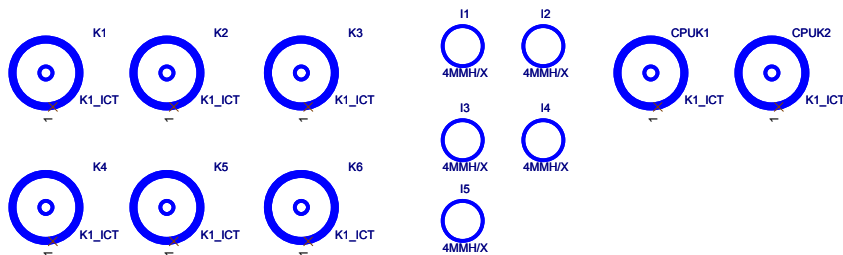
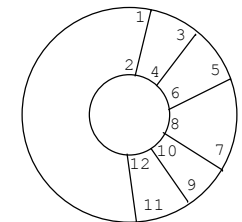
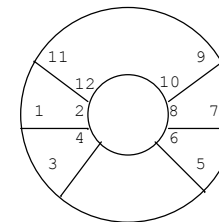
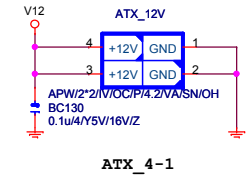
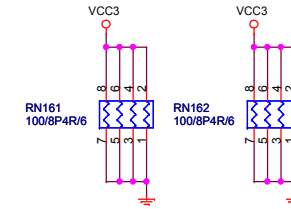


S3 TURN OFF 2_5LEVEL, 避免DDR18V DROP
(因PSU +12V太晚關, 造成OP TURN ON, 導致DDR18V LOADING WORSE)

ATX POWER CONNECTOR



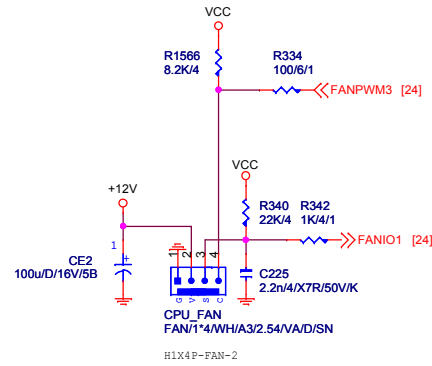
FIX PWR AcBel (ATX-400C-A2ADB)



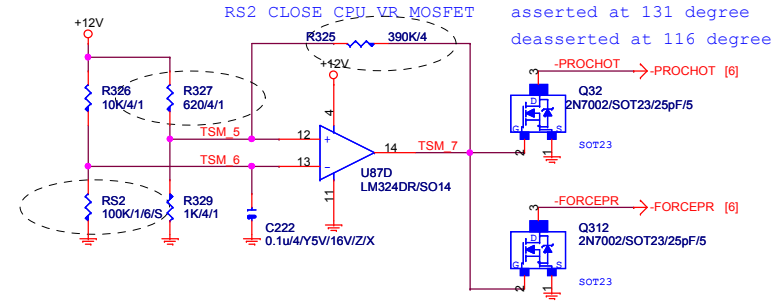
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Title		
ATX POWER CONNECTOR		
Size	Document Number	Rev
B	GA-G31M-ES2C	1.12
Date:	Friday, November 21, 2008	Sheet 31 of 33

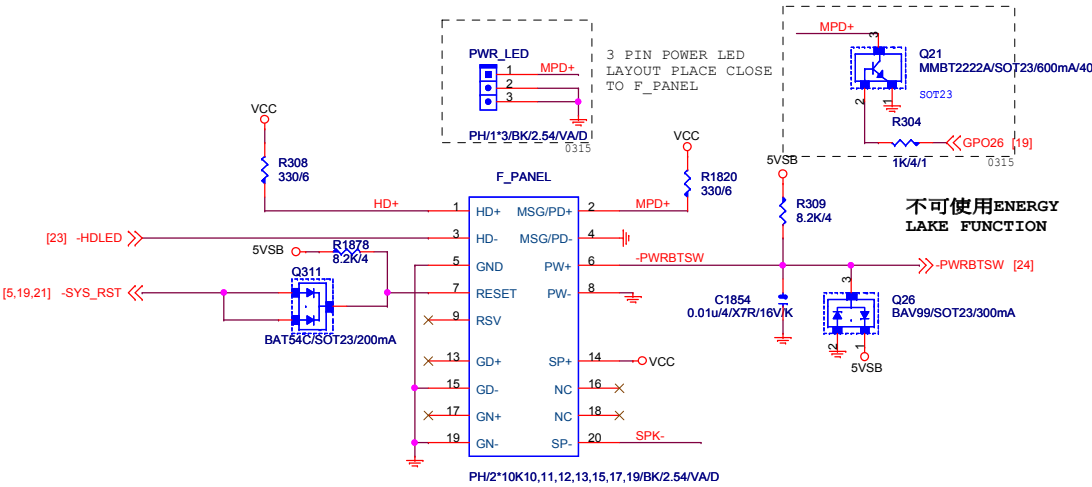
CPU SMART FAN SMART FAN



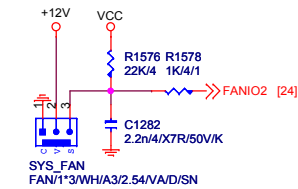
PROCESSOR HOT



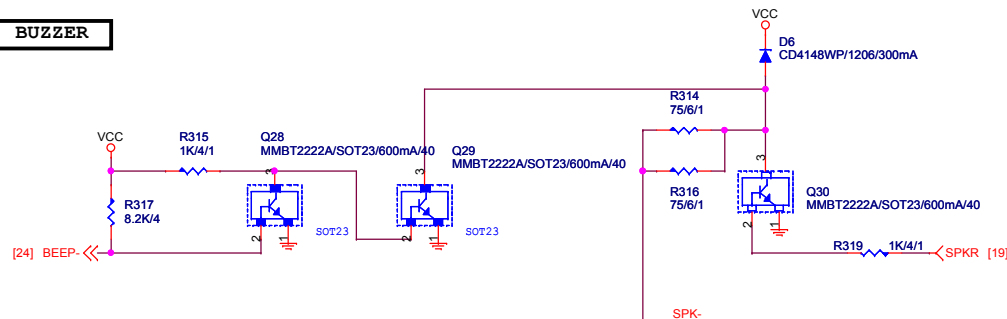
INTEL FRONT PANEL



SYS_FAN



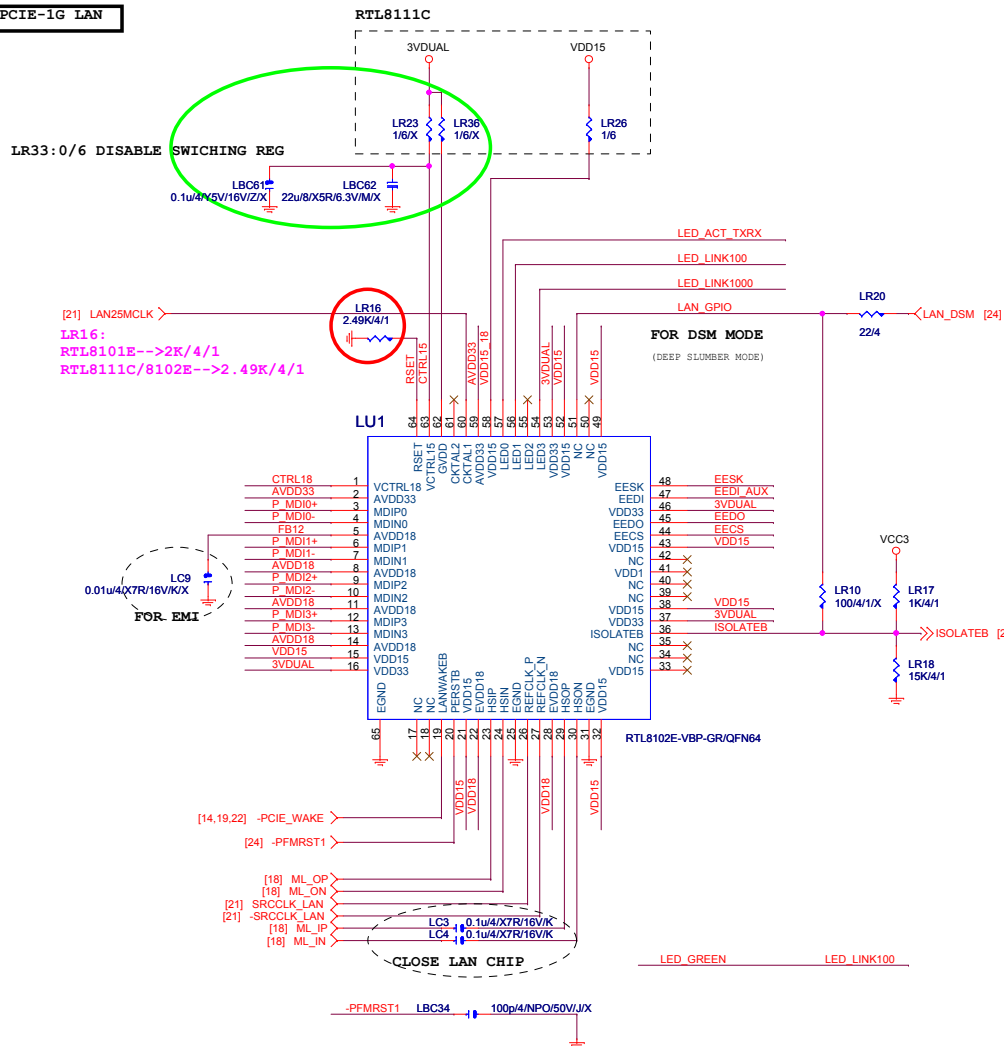
BUZZER



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Title		FRONT PANEL	
Size	Custom	Document Number	GA-G31M-ES2C
Date:	Friday, November 21, 2008	Sheet	32 of 33
		Rev	1.12

PCIE-1G LAN

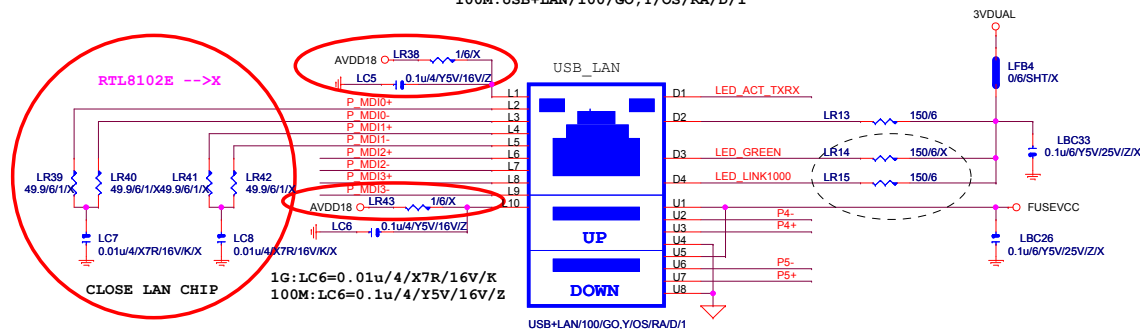


USB_LAN CONNECTOR

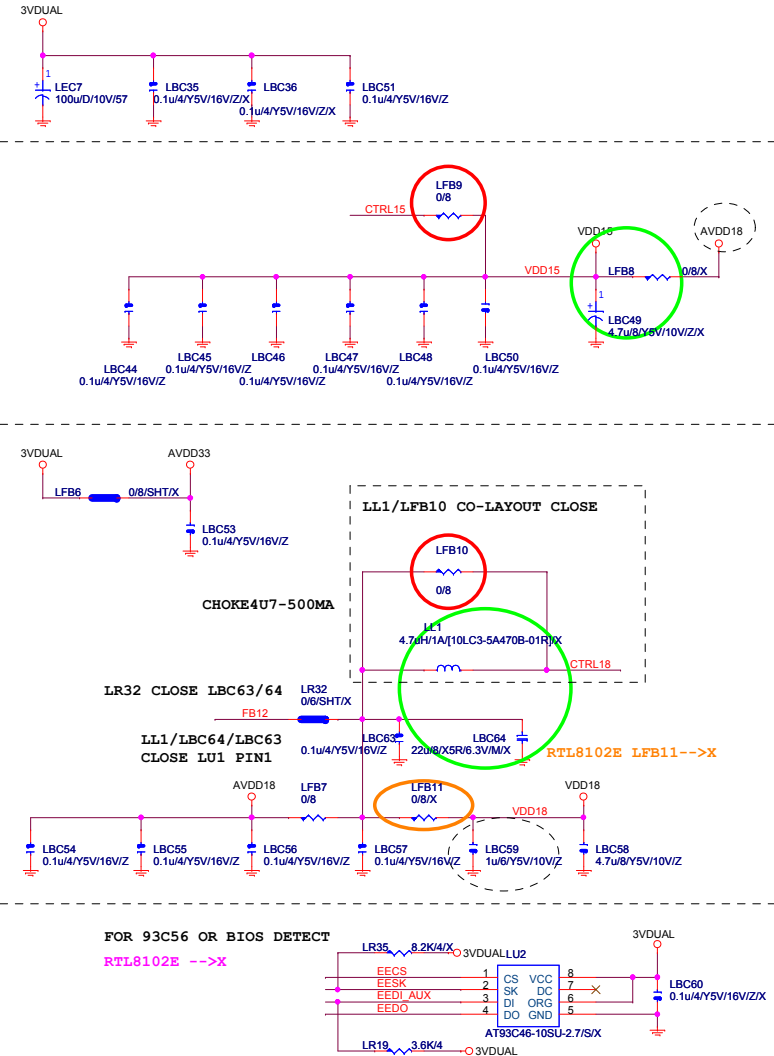
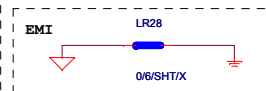
```
RTL8101E:LR38/LC5/LR43/LC6-->O
RTL8102E:LC5/LC6-->O
RTL8111C:LC6-->O
```

```
RTL8101E :L1+L10-->AVDD18+0.1U(BIOS DISABLE MDI-X FUNCTION)
```

```
1G  :USB+LAN/1G/GO,Y/OS/RA/D/1
100M:USB+LAN/100/GO,Y/OS/RA/D/1
```



USB_LAN



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REALTEK RTL8111C/8102E

Size	Document Number	GA-G31M-ES2C
Custom		

Custom	SA-COTM-LO20	1.12
Date:	Friday, November 21, 2008	Sheet 33 of 33