

SHEET TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B
06	P4 LGA775 C
07	P4 LGA775 D,E,F,G
08	GMCH-LAKEPORT HOST
09	GMCH-LAKEPORT DDRII
10	GMCH-LAKEPORT PCI E, DMI
11	GMCH-LAKEPORT INT VGA
12	GMCH-LAKEPORT GND
13	GMCH-LAKEPORT PWR
14	DDRII CHANNEL A 1
15	DDRII CHANNEL B 2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	GB/CK410M-OC CLOCK.
22	PCI SLOT 1,2,3
23	IDE/FLOPPY
24	ITE 8718 GBIX
25	COM LPT
26	BIOS, CI, HWM, KB/MS
27	AZALIA ALC888

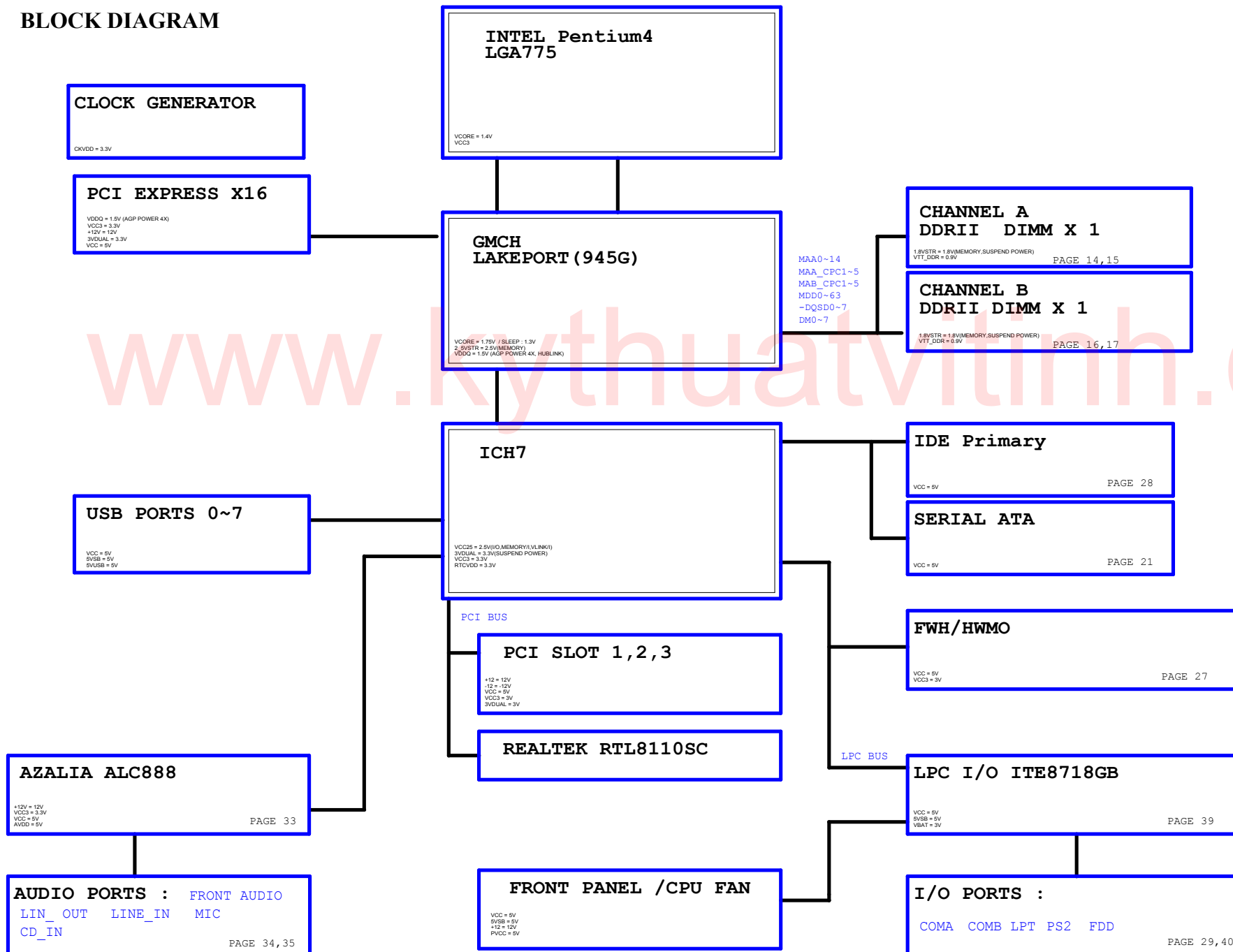
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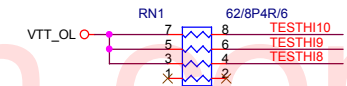
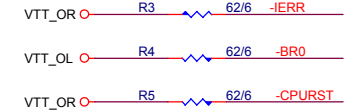
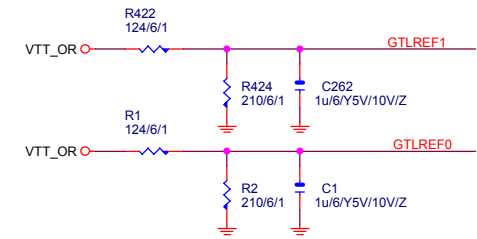
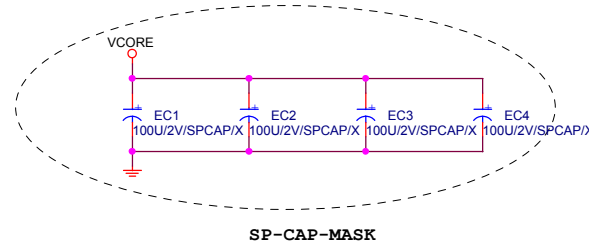
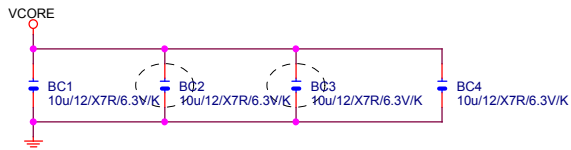
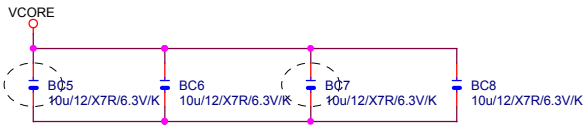
28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM ISL6312
31	ATX, OTHERS POWER
32	REALTEK RTL8110SC
33	FRONT PANEL

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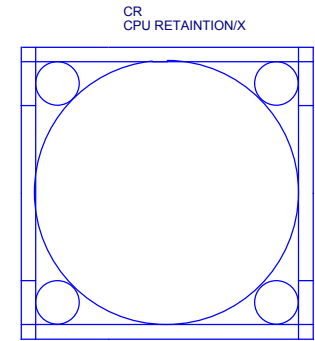
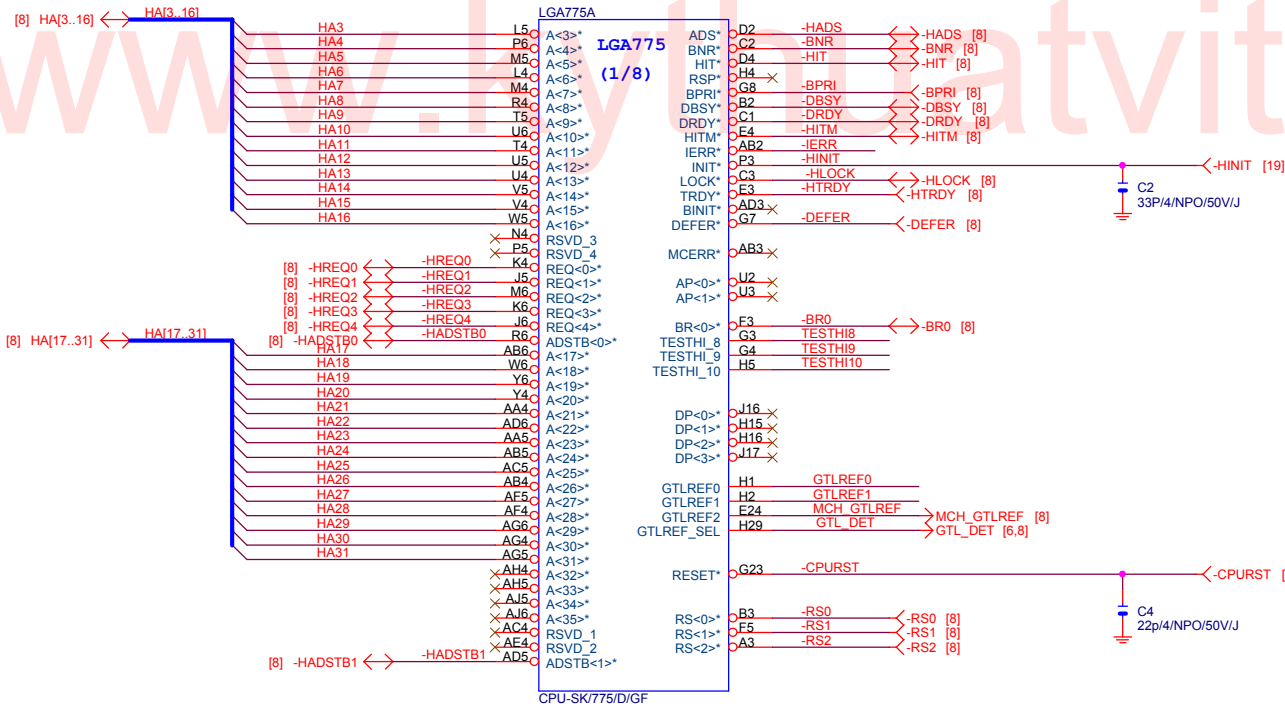
Title		
Cover Sheet		
Size	Document Number	Rev
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BLOCK DIAGRAM

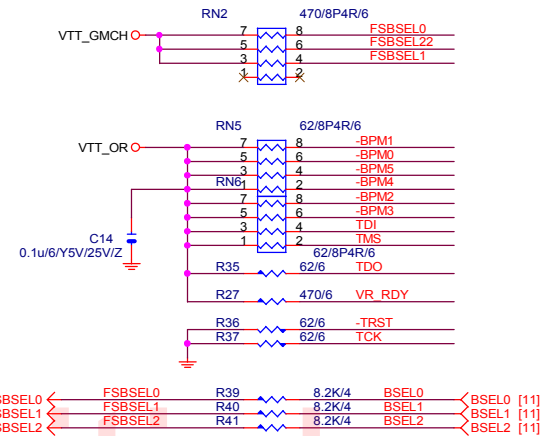
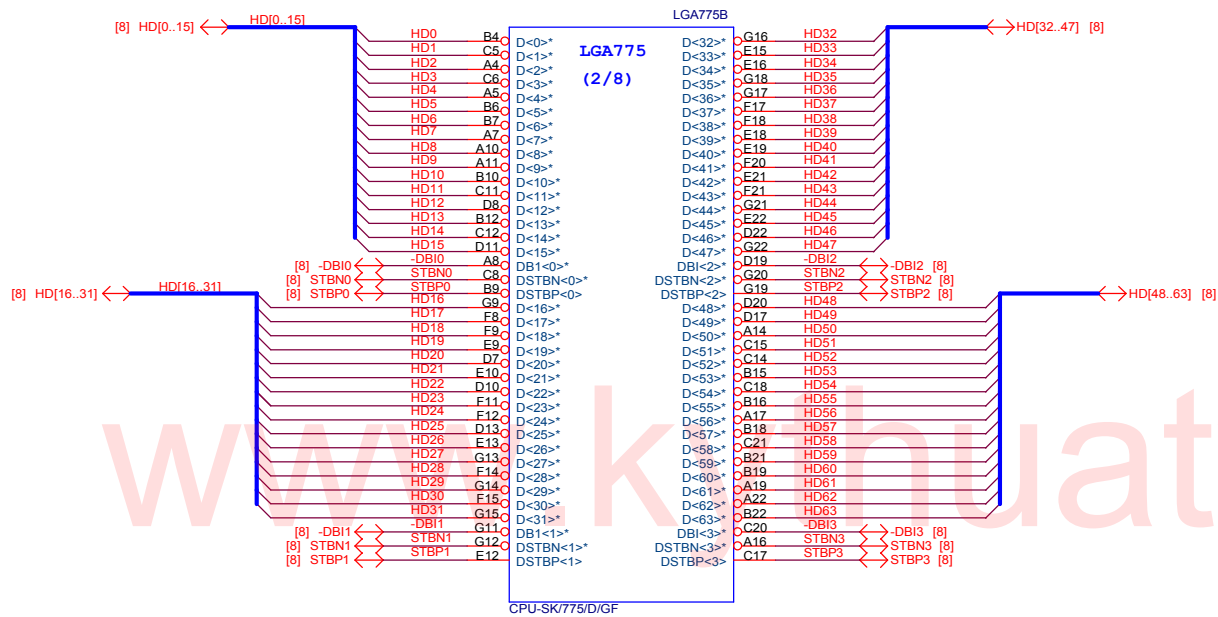




LGA775-D [DIP TYPE]

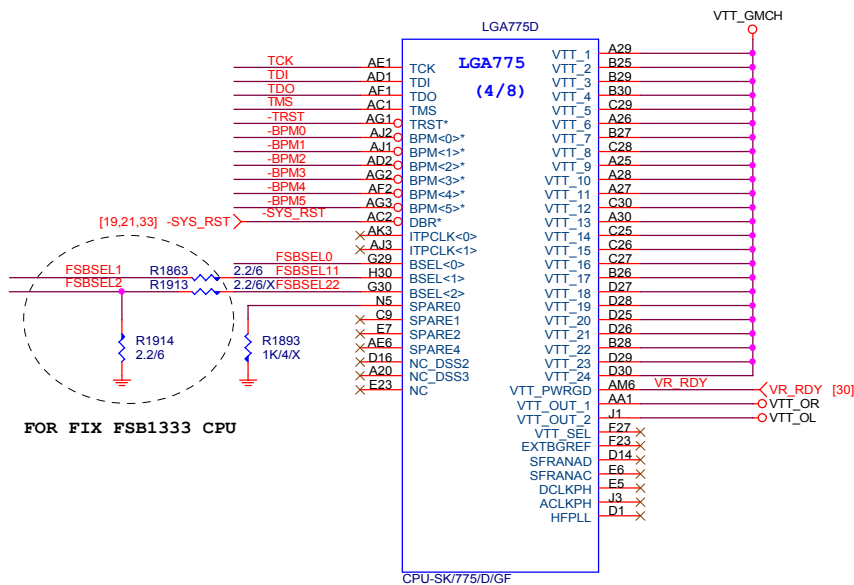
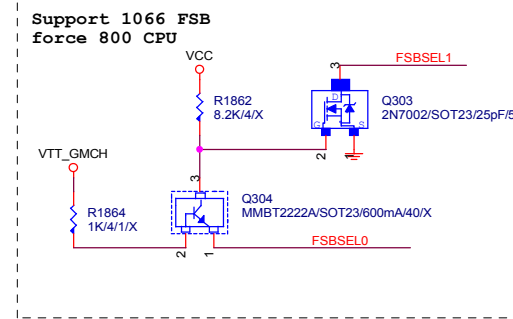


Gigabyte Technology		
Title P4_LGA775-A		
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CPU

NA	FSB	FSA	Clock	
FSBSEL3	FSBSEL1	FSBSEL0	100MHz	X
1	0	1	133MHz	
0	0	1	166MHz	
0	1	1	200MHz	
0	0	0	266MHz	

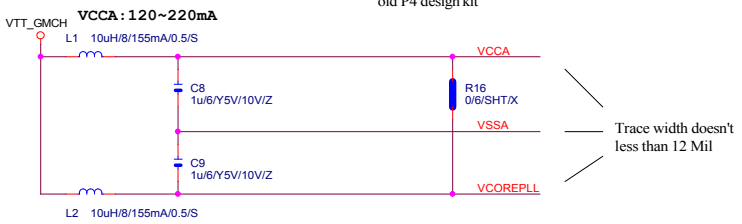


FOR FIX FSB1333 CPU

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Title			P4_LGA775-B,D		
Size	Document Number	945GME-DS2			Rev
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Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



Trace width doesn't
less than 12 Mil

As close as possible to
CPU socket

VCOREPLL: 100mA



CPU

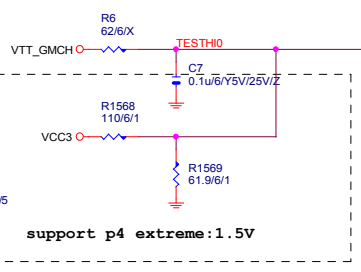
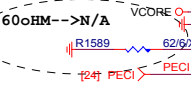
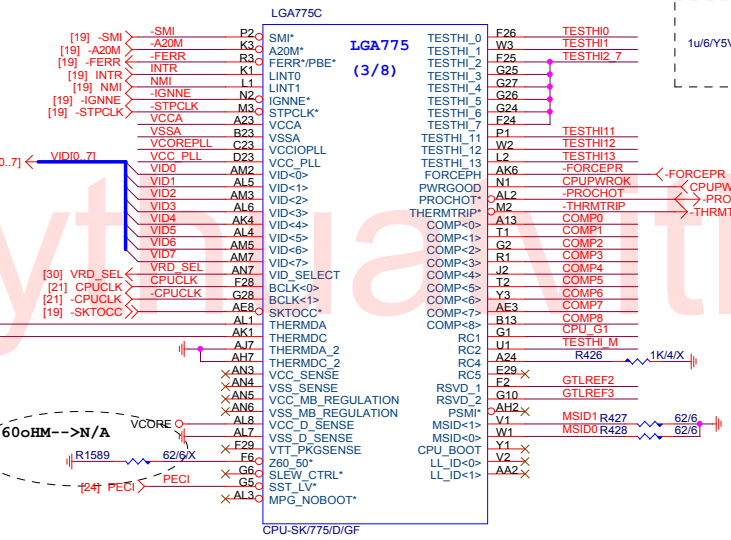
FSA	FSB	NA	Clock
FSBSEL0	FSBSEL1	FSBSEL3	Clock
1	0	1	100MHz
1	0	0	133MHz
1	1	0	166MHz
0	1	0	200MHz
0	0	0	266MHz

	RATIO
X	2.66/3.33
	2.00/2.5

945 Design Guide rev1.5 spec.
VCCA=120~220mA
公板為125mA

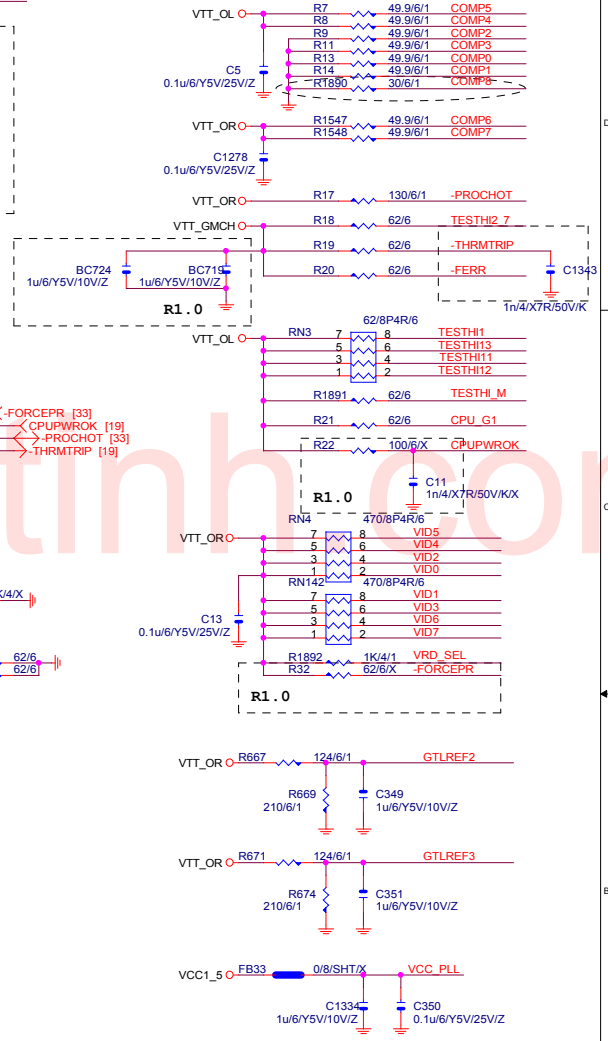
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10LI2-12100A-01=INDUCTOR 10uH 120mA TDK

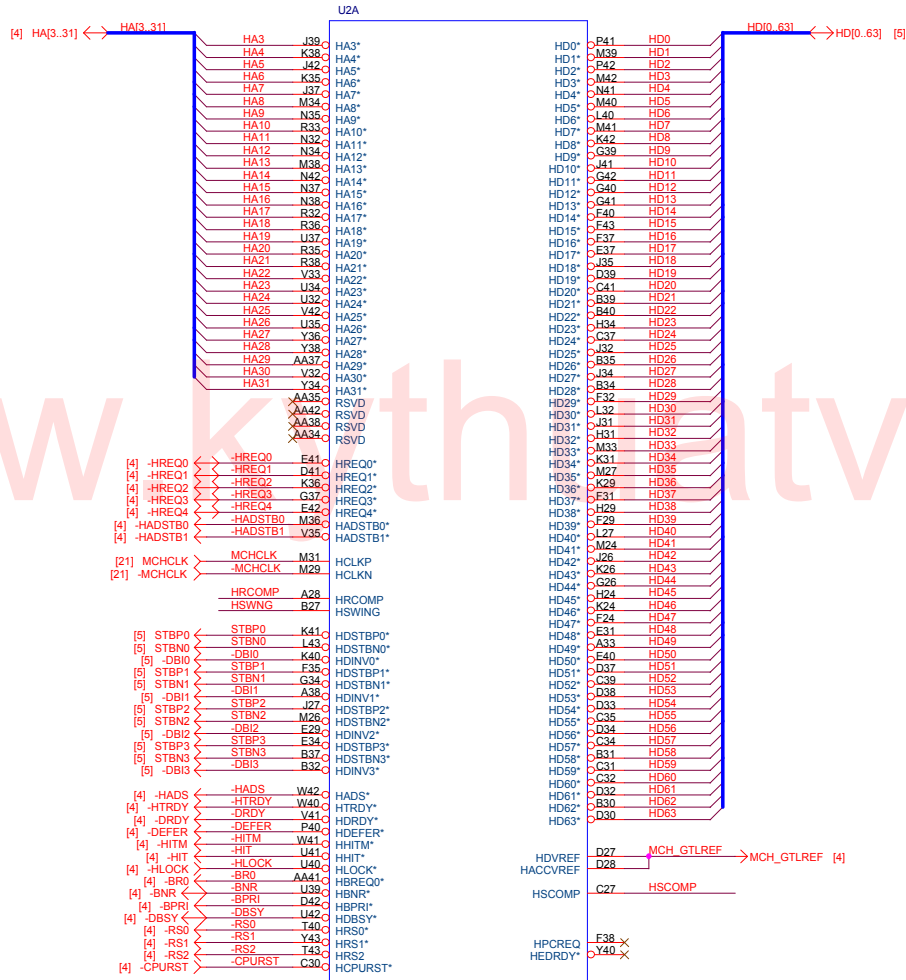
限用兩種



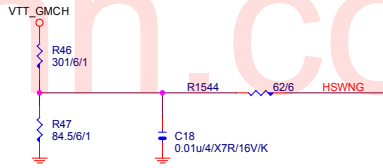
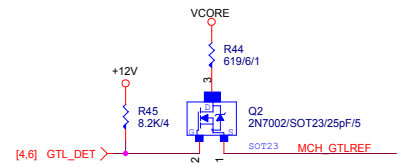
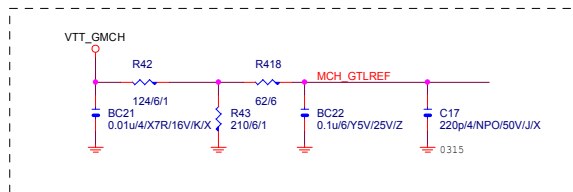
Place outside of CPU socket

COMP8 CHANGE 30/6/1 FOR CONROE CPU

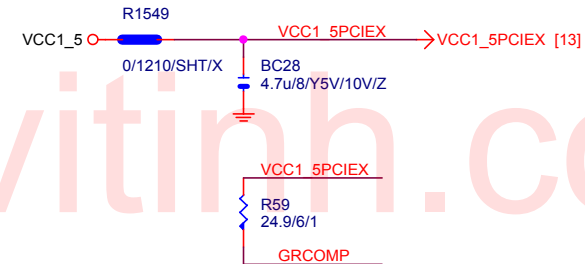
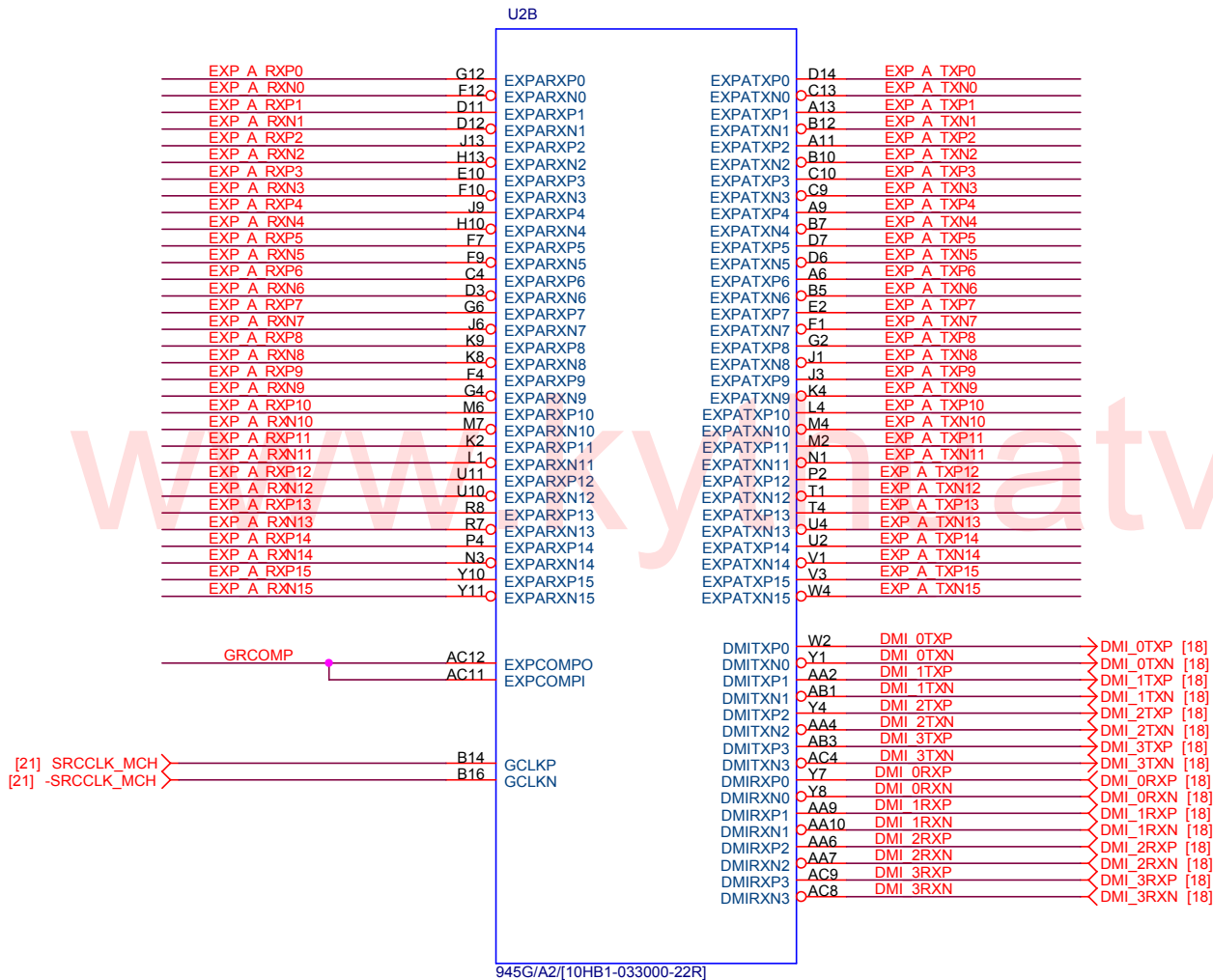




945G/A2[10HB1-033000-22R] Pb-Free

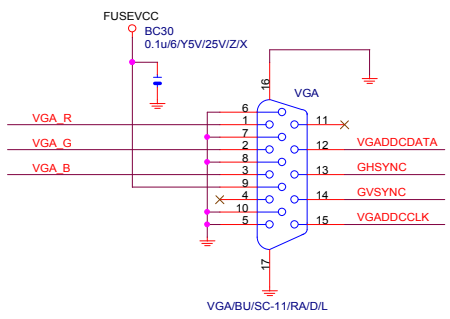
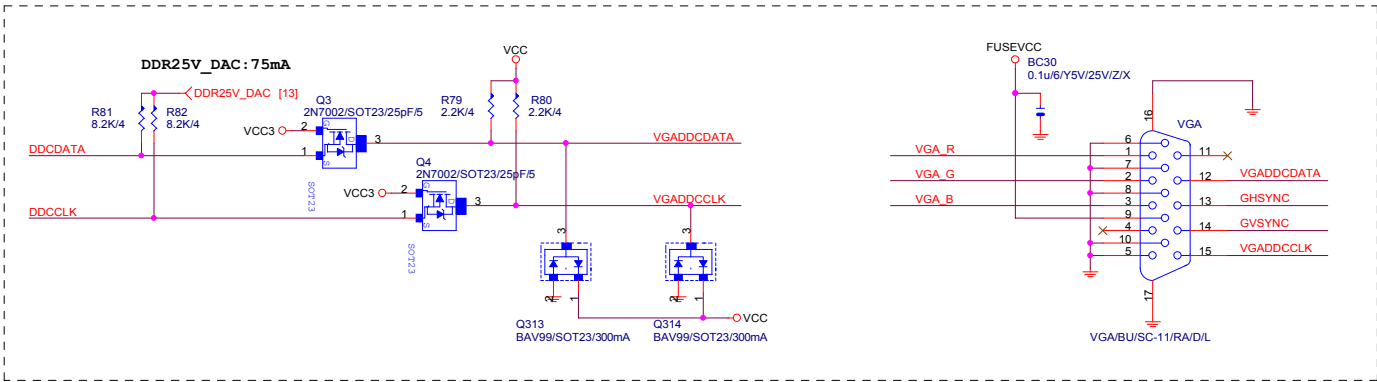
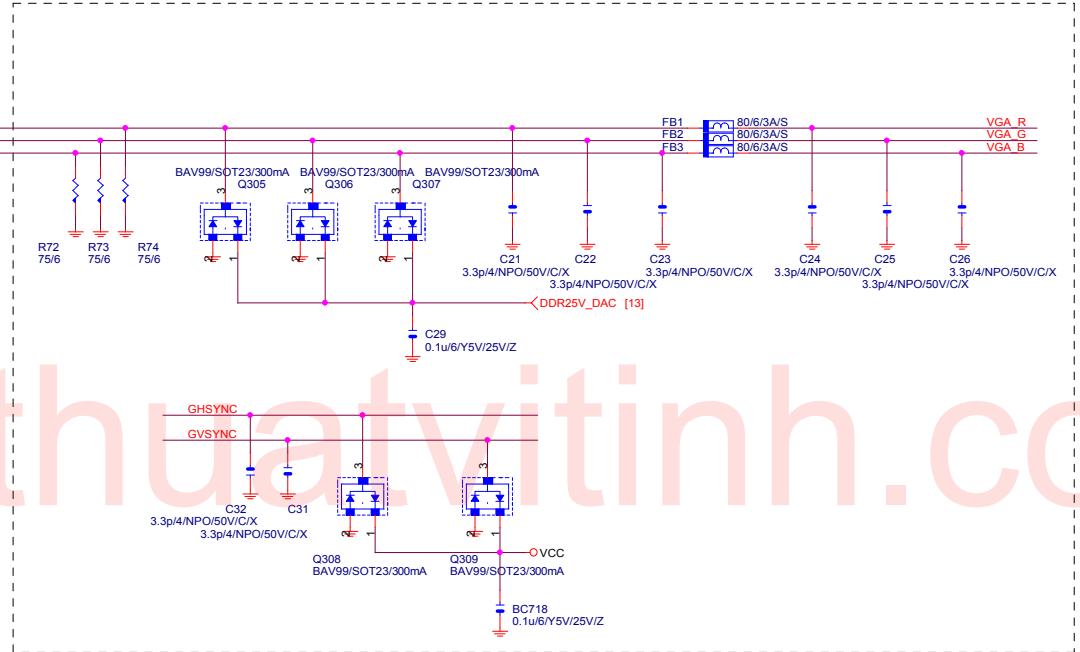
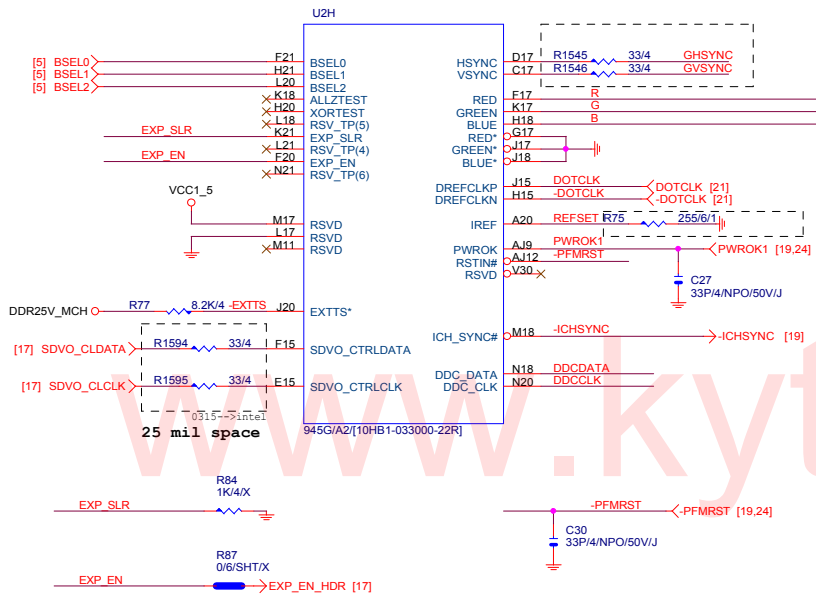


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Title GMCH-HOST		
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GMCH-PCI E & DMI		
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U2C

A4	VSS	N2
A16	VSS	N6
A22	VSS	N8
A26	VSS	N13
A31	VSS	N15
A35	VSS	N24
B4	VSS	N26
B6	VSS	N27
B9	VSS	N29
B11	VSS	N31
B13	VSS	N33
B21	VSS	N36
B22	VSS	N39
B28	VSS	N43
B33	VSS	P3
B38	VSS	P14
C3	VSS	P15
C5	VSS	P24
C7	VSS	P26
C12	VSS	P27
C14	VSS	P29
C22	VSS	P30
C40	VSS	R6
D2	VSS	R9
D5	VSS	R12
D10	VSS	R14
D16	VSS	R30
D20	VSS	R31
D21	VSS	R34
E3	VSS	R37
E4	VSS	R39
E7	VSS	T2
E9	VSS	T2
F12	VSS	T2
F13	VSS	U3
F17	VSS	U5
F18	VSS	U9
F20	VSS	U12
F21	VSS	U14
F32	VSS	U14
F2	VSS	U31
F6	VSS	U33
F8	VSS	U36
F18	VSS	U38
F26	VSS	V2
F34	VSS	V8
F42	VSS	V11
G3	VSS	V12
G5	VSS	V14
G7	VSS	V24
G9	VSS	V36
G10	VSS	V37
G13	VSS	V38
G15	VSS	V39
G18	VSS	V43
G20	VSS	W3
G21	VSS	W3
G24	VSS	Y2
G27	VSS	Y5
G29	VSS	Y6
G31	VSS	Y9
G32	VSS	Y12
G35	VSS	Y14
G38	VSS	Y14
H12	VSS	Y31
H17	VSS	Y35
H26	VSS	Y39
H27	VSS	Y42
H32	VSS	Y42
J2	VSS	AA3
J5	VSS	AA8
J7	VSS	AA11
J10	VSS	AA12
J12	VSS	AA14
J21	VSS	AA21
J24	VSS	AA23
J29	VSS	AA31
J38	VSS	AA33
J43	VSS	AA36
K3	VSS	AB2
K5	VSS	AB43
K6	VSS	AC2
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K10	VSS	AC7
K12	VSS	AC10
K13	VSS	AC14
K15	VSS	AR6
K20	VSS	AR15
K27	VSS	AR20
K32	VSS	AR23
K34	VSS	AR31
K37	VSS	AR32
K39	VSS	AR37
L2	VSS	AR39
L12	VSS	AR43
L13	VSS	AT12
L24	VSS	AT17
L26	VSS	AT18
L29	VSS	AT21
L31	VSS	AT23
L42	VSS	AT26
M3	VSS	AT27
M5	VSS	AT31
M8	VSS	AU6
M9	VSS	AU9
M10	VSS	AU12
M13	VSS	AU13
M20	VSS	AU15
M21	VSS	AU17
M35	VSS	AU20
M37	VSS	AD18
		AD20
		AD22
		AD24
		AD27
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		AE19
		AE21
		AE23
		AE25
		AF18
		AF20
		AF22
		AF24
		AY1
		BC4

945G/A2/[10HB1-033000-22R]

U2E

AD7	VSS	AD7
AD9	VSS	AD9
AD11	VSS	AD11
AD13	VSS	AD13
AD33	VSS	AD33
N24	VSS	N24
AD37	VSS	AD37
AD42	VSS	AD42
AF1	VSS	AF1
AF2	VSS	AF2
N33	VSS	N33
AF5	VSS	AF5
N39	VSS	N39
N43	VSS	N43
P3	VSS	P3
P14	VSS	P14
P15	VSS	P15
AG31	VSS	AG31
P26	VSS	P26
P27	VSS	P27
AG37	VSS	AG37
AG38	VSS	AG38
R6	VSS	R6
R9	VSS	R9
A17	VSS	A17
R12	VSS	R12
A110	VSS	A110
A130	VSS	A130
A131	VSS	A131
A133	VSS	A133
A135	VSS	A135
A137	VSS	A137
T2	VSS	T2
AK24	VSS	AK24
AK26	VSS	AK26
AK29	VSS	AK29
AK30	VSS	AK30
AL1	VSS	AL1
AL3	VSS	AL3
AL7	VSS	AL7
AL10	VSS	AL10
AL12	VSS	AL12
AL13	VSS	AL13
AL15	VSS	AL15
AL18	VSS	AL18
AL21	VSS	AL21
AL23	VSS	AL23
AL24	VSS	AL24
AL27	VSS	AL27
AL32	VSS	AL32
AL33	VSS	AL33
V38	VSS	V38
AL37	VSS	AL37
AM5	VSS	AM5
AM7	VSS	AM7
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AM3	VSS	AM3
AM33	VSS	AM33
Y6	VSS	Y6
AM36	VSS	AM36
Y9	VSS	Y9
AM39	VSS	AM39
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AE21	VSS	AE21
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AF20	VSS	AF20
AF22	VSS	AF22
AF24	VSS	AF24
AY1	VSS	AY1
BC4	VSS	BC4

VSS	AU21
VSS	AU24
VSS	AU26
VSS	AU29
VSS	AU32
VSS	AU34
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VSS	AV17
VSS	AV37
VSS	AW10
VSS	BA4
VSS	BA42
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VSS	BB6
VSS	BB11
VSS	BB14
VSS	BB19
VSS	BB34
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VSS	AC29
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NC	AV27
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NC	AW26
NC	BA2
NC	BB1
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RSVD	AG28
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RSVD	AJ24
RSVD	AJ27
RSVD	AK40
RSVD	AW17
RSVD	AW19
RSVD	AY14
RSVD	BC16
RSVD	AJ25
RSVD	AG29

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1. 425~1.575V

- VCC1_5
- N17
- P17
- P20
- P21
- AA22
- AB21
- AB22
- AB23
- AC22
- AD14
- AE9
- AF7
- AF8
- AF9
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- R15
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- AE26
- AE27
- AE29

1. 7~1.9V

- VCCSM
- BB16
- AW15
- BB42
- BB13
- BC18
- BC22
- BC26
- BB20
- VCCSM
- AW24
- B046
- VCCSM
- BC31
- BB38
- BB33
- VCCSM
- BB28
- VCCSM
- BB24
- AW29
- VCCSM
- AW31
- VCCSM
- AW34
- AX41
- VCCSM
- AW42
- VCCSM
- AW23
- VCCSM
- AW18
- VCCSM
- BC40
- VCCSM
- AW35
- VCCSM
- AX43
- VCCSM
- AW20
- VCCSM
- AW21
- VCCSM
- AW13
- VCCSM
- AW21

1. 14~1.26V

- VTT
- C23
- G23
- VTT
- P23
- E23
- VTT
- D23
- D24
- VTT
- D25
- VTT
- B25
- VTT
- B23
- VTT
- B26
- VTT
- H23
- VTT
- J23
- VTT
- K23
- VTT
- L23
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- M23
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- A24
- VTT
- F23
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- C26

2. 375~2.625V

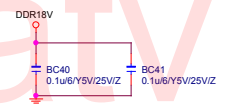
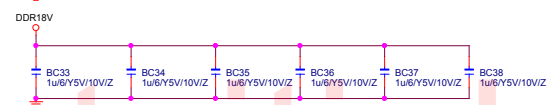
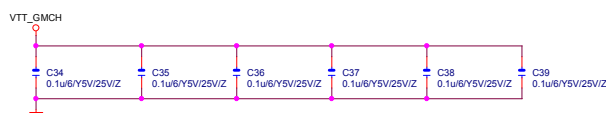
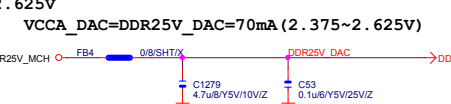
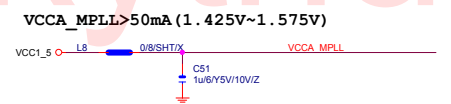
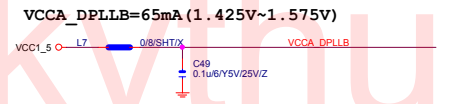
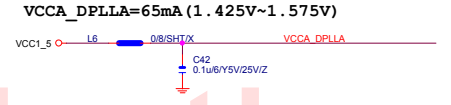
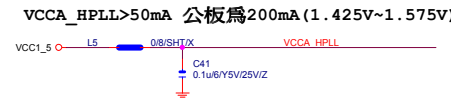
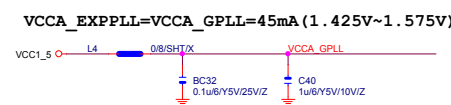
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- B19
- VCCA_MPLL
- B20
- VCCA_HPPLL
- C21
- VCCA_DPLL
- C19
- VCCA_DAC
- B18
- DDR25V_DAC
- C18
- D19
- VCCA_DAC
- VCC2
- VCCA_GPLL
- DDR25V_MCH
- B17
- VSSA_DAC
- A18

1. 425~1.575V

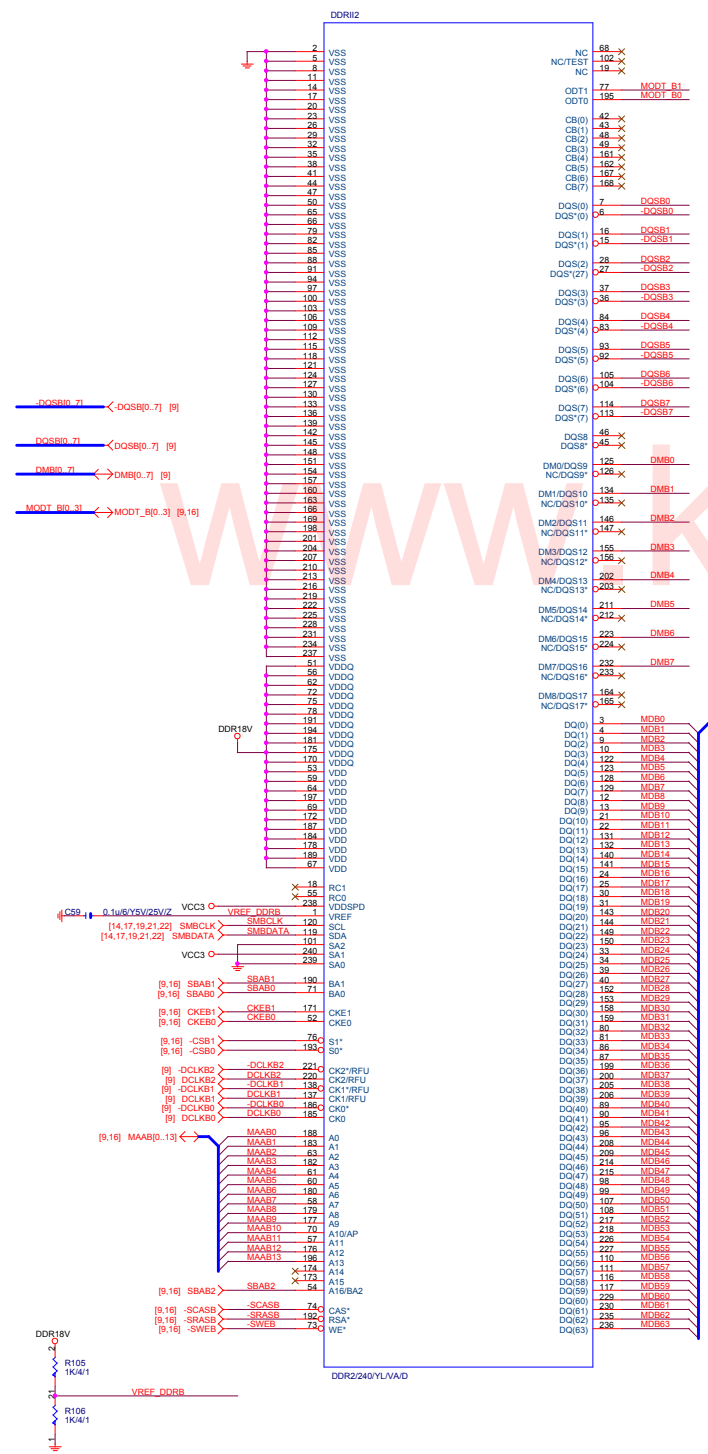
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- AC5
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- N11
- AE3
- N9
- VCC_EXP
- AD10
- AC6
- VCC_EXP
- AD2
- VCC_EXP
- AD4
- VCC_EXP
- AD5
- VCC_EXP
- AD6
- VCC_EXP
- Y13
- VCC_EXP
- NE
- VCC_EXP
- U8
- VCC_EXP
- AC13
- VCC_EXP
- U7
- VCC_EXP
- R10
- VCC_EXP
- U6
- VCC_EXP
- V6
- VCC_EXP
- Y7
- VCC_EXP
- V9
- VCC_EXP
- V10

VCC1_5

- VCC
- AG15
- AG17
- AG18
- AG19
- AG20
- AG21
- AG22
- AG23
- AG24
- VCC
- AJ15
- AJ17
- AJ18
- AJ20
- VCC
- AG19
- AG21
- AG22
- AG23
- AG24
- VCC
- AG15
- AG17
- AG18
- AG20
- VCC
- AG15
- AG17
- AG18
- AG20



945 Design Guide rev1.5 spec.
 VCCA_EXPPLL=VCCA_GPLL=45mA(1.425V~1.575V)
 VCCA_HPPLL>50mA 公板為200mA(1.425V~1.575V)
 VCCA_DPLLA=65mA(1.425V~1.575V)
 VCCA_DPLLB=65mA(1.425V~1.575V)
 VCCA_MPLL>50mA(1.425V~1.575V)
 VCCA_DAC=DDR25V_DAC=70mA(2.375~2.625V)



NC	68	X	
NC/TEST	102	X	
NC	19	X	
ODT1	77		MODT_B1
ODT0	195		MODT_B0
CB(0)	42	X	
CB(1)	43	X	
CB(2)	49	X	
CB(3)	48	X	
CB(4)	161	X	
CB(5)	162	X	
CB(6)	167	X	
CB(7)	168	X	
DQS(0)	7		DQS0
DQS*(0)	6		DQS0
DQS(1)	16		DQS1
DQS*(1)	15		DQS1
DQS(2)	28		DQS2
DQS*(2)	27		DQS2
DQS(3)	37		DQS3
DQS*(3)	36		DQS3
DQS(4)	84		DQS4
DQS*(4)	83		DQS4
DQS(5)	83		DQS5
DQS*(5)	82		DQS5
DQS(6)	105		DQS6
DQS*(6)	104		DQS6
DQS(7)	114		DQS7
DQS*(7)	113		DQS7
DQS8	46	X	
DQS8*	45	X	
DM0/DQS9	125		DMB0
NC/DQS9*	126	X	
DM1/DQS10	134		DMB1
NC/DQS10*	135	X	
DM2/DQS11	146		DMB2
NC/DQS11*	147	X	
DM3/DQS12	155		DMB3
NC/DQS12*	156	X	
DM4/DQS13	202		DMB4
NC/DQS13*	203	X	
DM5/DQS14	211		DMB5
NC/DQS14*	212	X	
DM6/DQS15	223		DMB6
NC/DQS15*	224	X	
DM7/DQS16	232		DMB7
NC/DQS16*	233	X	
DM8/DQS17	184		
NC/DQS17*	185	X	
DQ(0)	3		MDB0
DQ(1)	4		MDB1
DQ(2)	9		MDB2
DQ(3)	10		MDB3
DQ(4)	122		MDB4
DQ(5)	123		MDB5
DQ(6)	128		MDB6
DQ(7)	129		MDB7
DQ(8)	12		MDB8
DQ(9)	13		MDB9
DQ(10)	21		MDB10
DQ(11)	22		MDB11
DQ(12)	131		MDB12
DQ(13)	132		MDB13
DQ(14)	140		MDB14
DQ(15)	141		MDB15
DQ(16)	24		MDB16
DQ(17)	25		MDB17
DQ(18)	30		MDB18
DQ(19)	31		MDB19
DQ(20)	143		MDB20
DQ(21)	144		MDB21
DQ(22)	149		MDB22
DQ(23)	150		MDB23
DQ(24)	33		MDB24
DQ(25)	34		MDB25
DQ(26)	39		MDB26
DQ(27)	40		MDB27
DQ(28)	152		MDB28
DQ(29)	153		MDB29
DQ(30)	158		MDB30
DQ(31)	159		MDB31
DQ(32)	80		MDB32
DQ(33)	81		MDB33
DQ(34)	86		MDB34
DQ(35)	87		MDB35
DQ(36)	159		MDB36
CK2*/RFU	200		MDB37
CK1*/RFU	206		MDB38
CK1*/RFU	206		MDB39
CK0*	89		MDB40
CK0*	90		MDB41
CK0*	91		MDB42
CK0*	95		MDB43
CK0*	96		MDB44
CK0*	208		MDB45
CK0*	214		MDB46
CK0*	215		MDB47
CK0*	98		MDB48
CK0*	99		MDB49
CK0*	107		MDB50
CK0*	108		MDB51
CK0*	217		MDB52
CK0*	218		MDB53
CK0*	226		MDB54
CK0*	227		MDB55
CK0*	110		MDB56
CK0*	111		MDB57
CK0*	116		MDB58
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CK0*	235		MDB62
CK0*	236		MDB63

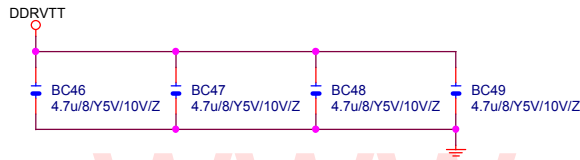
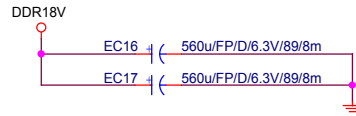
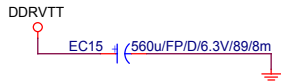
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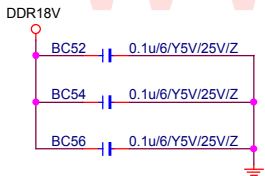
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Size	Document Number	945GME-DS2		Rev.	3.0
Column					
Date					Sheet 15 of 33

DDR TERMINATION CHANNEL A

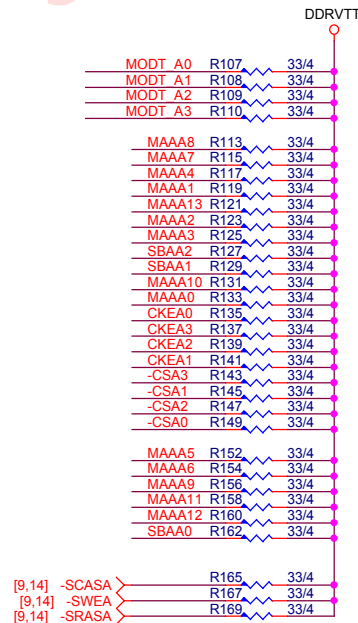
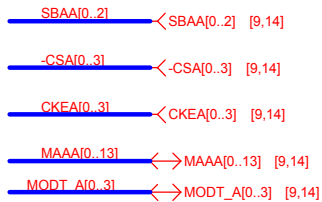
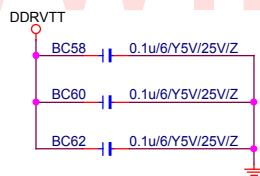
DDRVTT Decouple



DDR18V Decouple

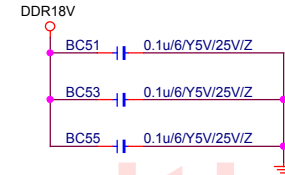


DDRVTT Decouple

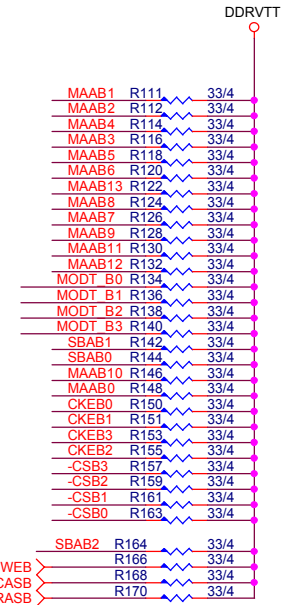
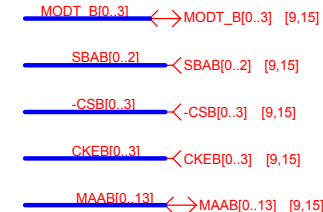
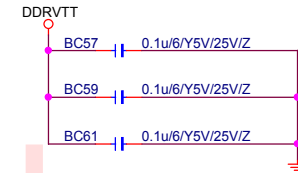


DDR TERMINATION CHANNEL B

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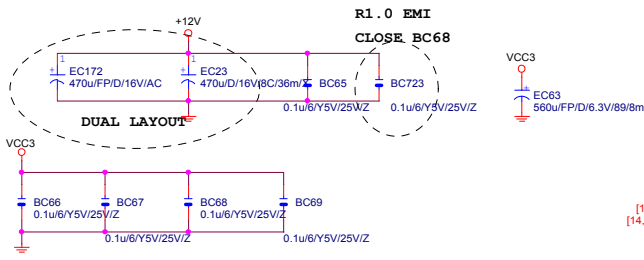


DDRVTT Decouple



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DDRII TERMINATOR		
Size Custom	Document Number	Rev
	945GME-DS2	3.0
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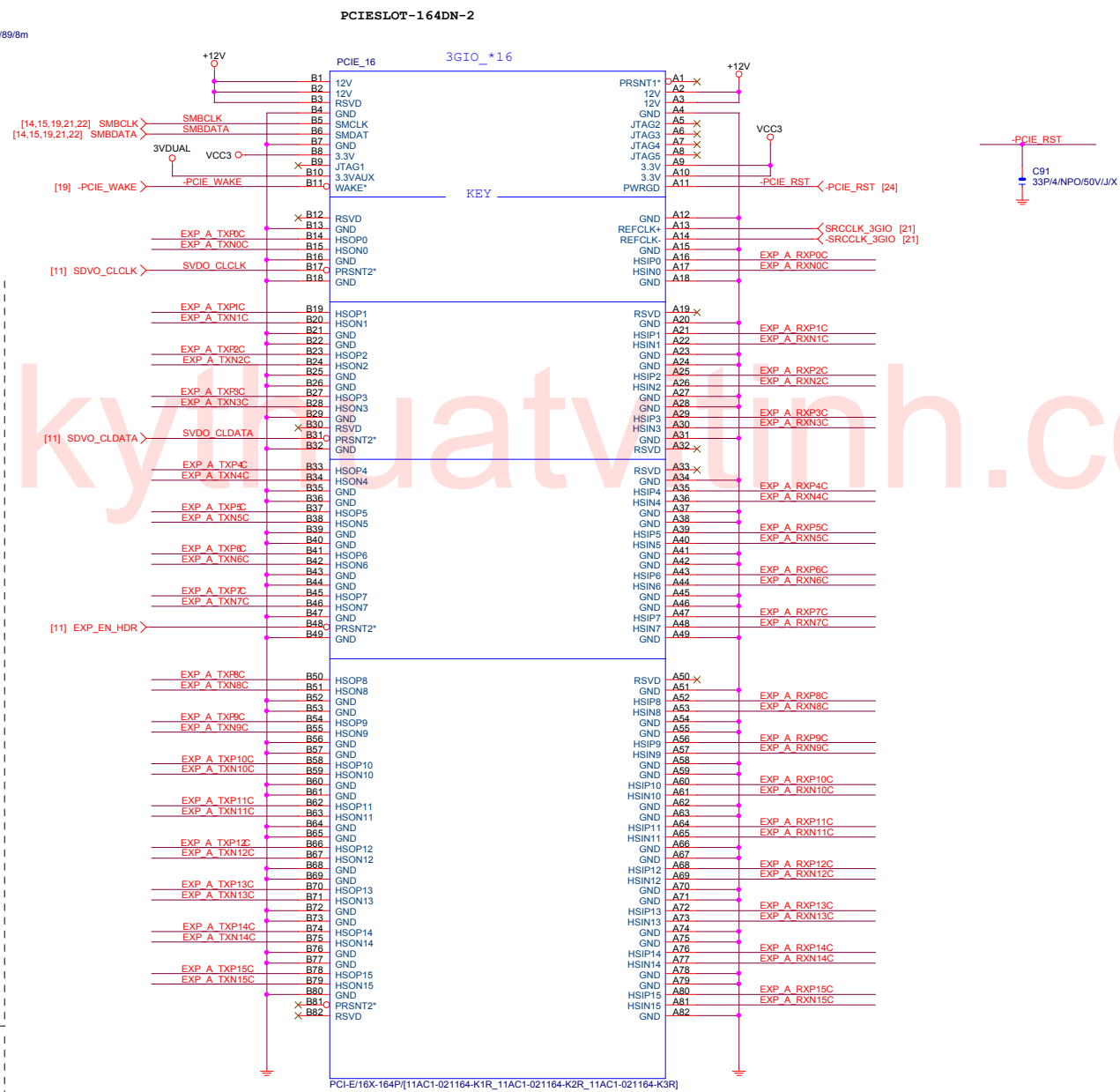


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EXP A TXP2	C96	0.1u/4Y5V/16V/Z	EXP A TXP2C
EXP A TXN2	C97	0.1u/4Y5V/16V/Z	EXP A TXN2C
EXP A TXP3	C98	0.1u/4Y5V/16V/Z	EXP A TXP3C
EXP A TXN3	C99	0.1u/4Y5V/16V/Z	EXP A TXN3C
EXP A TXP4	C100	0.1u/4Y5V/16V/Z	EXP A TXP4C
EXP A TXN4	C101	0.1u/4Y5V/16V/Z	EXP A TXN4C
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EXP A TXN5	C103	0.1u/4Y5V/16V/Z	EXP A TXN5C
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EXP A TXN7	C107	0.1u/4Y5V/16V/Z	EXP A TXN7C
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EXP A TXN8	C109	0.1u/4Y5V/16V/Z	EXP A TXN8C
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EXP A TXN15	C123	0.1u/4Y5V/16V/Z	EXP A TXN15C

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EXP A RXP15	SR31	0/4/SHT/X	EXP A RXP15C
EXP A RXN15	SR32	0/4/SHT/X	EXP A RXN15C

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[18] PCIE_OP3	PCIE_OP3	SR39	0/4/X	EXP A TXP3C
[18] PCIE_ON3	PCIE_ON3	SR40	0/4/X	EXP A TXN3C

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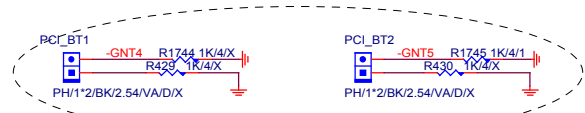
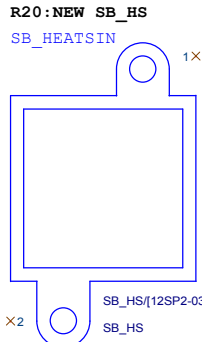
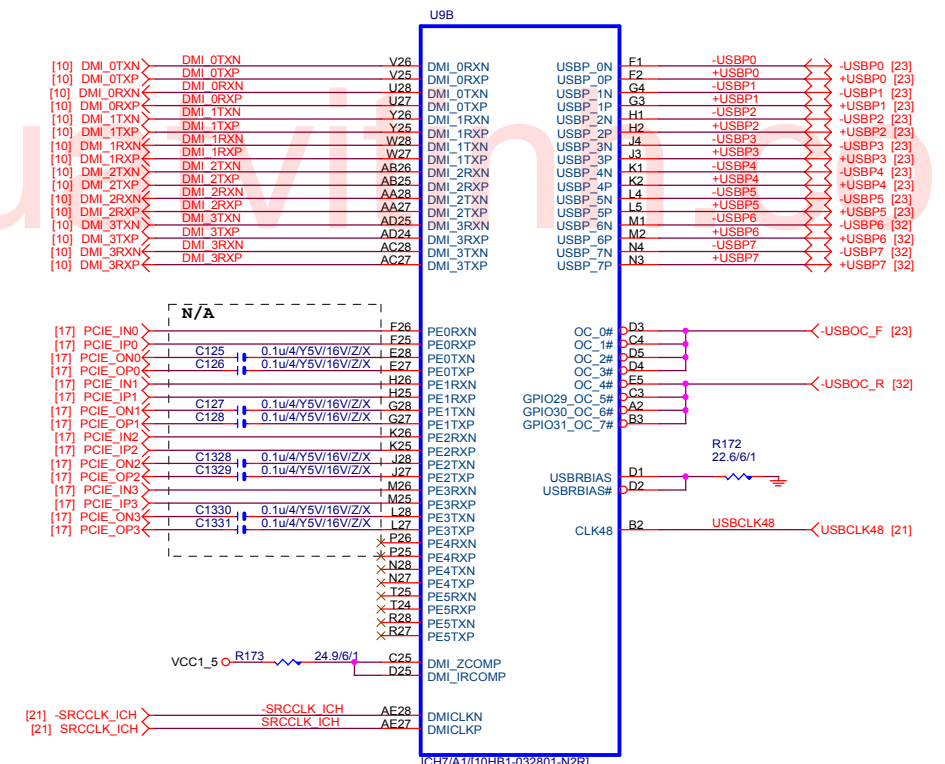
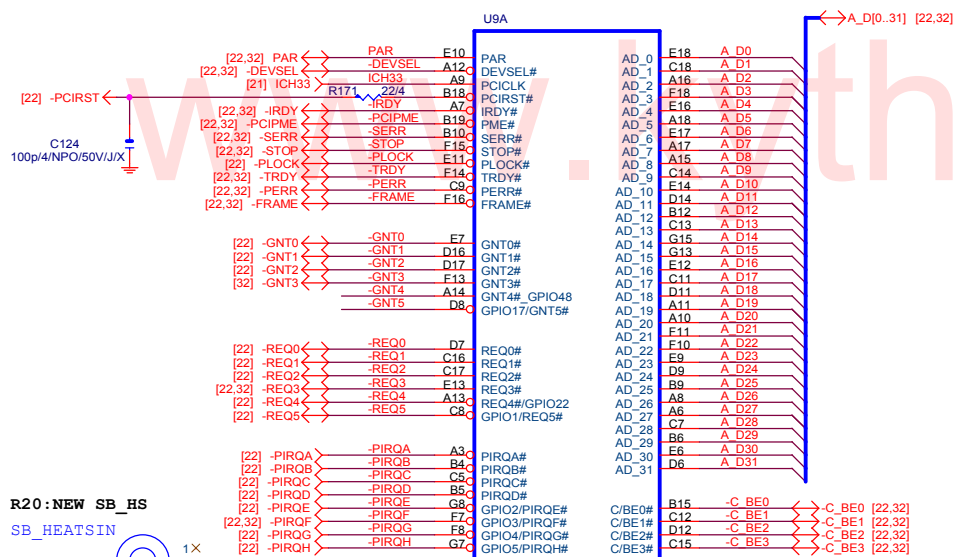


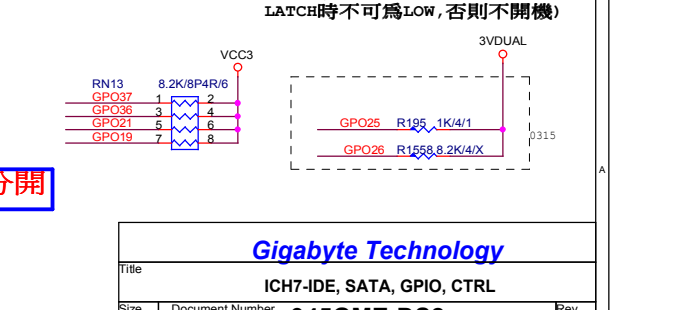
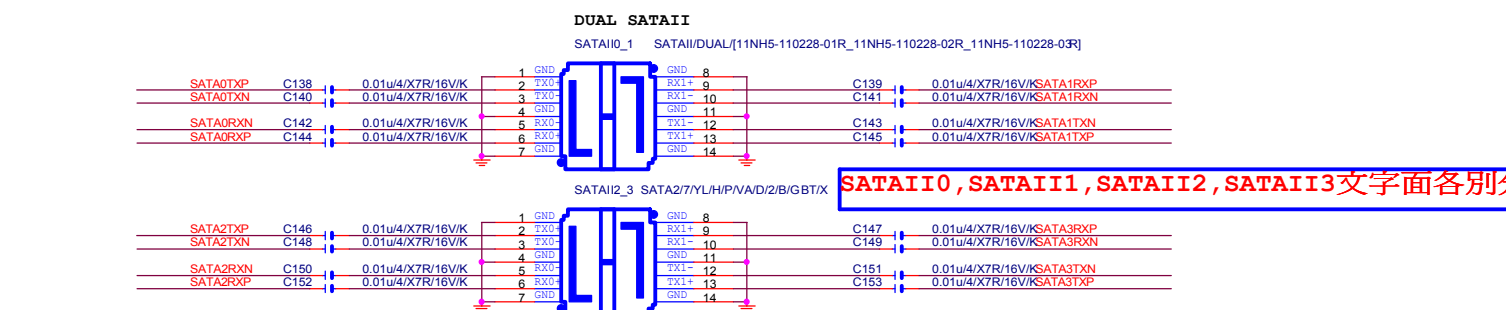
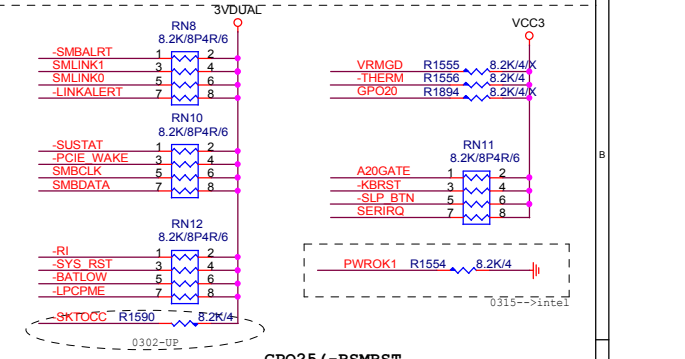
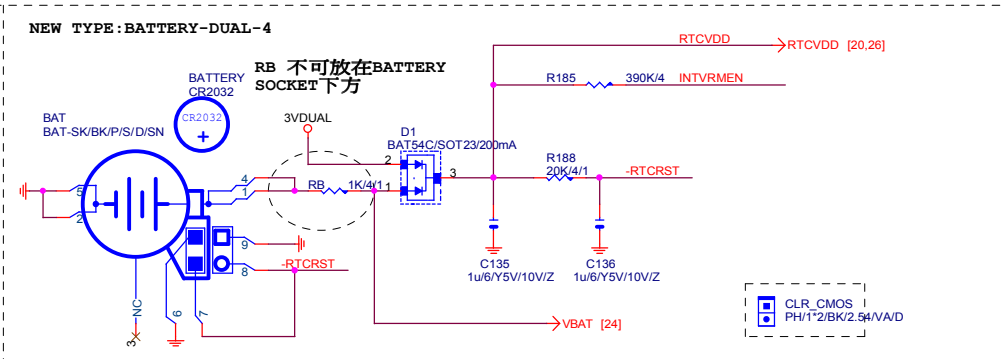
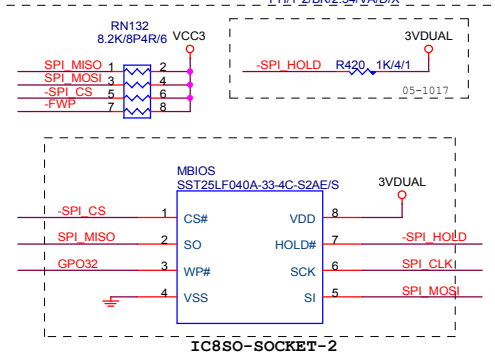
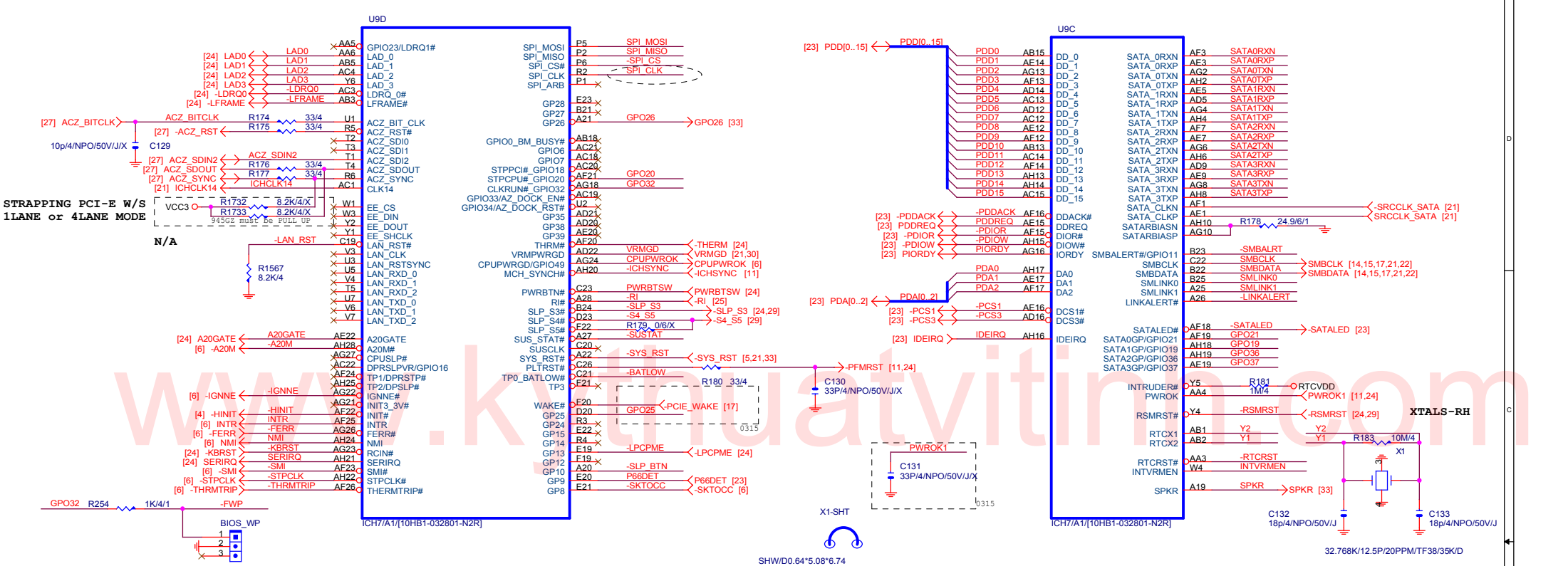
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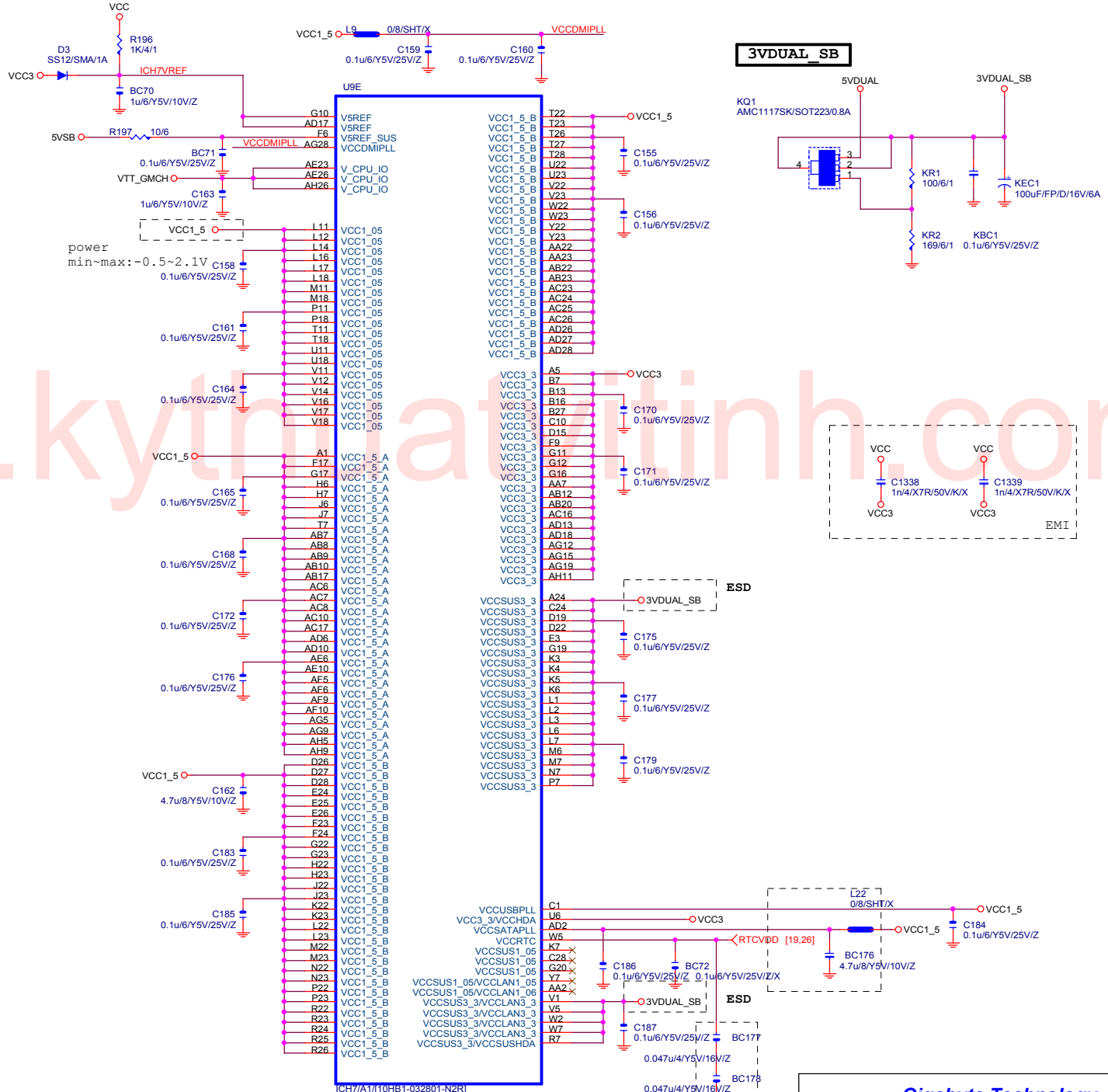
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Date:	Thursday, February 08, 2007	Sheet	17	of 33





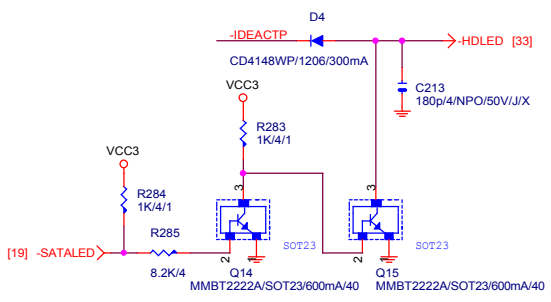
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B8	VSS4	R17
B11	VSS5	R18
B14	VSS6	T6
B17	VSS7	T12
B20	VSS8	T13
B26	VSS9	T14
B28	VSS10	T15
C2	VSS11	T16
C6	VSS12	T17
D10	VSS13	U4
D13	VSS14	U12
D18	VSS15	U13
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D24	VSS17	U15
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E2	VSS19	U17
E8	VSS20	U24
E15	VSS21	U25
F3	VSS22	U26
F4	VSS23	U27
F12	VSS24	U28
F27	VSS25	V2
F28	VSS26	V4
G1	VSS27	V7
G2	VSS28	V8
G5	VSS29	W6
G6	VSS30	W24
G9	VSS31	W25
G14	VSS32	W26
G18	VSS33	Y3
G21	VSS34	Y24
G24	VSS35	Y28
G25	VSS36	AA1
G26	VSS37	AA24
H3	VSS38	AA25
H4	VSS39	AA26
H5	VSS40	AB4
H27	VSS42	AB8
H28	VSS43	AB11
J1	VSS44	AB14
J2	VSS45	AB16
J5	VSS46	AB19
J24	VSS47	AB21
J25	VSS48	AB24
J26	VSS49	AB27
K24	VSS50	AB28
K27	VSS51	AC2
K28	VSS52	AC5
L13	VSS53	AC9
L15	VSS54	AC11
L24	VSS55	AD3
L25	VSS56	AD4
L26	VSS57	AD7
M3	VSS58	AD8
M4	VSS59	AD11
M5	VSS60	AD15
M12	VSS61	AD19
M13	VSS62	AD23
M14	VSS63	AE2
M15	VSS64	AE4
M16	VSS65	AE8
M17	VSS66	AE11
M24	VSS67	AE13
M27	VSS68	AE18
M28	VSS69	AE21
N1	VSS70	AE24
N2	VSS71	AE25
N5	VSS72	AF2
N6	VSS73	AF4
N11	VSS74	AF8
N12	VSS75	AF11
N13	VSS76	AF17
N14	VSS77	AF27
N15	VSS78	AG1
N16	VSS79	AG3
N17	VSS80	AG7
N18	VSS81	AG14
N24	VSS82	AG17
N25	VSS83	AG20
N26	VSS84	AG25
P3	VSS85	AH1
P4	VSS86	AH3
P12	VSS87	AH7
P13	VSS88	AH12
P14	VSS89	AH23
P15	VSS90	AH27
P16	VSS91	C27
P17	VSS92	E4
P24	VSS93	AG11
P27	VSS94	
P28	VSS95	
R1	VSS96	
R11	VSS97	
R12	VSS98	
R13	VSS99	
	VSS100	

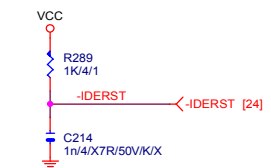


Gigabyte Technology		
ICHT7-PWR & GND		
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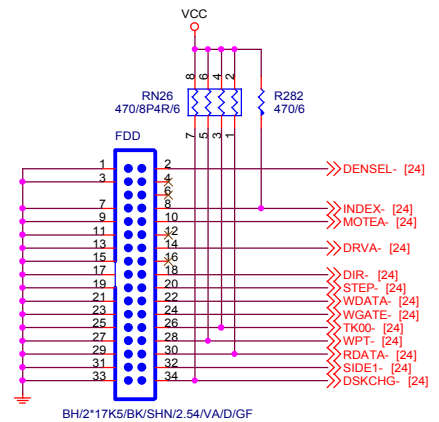
IDE/SATA LED



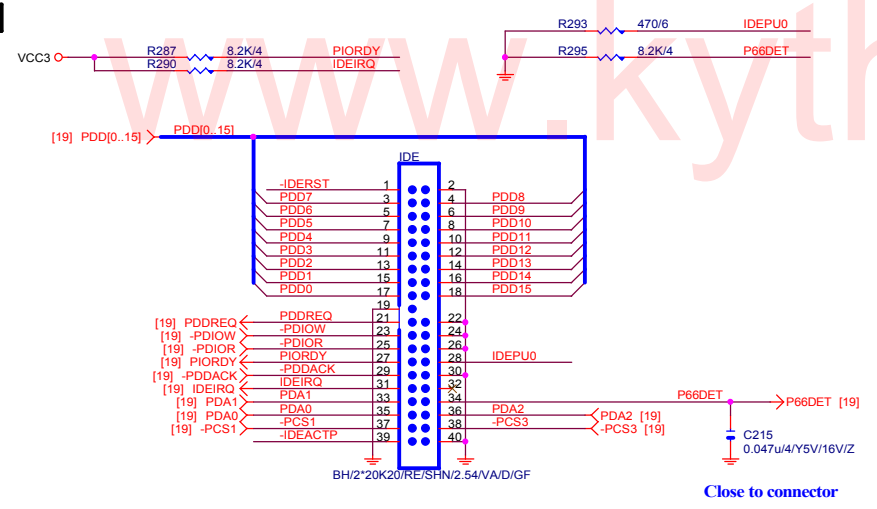
IDE RESET



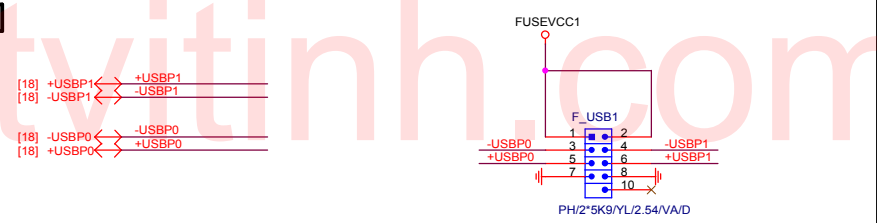
FLOPPY



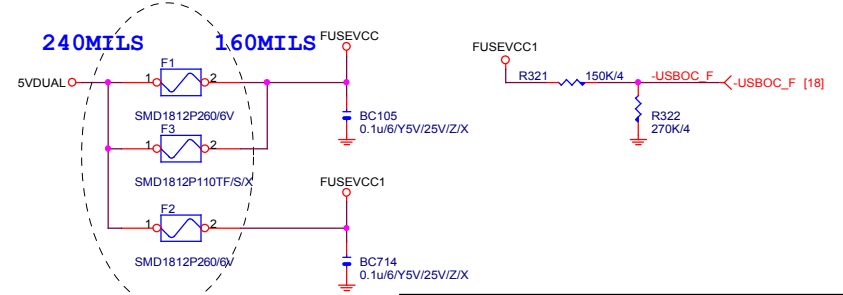
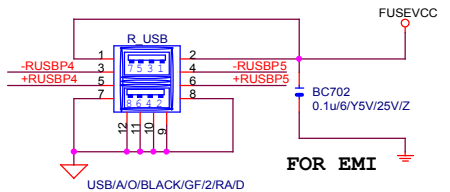
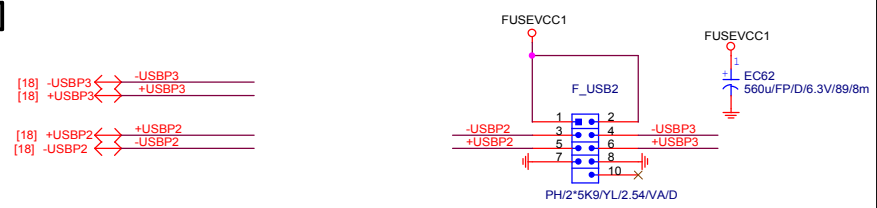
IDE

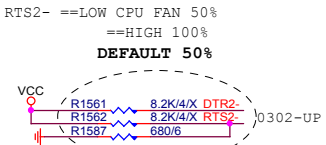


FRONT USB1

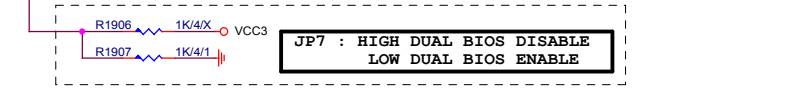
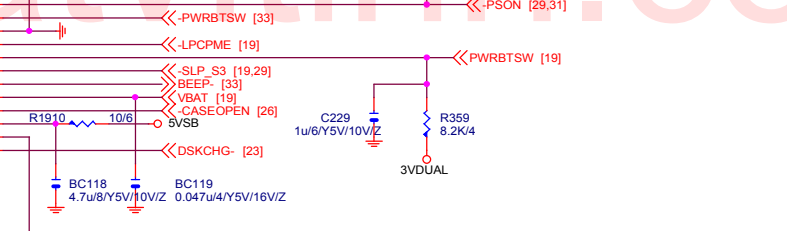
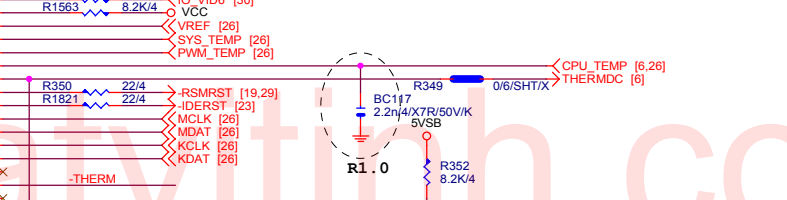
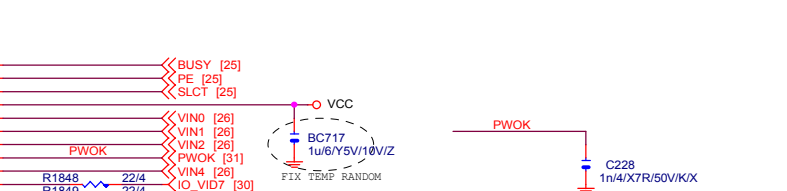
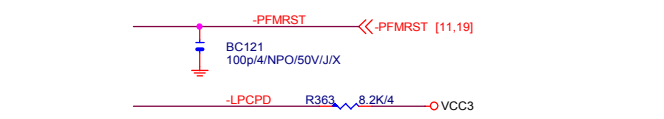
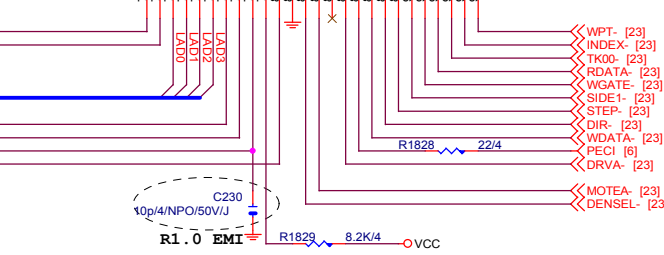
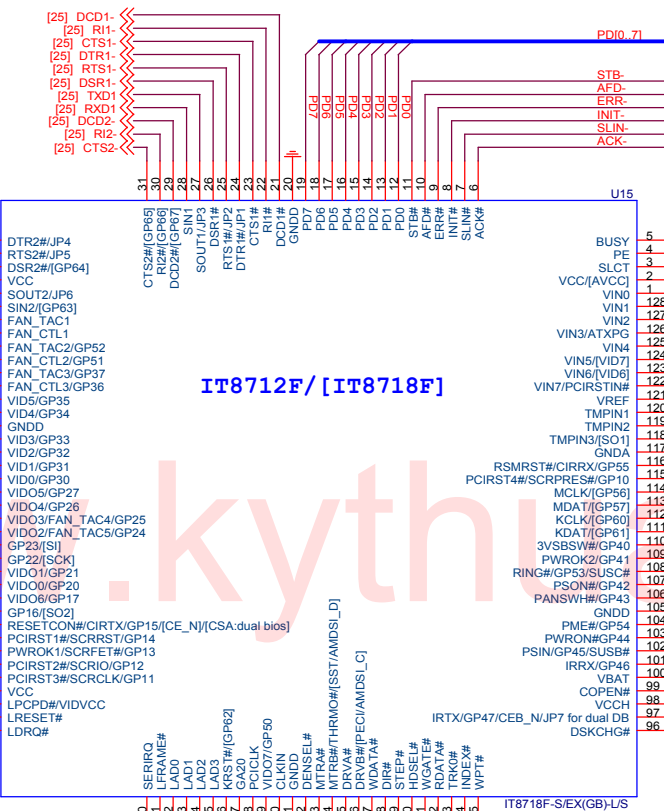
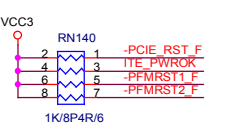
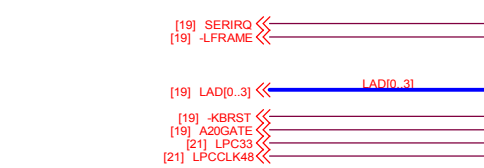
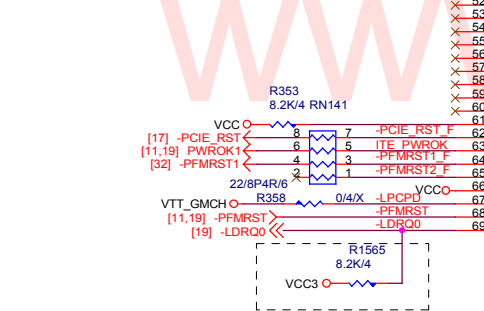
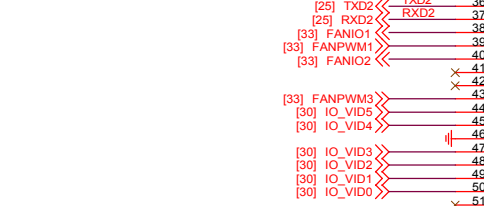


FRONT USB2





SOUT2	1	VID pins threshold voltage select: Vih/Vil : 2.0/0.8V
	0	VID pins threshold voltage select: Vih/Vil : 0.8/0.4V

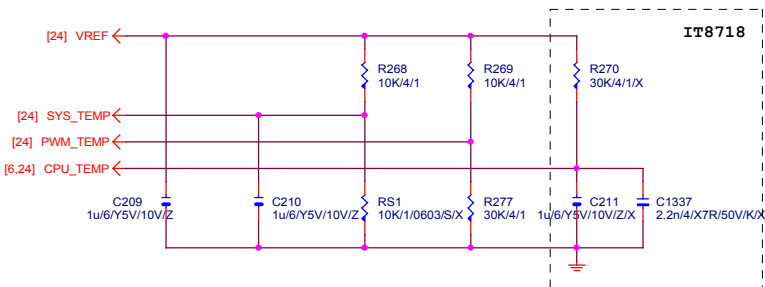


**JP7 : HIGH DUAL BIOS DISABLE
 LOW DUAL BIOS ENABLE**

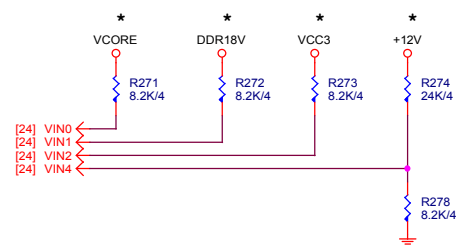
Dual BIOS:
 GB logo :Pin 61 (GP15/CSA)
 GB logo :Pin 59 (GP17/CSB)
 Pin 59 Dual BIOS ,Power On Strapping:
 H ==>Dual BIOS function Enable
 L ==>Dual BIOS function Disable

1.2V or 3.3V tolerance select.
 1.2V OUTPUT 接 VTT_GMCH
 3.3V OUTPUT 接 3.3V
 LPCPD#=#VIDVCC

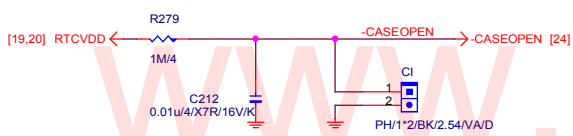
TEMP H/W MONITOR



VOLTAGE-- H/W MONITOR

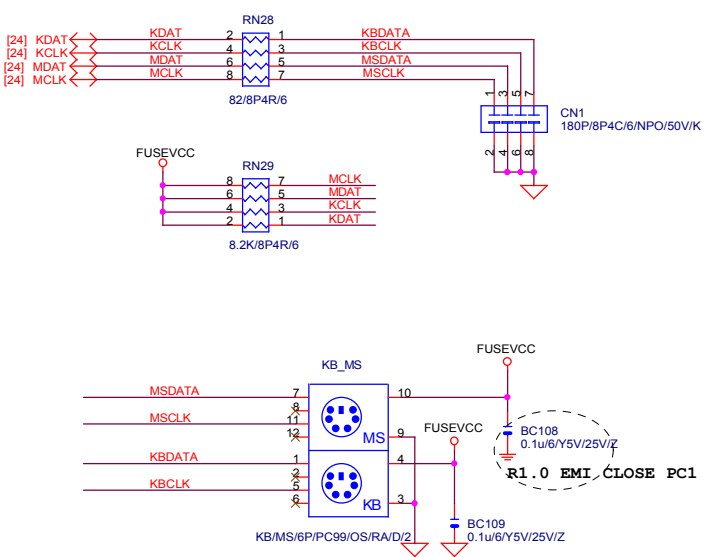


CASE OPEN

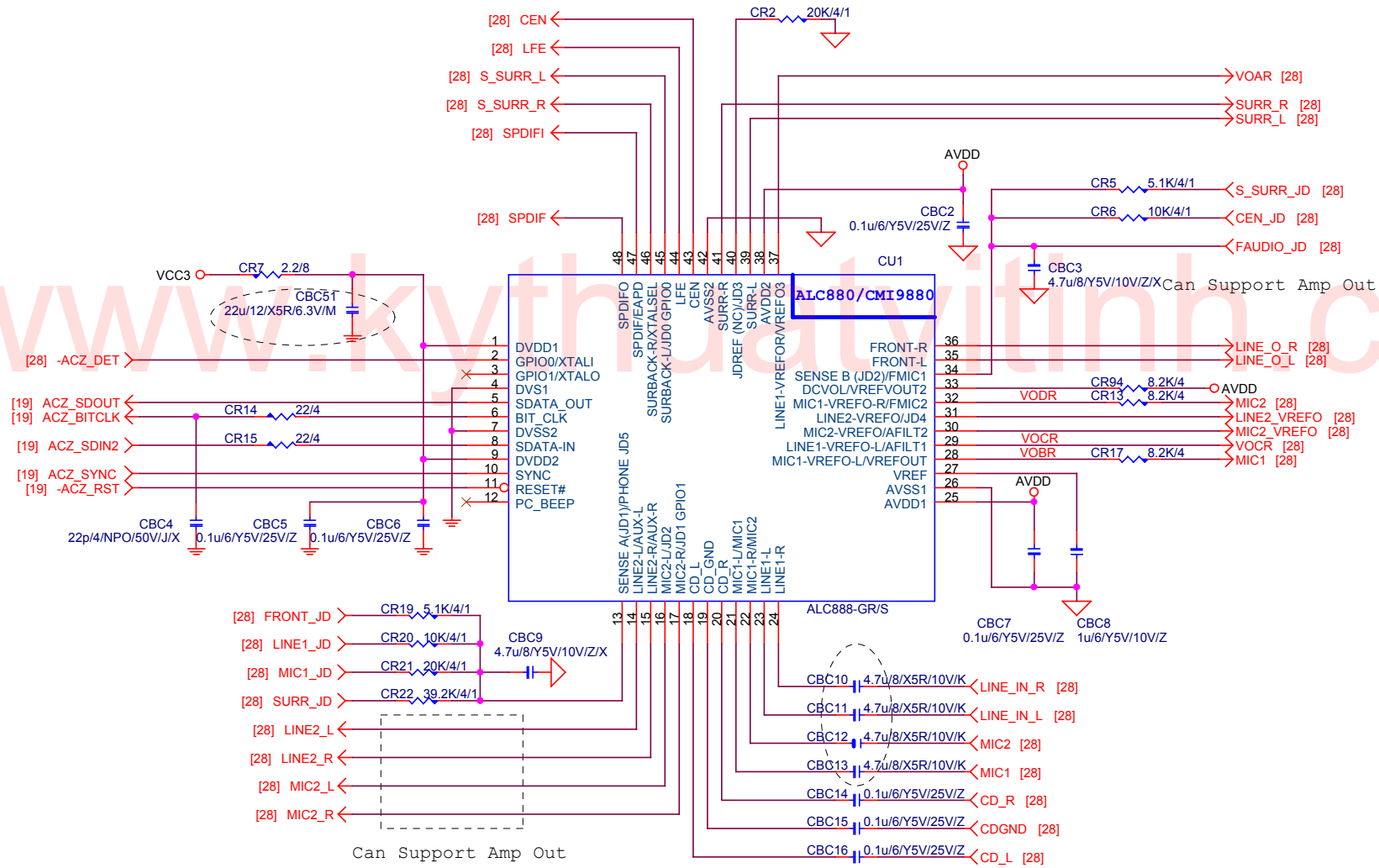


Case Open Circuits

KB/MS



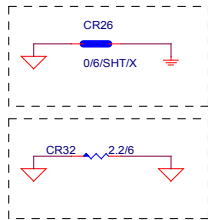
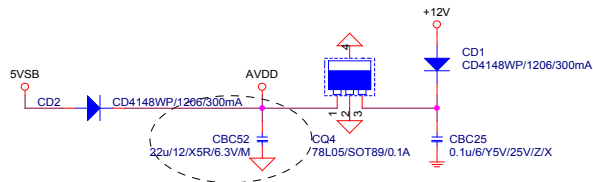
Gigabyte Technology		
BIOS/HW-MONITOR/CI/KB/MS		
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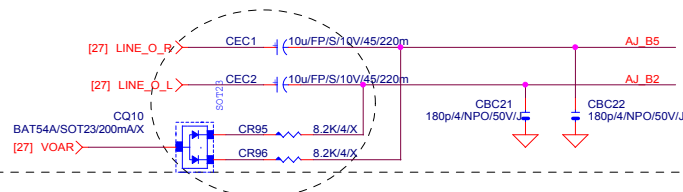
Gigabyte Technology

Title		
AZALIA ALC888		
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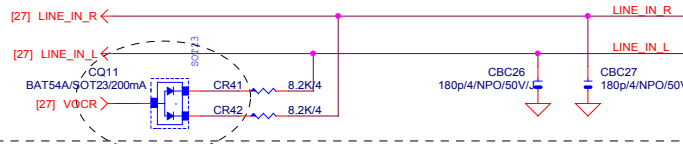
CODEC POWER/EMI PAD



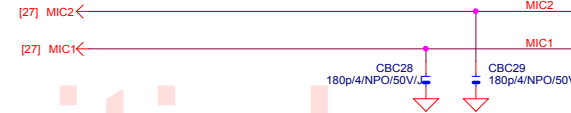
LINE-OUT



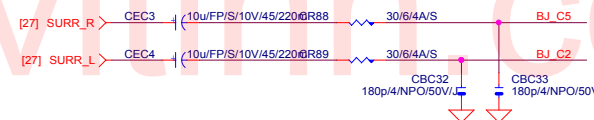
LINE-IN



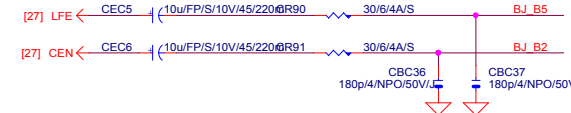
MIC-IN



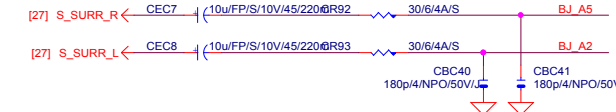
SURROUND



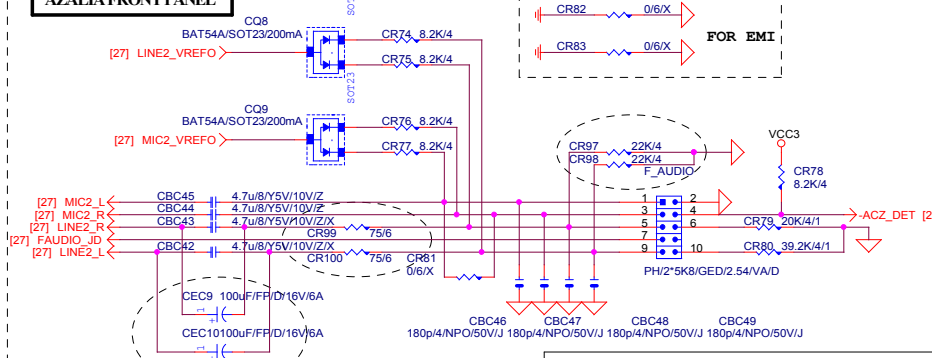
CEN/LFE



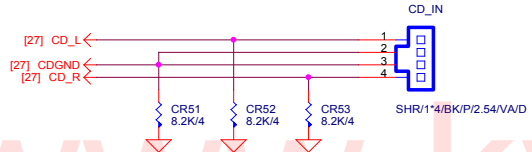
SURRBACK



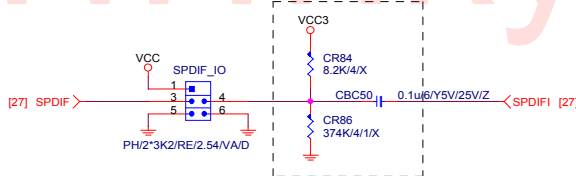
AZALIA FRONT PANEL



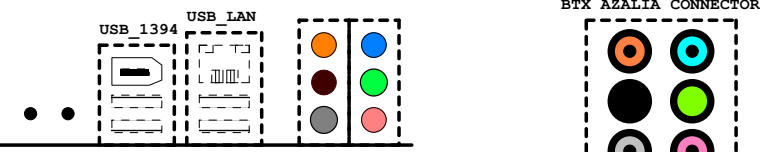
CD IN



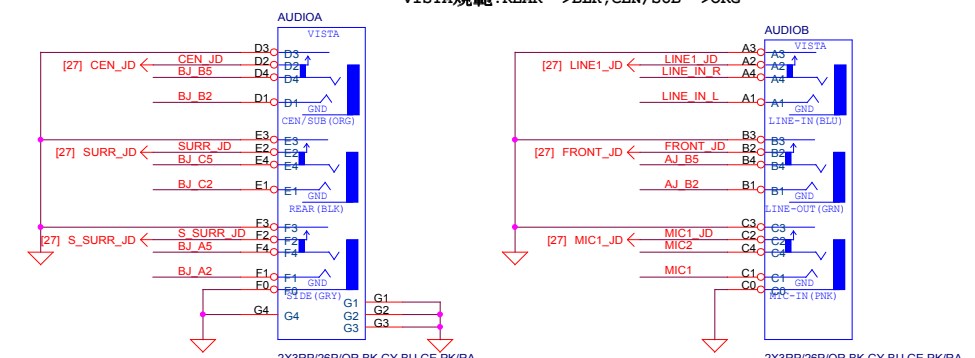
SPDIF



AZALIA JACK

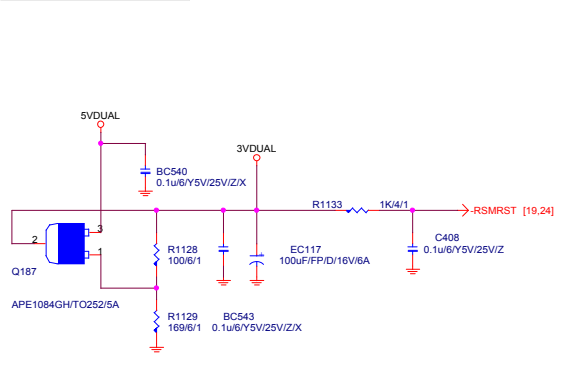


3RP/26P/OR, BK, GY, BU, GE, PK/RA/D/1/B
VISTA規範: REAR-->BLK, CEN/SUB-->ORG

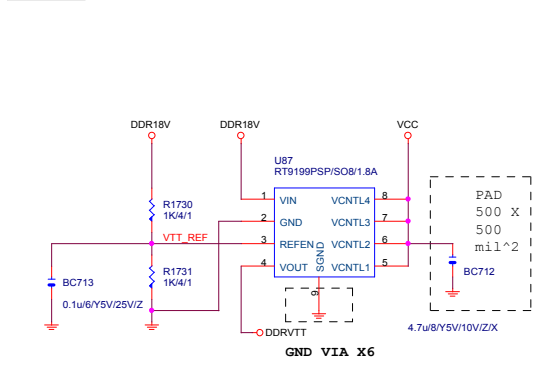


Gigabyte Technology			
AUDIO JACK			
945GME-DS2			
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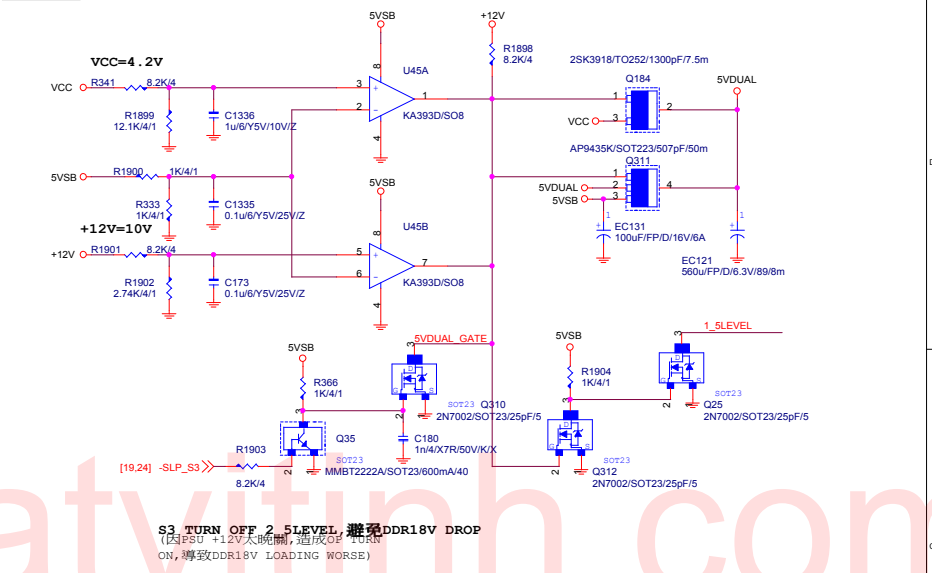
3VDUAL



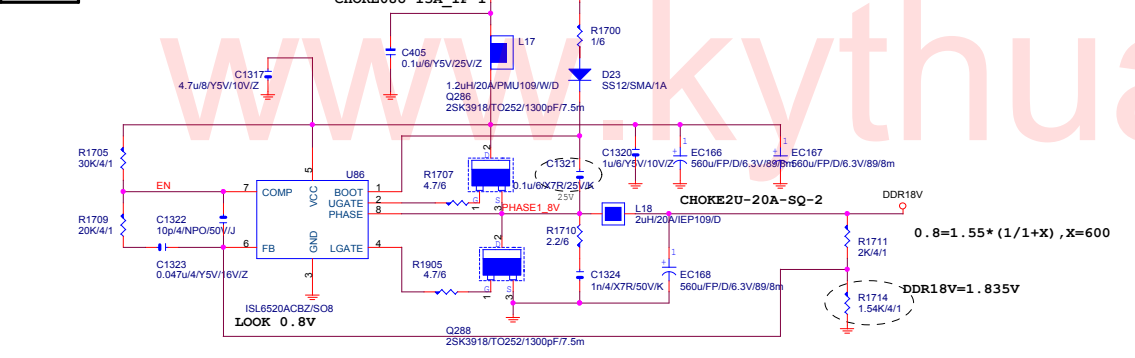
DDRVTT



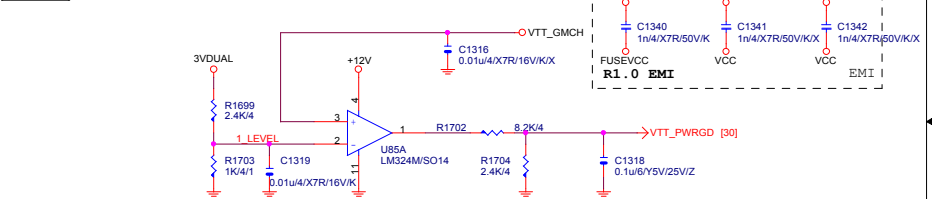
5VDUAL



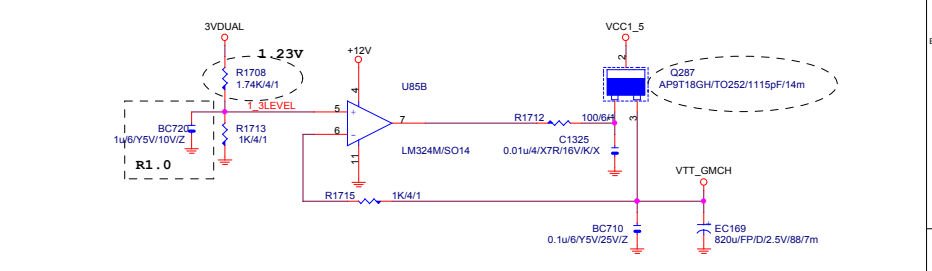
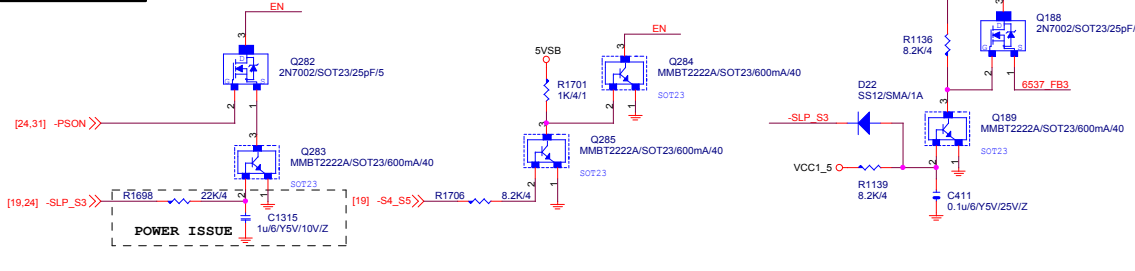
DDR18V



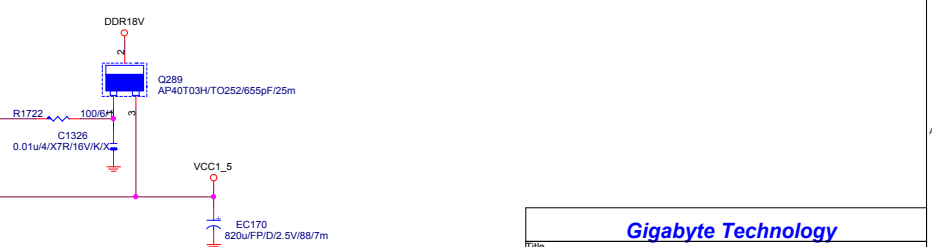
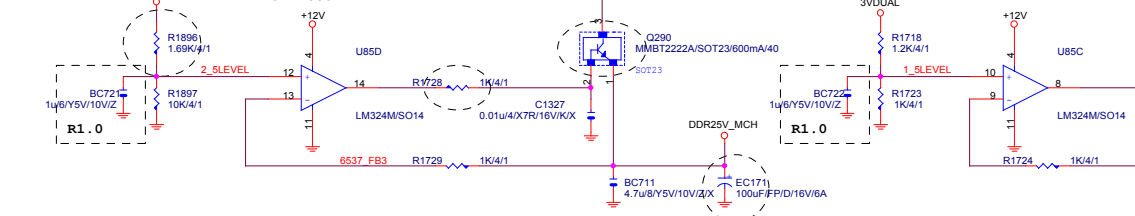
POWER

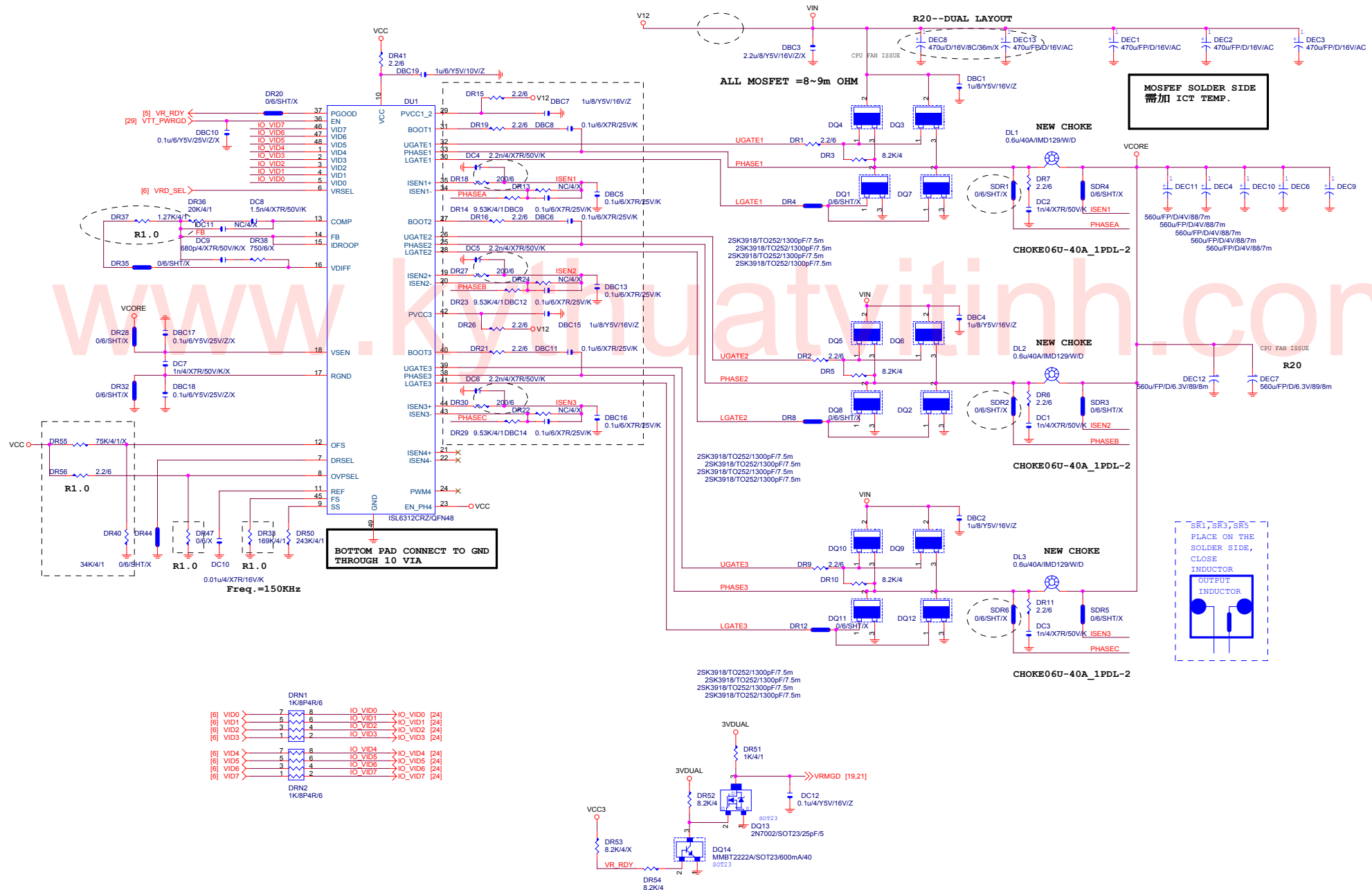


POWER SEQUENCY



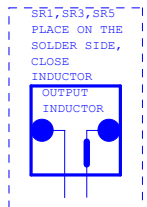
FIX DATA-SW ISSUE



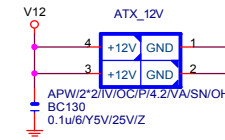
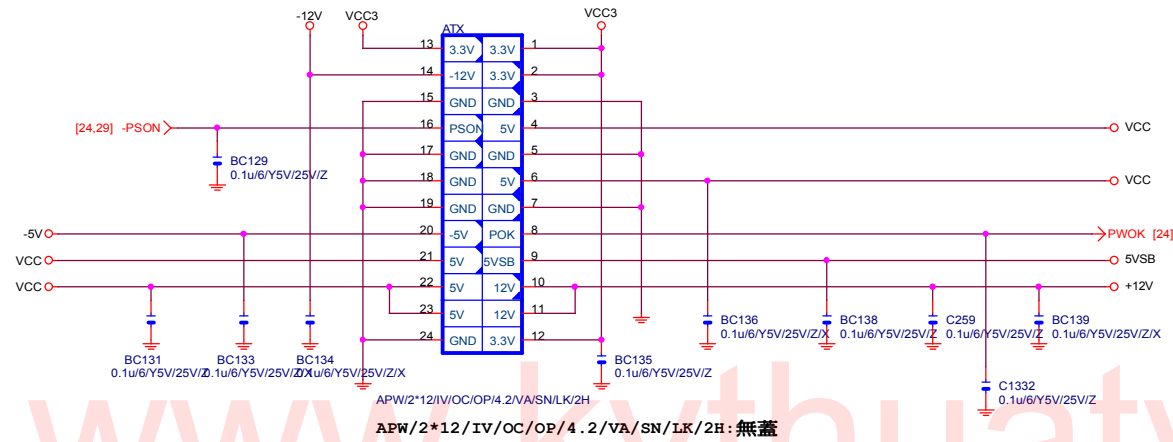


**MOSFET SOLDER SIDE
帶加 ICT TEMP.**

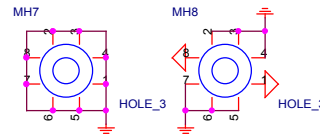
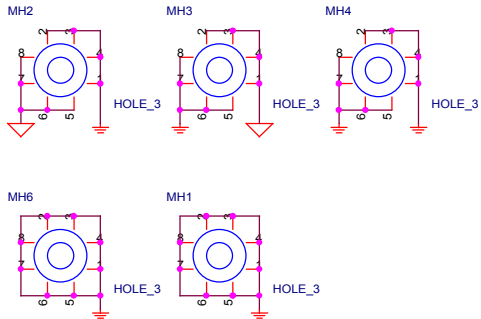
**BOTTOM PAD CONNECT TO GND
THROUGH 10 VIA**



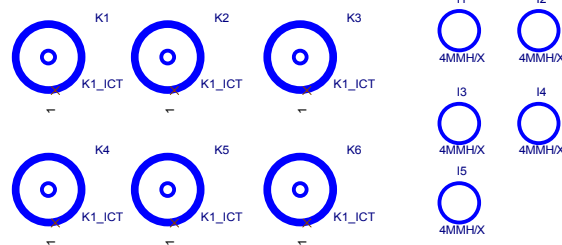
ATXPWR_24-2
ATX POWER CONNECTOR



HOLE_3-RH-2



HOLE_3-RH-5MM

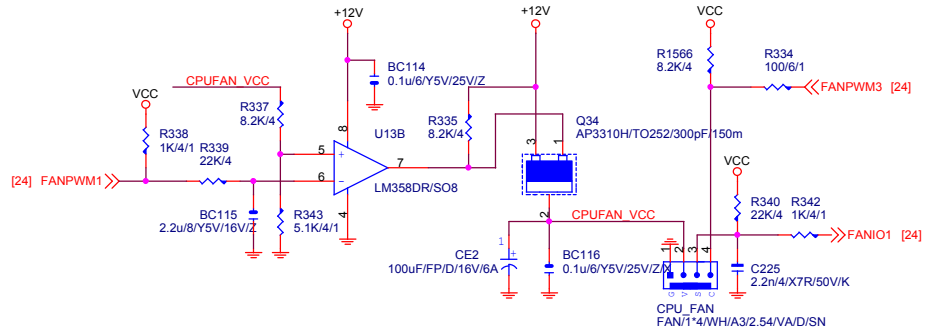


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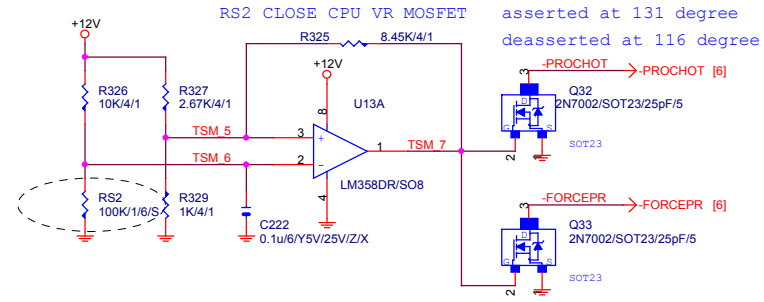
ATX POWER CONNECTOR

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CPU SMART FAN SMART FAN

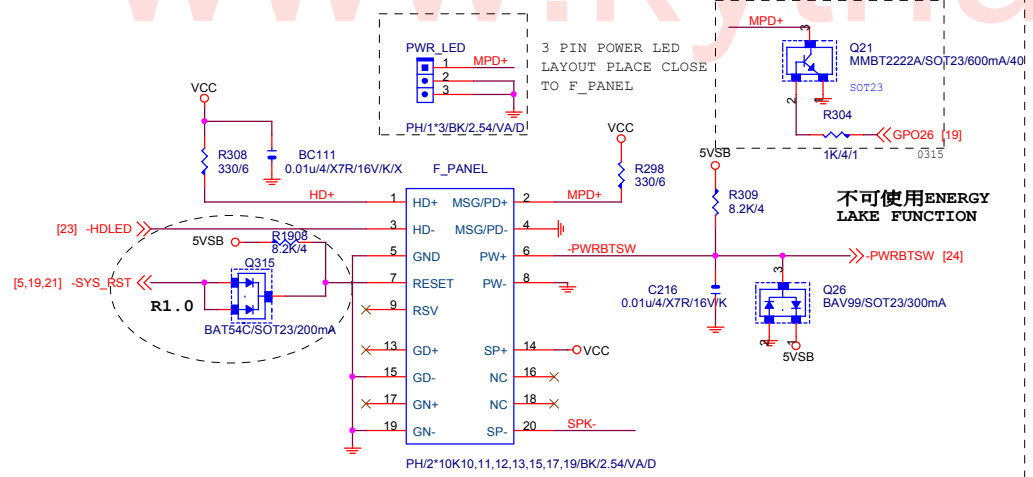


PROCESSOR HOT

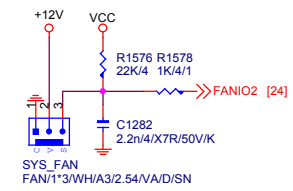


INTEL FRONT PANEL

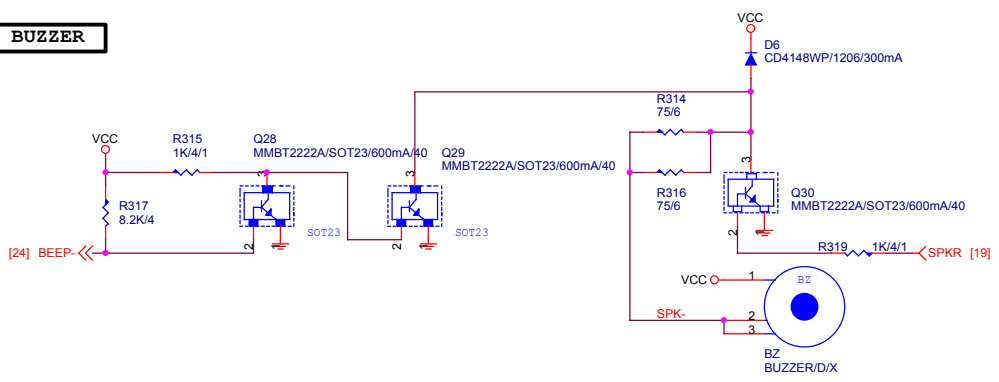
MPD- : (GPIO25--VCCSUS3+HI+HI+DEFINED (C3/C4/S/1/S3/S/4/S5))-->INTEL



SYS_FAN



BUZZER



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FRONT PANEL			
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