Model Name:945G-DS3 *Revision 2.02*

SHEET	TITLE
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	L775_A
05	L775_B,D
06	L775_C
07	L775_E,F,G,H
08	BROADWATER HOST
09	BROADWATER DDR
10	BROADWATER PCI_EXP_16,DMI
11	BROADWATER VGA,MISC
12	BROADWATER GND
13	BROADWATER PWR
14	DDRII CHANNEL A-SHARE 1,2
15	DDRII CHANNEL B-SHARE 1,2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 DMI, PCI, USB
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	ICS9LPRS587 CLOCK.
22	ATX,ATX_12V CONNECT,BIOS
23	PCI EXPRESS*1 SLOT 1,2,3
24	PCI SLOT 1,2,3
25	HWM/ FAN CONTROL
26	IDE/FLOPPY
27	KB_PS2

SHEET	TITLE
28	FRONT PANEL
29	FRONT USB,REAL USB CONNECT
30	PROCESSOR HOT
31	ALC883
32	REAR AUDIO JACK
33	FRONT AUDIO CONNECTOR
34	MARVELL 88E8056
35	ITE 8712/8718 GBKX
36	COM_LPT
37	VCORE PWM
38	DISCRETE POWER
39	GPIO DEFINE
40	GPIO DEFINE
41	
42	
43	
44	

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Title	Cover Sheet	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
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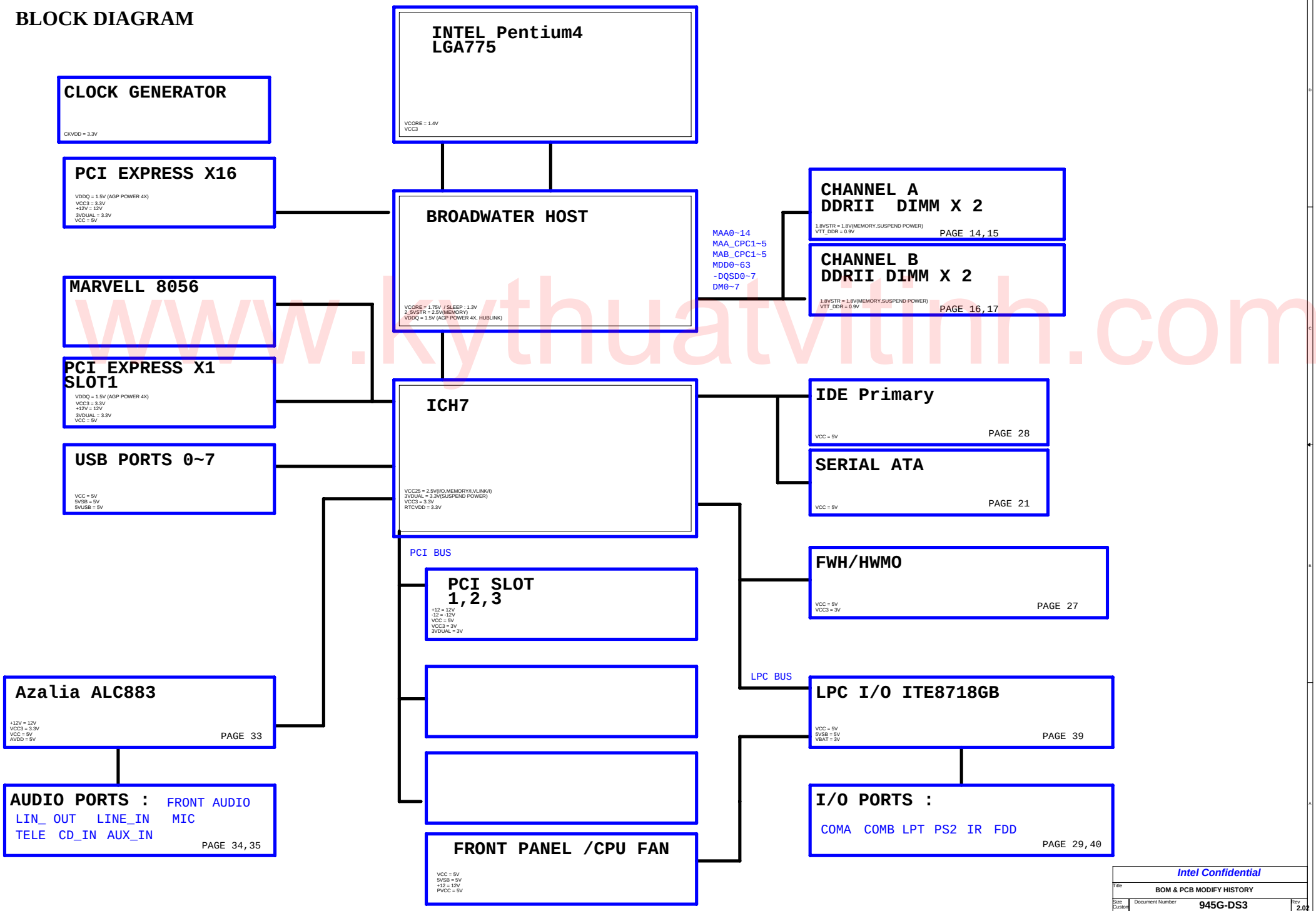
BLOCK DIAGRAM



Title	BOM & PCB MODIFY HISTORY	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 2 of 40	

Model Name: 945G-DS3
Version:2.02

Component value change history

2006

Data	Change Item	Reason
1.0A	EVT Release	
1.0B	1. PCB Rev1.0 --> REv1.01	
	2. DR68 200/6 --> 300/6	
	3. PROCHOT R379 2K/6/1 --> 1.78K/6/1	
	4.包材修改	
	5. ADD COMP8 R101=30/6/1	
	6. PROCHOT & STPCLK D13=BAT54A REMOVE	
	7. CONROE 266MHZ --> 133MHZ R189=8.2K/4 , R169=22K/4 , ADD BC51 0.1U/4	
	8. DDRVTT ADD BC79,BC84=0.1U/6/Y5V/25V/Z	
	9. BC465,BC467,BC464,BC461 加替料 10CM3-041005-51R/54R/56R/57R/2AR	
	10. REMOVE PCI_BT1 , PCI_BT2	
1.0C	1. LPC DUAL BIOS SELECT REMOVE R458 , ADD R459=1K/4	
	2. RTL8111B ADD CAP FOR LBC45,35,55=0.1U/6 , LEC4,LEC6=100u/D	
2.0A	1. PWM 6 PHASE	
	2. VISTA SUPPORT	
2.0B	1. R224 1K/4 REMOVE	
	2. R2016 1K/4 --> 100/4	
	3. F2,F3 SMD FUSE 1.1A --> 1.6A	
	4. DL4,DL1,DL3:11LC5-40600C-W1R/W2R/W3R/W4R	
	5. L5,L13:11LC5-20200B-E1R/E2R/E3R/E4R	
	6. L12,L6:11LC5-20120B-W1R/W2R/W3R/W4R	
	7. PCB Rev2.0 --> Rev2.01	
	8. 包材修改	
	9. 大陸智恩PCB REMOVE	
2.0B-ECN	1. REMOVE CPU IMPSEL R147=62/4	
2.0C	1.PCB REV2.01 -> REV2.02 (智恩,育富,鼎富,精成,全成信)	
	2. I/O SHIELD CHAMDE ADD 貼銘板	

Circuit or PCB layout change for next version

DATE	Change Item	Reason
1.0	1. GA-945PL-S3 Rev1.01 --> GA-945P-S3 Rev1.0	
1.01	文字面 Dual Channel DDRII移除	
	LAN LED LD1 REMOVE	
2.0	1. S/B HEATSINK轉90,度	
	2. CEC11,CBC32 & CEC12,CBC37不要CO-LAYOUT	
2.01	1. 2uH Footprint : CHOKE2U-20A-SQ-1	
	2. POWER NET VCC1_5PCIEX CHANGE TO VCC1_5	
	3. CPU PIN.E7 ADD CPU_TP18	
	4. EC22 & CPU_FAN 靠太近	
	5. 文字面要出 GA-945P-DS3 , GA-945G-DS3, GA-945P-S2,GA-945G-S2	
2.02	1. ADD U23 FOR HSYNC & VSYNC LEVEL SHIFT	

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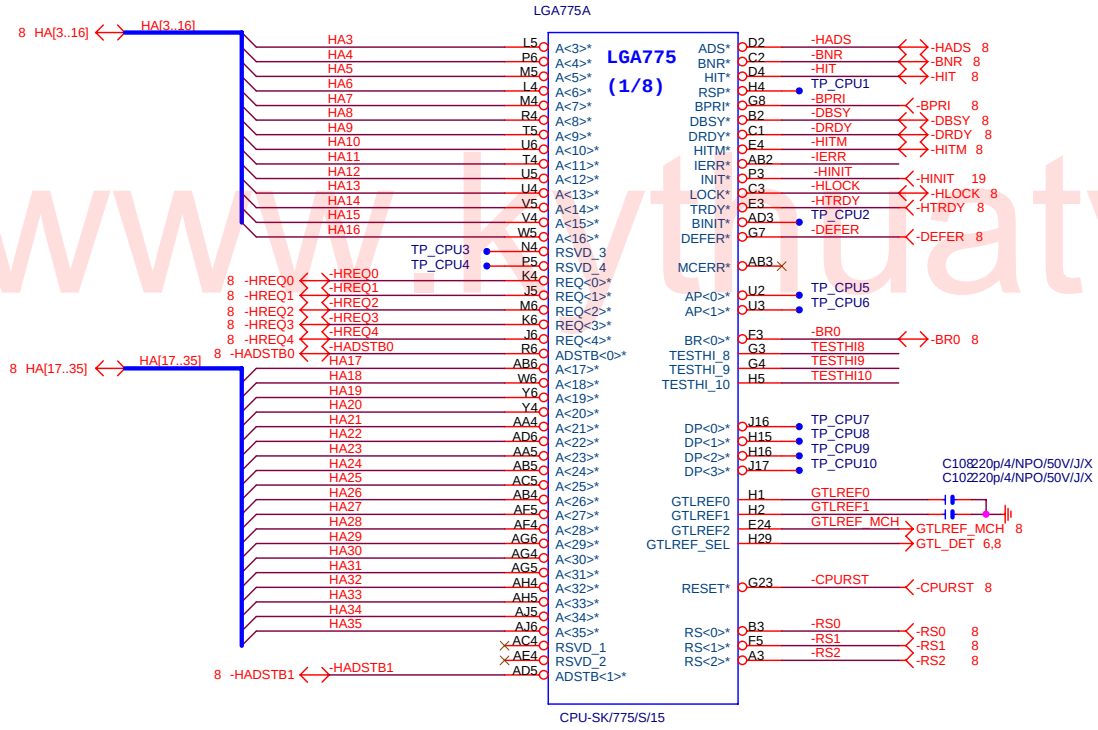
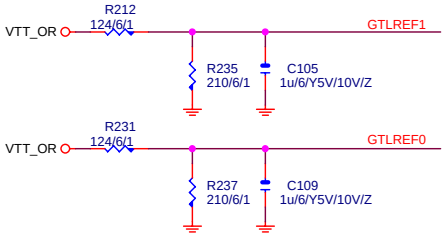
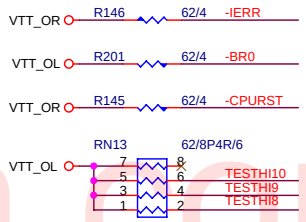
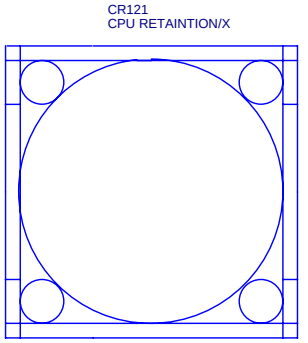
Title	BOM & PCB MODIFY HISTORY		
Size: Custom	Document Number: 945P-DS3		Rev: 2.02
Date: Friday, November 17, 2006	Sheet 3 of 40		

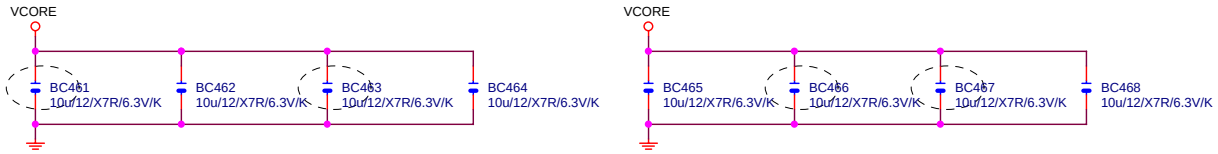
HA/REQ:5/13
ADSTB:5/17



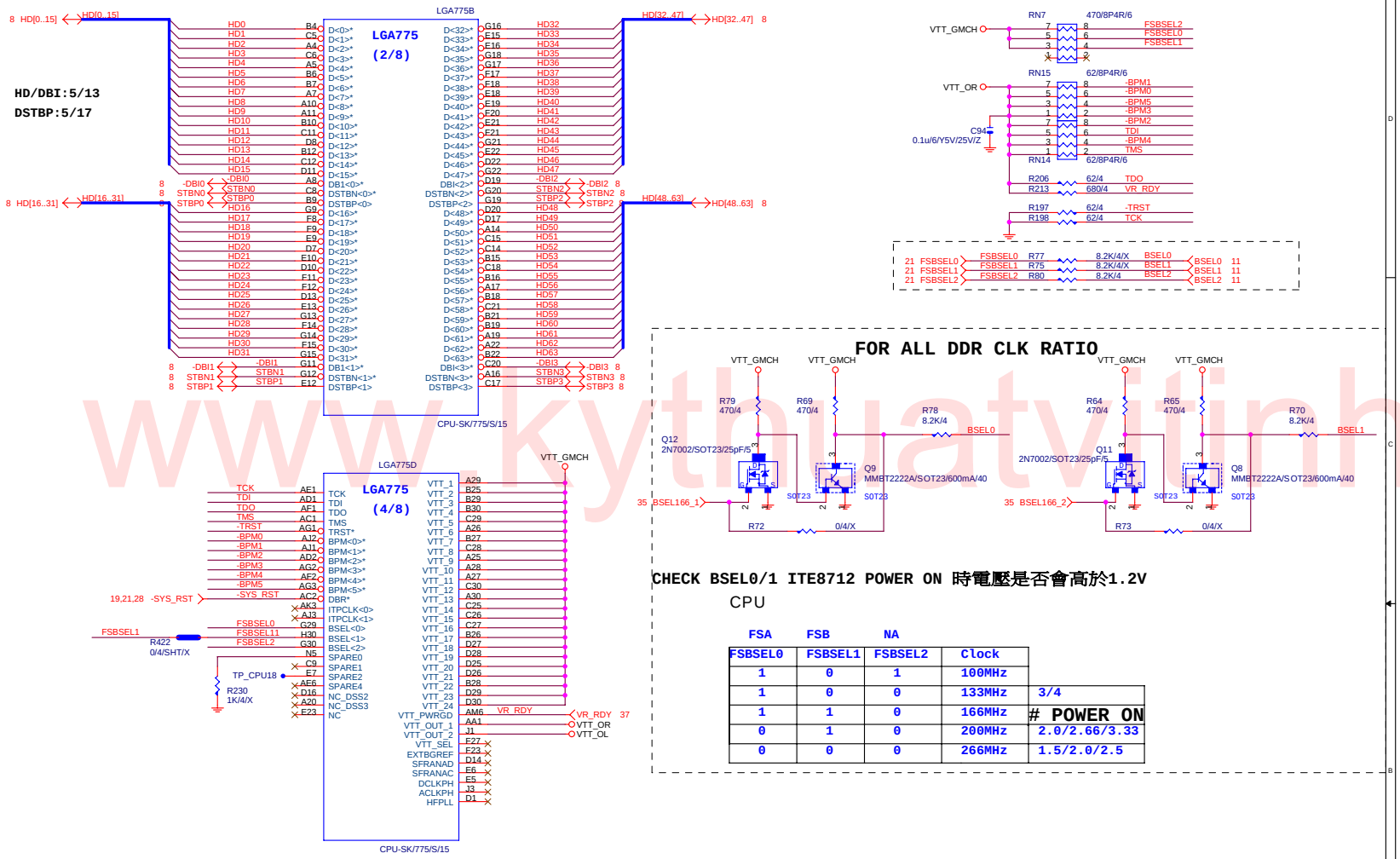




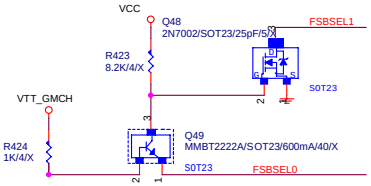




Gigabyte Technology		
Title: P4_LGA775-A		
Size: B	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 4 of 40	



FSA	FSB	NA	
FSBSEL0	FSBSEL1	FSBSEL2	Clock
1	0	1	100MHz
1	0	0	133MHz 3/4
1	1	0	166MHz # POWER ON
0	1	0	200MHz 2.0/2.66/3.33
0	0	0	266MHz 1.5/2.0/2.5



Gigabyte Technology			
Title	P4_LGA775-B,D		
Size: Custom	Document Number: 945G-DS3		Rev: 2.02
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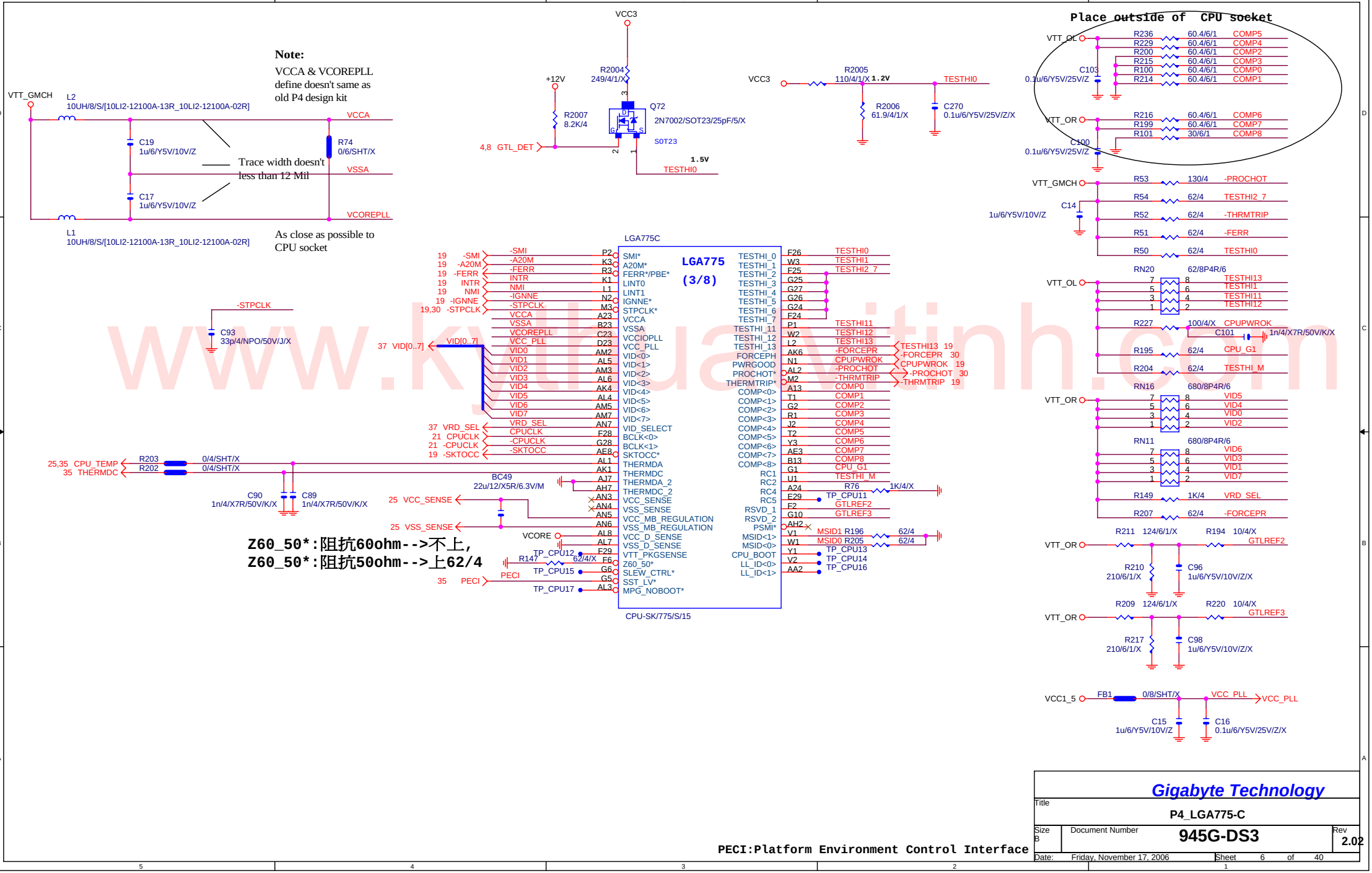
Note:
VCCA & VCOREPLL define doesn't same as old P4 design kit
Trace width doesn't less than 12 Mil
As close as possible to CPU socket
Place outside of CPU socket
LGA775C
LGA775 (3/8)
CPU-SK/775/S/15
Z60_50*:阻抗60ohm-->不上,
Z60_50*:阻抗50ohm-->上62/4
PECI:Platform Environment Control Interface
Gigabyte Technology
P4_LGA775-C
945G-DS3
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Friday, November 17, 2006
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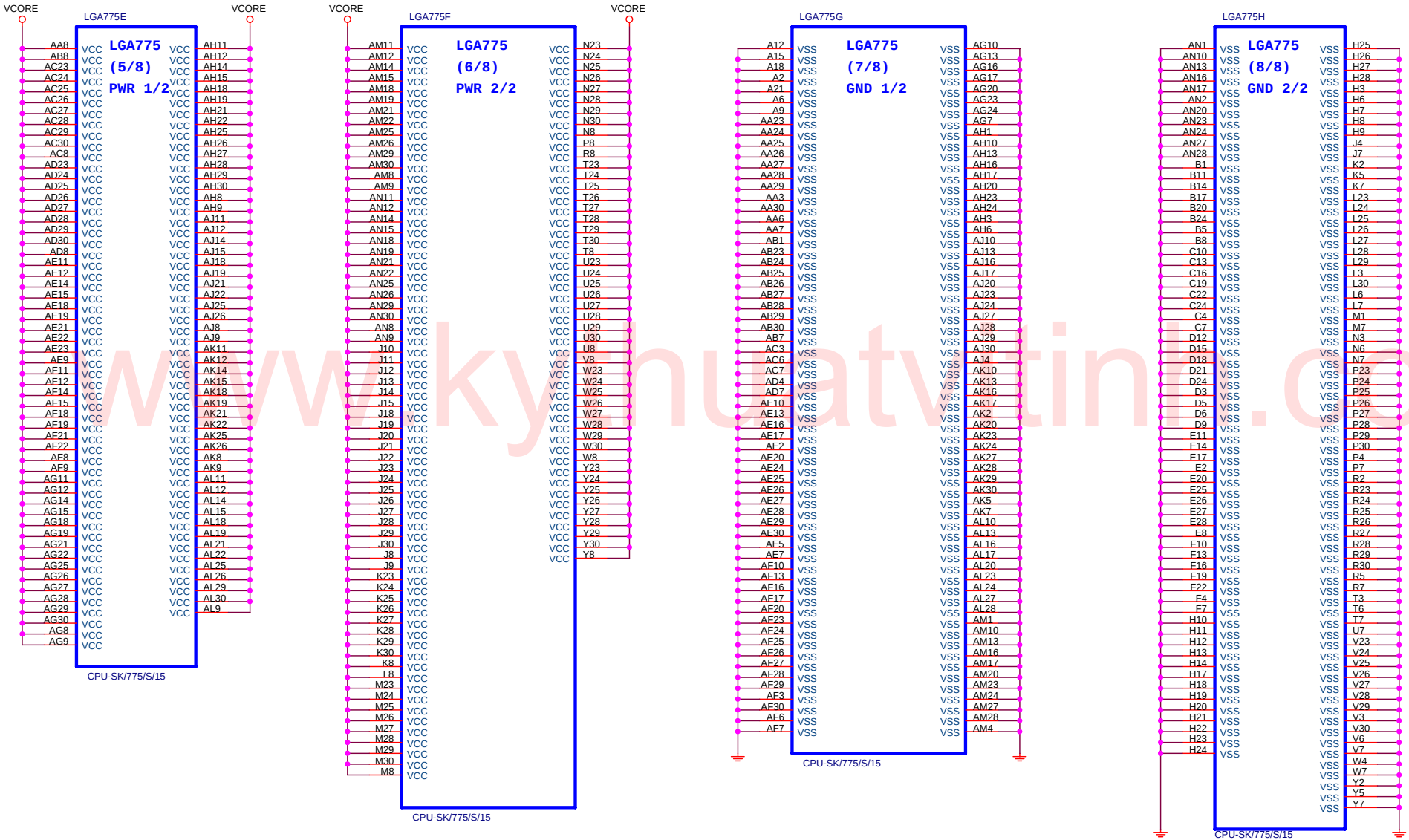
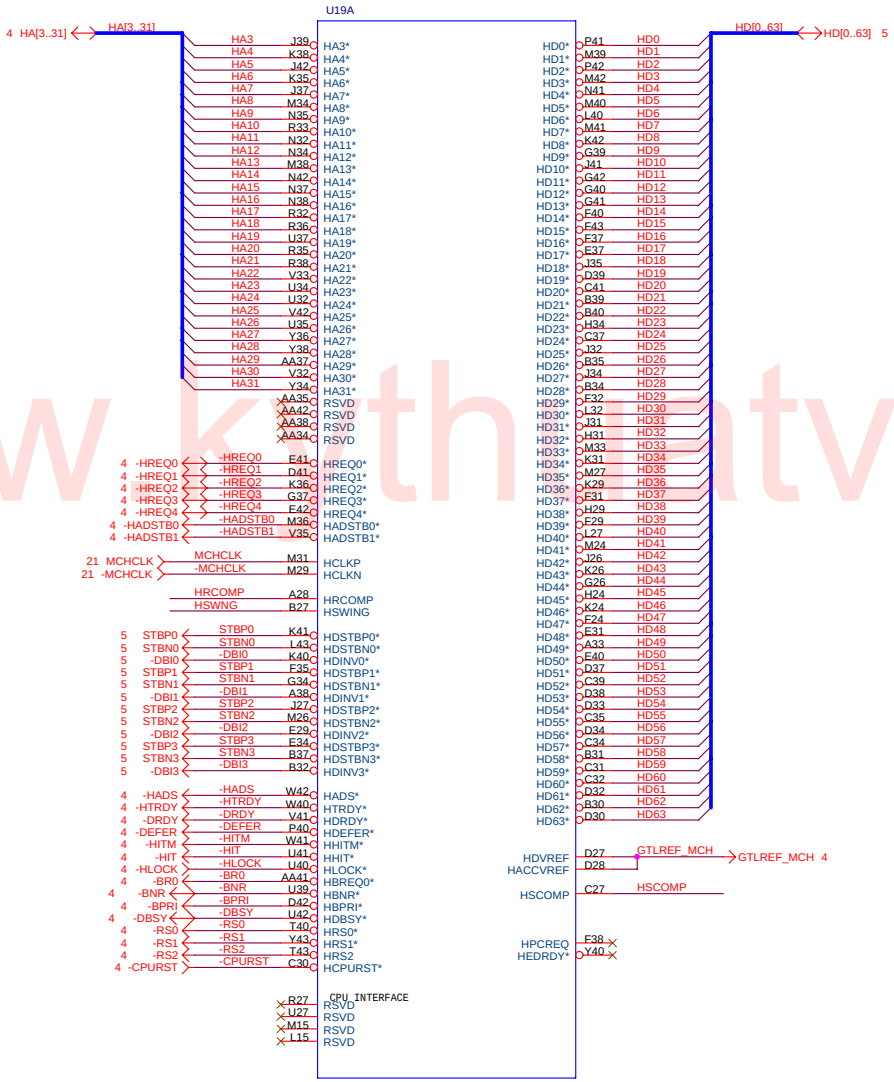


Gigabyte Technology		
Title: P4_LGA775-E,F,G,H		
Size: B	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 7 of 40	



GIGABYTE THCHNOLOGIES , I

Title	GMCH-HOST	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
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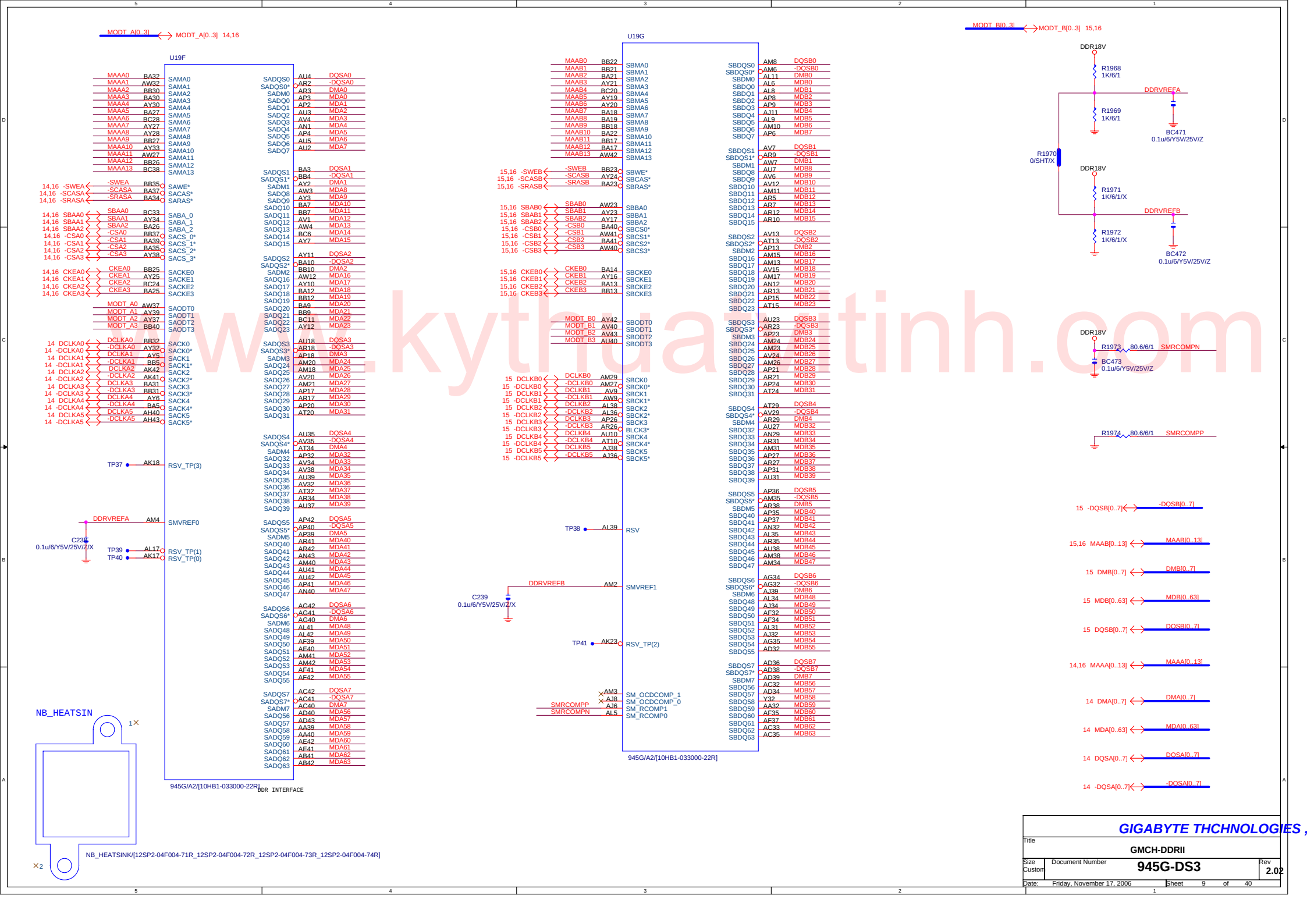

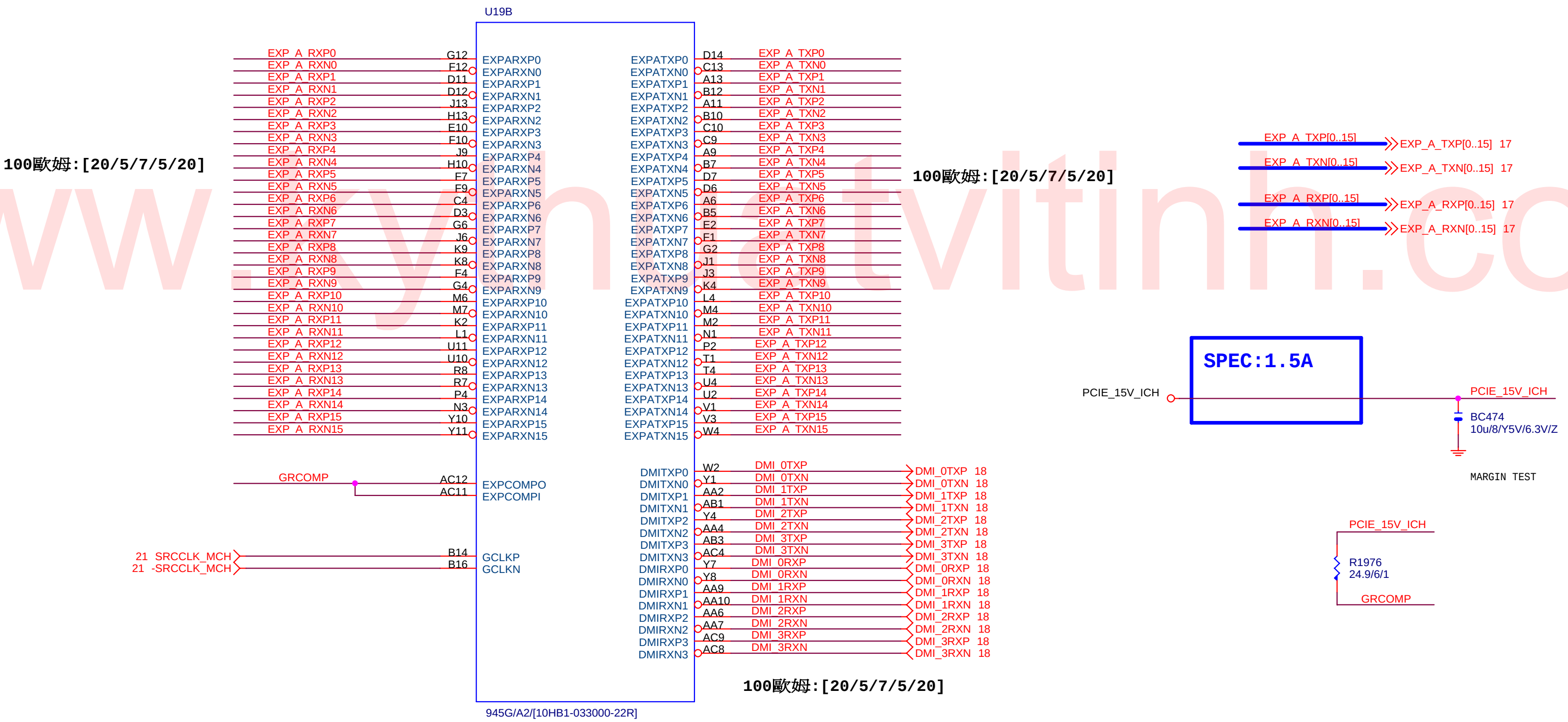

GIGABYTE THCHNOLOGIES , IN

Title: GMCH-PCI E & DMI

Size	Document Number	Rev
Custom	945G-DS3	2.02

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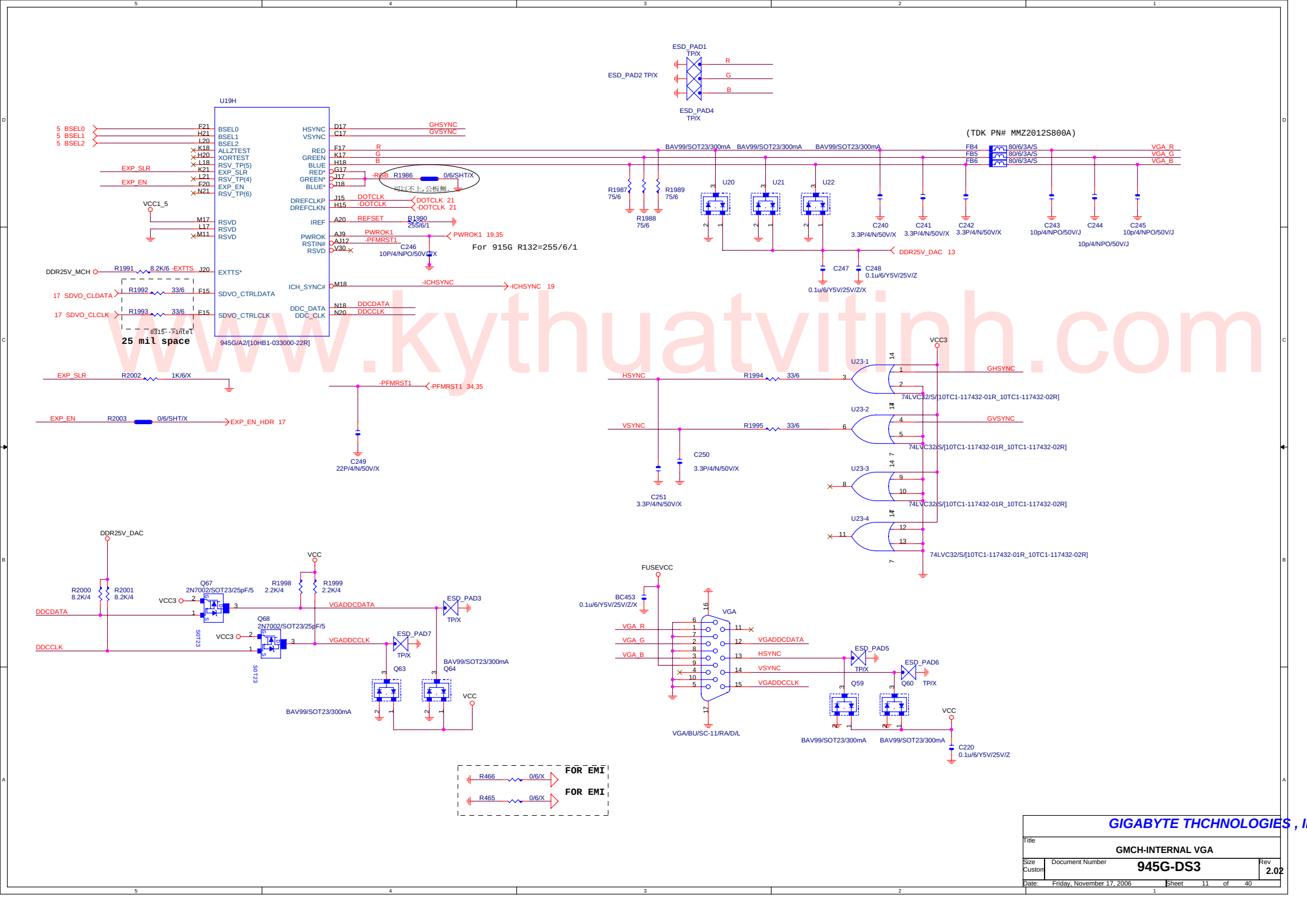
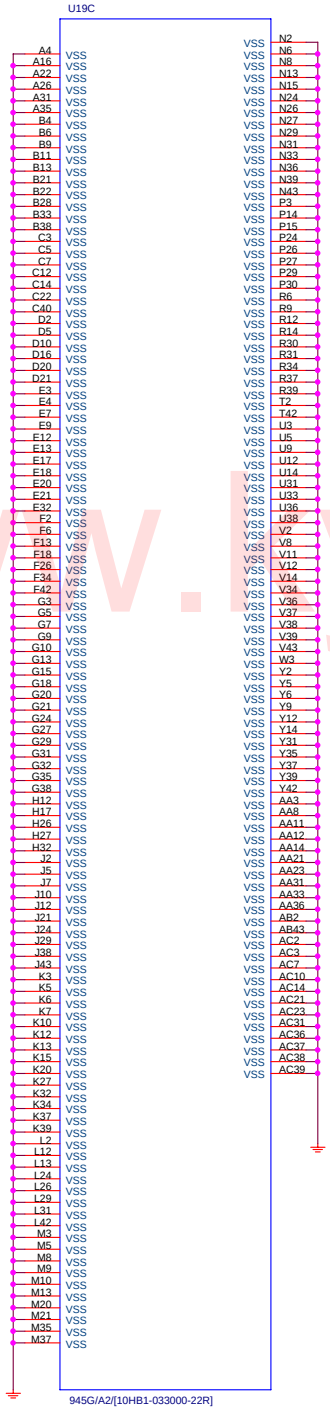
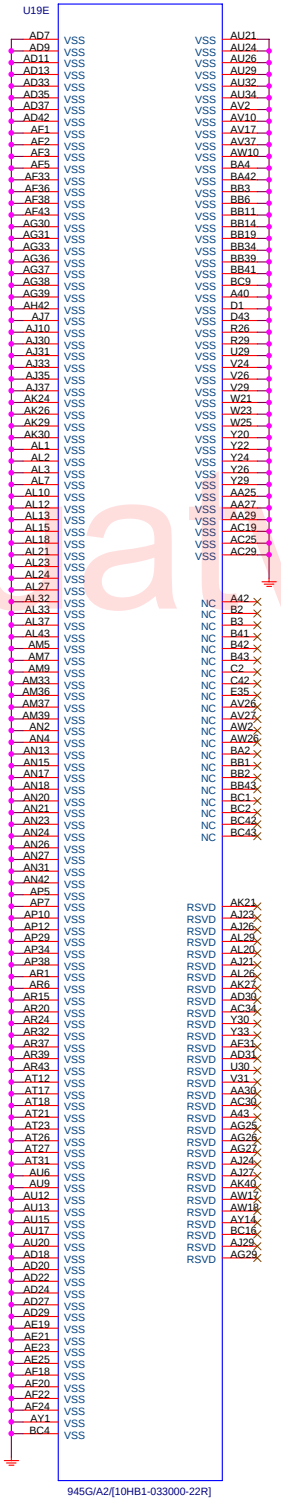


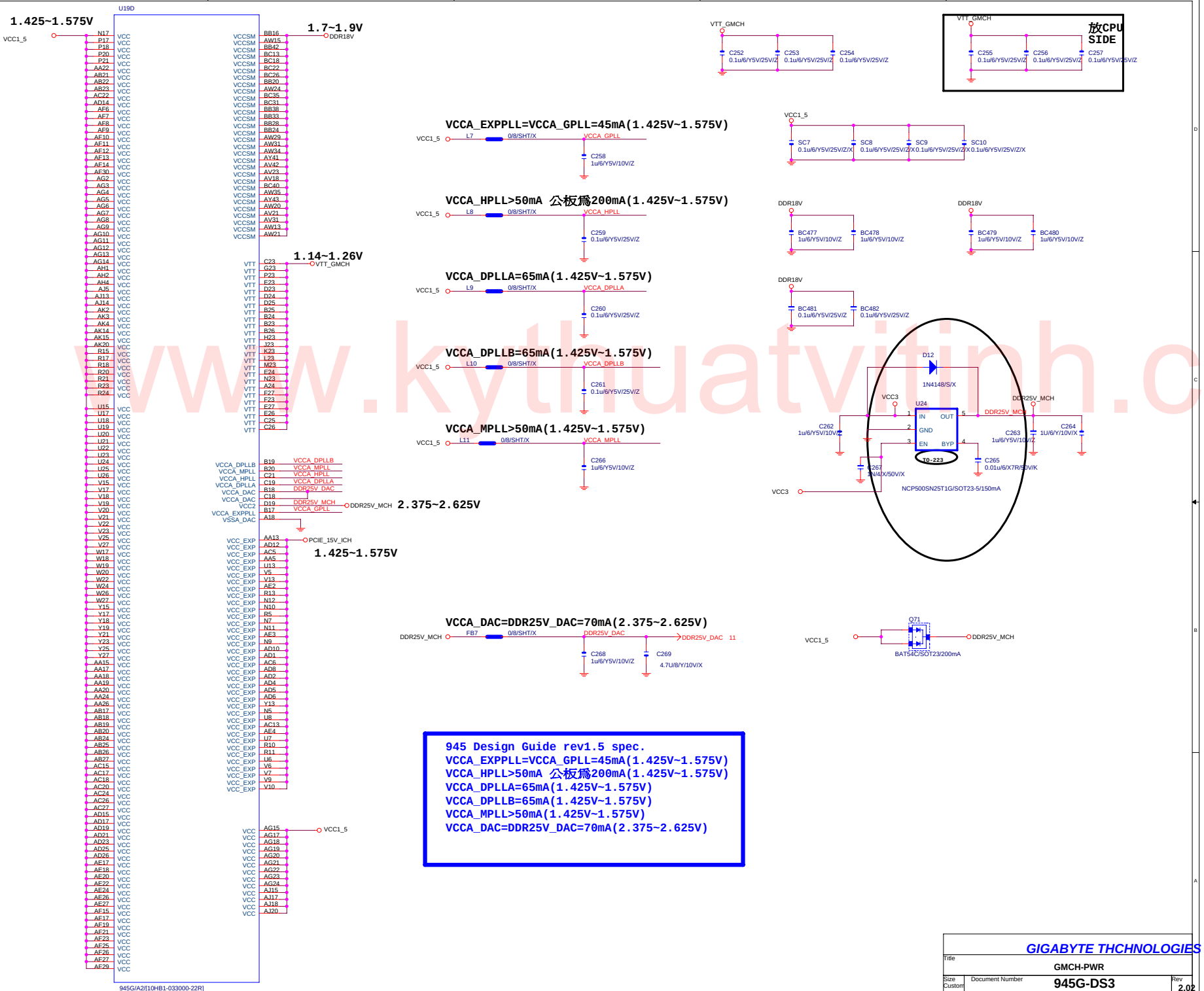
GIGABYTE THCHNOLOGIES , I

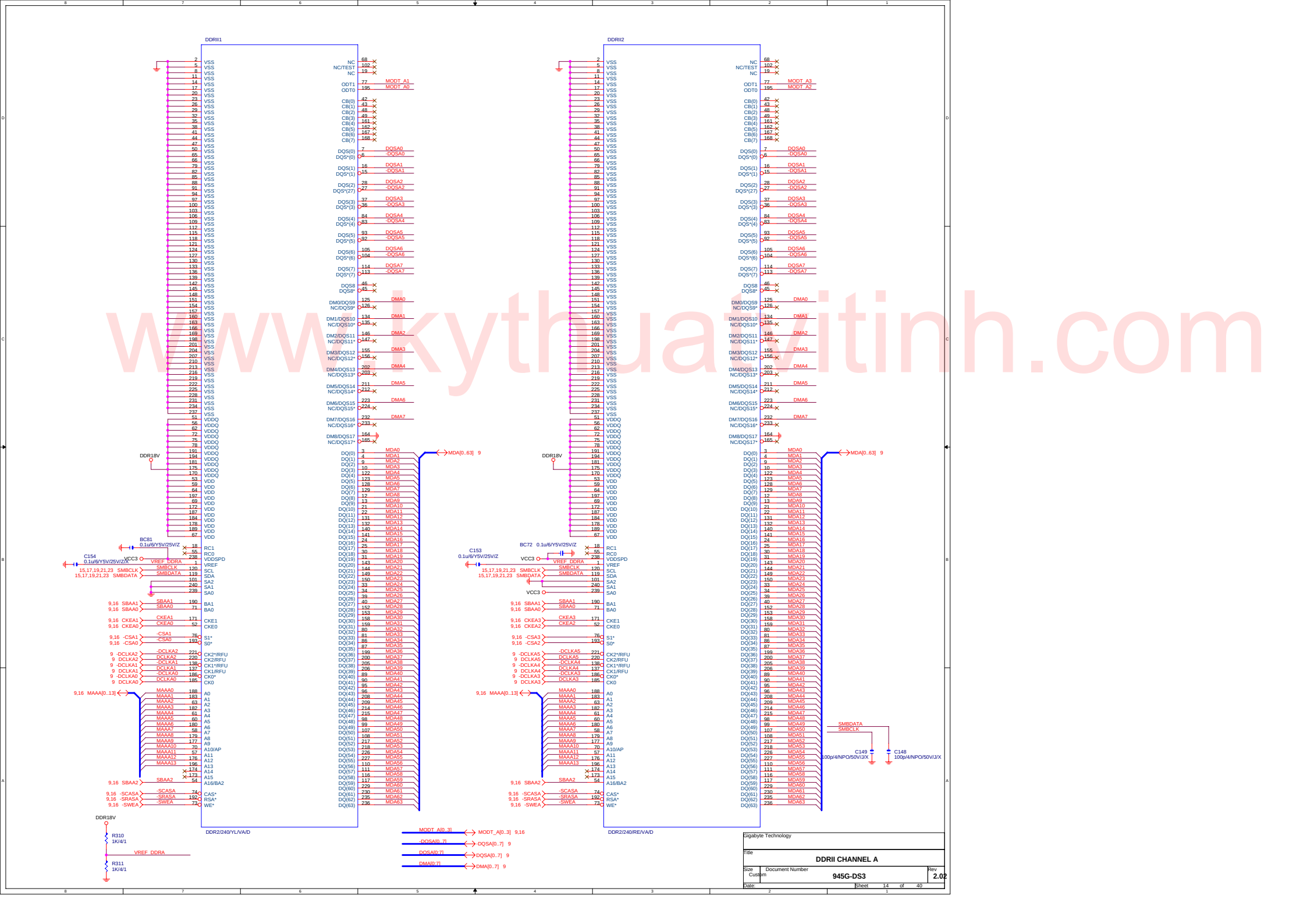
Title	GMCH-INTERNAL VGA	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 11 of 40	

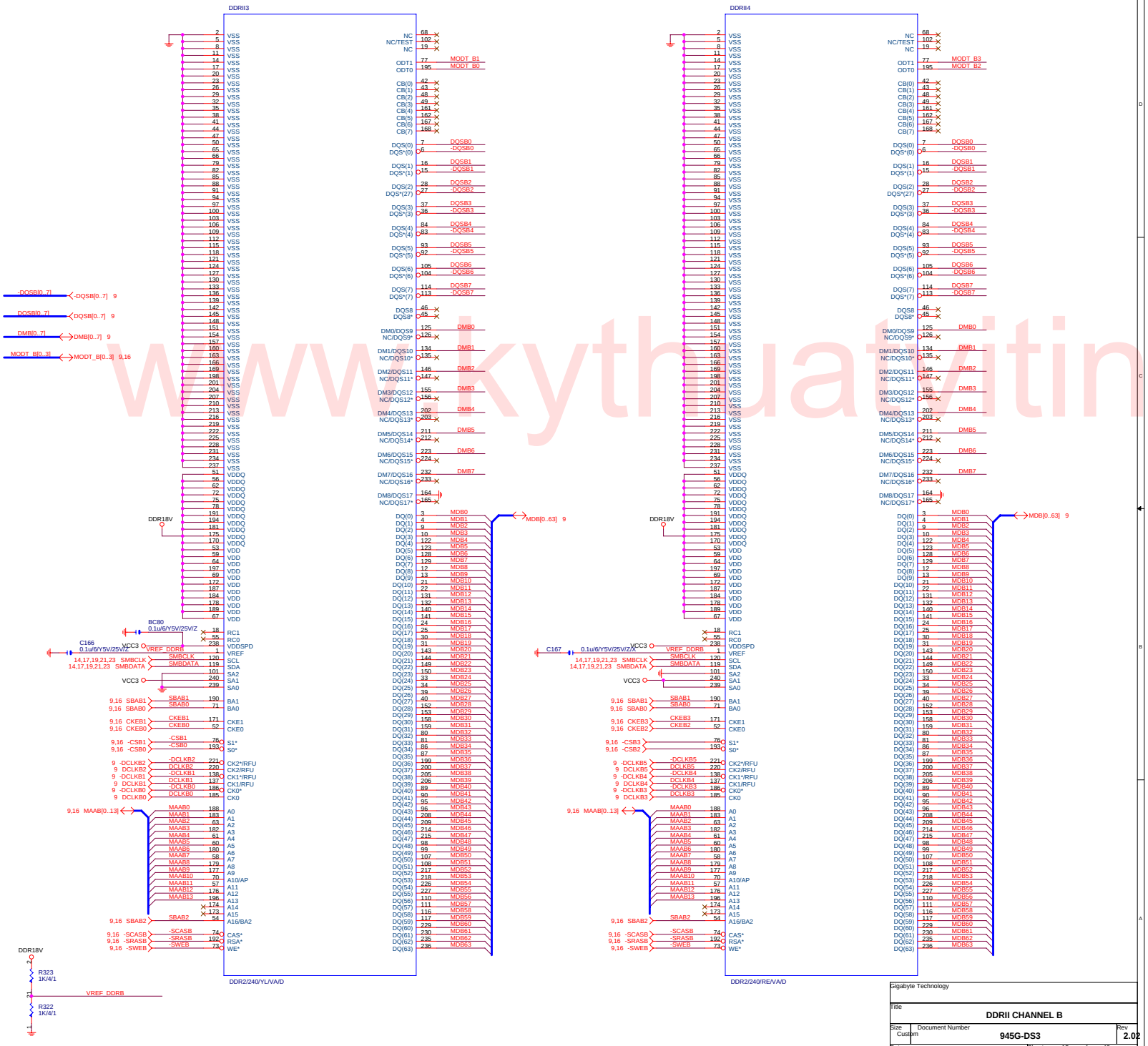




GIGABYTE THCHNOLOGIES , INC.			
Title: GMCH-GND			
Size: Custom	Document Number: 945G-DS3		Rev: 2.02
Date: Friday, November 17, 2006	Sheet 12 of 40		







Gigabyte Technology		
Title	DDRII CHANNEL B	
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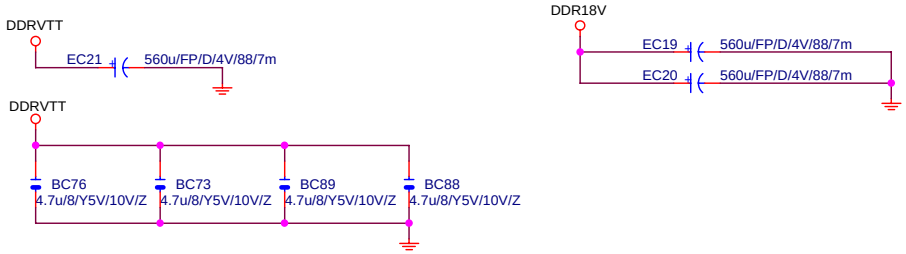
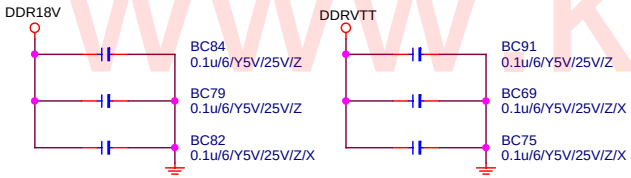
DDR TERMINATION CHANNEL A

DDRVTT Decouple



DDR18V Decouple

DDRVTT Decouple



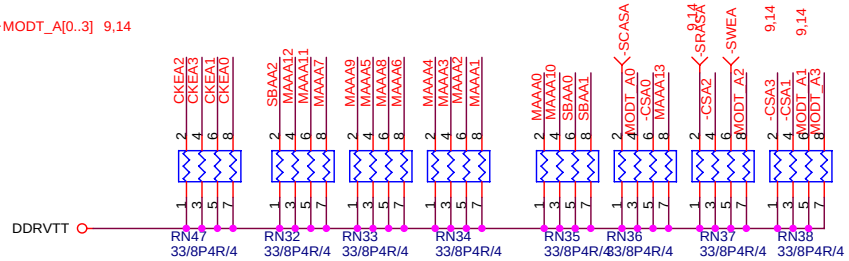
SBAA[0..2] SBAA[0..2] 9,14

-CSA[0..3] -CSA[0..3] 9,14

CKEA[0..3] CKEA[0..3] 9,14

MAAA[0..13] MAAA[0..13] 9,14

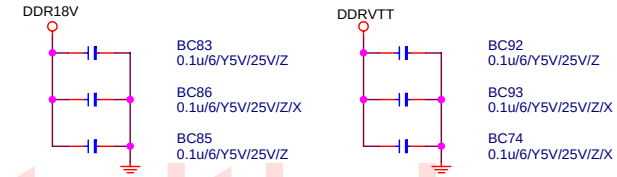
MODT_A[0..3] MODT_A[0..3] 9,14



DDR TERMINATION CHANNEL B

DDR18V Decouple

DDRVTT Decouple



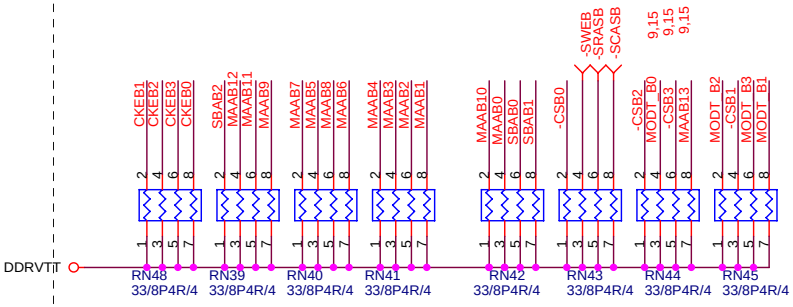
MODT_B[0..3] MODT_B[0..3] 9,15

SBAB[0..2] SBAB[0..2] 9,15

-CSB[0..3] -CSB[0..3] 9,15

CKEB[0..3] CKEB[0..3] 9,15

MAAB[0..13] MAAB[0..13] 9,15



Gigabyte Technology

Title: DDRII TERMINATOR

Size: Custom | Document Number: 945G-DS3 | Rev: 2.02

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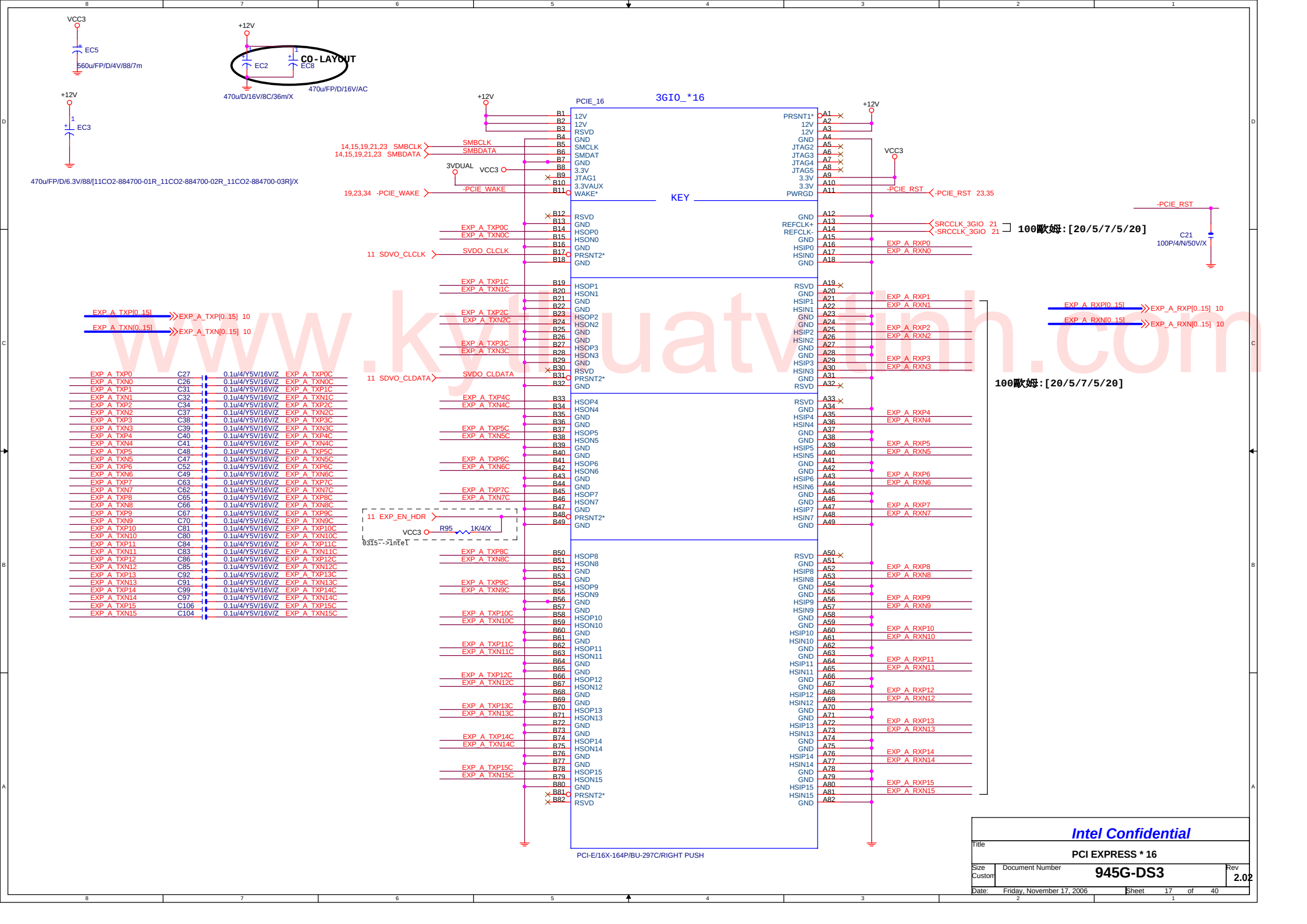

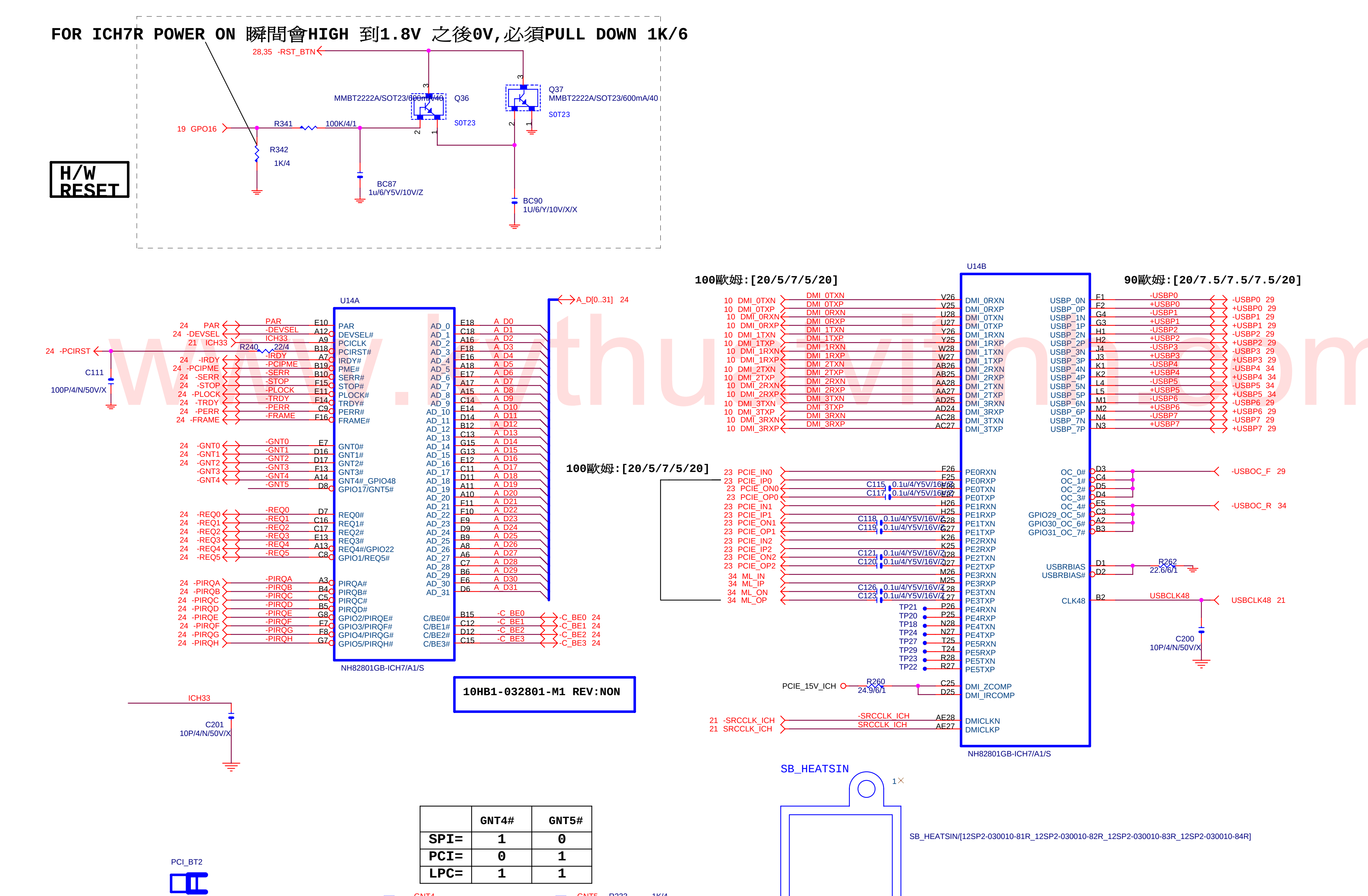

FOR ICH7R POWER ON 瞬間會HIGH 到1.8V 之後0V,必須PULL DOWN 1K/6
H/W RESET
28,35 -RST_BTN
19 GPO16
R341 100K/4/1
R342 1K/4
BC87 1u/6/Y5V/10V/Z
BC90 1U/6/Y/10V/X/X
MMBT2222A/SOT23/600mA/40 Q36 SOT23
Q37 MMBT2222A/SOT23/600mA/40 SOT23
U14A
NH82801GB-ICH7/A1/S
24 -PCIRST
C111 100P/4/N/50V/X
R240 22/4
A_D[0..31] 24
10HB1-032801-M1 REV:NON
ICH33
C201 10P/4/N/50V/X
100歐姆:[20/5/7/5/20]
90歐姆:[20/7.5/7.5/7.5/20]
100歐姆:[20/5/7/5/20]
U14B
NH82801GB-ICH7/A1/S
-USBOC_F 29
-USBOC_R 34
R262 22.6/6/1
USBCLK48 21
C200 10P/4/N/50V/X
PCIE_15V_ICH
R260 24.9/6/1
21 -SRCCLK_ICH
21 SRCCLK_ICH
SB_HEATSIN
SB_HEATSIN/[12SP2-030010-81R_12SP2-030010-82R_12SP2-030010-83R_12SP2-030010-84R]
PCI_BT2
JP/1*2/BU/OH/O::[1-2]CLOSE/X
PCI_BT1 -GNT4 R219 1K/4/X
PH/1*2/BK/2.54/VA/D/X
PCI_BT2 -GNT5 R233 1K/4 R226 1K/4/X
PH/1*2/BK/2.54/VA/D/X

	GNT4#	GNT5#
SPI=	1	0
PCI=	0	1
LPC=	1	1

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Title: ICH6-PCI, DMI, LAN, USB

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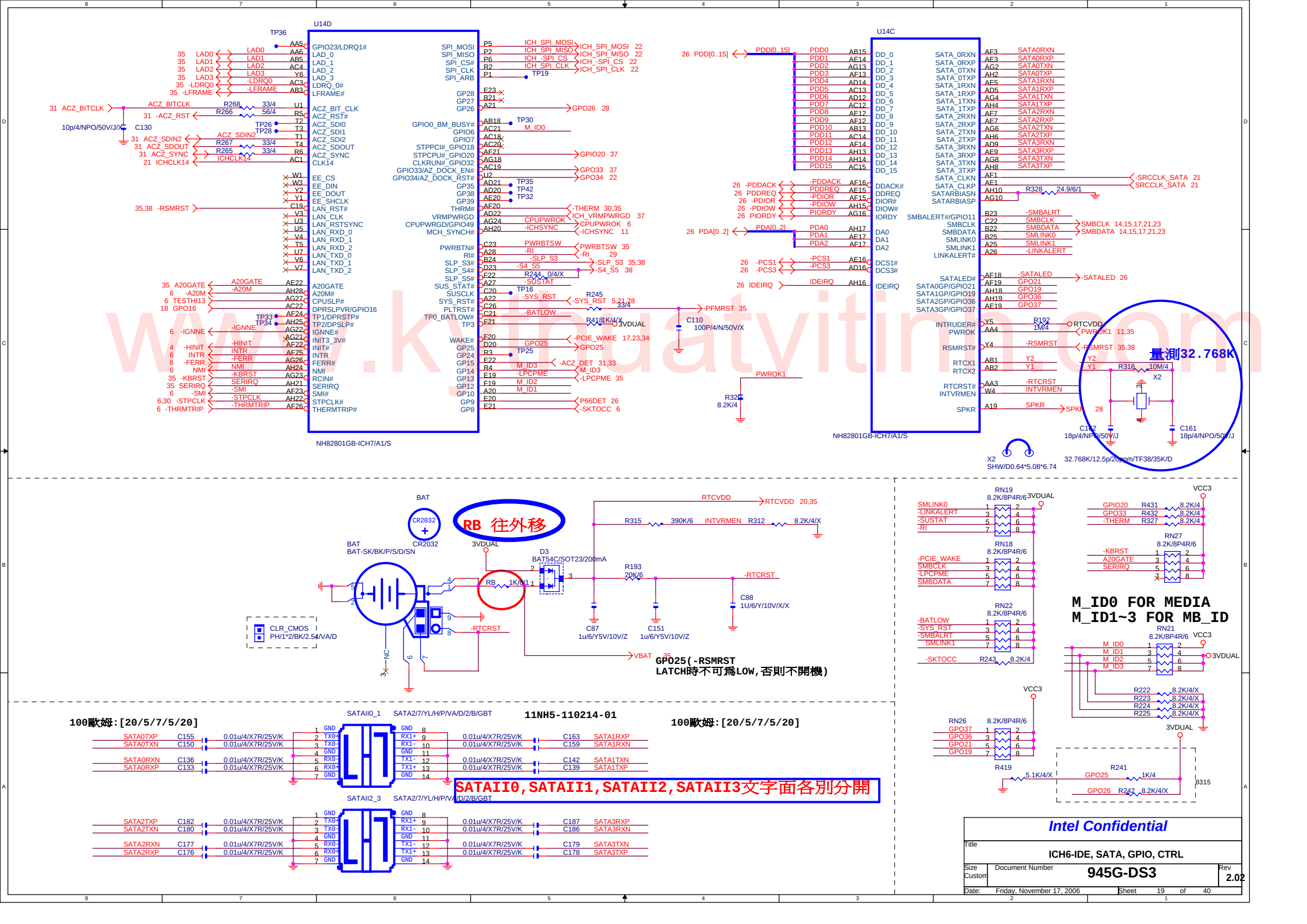
U14D
NH82801GB-ICH7/A1/S
U14C
NH82801GB-ICH7/A1/S
量測32.768K
RB 往外移
BAT
CR2032
BAT-SK/BK/P/S/D/SN
CLR_CMOS
PH/1*2/BK/2.54/VA/D
D3
BAT54C/SOT23/200mA
GPO25(-RSMRST
LATCH時不可爲LOW,否則不開機)
M_ID0 FOR MEDIA
M_ID1~3 FOR MB_ID
100歐姆:[20/5/7/5/20]
11NH5-110214-01
100歐姆:[20/5/7/5/20]
SATAII0_1
SATA2/7/YL/H/P/VA/D/2/B/GBT
SATAII2_3
SATA2/7/YL/H/P/VA/D/2/B/GBT
SATAII0,SATAII1,SATAII2,SATAII3文字面各別分開
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ICH6-IDE, SATA, GPIO, CTRL
945G-DS3
2.02
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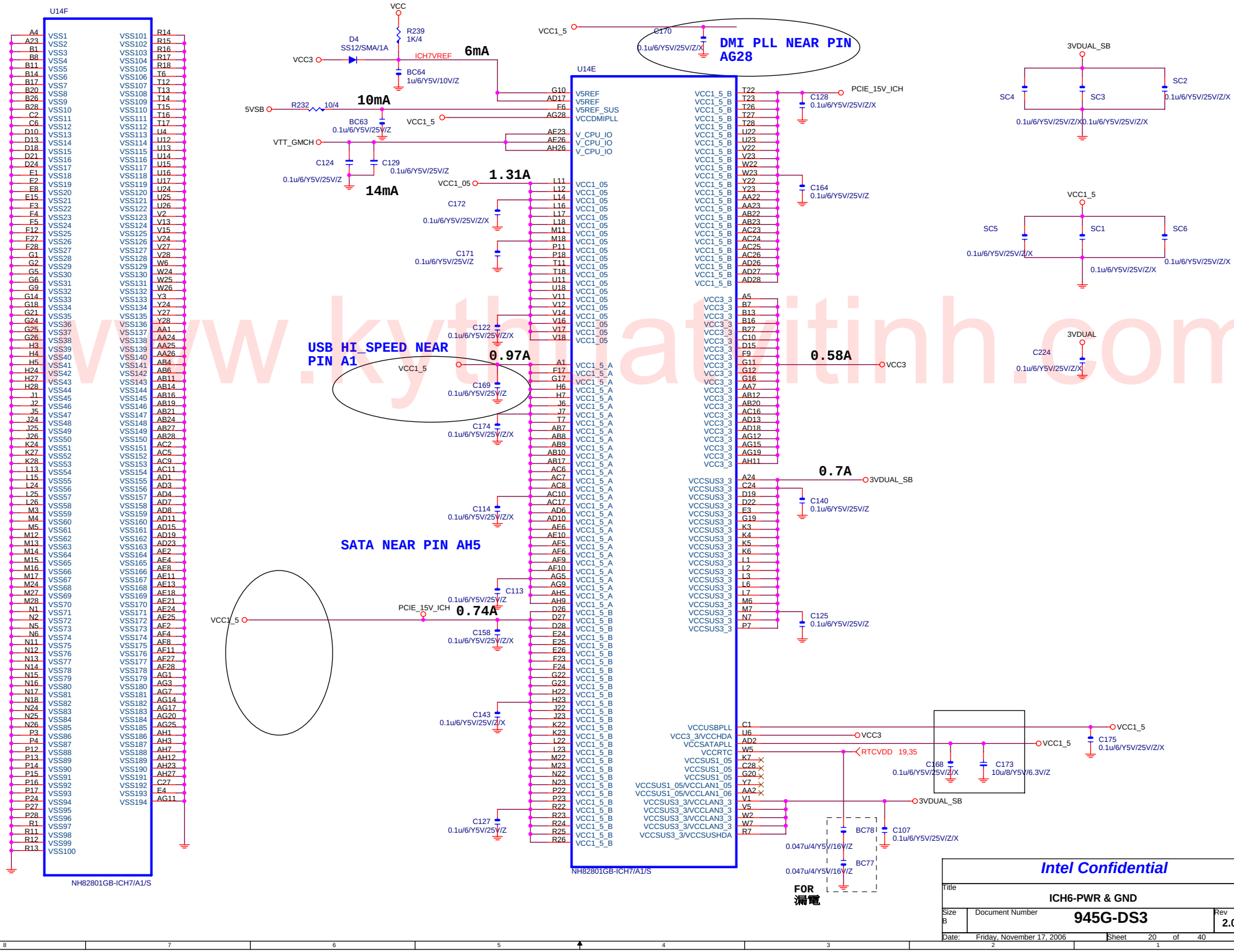

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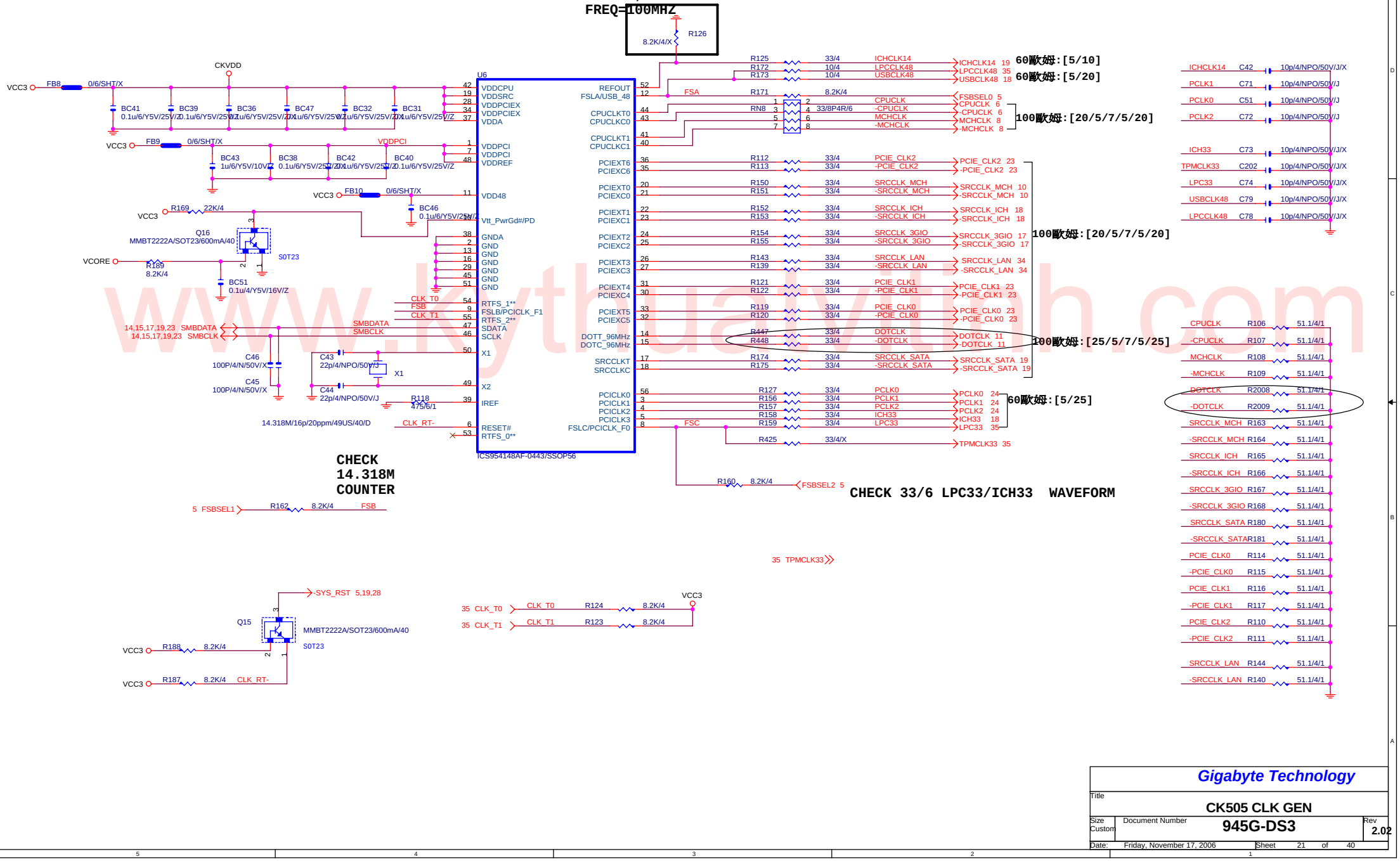
Title	ICH6-PWR & GND	
Size B	Document Number 945G-DS3	Rev 2.02
Date: Friday, November 17, 2006	Sheet 20 of 40	

CLK GEN CK505

GSEL=1, DOT FREQ=96MHZ
GSEL=0, DOT
FREQ=100MHZ



Gigabyte Technology		
Title	CK505 CLK GEN	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
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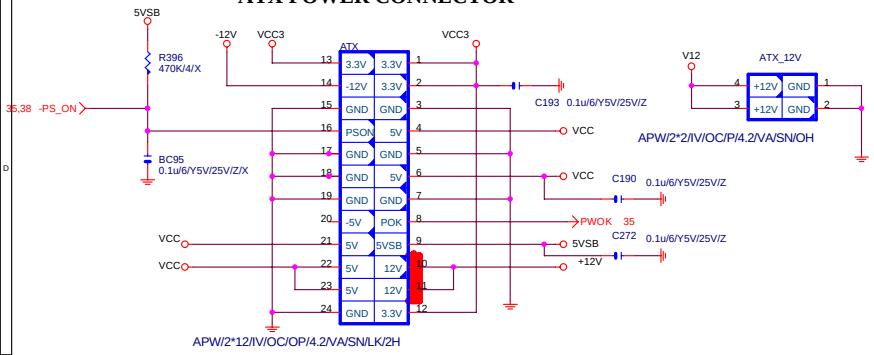
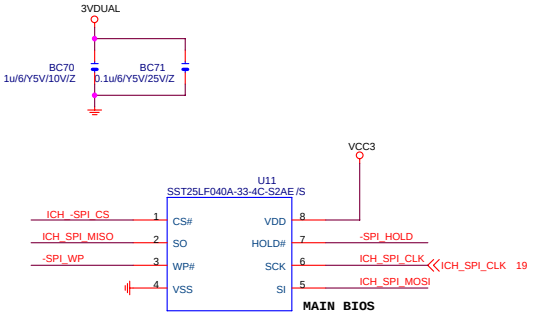
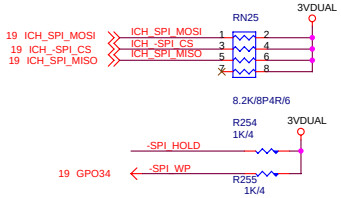
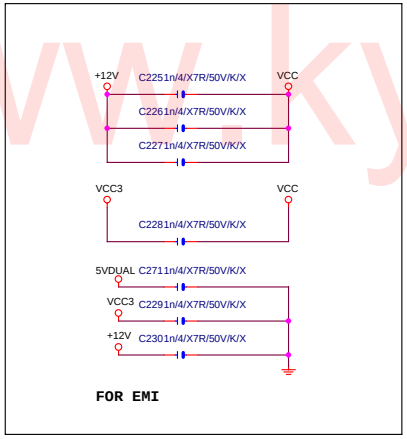
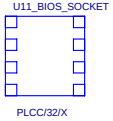
ATX POWER CONNECTOR









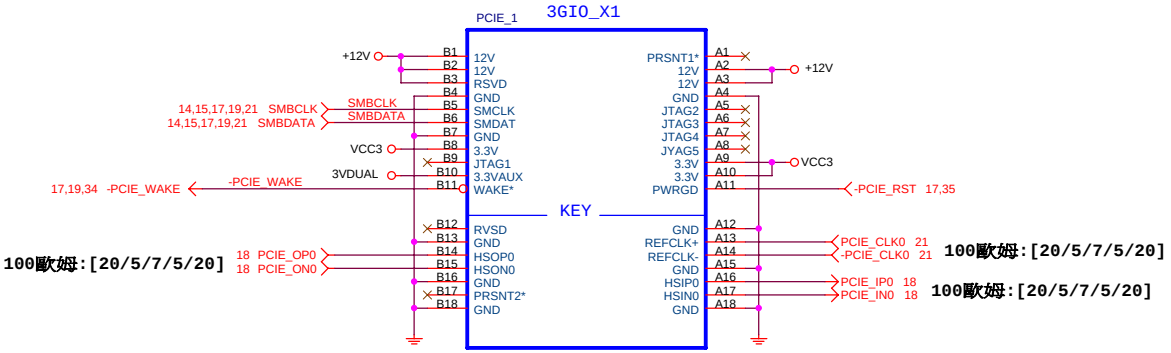


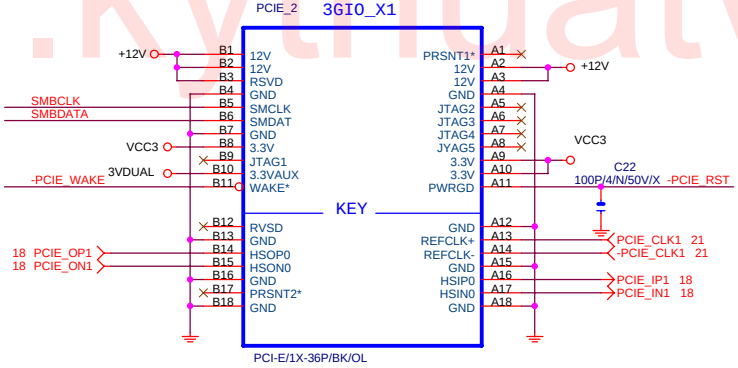
GIGABYTE

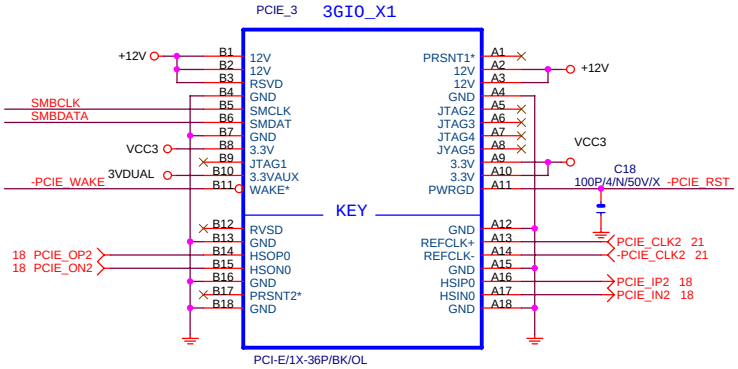
Title: ATX POWER CONNECTOR,DUAL BIOS

Size	Document Number	Rev
Custom	945G-DS3	2.02

Date: Friday, November 17, 2006 Sheet 22 of 40





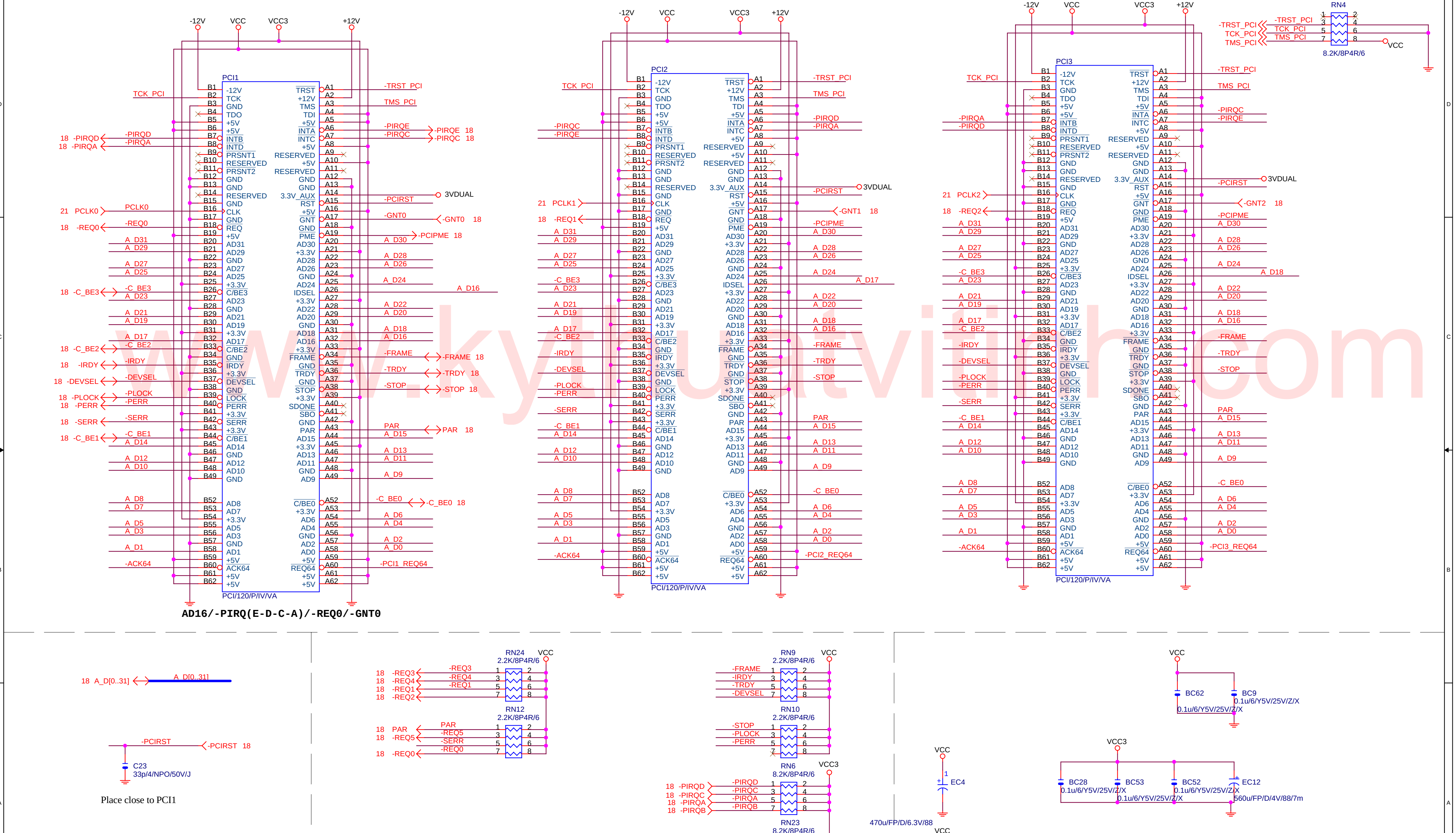


Intel Confidential

Title: PCI E SLOT 1, 2, 3

Size: B | Document Number: 945G-DS3 | Rev: 2.02

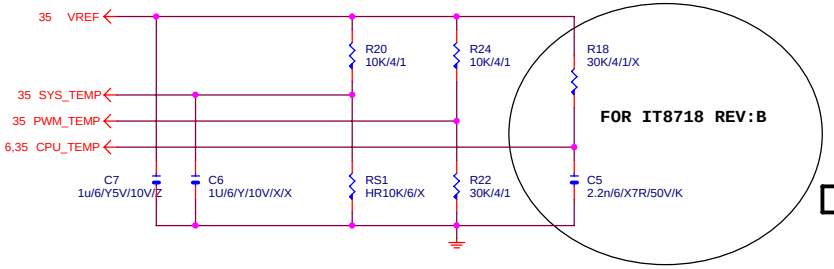
Date: Friday, November 17, 2006 | Sheet 23 of 40



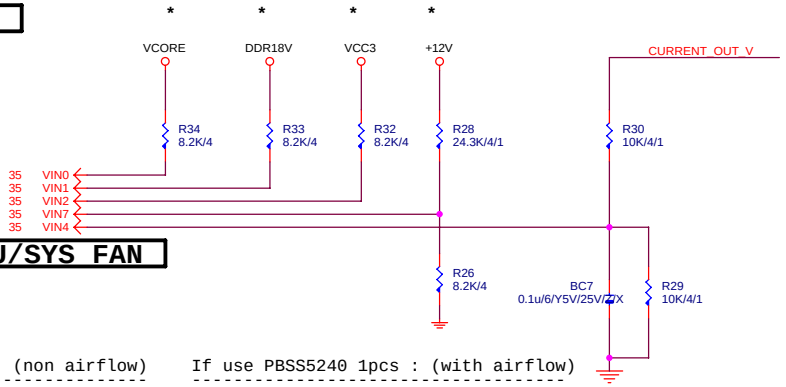
Intel Confidential

Title	PCI SLOT 1, 2		
Size: Custom	Document Number: 945G-DS3		Rev: 2.02
Date: Friday, November 17, 2006		Sheet 24 of 40	

TEMP. SENSE



VOLTAGE SENSE



CPU/SYS FAN

DUAL POWER

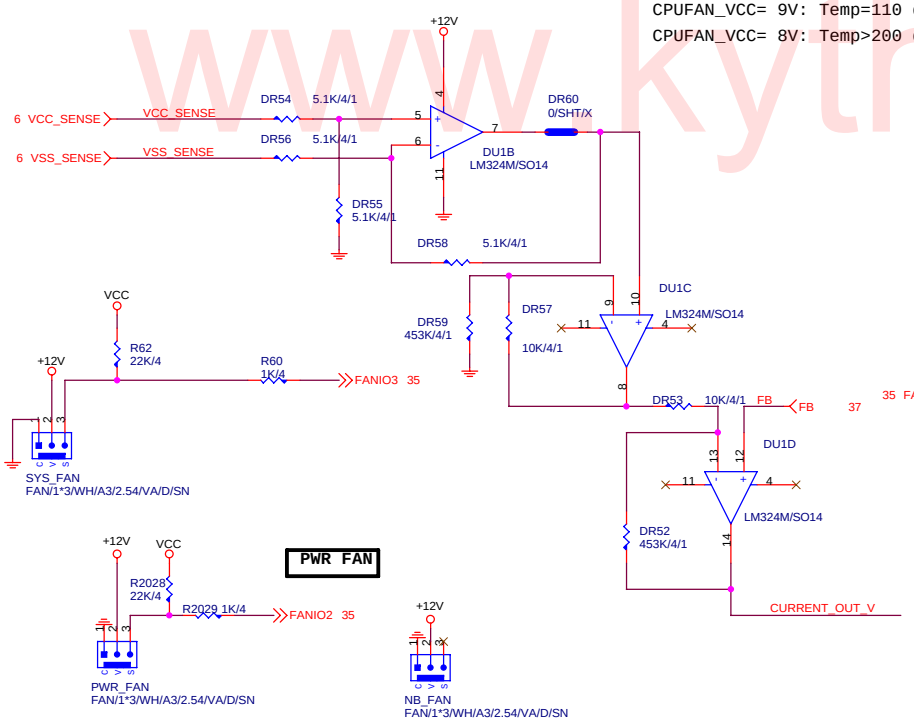
If use PBSS5240 1pcs : (non airflow)

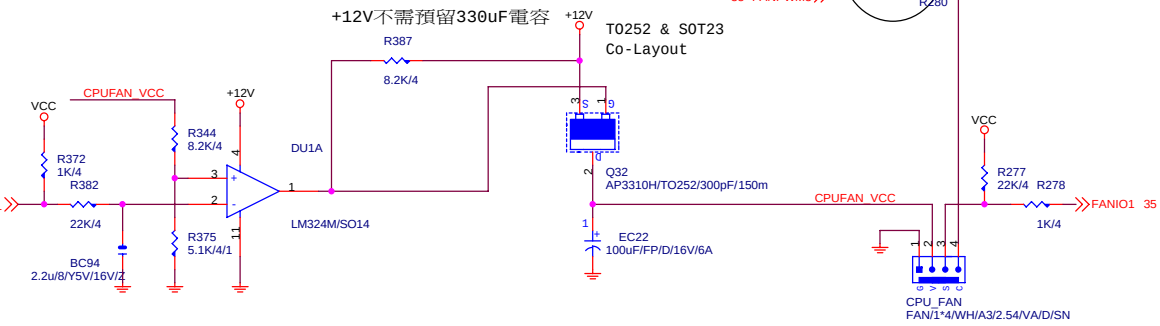
CPUFAN_VCC=12V: Temp=40 deg
CPUFAN_VCC=11V: Temp=82 deg
CPUFAN_VCC=10V: Temp=70 deg
CPUFAN_VCC= 9V: Temp=110 deg
CPUFAN_VCC= 8V: Temp>200 deg

If use PBSS5240 1pcs : (with airflow)

CPUFAN_VCC=12V: Temp=33 deg
CPUFAN_VCC=11V: Temp=62 deg
CPUFAN_VCC=10V: Temp=86 deg
CPUFAN_VCC= 9V: Temp=117 deg
CPUFAN_VCC= 8V: Temp>122 deg

ThermalTake FAN Power Consumption: 0.82A
Intel FAN Power Consumption Spec: 1.1A





GIGABYTE			
Title	HWM/FAN/CI/BIOS		
Size: Custom	Document Number: 945G-DS3		Rev: 2.02
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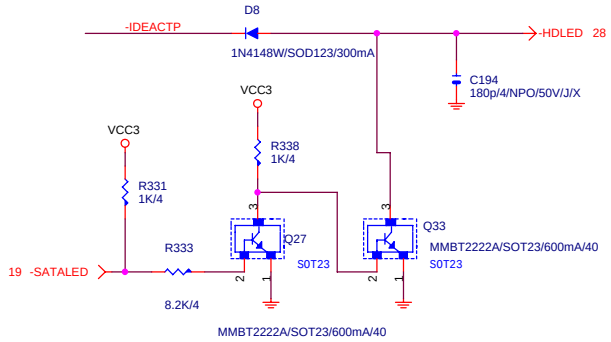
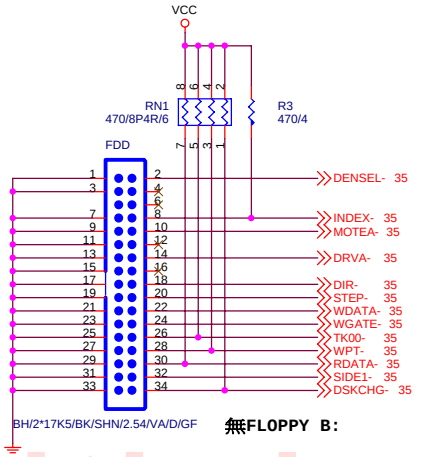
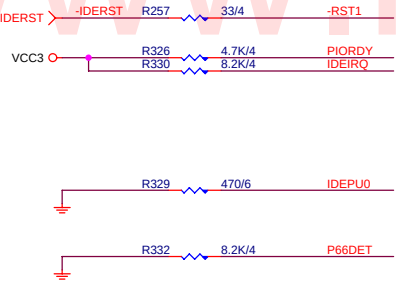
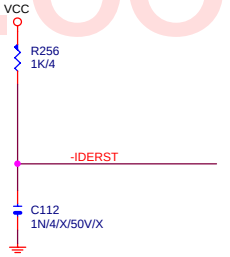


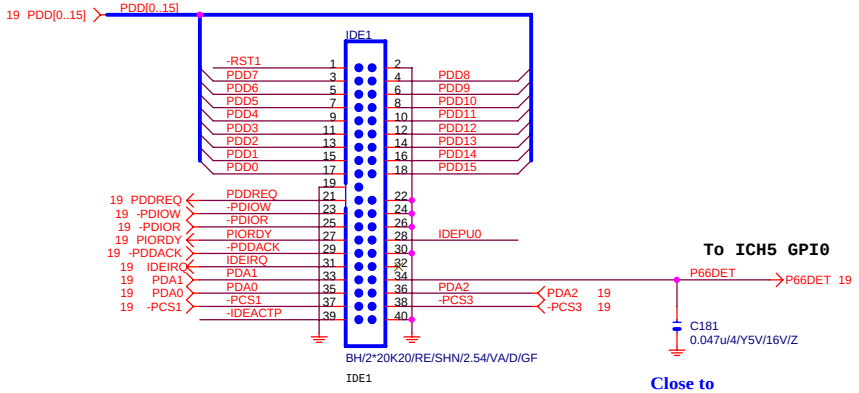


無FLOPPY B:





Default 不上,由IT8712 OUTPUT,REV0.2 驗證

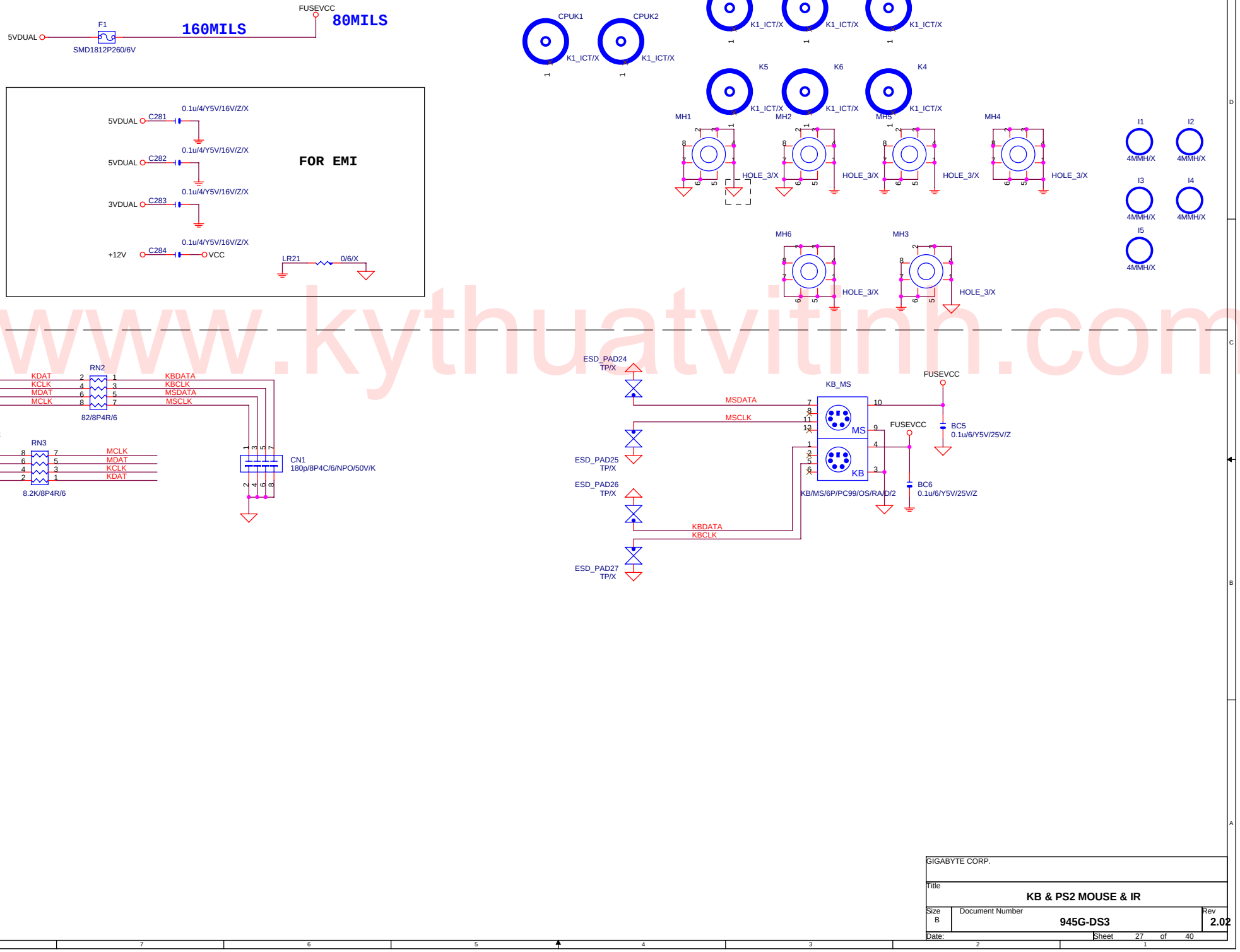


Close to connector

PRIMARY IDE CONNECTOR

Intel Confidential

Title: IDE		
Size: B	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 26 of 40	



GIGABYTE CORP.	
Title	KB & PS2 MOUSE & IR
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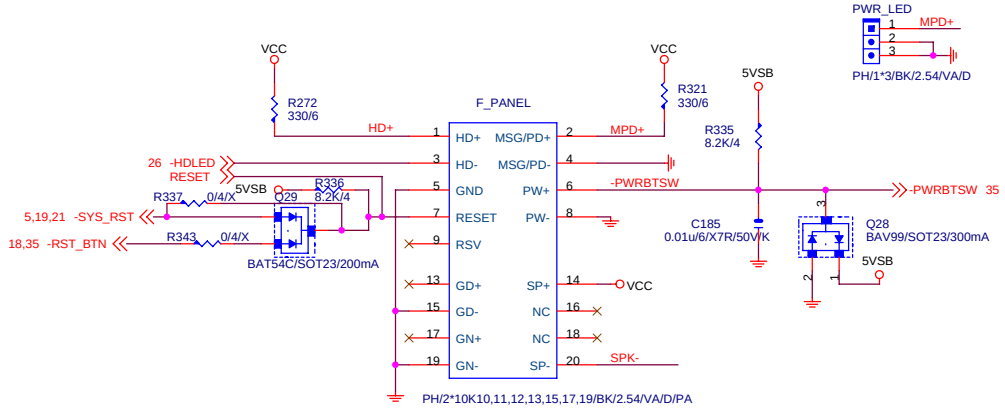
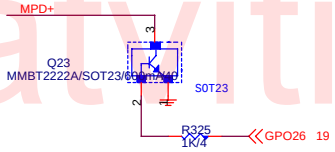
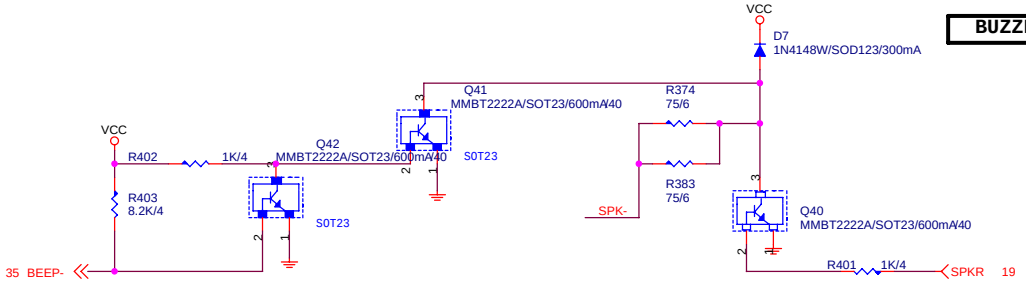
INTEL FRONT PANEL

3 PIN POWER LED
LAYOUT PLACE CLOSE
TO F_PANEL





BUZZER



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Title	FRONT PANEL	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
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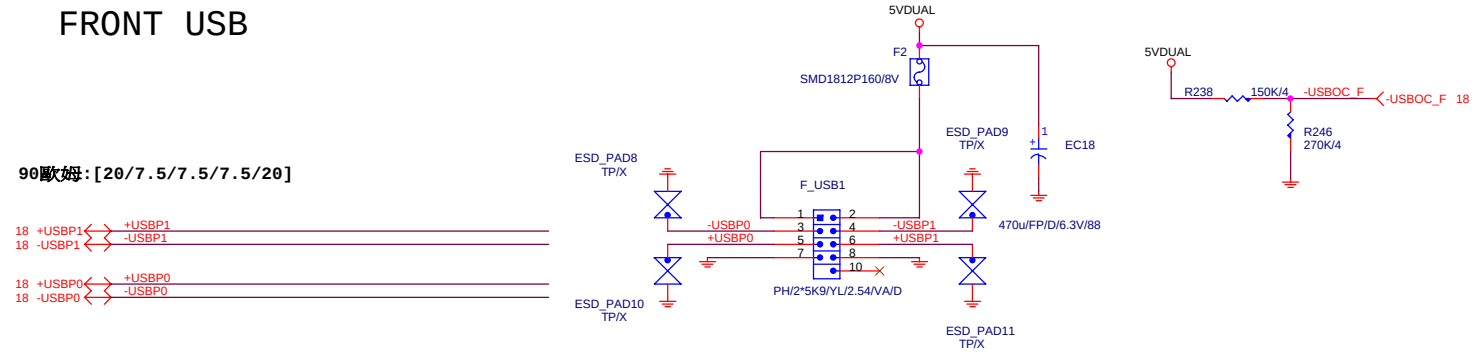
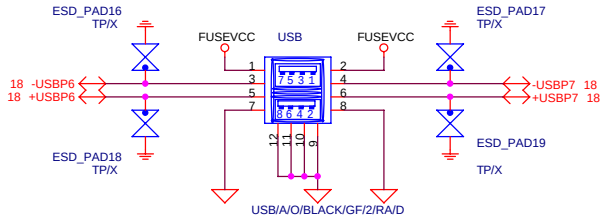
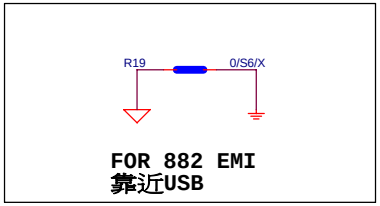
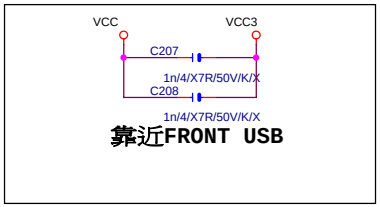
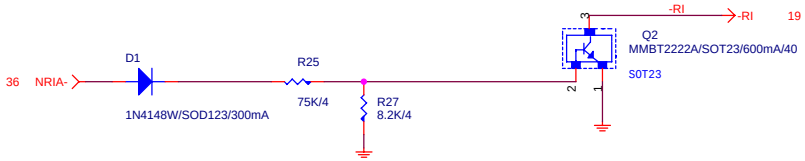
FRONT USB











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Title	FRONT USB CONNECTOR	
Size B	Document Number 945G-DS3	Rev 2.02
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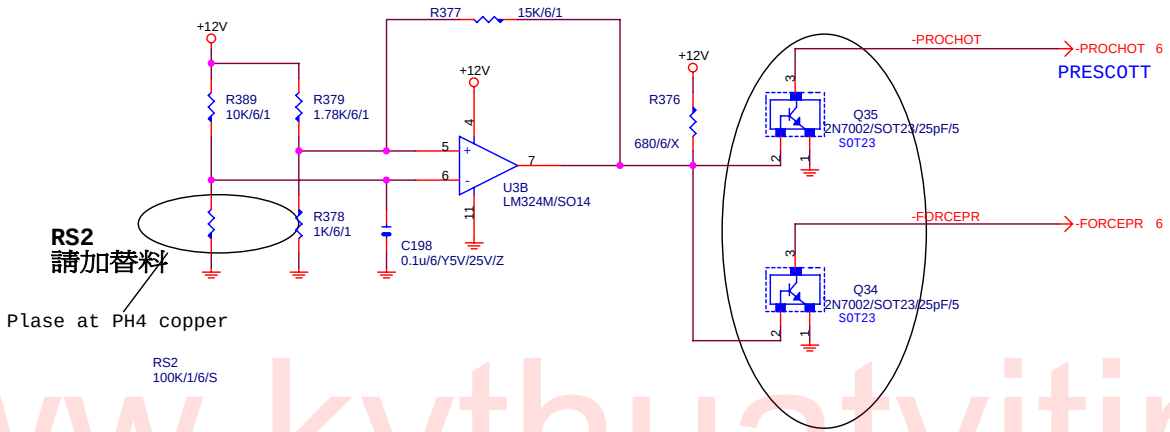
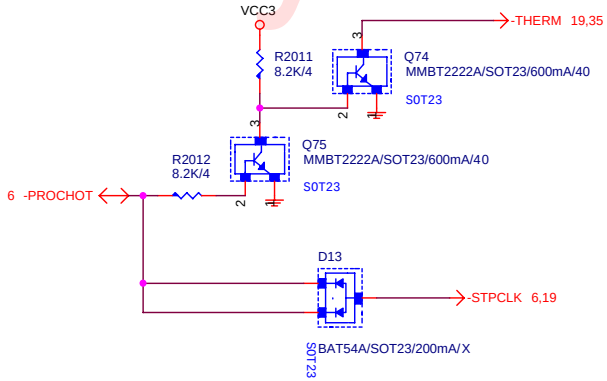
asserted at 131 degree
deasserted at 116 degree

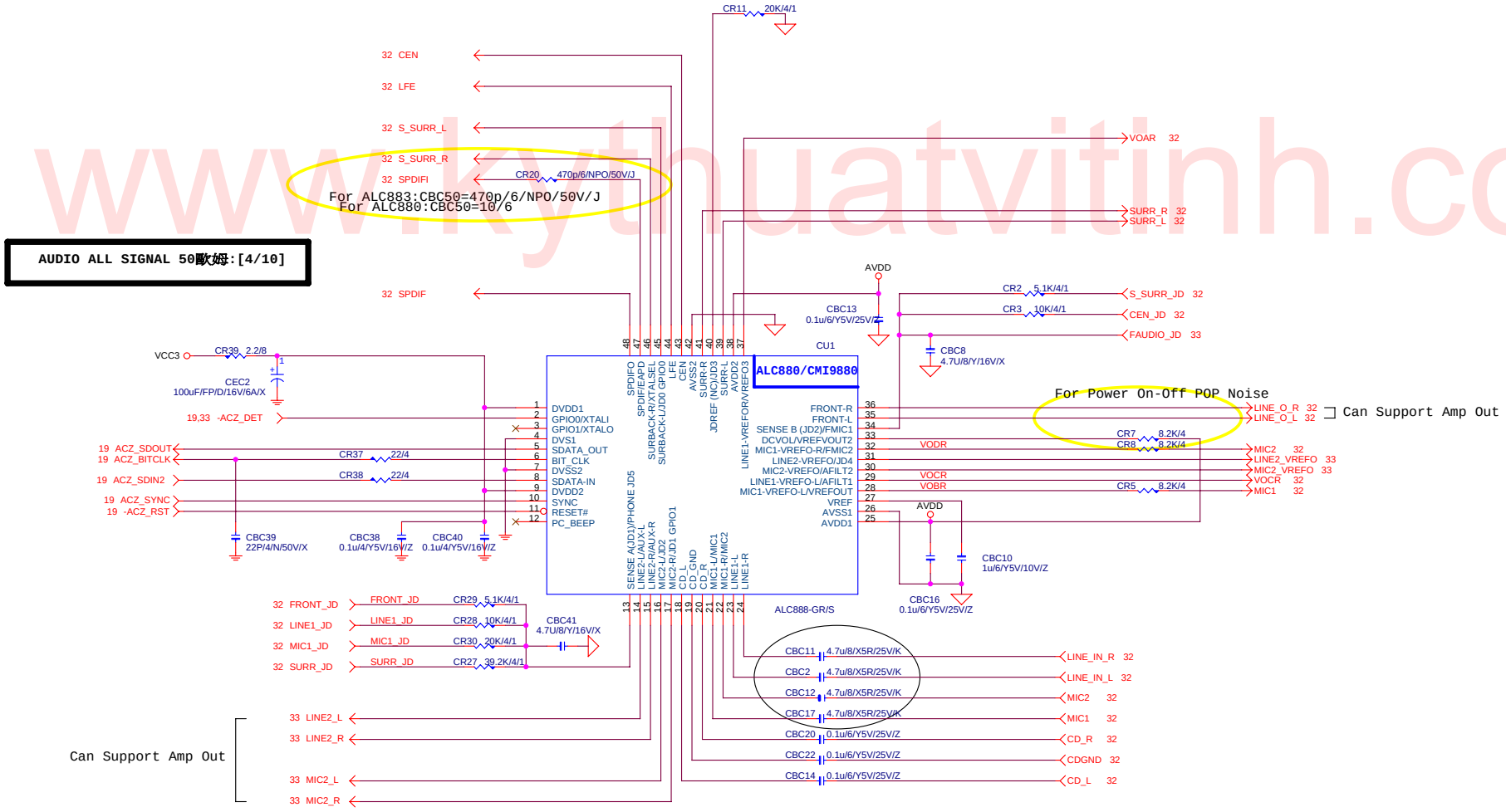
RS2 CLOSE CPU VR MOSFET





Intel Confidential

Title	FAN CONTROL	
Size B	Document Number 945G-DS3	Rev 2.02
Date: Friday, November 17, 2006	Sheet 30 of 40	



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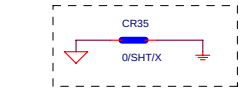
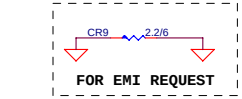
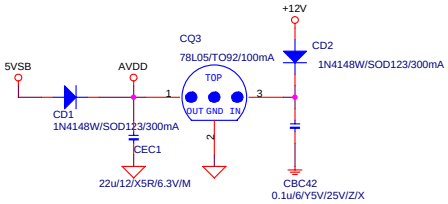
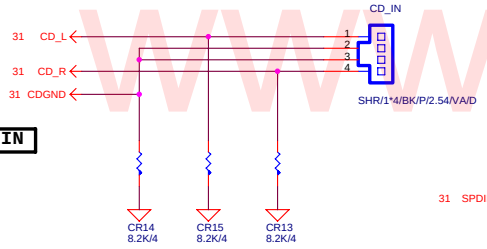
Title	AC97 ALC658	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 31 of 40	

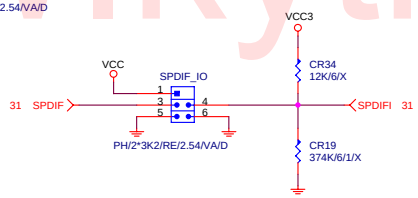




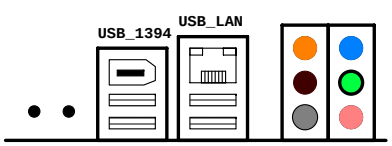


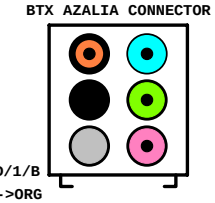


CD IN

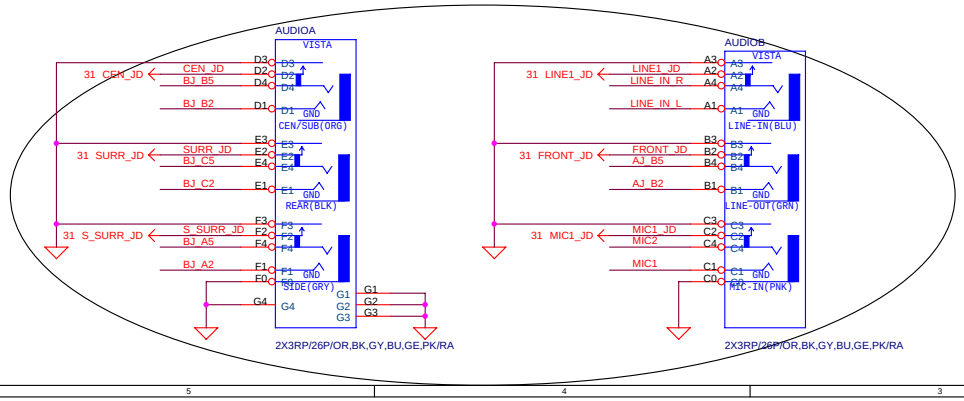


AZALIA JACK

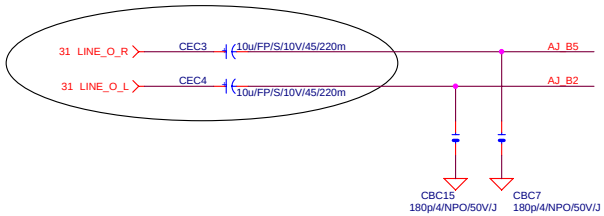




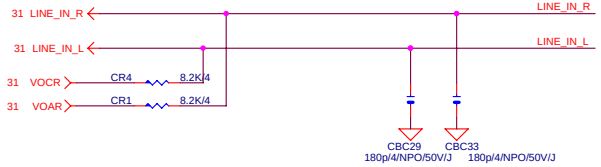
3RP/26P/OR,BK,GY,BU,GE,PK/RA/D/1/B
VISTA規範:REAR-->BLK,CEN/SUB-->ORG



LINE OUT
FRONT OUT

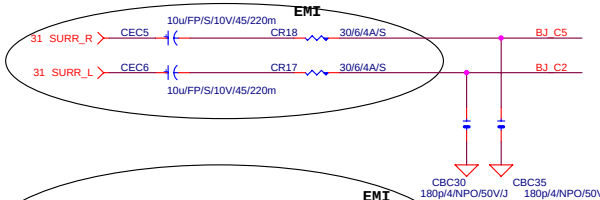


LINE-IN

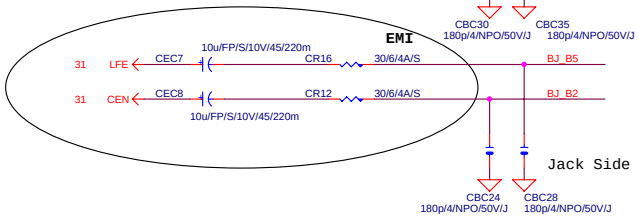


MIC

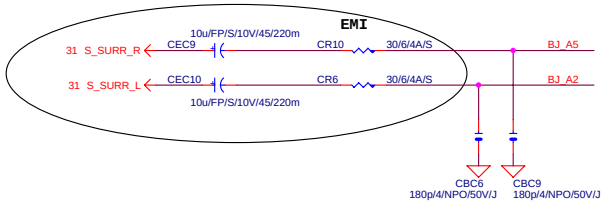
SURROUND



CEN/LFE



SURR BACK



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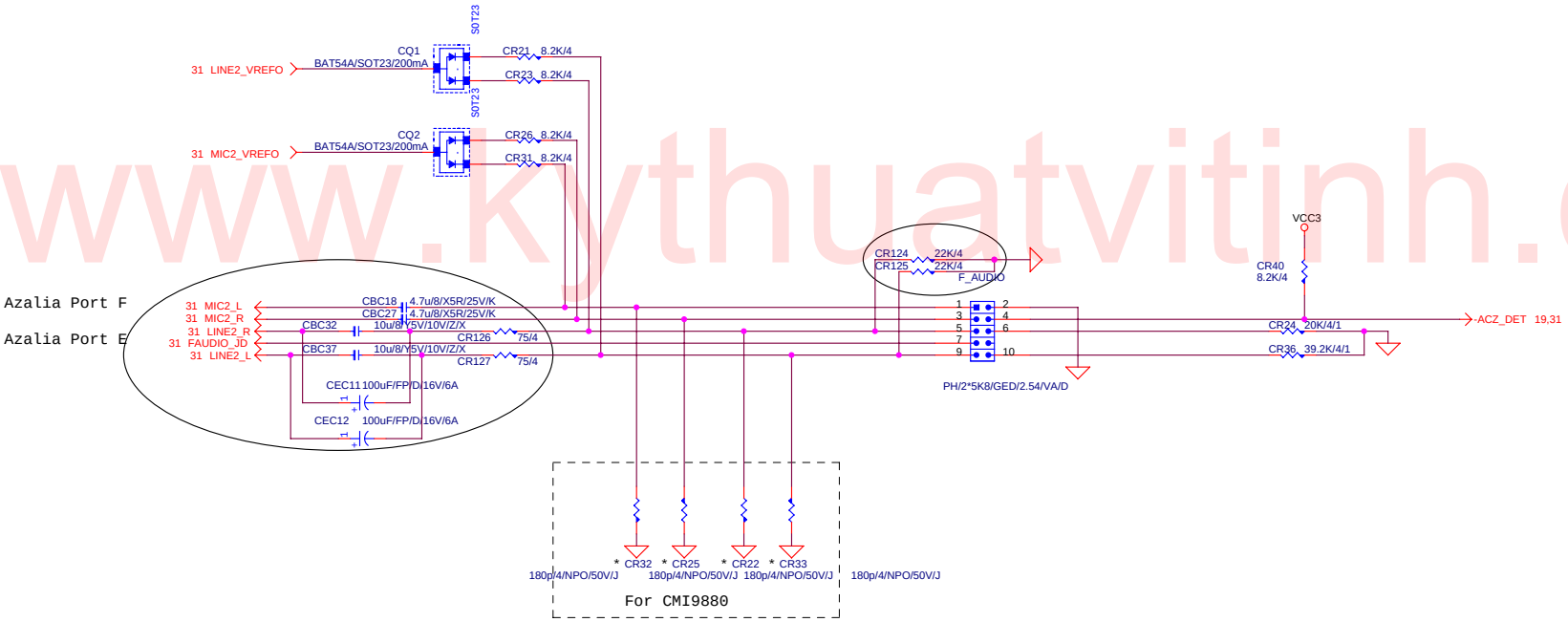
Title	AUDIO JACK	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 32 of 40	



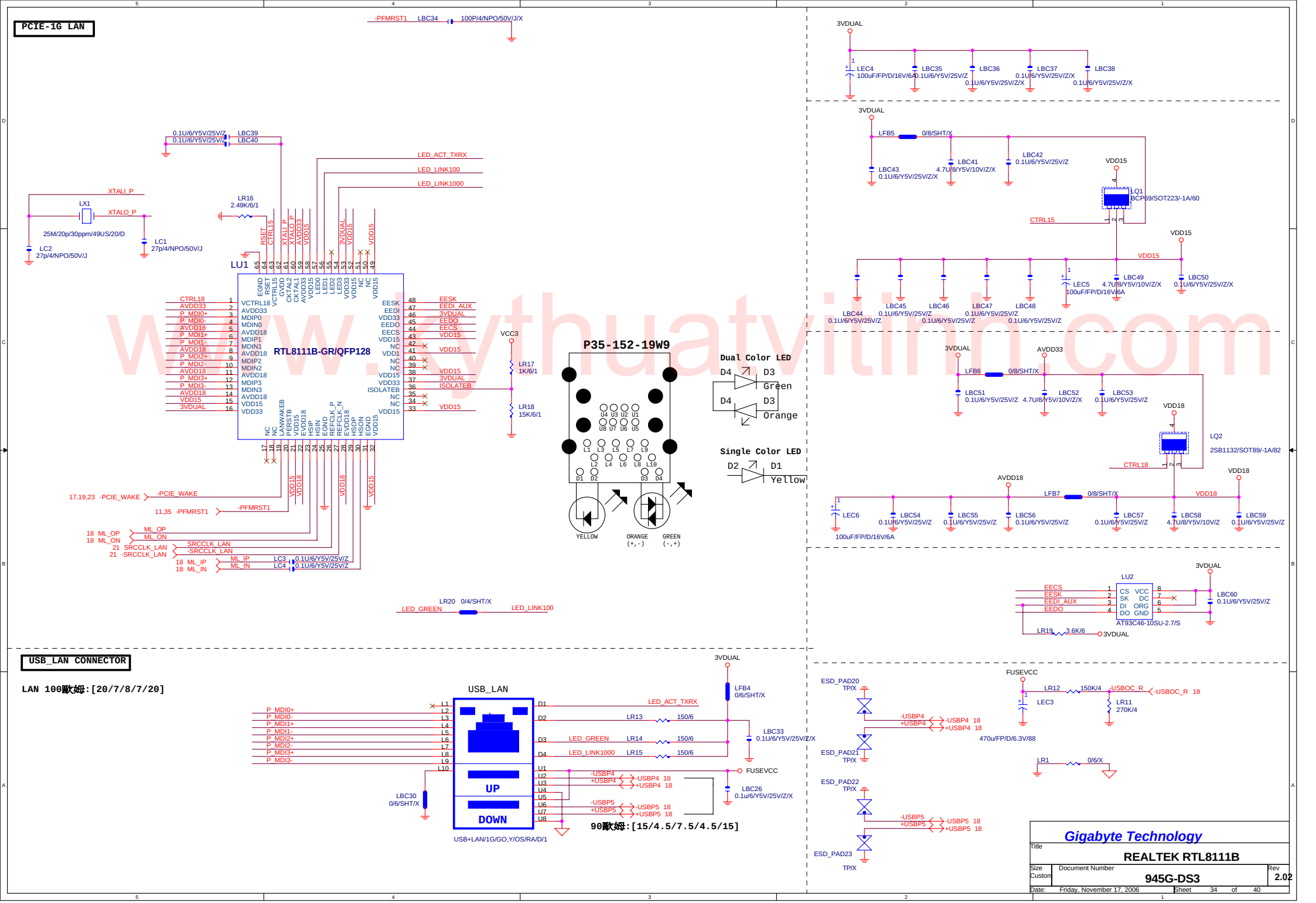
Intel Confidential

Title: FRONT AUDIO CONNECTOR

Size: Custom | Document Number: 945G-DS3 | Rev: 2.02

Date: Friday, November 17, 2006 | Sheet 33 of 40

PCIE-1G LAN

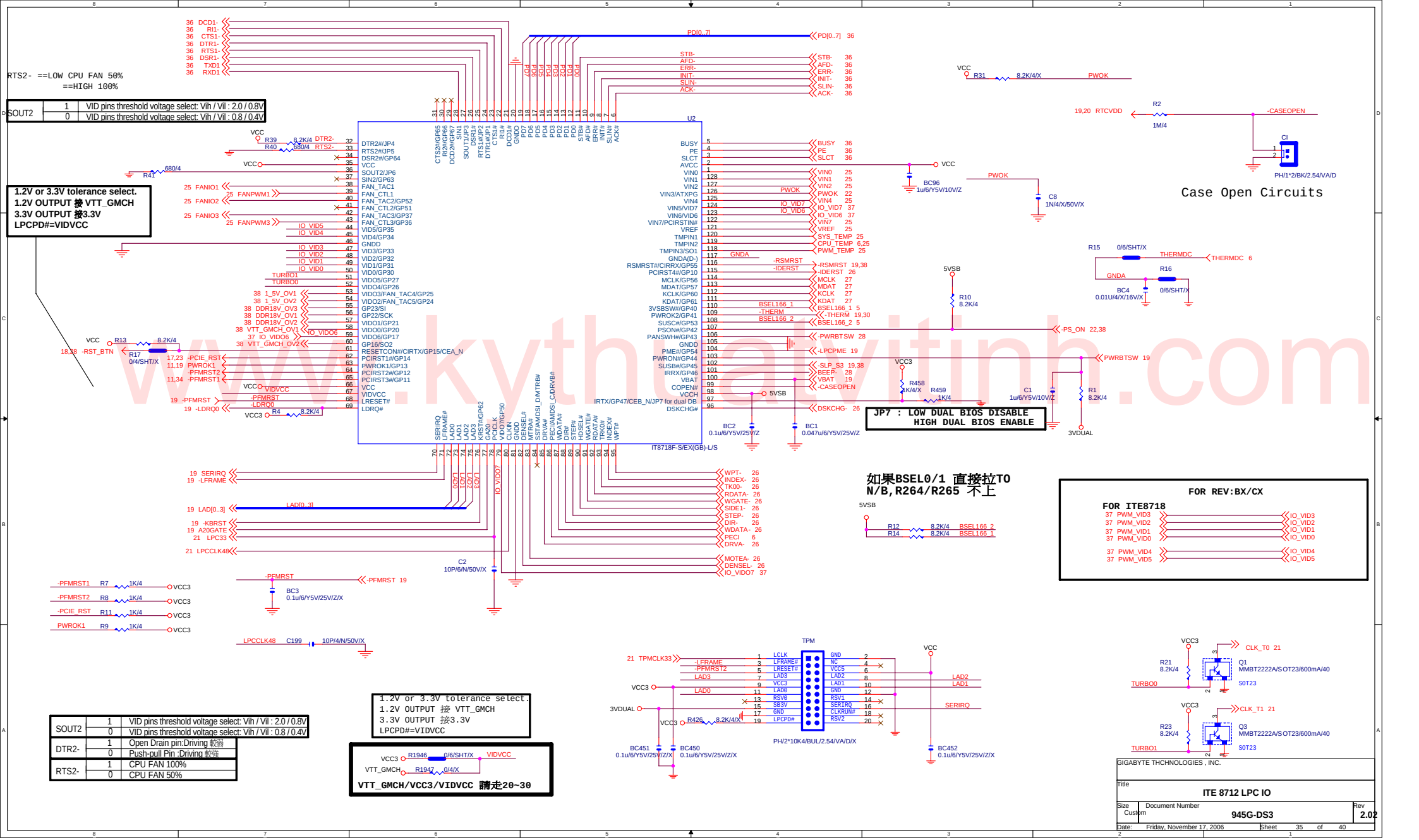


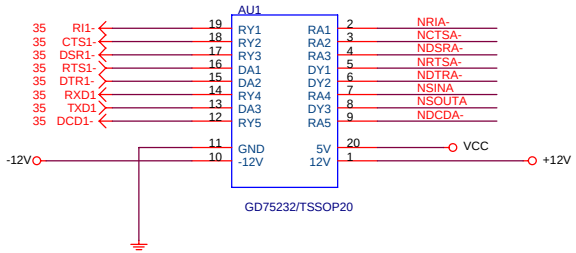
USB_LAN CONNECTOR

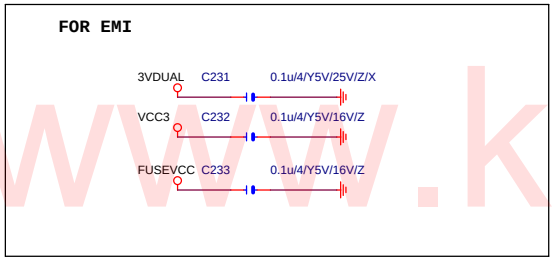
LAN 100歐姆:[20/7/8/7/20]

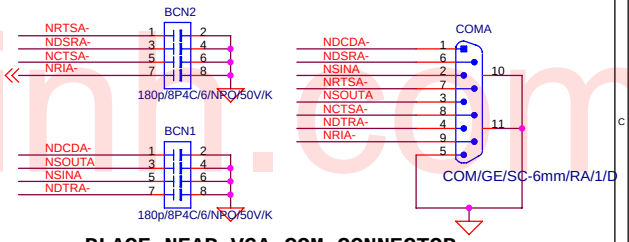
Gigabyte Technology

Title	REALTEK RTL8111B	
Size: Custom	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 34 of 40	

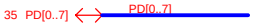


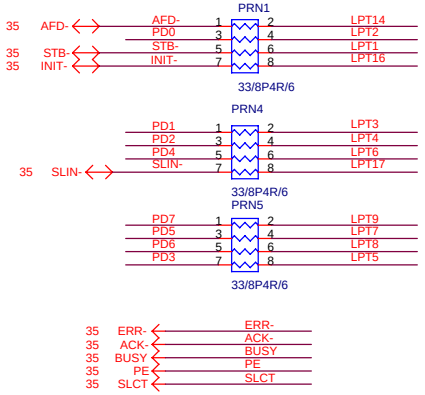


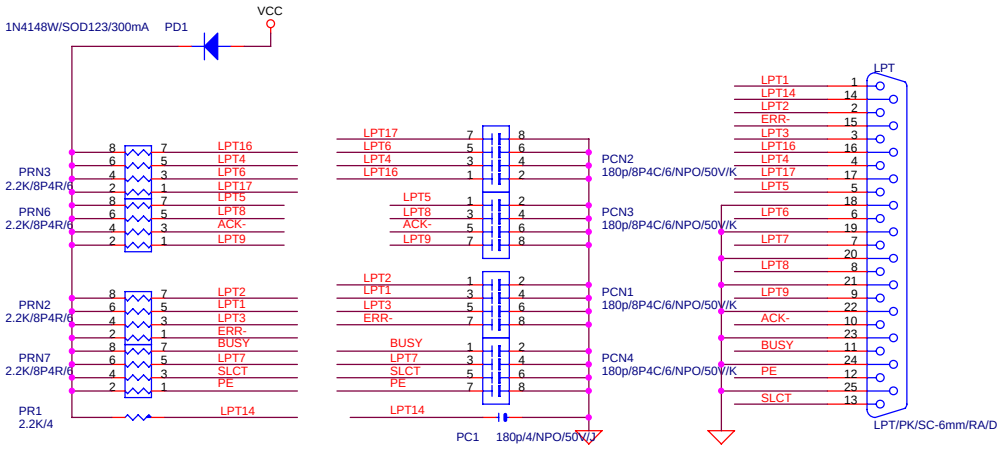




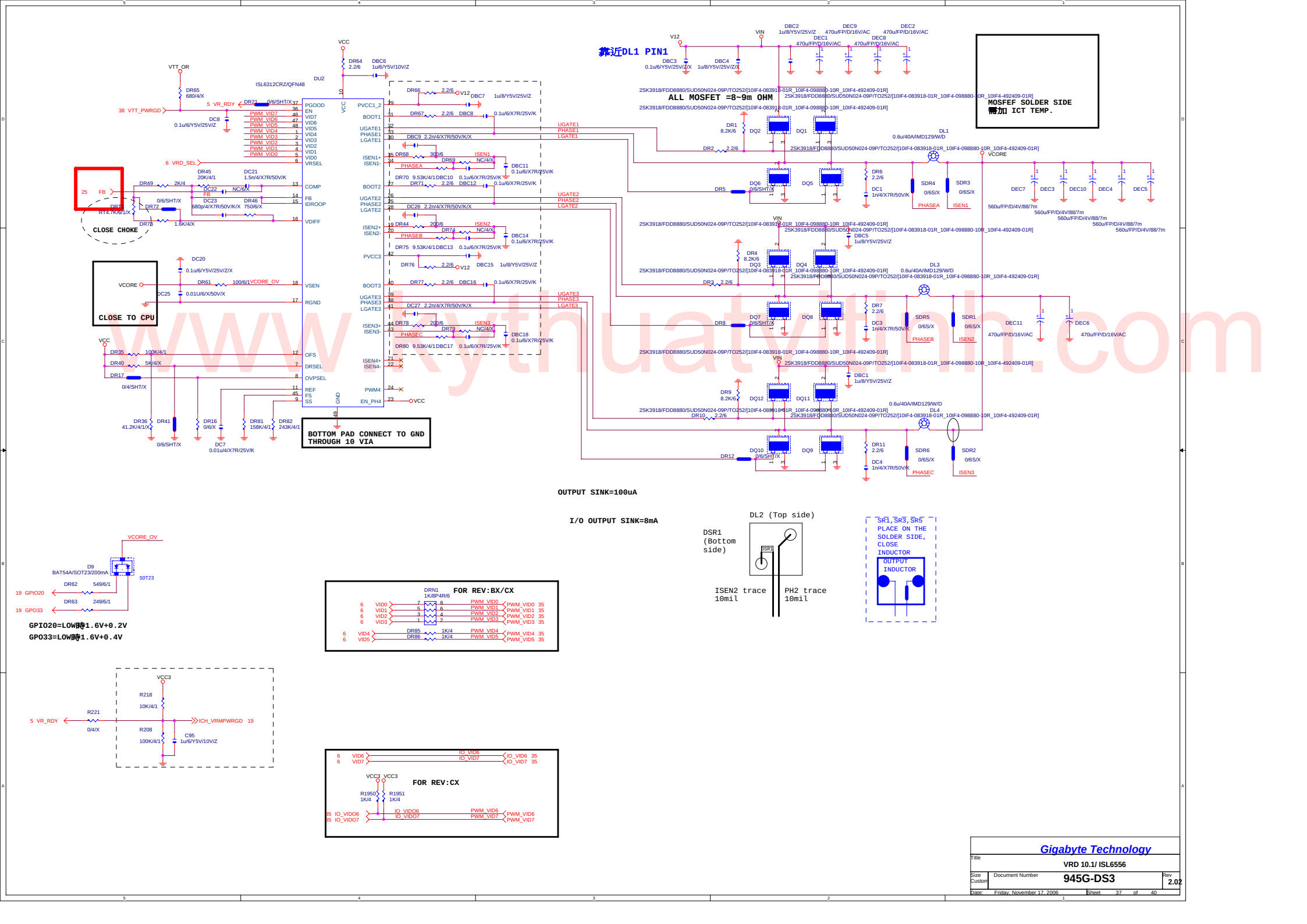
PLACE NEAR VGA_COM CONNECTOR







GIGABYTE THCHNOLOGIES , INC.		
Title: COM & IR & LPT PORT &FLOOPY		
Size: B	Document Number: 945G-DS3	Rev: 2.02
Date: Friday, November 17, 2006	Sheet 36 of 40	


ISL6312CRZ/QFN48
DU2
CLOSE CHOKE
CLOSE TO CPU
BOTTOM PAD CONNECT TO GND THROUGH 10 VIA
算近DL1 PIN1
ALL MOSFET =8~9m OHM
MOSFEF SOLDER SIDE
需加 ICT TEMP.
OUTPUT SINK=100uA
I/O OUTPUT SINK=8mA
GPIO20=LOW時1.6V+0.2V
GPIO33=LOW時1.6V+0.4V
FOR REV:BX/CX
FOR REV:CX
DL2 (Top side)
DSR1 (Bottom side)
ISEN2 trace 10mil
PH2 trace 10mil
SR1,SR3,SR5 PLACE ON THE SOLDER SIDE, CLOSE INDUCTOR
OUTPUT INDUCTOR
Gigabyte Technology
VRD 10.1/ ISL6556
945G-DS3
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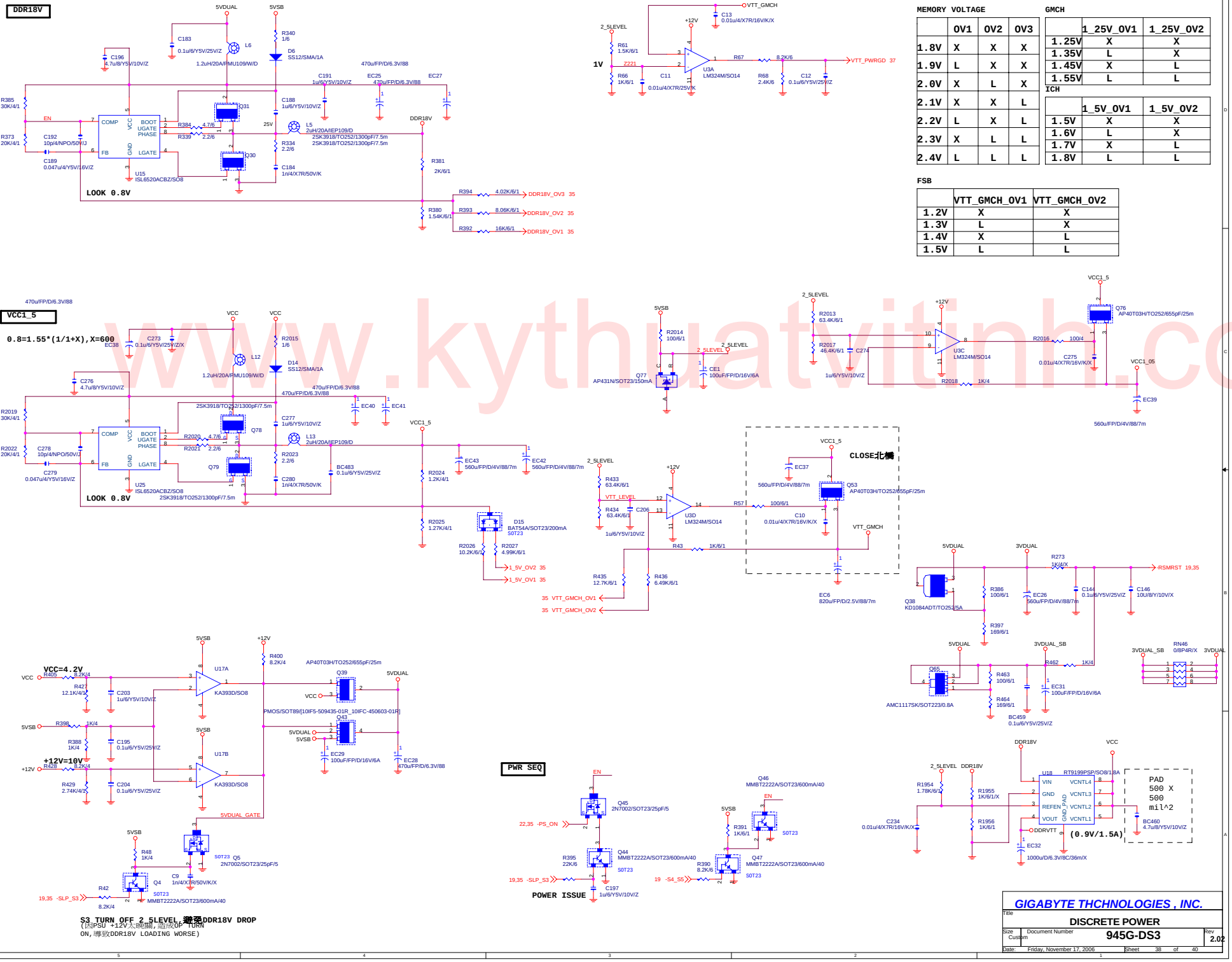

MEMORY VOLTAGE

	0V1	0V2	0V3
1.8V	X	X	X
1.9V	L	X	X
2.0V	X	L	X
2.1V	X	X	L
2.2V	L	X	L
2.3V	X	L	L
2.4V	L	L	L

GMCH

	1_25V_0V1	1_25V_0V2
1.25V	X	X
1.35V	L	X
1.45V	X	L
1.55V	L	L

ICH

	1_5V_0V1	1_5V_0V2
1.5V	X	X
1.6V	L	X
1.7V	X	L
1.8V	L	L

FSB

	VTT_GMCH_0V1	VTT_GMCH_0V2
1.2V	X	X
1.3V	L	X
1.4V	X	L
1.5V	L	L

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GPI26

INTEL ICH7 GPIO Implementation

紅字表示CPI/O 同PIN

GPI PIN

Pin Name	Pin Type	Power Well			GPIO Application
GPIO[0]	I/O	VCC3	GPI/BM_BUSY#	(NA)	(NA)
GPIO[1]	I/O	VCC	-REQ[5]	(P.U VCC)	-REQ[5]
GPIO[5:2]	I/OD	VCC	-PIRQ[H:E]	(P.U VCC)	-PIRQ[H:E]
GPIO[6]	I/O	VCC3	GPI	(NA)	M_ID0 FOR MEDIA
GPIO[7]	I/O	VCC3	GPI	(NA)	DUALBIOS_INPUT
GPIO[8]	I/O	3VDUAL	GPI	(P.U 3VDUAL)	-SKTOCC
GPIO[9]	I/O	3VDUAL	GPI	(NA)	P66DET
GPIO[10]	I/O	3VDUAL	GPI	(NA)	M_ID1 FOR MB_ID
GPIO[11]	I/O	3VDUAL	-SMBALERT	(P.U 3VDUAL)	-SMBALRT
GPIO[12]	I/O	3VDUAL	GPI	(NA)	M_ID2 FOR MB_ID
GPIO[13]	I/O	3VDUAL	GPI	(P.U 3VDUAL)	-LPCPME
GPIO[14]	I/O	3VDUAL	GPI	(NA)	M_ID3 FOR MB_ID
GPIO[15]	I/O	3VDUAL	GPI	(NA)	-ACZ_DET
GPIO[16]	I/O	VCC3	GPO	P.D 20K(INT.)	HW RESET
GPIO[17]	I/O	VCC3	GPO/-GNT[5]	(NA)	GPO/-GNT[5]
GPIO[18]	I/O	VCC3	GPO/toggle	(NA)	(NA)
GPIO[19]	I/O	VCC3	SATA1GP	(P.U VCC3)	SATA1GP
GPIO[20]	I/O	VCC3	GPO	(P.U VCC3)	TBL-
GPIO[21]	I/O	VCC3	SATA0GP	(P.U VCC3)	SATA0GP
GPIO[22]	I/O	VCC3	-REQ[4]	(P.U VCC)	-REQ[4]
GPIO[23]	I/O	VCC3	LDRQ1#	(NA)	(NA)
GPIO[24]	I/O	3VDUAL	GPO/reset not cleared	(NA)	(NA)
GPIO[25]	I/O	3VDUAL	GPO	(NA)	PWD_LED
GPIO[26]	I/O	3VDUAL	EL_RSVD	(P.D)	-SPI_WP
GPIO[27]	I/O	3VDUAL	EL_STATE0	(NA)	(NA)
GPIO[28]	I/O	3VDUAL	EL_STATE1	(NA)	(NA)
GPIO[29]	I/O	3VDUAL	OC5#	(P.U VCC 分壓))	OC5#
GPIO[30]	I/O	3VDUAL	OC6#	(P.U VCC 分壓))	OC6#
GPIO[31]	I/O	3VDUAL	OC7#	(P.U VCC 分壓))	OC7#
GPIO[32]	I/O	VCC3	GPO	(NA)	DUAL_BIOS
GPIO[33]	I/O	VCC3	GPO	(NA)	DUAL_BIOS
GPIO[34]	I/O	VCC3	GPO	(P.U VCC3)	FWP-
GPIO[35]	I/O	VCC3	SATACLKREQ#	(NA)	(NA)
GPIO[36]	I	VCC3	SATA2GP	(P.U VCC3)	SATA2GP
GPIO[37]	I	VCC3	SATA3GP	(P.U VCC3)	SATA3GP

GPO PIN

Pin Name	Pin Number	Power Well	Pin Type		GPIO Application
GPIO[38]	I/O	VCC3	GPI	(NA)	(NA)
GPIO[39]	I/O	VCC3	GPI	(NA)	(NA)
GPIO[40:47]		NOT IMPLEMENTED		NOT IMPLEMENTED	
GPIO[48]	I/O	VCC3	-GNT[4]	(NA)	-GNT[4]
GPIO[49]	I/O	VTT_GMCH	CPUPWRGD	(P.U VTT_OL)	CPUPWROK
PCI1	PCLK0	-PCIRST	-REQ0/-GNT0	-PIRQE	A_D16
PCI2	PCLK1	-PCIRST	-REQ1/-GNT1	-PIRQD	A_D17
PCI3	PCLK2	-PCIRST	-REQ2/-GNT2	-PIRQC	A_D18
1394b	1394CLK	-PFMRST2	-REQ3/-GNT3	-PIRQH	A_D23
IT8212	RAIDCLK	-PFMRST2	-REQ4/-GNT4	-PIRQG	A_D22

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ICH6 GPIO Table:

NAME	PWR LANE	USAGE	NAME	PWR LANE	USAGE
GPI0	V5REF	M/B ID (-REQ6)	GPI41	VCC3	M/B ID
GPI1	V5REF	-REQ5	GPO48	VCC3	-GNT4
GPI2	V5REF	-PIRQE	GPO49	V-CPUIO	CPUPWOK
GPI3	V5REF	-PIRQF			
GPI4	V5REF	-PIRQG			
GPI5	V5REF	-PIRQH			
GPI6	VCC3	-SLP_BTN			
GPI7	VCC3	DUAL BIOS			
GPI8	3VDAUL	-LANWAKE			
GPI9	3VDAUL	-USBOC4			
GPI10	3VDAUL	-USBOC5			
GPI11	3VDAUL	-SMBALT			
GPI12	VCC3	ATX_DET			
GPI13	3VDAUL	-LPCPME			
GPI14	3VDAUL	-USBOC6			
GPI15	3VDAUL	-USBOC7			
GPO16	VCC3	CPU OV1 (-GNT6)			
GPO17	VCC3	-GNT5			
GPO18	VCC3	CPU OV2			
GPO19	VCC3	DUAL BIOS			
GPO20	VCC3	BIOS T-BLOCK			
GPO21	VCC3	DUAL BIOS			
GPO23	VCC3	DDR OV0			
GPIO24	3VDAUL	GREEN LED			
GPIO25	3VDAUL	DDR OV1			
GPI26	VCC3	SATA_GP0			
GPIO27	3VDAUL	+PWRLED			
GPIO28	3VDAUL	-PWRLED			
GPI29	VCC3	SATA GP1			
GPI30	VCC3	SATA GP2			
GPI31	VCC3	SATA GP3			
GPIO32	VCC3	BIOS WP			
GPIO33	VCC3	AZALIA DET			
GPIO34	VCC3	PWRLED			
GPI40	V5REF	-REQ4			

PWROK/RESET Table:

ITE8712BHX PIN	NET NAME	TARGET
PIN62/-PCIRST1	-PCIE_RST	1. PCI-E * 1 Slot1 2. PCI-E * 1 Slot2 3. PCI-E * 1 Slot3 4. PCI-E * 16 Slot
PIN64/-PCIRST2	-PFMRST2	1. Onboard PCI Lan 2. Onboard 1394 Chip 3. OnBoard FWH
PIN65/-PCIRST3	-PFMRST1	1. Onboard PCI-E Lan 2. Onboard SATA Chip 3. GMCH
PIN115/-PCIRST4	-PFMRST_ -IDERST	Reserved For IDE
PIN63/PWROK1	PWROK1	1. GMCH 2. ICH6 3. 5VDUAL SWITCH 4. DPS CONTROL
PIN109/PWROK2	-THERM	1. ICH6

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