


Model Name: 8I915P DUO PRO
REV1.2

SHEET	TITLE
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B
06	P4_LGA775_C
07	P4_LGA775_D
08	VCORE POWER
09	GMCH-GRANTSDALE_HOST
10	GMCH-GARNTSDALE_DDR
11	GMCH-GRANTSDALE_PCI E, DMI
12	GMCH-GRANTSDALE_INT VGA
13	GMCH-GRANTSDALE_GND
14	GMCH-GRANTSDALE_PWR
15	DDR CHANNEL A
16	DDR CHANNEL B
17	DDR TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH6 PCI, USB, DMI, LAN
20	ICH6 IDE, GPIO, SATA, CTRL
21	ICH6 VCC, GND
22	CLK GEN

SHEET	TITLE
23	PCI SLOT
24	PCI EXPRESS*1 SLOT
25	ITE8712HX
26	HWMO/FAN/FWH BIOS
27	KB_MS/GAME
28	COM/LPT/FDD
29	(FRONT+REAR)USB/RING/IDE
30	AZALIA CODEC ALC880/CMI9880
31	AUDIO JACK
32	LAN BCM5705E/5751
33	LAN BCM5751
34	ATX POWER CONN.
35	ALL POWER
36	1394 TSB43AB23
37	FRONT PANEL/BZ
38	RAID VIA6410
39	RAID IDE CONNECTOR
40	GPIO TABLE
41	RESET TABLE

 COMPONENT SIDE
 (1 oz. Copper)
 VCC SIDE
 (1 oz. Copper)
 GND SIDE
 (1 oz. Copper)
 SOLDER SIDE
 (1 oz. Copper)

GIGABYTE			
Title Cover Sheet			
Size	Document Number	8I915P DUO PRO	Rev
Custom			1.2
Date:	星期四, 八月 26, 2004	Sheet	1 of 41

BLOCK DIAGRAM

INTEL Pentium4 LGA775

PAGE 4, 5, 6, 7

V_{CORE} = 1.75V / SLEEP: 1.3V
V_{CC3}

CLOCK GENERATOR

PAGE 22

CKVDD = 3.3V

PWM/OTHER POWER

PAGE 8, 34, 35

V_{CORE} = 1.75V (650-1100MHz) / SLEEP: 1.3V
5V_{SB}-12V+12V,V_{CC},V_{CC3},3V_{DUAL}
V_{TT},DDR2,5V_{STR}

GMCH GRANTS DALE

PAGE 9,10,11,12,13,14

V_{CORE} = 1.75V / SLEEP: 1.3V
2.5V_{STR} = 2.5V(MEMORY,SUSPEND POWER)
V_{DDQ} = 1.5V (AGP POWER 4X, HUBLINK)

CHANNEL A
DDR II DIMM X 1
DDR I DIMM X 1

PAGE 15,17

2.5V_{STR} = 2.5V(MEMORY,SUSPEND POWER)
V_{TT},DDR = 1.25V

CHANNEL B
DDR II DIMM X 1
DDR I DIMM X 1

PAGE 16,17

2.5V_{STR} = 2.5V(MEMORY,SUSPEND POWER)
V_{TT},DDR = 1.25V

PCI EXPRESS BY 16 PORTS

PAGE 18

V_{DDQ} = 1.5V (AGP POWER 4X)
V_{CC3} = 3.3V
+12V = 12V
3V_{DUAL} = 3.3V
V_{CC} = 5V

GAD0-31
ADSTB0,ADSTB0-
ADSTB1,ADSTB1-
SBA0-7
SBSTB,SBSTB-
GCBE0-3-
ST0-2

MAA0-14
MAA_CPC1-5
MAB_CPC1-5
MDD0-63
-DQSD0-7
DM0-7

USB PORTS 0~7

PAGE 29

V_{CC} = 5V
5V_{SB} = 5V
5V_{USB} = 5V

ICH6

PAGE 19,20,21

V_{CC2S} = 2.5V(I/O,MEMORY,VLINK)
3V_{DUAL} = 3.3V(SUSPEND POWER)
V_{CC3} = 3.3V
RTVDD = 3.3V

IDE Primary

PAGE 29

V_{CC} = 5V

SERIAL ATA

PAGE 20

V_{CC} = 5V

AC97/Azalia ALC880/CMI9880

PAGE 30

+12V = 12V
V_{CC3} = 3.3V
V_{CC} = 5V
AVDD = 5V

AZALIA LINK

AUDIO PORTS : FRONT AUDIO

PAGE 31

LIN_ OUT LINE_IN MIC
TELE CD_IN AUX_IN

FRONT PANEL/BZ

PAGE 37

V_{CC} = 5V
5V_{SB} = 5V
+12V = 12V
PVCC = 5V

**PCI SLOT
PCI EXPRESS SLOT**

PAGE 23,24

+12 = 12V
-12 = -12V
V_{CC} = 5V
V_{CC3} = 3.3V
3V_{DUAL} = 3V

LAN BCM5721/5751

PAGE 32,33

1394 IT TSB43AB23

PAGE 36

RAID VIA6410

PAGE 38,39

FWH/HWMO

PAGE 26

V_{CC} = 5V
V_{CC3} = 3V

LPC ITE8712HX

PAGE 25

V_{CC} = 5V
5V_{SB} = 5V
VBAT = 3V

I/O PORTS :

PAGE 27,28,29

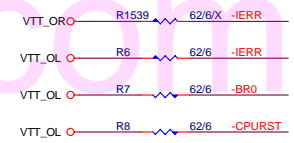
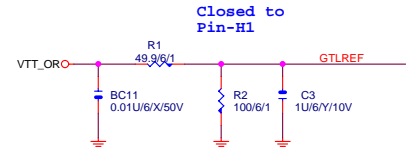
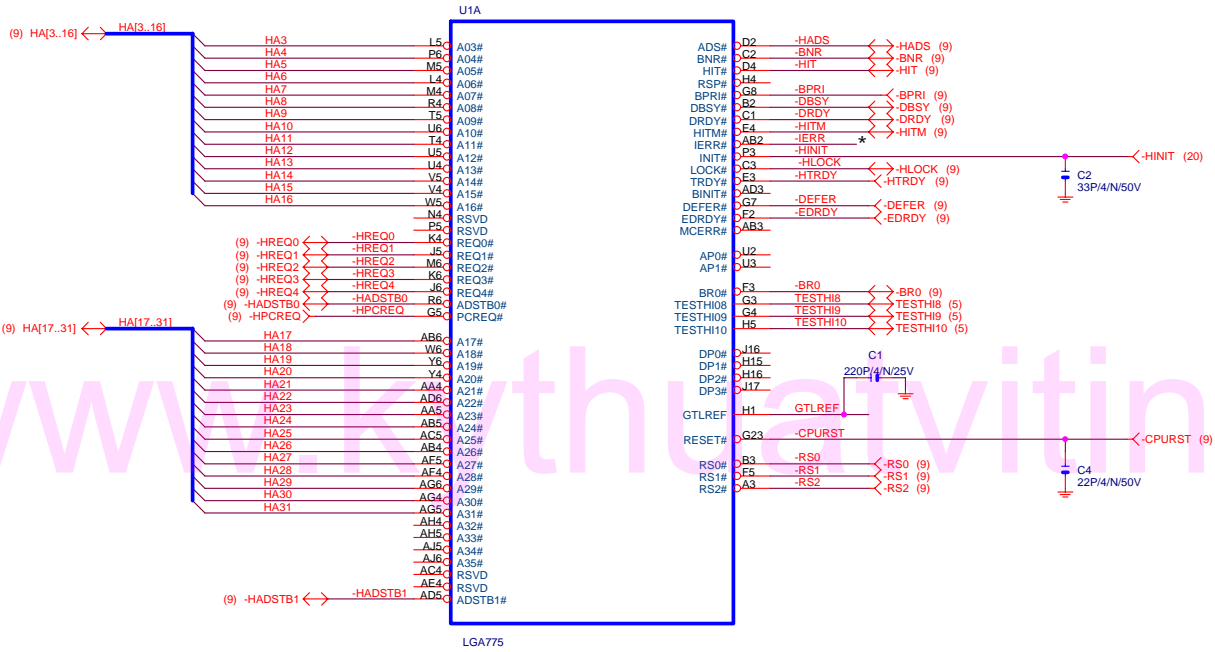
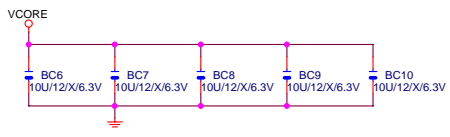
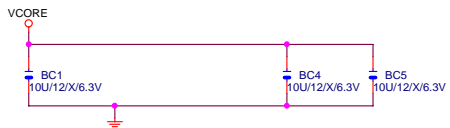
COMA COMB LPT PS2 IR FDD

HL0-10
CONTROL BUS HUB LINK

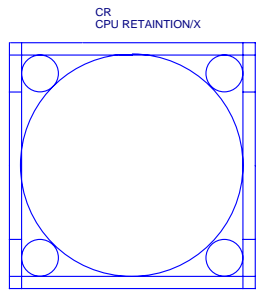
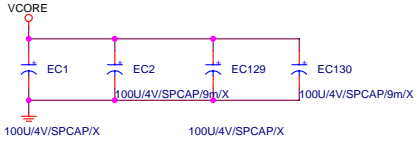
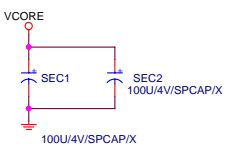
PCI BUS

LPC BUS

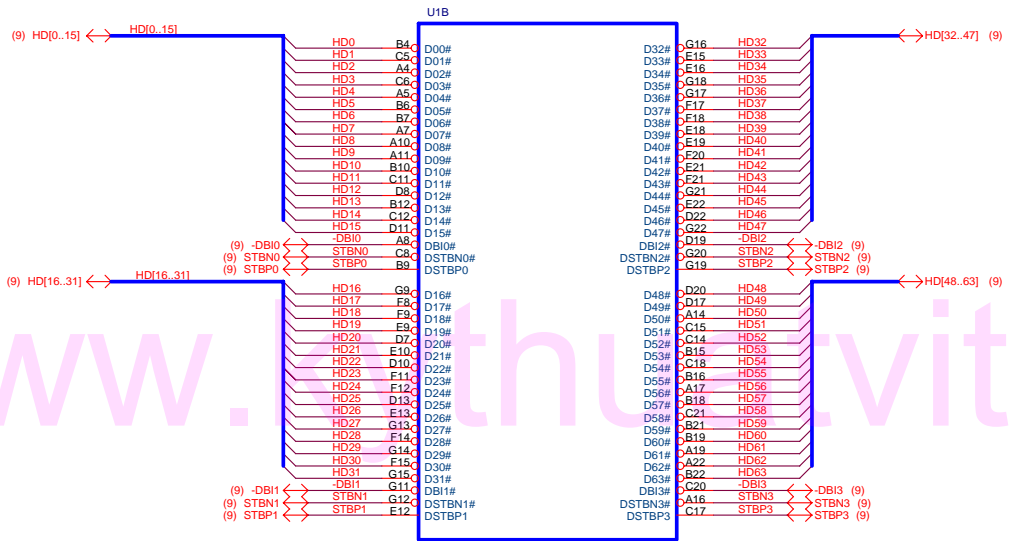
GIGABYTE			
BOM & PCB MODIFY HISTORY			
File			
Size	Document Number	81915P DUO PRO	Rev
Customer			1.2
Date	8/10/04	Sheet	2 of 41



SP-CAP X 4PCS



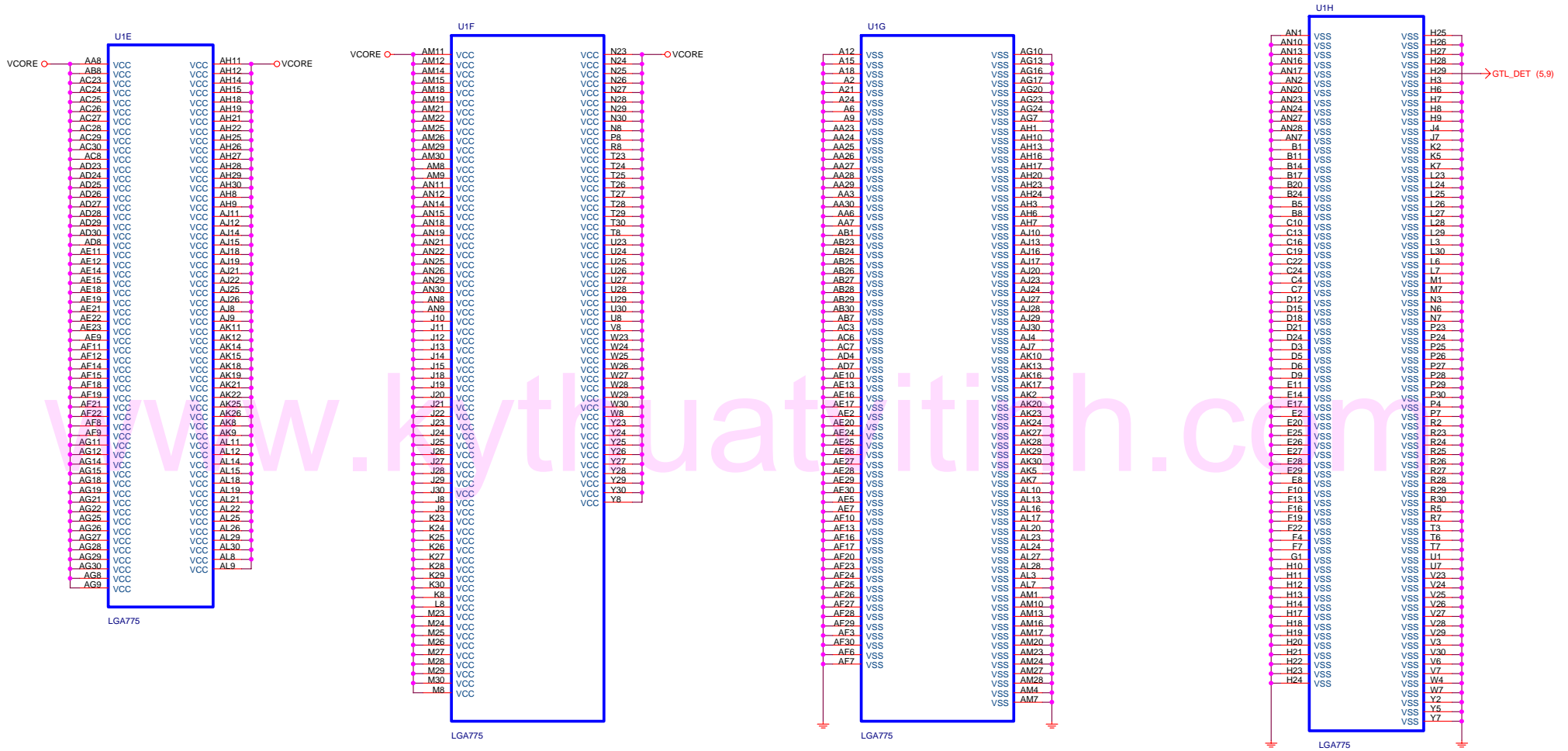
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Title P4_LGA775-A		
Size Custom	Document Number 8I915P DUO PRO	Rev 1.2
Date: 星期四, 八月 26, 2004	Sheet 4	of 41

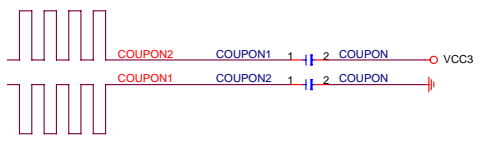
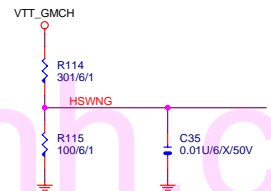
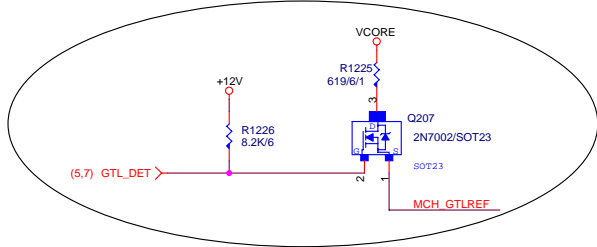
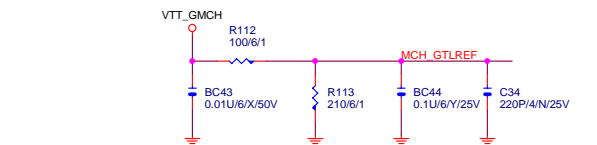
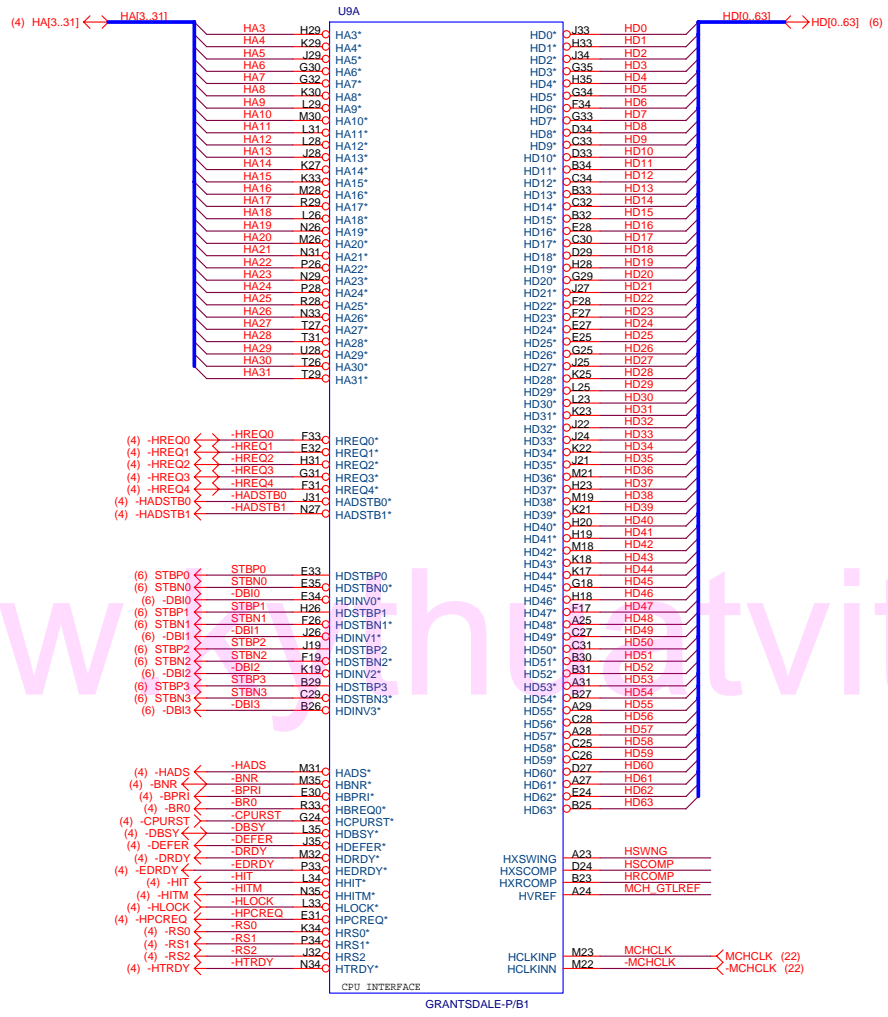


LGA775

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GIGABYTE		
Title P4_LGA775-C		
Size Custom	Document Number 8I915P DUO PRO	Rev 1.2
Date: 星期四, 八月 26, 2004	Sheet 6	of 41

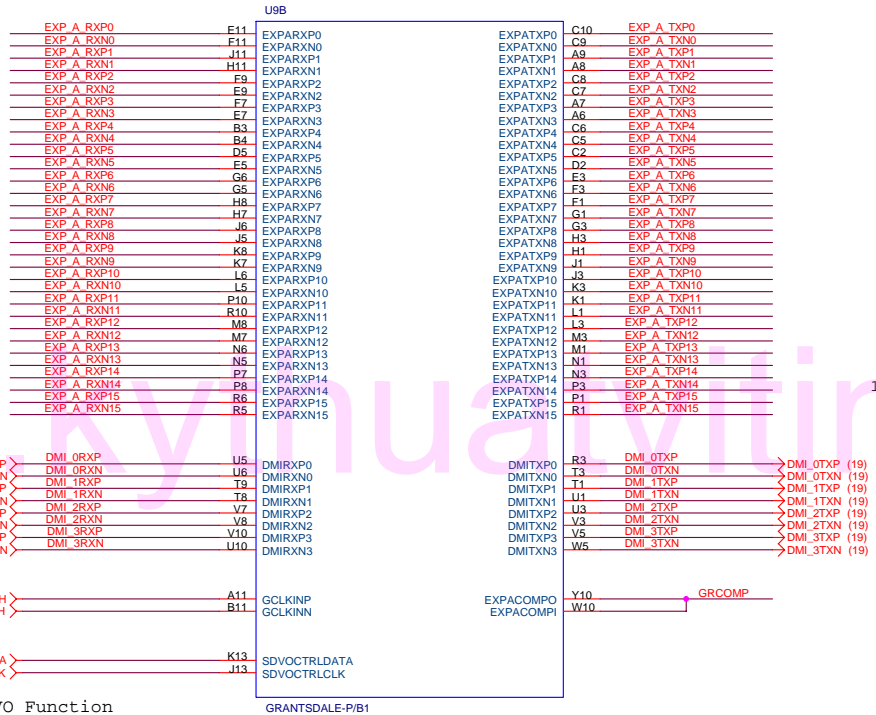




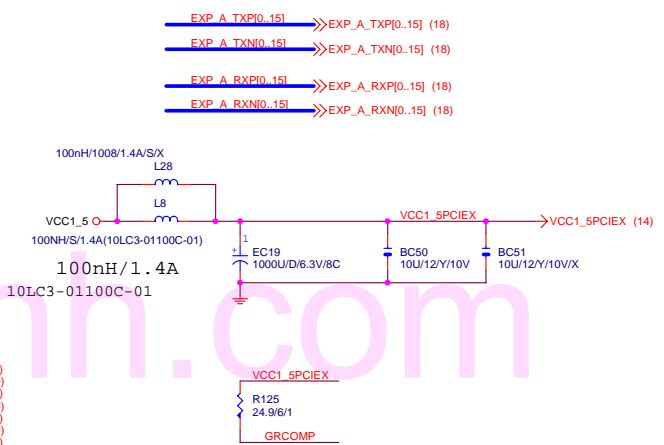
GIGABYTE			
Title GMCH-HOST			
Size Custom	Document Number 8I915P DUO PRO	Rev 1.2	
Date: 星期四, 八月 26, 2004	Sheet 9	of 41	

USE		GRANTS DALE-P/B1	
(15,17) MAA0_SBA1	AP26	SAMA0/SABA1	SADQ50
(15,17) MAA1	AR24	SAMA1/RSV	SADQ50*/RSV
(15,17) MAA2	AL24	SAMA2/RSV	SADQ51
(15,17) MAA3	AP23	SAMA3/RSV	SADQ51*/RSV
(15,17) MAA4	AP22	SAMA4/RSV	SADQ52
(15,17) MAA5_MAA1	AP22	SAMA5/SAMA1	SADQ52*/RSV
(15,17) MAA6_MAA10	AP22	SAMA6/SAMA10	SADQ53
(15,17) MAA7_MAA3	AP21	SAMA7/SAMA3	SADQ53*/RSV
(15,17) MAA8_MAA0	AN22	SAMA8/SAMA0	SADQ54
(15,17) MAA9_MAA2	AN22	SAMA9/SAMA2	SADQ54*/RSV
(15,16,17) MAA10_SBA80	AM27	SAMA10/SBA80	SADQ55
(15,17) MAA11_MAA4	AM21	SAMA11/SAMA4	SADQ55*/RSV
(15,17) MAA12_MAA6	AR20	SAMA12/SAMA6	SADQ56
(15,17) MAA13_SWEA	AP31	SAMA13/SAVE*	SADQ56*/RSV
(15,17) SWEA_SBA0	AN23C	SAWE*/SABA0	SADQ57
(15,16,17) SBAA10_SBA8	AN23C	SACA5*/SARAS*	SADQ57*/RSV
(15,16,17) SBAA11_SBA8	AN23C	SACA6*/SARAS*	SADQ58
(15,16,17) SBAA12_SBA8	AN23C	SACA7*/SARAS*	SADQ58*/RSV
(15,16,17) SBAA13_SBA8	AN23C	SACA8*/SARAS*	SADQ59
(15,16,17) SBAA14_SBA8	AN23C	SACA9*/SARAS*	SADQ59*/RSV
(15,16,17) SBAA15_SBA8	AN23C	SACA10*/SARAS*	SADQ60
(15,16,17) SBAA16_SBA8	AN23C	SACA11*/SARAS*	SADQ60*/RSV
(15,16,17) SBAA17_SBA8	AN23C	SACA12*/SARAS*	SADQ61
(15,16,17) SBAA18_SBA8	AN23C	SACA13*/SARAS*	SADQ61*/RSV
(15,16,17) SBAA19_SBA8	AN23C	SACA14*/SARAS*	SADQ62
(15,16,17) SBAA20_SBA8	AN23C	SACA15*/SARAS*	SADQ62*/RSV
(15,16,17) SBAA21_SBA8	AN23C	SACA16*/SARAS*	SADQ63
(15,16,17) SBAA22_SBA8	AN23C	SACA17*/SARAS*	SADQ63*/RSV
(15,16,17) SBAA23_SBA8	AN23C	SACA18*/SARAS*	SADQ64
(15,16,17) SBAA24_SBA8	AN23C	SACA19*/SARAS*	SADQ64*/RSV
(15,16,17) SBAA25_SBA8	AN23C	SACA20*/SARAS*	SADQ65
(15,16,17) SBAA26_SBA8	AN23C	SACA21*/SARAS*	SADQ65*/RSV
(15,16,17) SBAA27_SBA8	AN23C	SACA22*/SARAS*	SADQ66
(15,16,17) SBAA28_SBA8	AN23C	SACA23*/SARAS*	SADQ66*/RSV
(15,16,17) SBAA29_SBA8	AN23C	SACA24*/SARAS*	SADQ67
(15,16,17) SBAA30_SBA8	AN23C	SACA25*/SARAS*	SADQ67*/RSV
(15,16,17) SBAA31_SBA8	AN23C	SACA26*/SARAS*	SADQ68
(15,16,17) SBAA32_SBA8	AN23C	SACA27*/SARAS*	SADQ68*/RSV
(15,16,17) SBAA33_SBA8	AN23C	SACA28*/SARAS*	SADQ69
(15,16,17) SBAA34_SBA8	AN23C	SACA29*/SARAS*	SADQ69*/RSV
(15,16,17) SBAA35_SBA8	AN23C	SACA30*/SARAS*	SADQ70
(15,16,17) SBAA36_SBA8	AN23C	SACA31*/SARAS*	SADQ70*/RSV
(15,16,17) SBAA37_SBA8	AN23C	SACA32*/SARAS*	SADQ71
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(15,16,17) SBAA39_SBA8	AN23C	SACA34*/SARAS*	SADQ72
(15,16,17) SBAA40_SBA8	AN23C	SACA35*/SARAS*	SADQ72*/RSV
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(15,16,17) SBAA42_SBA8	AN23C	SACA37*/SARAS*	SADQ73*/RSV
(15,16,17) SBAA43_SBA8	AN23C	SACA38*/SARAS*	SADQ74
(15,16,17) SBAA44_SBA8	AN23C	SACA39*/SARAS*	SADQ74*/RSV
(15,16,17) SBAA45_SBA8	AN23C	SACA40*/SARAS*	SADQ75
(15,16,17) SBAA46_SBA8	AN23C	SACA41*/SARAS*	SADQ75*/RSV
(15,16,17) SBAA47_SBA8	AN23C	SACA42*/SARAS*	SADQ76
(15,16,17) SBAA48_SBA8	AN23C	SACA43*/SARAS*	SADQ76*/RSV
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(15,16,17) SBAA74_SBA8	AN23C	SACA69*/SARAS*	SADQ89*/RSV
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(15,16,17) SBAA77_SBA8	AN23C	SACA72*/SARAS*	SADQ91
(15,16,17) SBAA78_SBA8	AN23C	SACA73*/SARAS*	SADQ91*/RSV
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(15,16,17) SBAA80_SBA8	AN23C	SACA75*/SARAS*	SADQ92*/RSV
(15,16,17) SBAA81_SBA8	AN23C	SACA76*/SARAS*	SADQ93
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(15,16,17) SBAA84_SBA8	AN23C	SACA79*/SARAS*	SADQ94*/RSV
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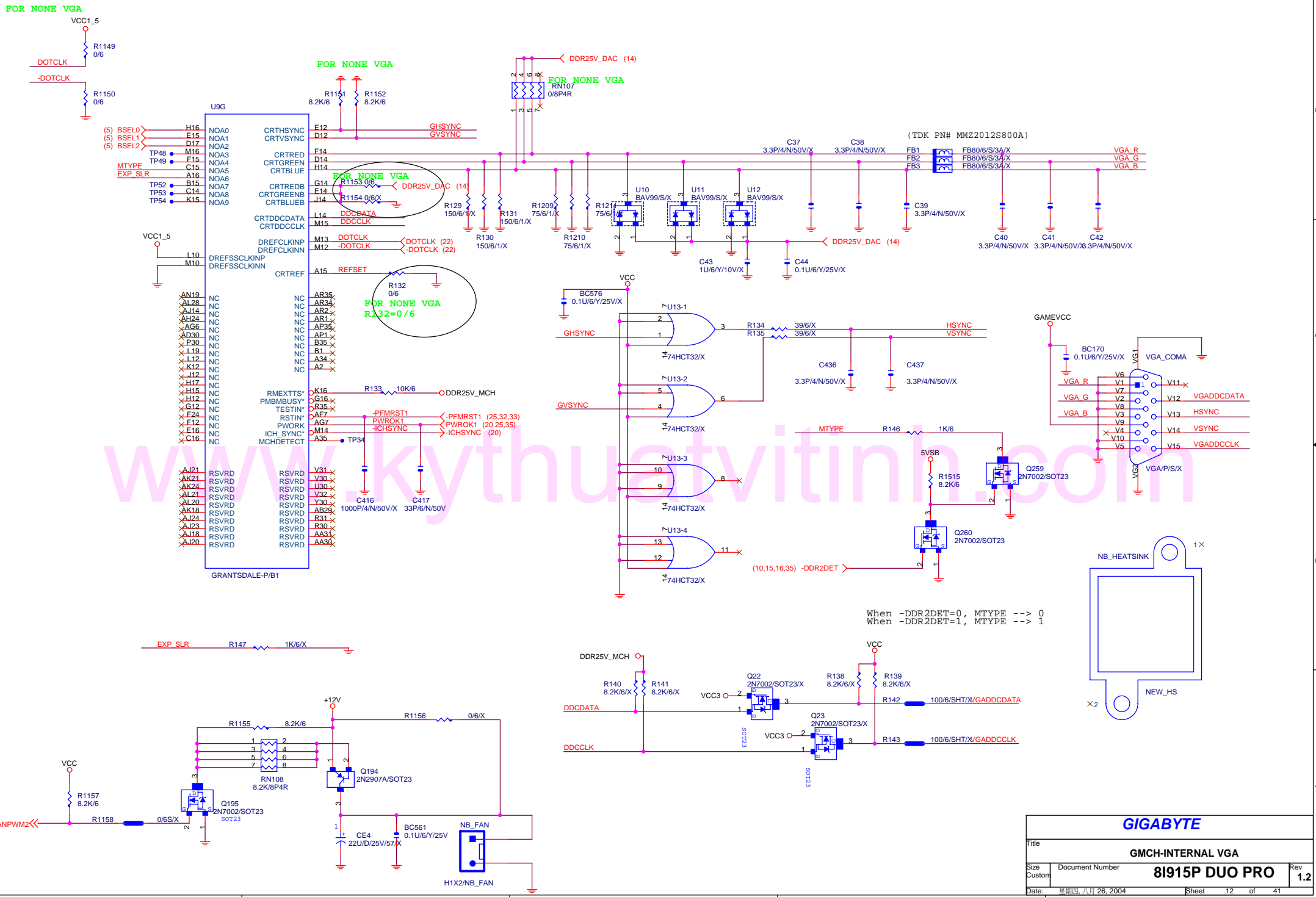
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(15,16,17) MAB2_MAB5	AN15	SAMA2/SAMA5	SBDQ31
(15,16,17) MAB3_MAB7	AL15	SAMA3/SAMA7	SBDQ31*/RSV
(15,16,17) MAB4_MAB9	AP14	SAMA4/SAMA8	SBDQ32
(15,16,17) MAB5_MAB9	AM12	SAMA5/SAMA10	SBDQ32*/RSV
(15,16,17) MAB6_MAA12	AP13	SAMA6/SAMA12	SBDQ33
(15,16,17) MAB7_CKEA0	AL12	SAMA7/SAMA0	SBDQ33*/RSV
(15,16,17) MAB8_MAB9	AN13	SAMA8/SAMA9	SBDQ34
(15,16,17) MAB9_MAB11	AP15	SAMA9/SAMA11	SBDQ34*/RSV
(15,16,17) MAB10_MAA11	AP11	SAMA10/SAMA11	SBDQ35
(15,16,17) MAB11_CKEA2	AR11	SAMA11/SACK2E	SBDQ35*/RSV
(15,16,17) MAB12_CKEA3	AL13	SAMA12/SACK3E	SBDQ36
(15,16,17) MAB13_CS3A3	AL33	SAMA13/SAC3S*	SBDQ36*/RSV
(15,16,17) SBWE_MAB6	AP17C	SBWE*/SBMA6	SBDQ37
(15,16,17) SCASB_MAB1	AP18C	SCAS*/SBMA1	SBDQ37*/RSV
(15,16,17) SRASB_MAB2	AN17C	SRAS*/SBMA2	SBDQ38
(15,16,17) SBAB0_MAB3	AR16	SBAB0/SBMA3	SBDQ38*/RSV
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(15,16,17) SBAB2_CKEA1	AN11	SBAB2/SACK2E	SBDQ39*/RSV
(15,16,17) CSB0_CS8	AN33C	CSB0*/SBCS2*	SBDQ40
(15,16,17) CSB1_CS0	AM34C	SBCS1*/SACS0*	SBDQ40*/RSV
(15,16,17) CSB2_CS80	AP34C	SBCS2*/SACS0*	SBDQ41
(15,16,17) CSB3_CS81	AN34C	SBCS3*/SBCS1*	SBDQ41*/RSV
(15,16,17) CKEB0_CKEB2	AP10	SCKE0/SBCKE*	SBDQ42
(15,16,17) CKEB1_CKEB0	AN10	SCKE1/SBCKE0	SBDQ42*/RSV
(15,16,17) CKEB2_CKEB3	AR9	SCKE2/SBCKE3	SBDQ43
(15,16,17) CKEB3_CKEB1	AM9	SCKE3/SBCKE1	SBDQ43*/RSV
(15,16,17) MODTB0_CS83	AM33	SBODT0/SBCS3*	SBDQ44
(15,16,17) MODTB1_CS84	AL34	SBODT1/SACAS*	SBDQ44*/RSV
(15,16,17) MODTB2_CS81	AL35	SBODT2/SACAS1*	SBDQ45
(15,16,17) MODTB3_CS82	AK34	SBODT3/SAC3S2*	SBDQ45*/RSV
(15,16,17) DQSA0	AG1	DQSA0	SBDQ50
(15,16,17) DQSA1	AG2	DQSA1	SBDQ50*/RSV
(15,16,17) DQSA2	AG3	DQSA2	SBDQ51
(15,16,17) DQSA3	AG4	DQSA3	SBDQ51*/RSV
(15,16,17) DQSA4	AG5	DQSA4	SBDQ52
(15,16,17) DQSA5	AG6	DQSA5	SBDQ52*/RSV
(15,16,17) DQSA6	AG7	DQSA6	SBDQ53
(15,16,17) DQSA7	AG8	DQSA7	SBDQ53*/RSV
(15,16,17) DQSA8	AG9	DQSA8	SBDQ54
(15,16,17) DQSA9	AG10	DQSA9	SBDQ54*/RSV
(15,16,17) DQSA10	AG11	DQSA10	SBDQ55
(15,16,17) DQSA11	AG12	DQSA11	SBDQ55*/RSV
(15,16,17) DQSA12	AG13	DQSA12	SBDQ56
(15,16,17) DQSA13	AG14	DQSA13	SBDQ56*/RSV
(15,16,17) DQSA14	AG15	DQSA14	SBDQ57
(15,16,17) DQSA15	AG16	DQSA15	SBDQ57*/RSV
(15,16,17) DQSA16	AG17	DQSA16	SBDQ58
(15,16,17) DQSA17	AG18	DQSA17	SBDQ58*/RSV
(15,16,17) DQSA18	AG19	DQSA18	SBDQ59
(15,16,17) DQSA19	AG20	DQSA19	SBDQ59*/RSV
(15,16,17) DQSA20	AG21	DQSA20	SBDQ60
(15,16,17) DQSA21	AG22	DQSA21	SBDQ60*/RSV
(15,16,17) DQSA22	AG23	DQSA22	SBDQ61
(15,16,17) DQSA23	AG24	DQSA23	SBDQ61*/RSV
(15,16,17) DQSA24	AG25	DQSA24	SBDQ62
(15,16,17) DQSA25	AG26	DQSA25	SBDQ62*/RSV
(15,16,17) DQSA26	AG27	DQSA26	SBDQ63
(15,16,17) DQSA27	AG28	DQSA27	SBDQ63*/RSV
(15,16,17) DQSA28	AG29	DQSA28	SBDQ64
(15,16,17) DQSA29	AG30	DQSA29	SBDQ64*/RSV
(15,16,17) DQSA30	AG31	DQSA30	SBDQ65
(15,16,17) DQSA31	AG32	DQSA31	SBDQ65*/RSV
(15,16,17) DQSA32	AG33	DQSA32	SBDQ66
(15,16,17) DQSA33	AG34	DQSA33	SBDQ66*/RSV
(15,16,17) DQSA34	AG35	DQSA34	SBDQ67
(15,16,17) DQSA35	AG36	DQSA35	SBDQ67*/RSV
(15,16,17) DQSA36	AG37	DQSA36	SBDQ68
(15,16,17) DQSA37	AG38	DQSA37	SBDQ68*/RSV
(15,16,17) DQSA38	AG39	DQSA38	SBDQ69
(15,16,17) DQSA39	AG40	DQSA39	SBDQ69*/RSV
(15,16,17) DQSA40	AG41	DQSA40	SBDQ70
(15,16,17) DQSA41	AG42	DQSA41	SBDQ70*/RSV
(15,16,17) DQSA42	AG43	DQSA42	SBDQ71
(15,16,17) DQSA43	AG44	DQSA43	SBDQ71*/RSV
(15,16,17) DQSA44	AG45	DQSA44	SBDQ72
(15,16,17) DQSA45	AG46	DQSA45	SBDQ72*/RSV
(15,16,17) DQSA46	AG47	DQSA46	SBDQ73
(15,16,17) DQSA47	AG48	DQSA47	SBDQ73*/RSV
(15,16,17) DQSA48	AG49	DQSA48	SBDQ74
(15,16,17) DQSA49	AG50	DQSA49	SBDQ74*/RSV
(15,16,17) DQSA50	AG51	DQSA50	SBDQ75
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(15,16,17) DQSA52	AG53	DQSA52	SBDQ76
(15,16,17) DQSA53	AG54	DQSA53	SBDQ76*/RSV
(15,16,17) DQSA54	AG55	DQSA54	SBDQ77
(15,16,17) DQSA55	AG56	DQSA55	SBDQ77*/RSV
(15,16,17) DQSA56	AG57	DQSA56	SBDQ78
(15,16,17) DQSA57	AG58	DQSA57	SBDQ78*/RSV
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(15,16,17) DQSA6			



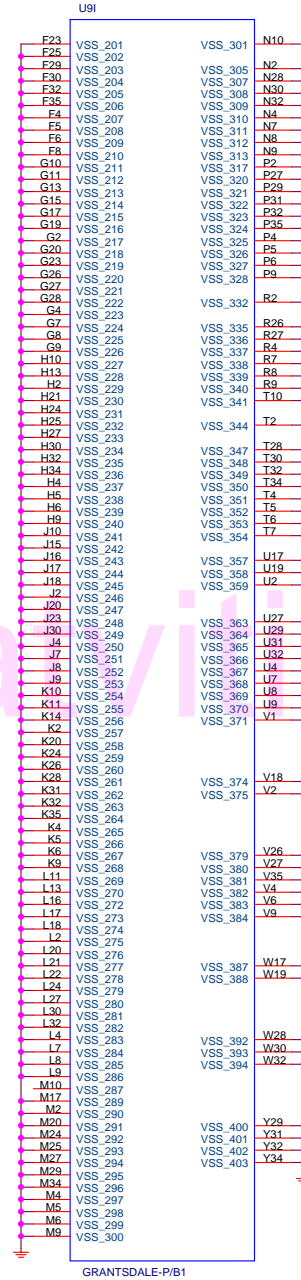
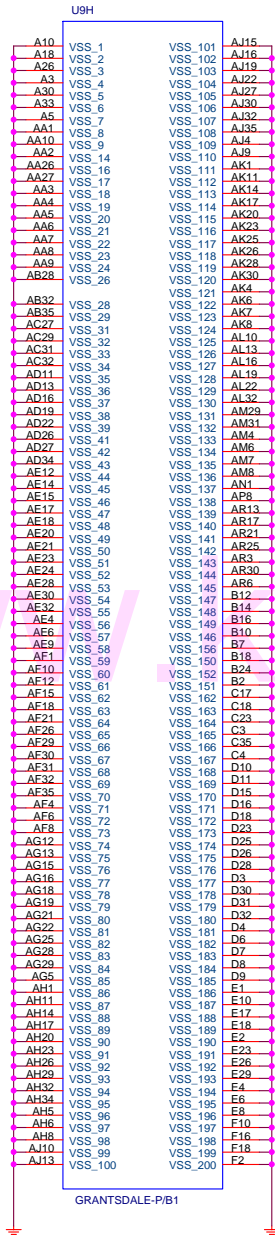
For DVO Function



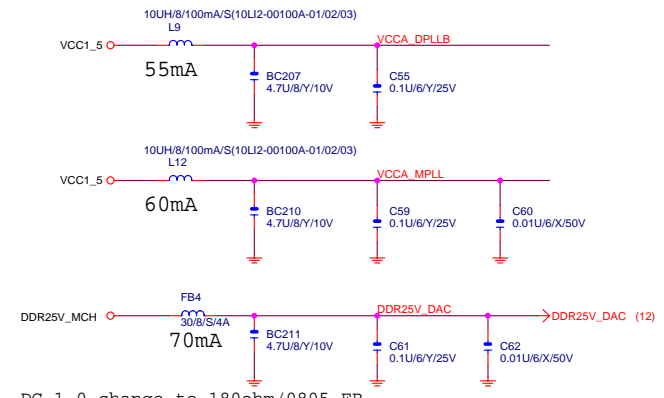
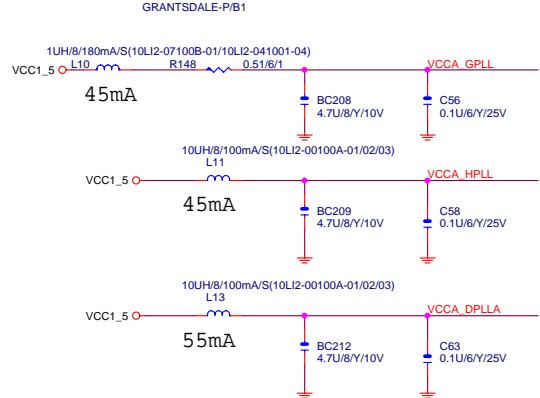
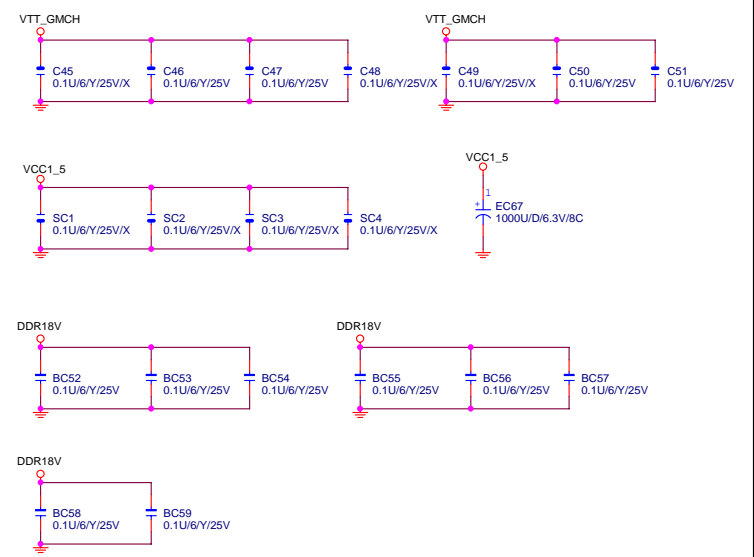
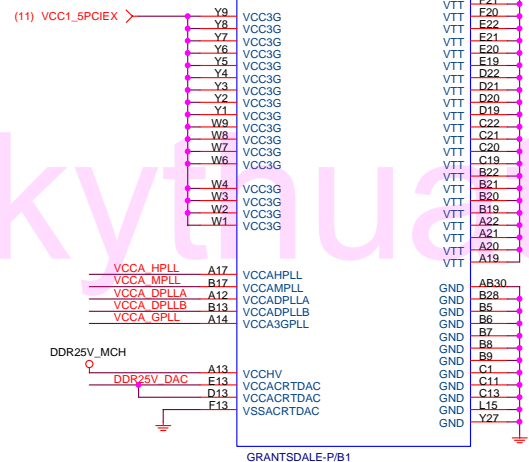
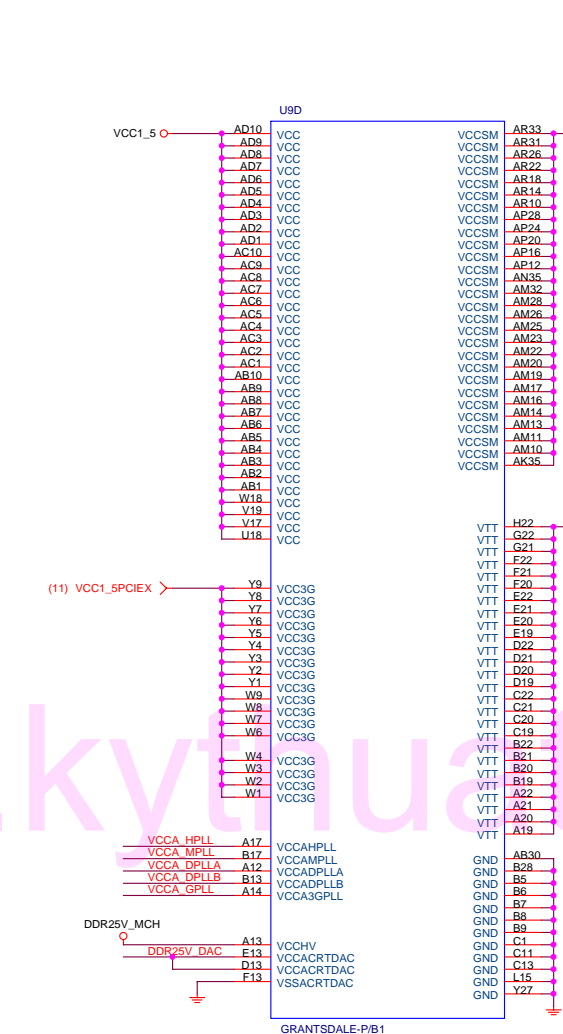
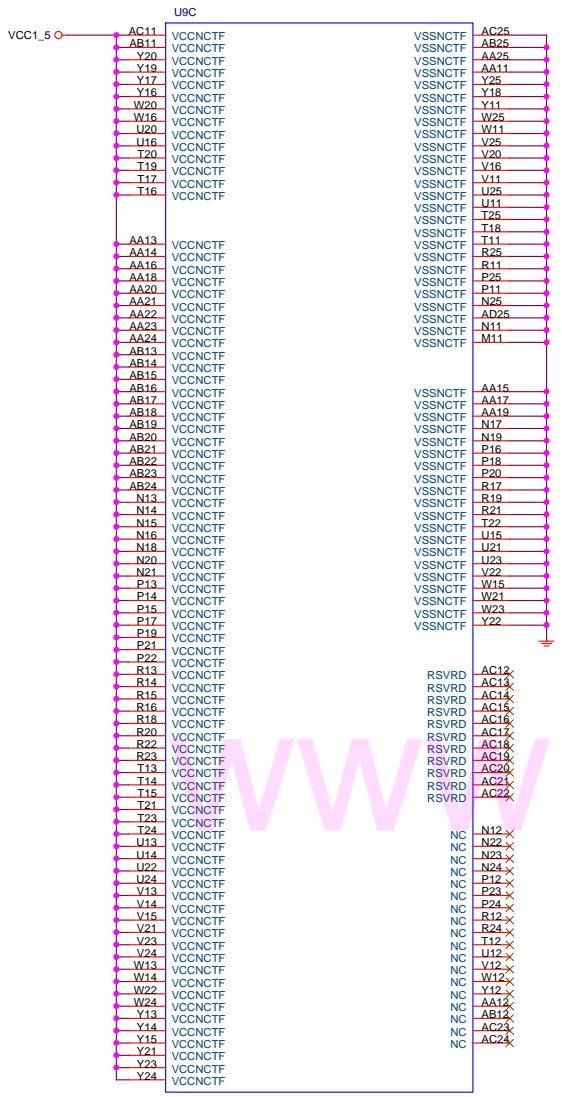
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GIGABYTE			
GMCH-INTERNAL VGA			
Title	Document Number		
Size	8I915P DUO PRO		
Custom	Rev 1.2		
Date:	星期四, 八月 26, 2004	Sheet	12 of 41

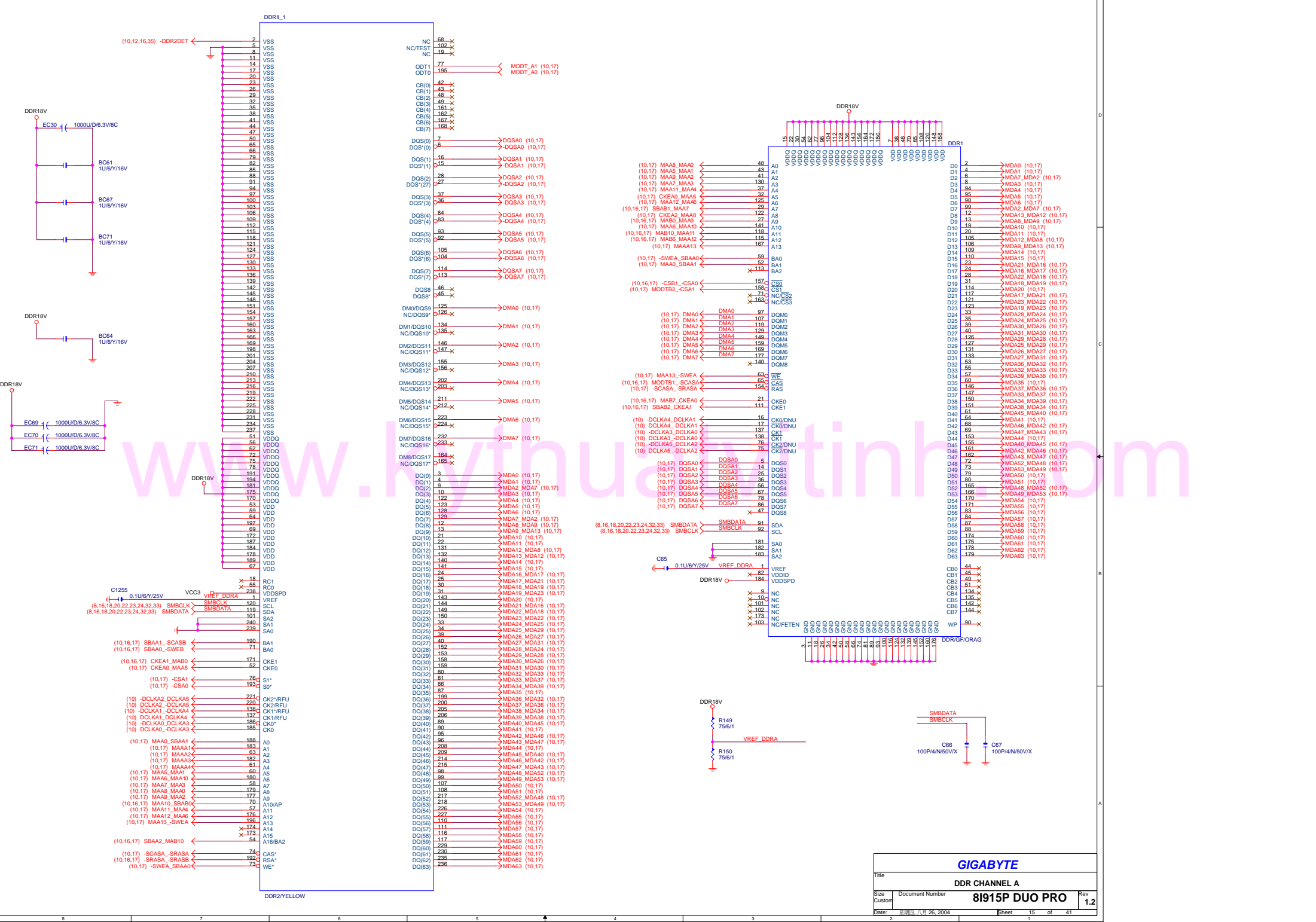


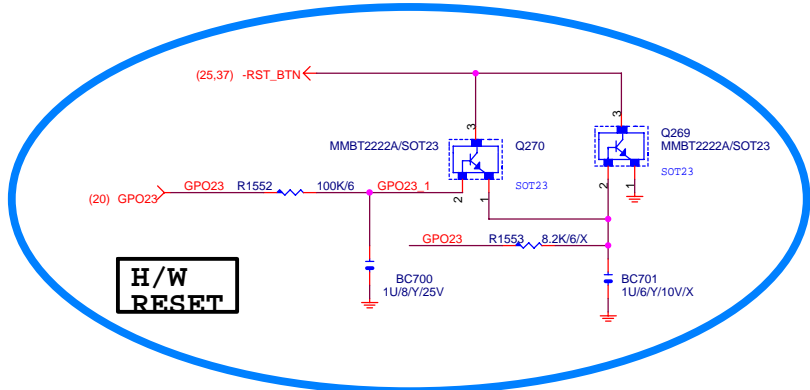
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Size	Document Number	8I915P DUO PRO	Rev
Custom			1.2
Date:	星期四, 八月 26, 2004	Sheet	13 of 41



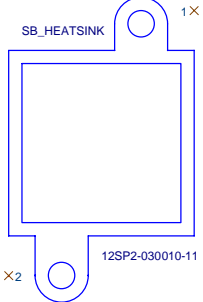
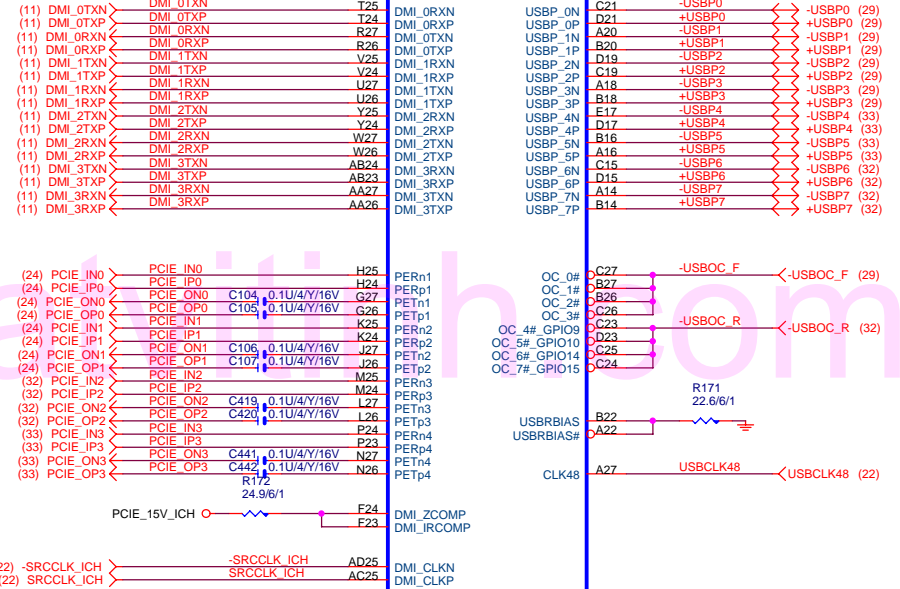
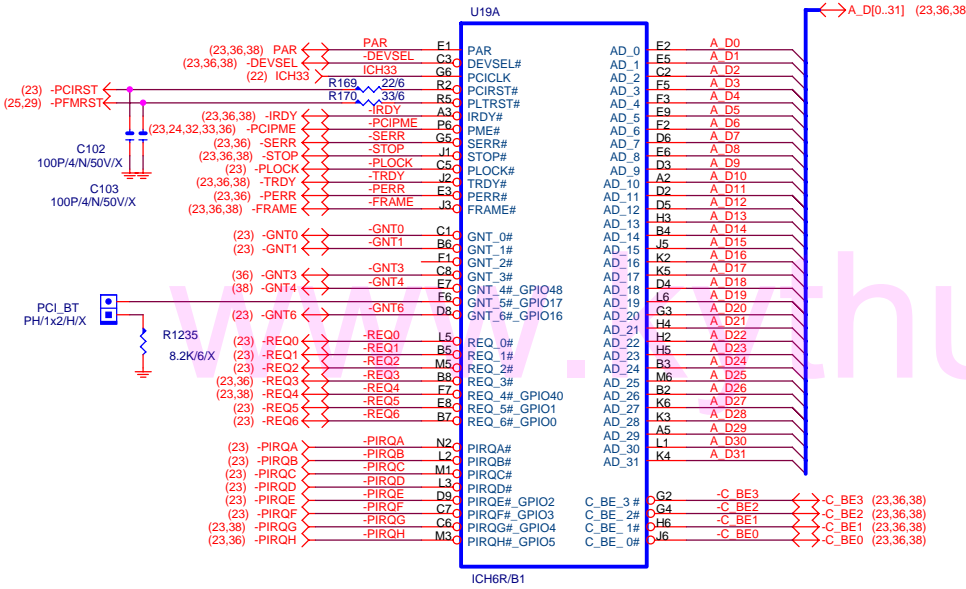
DG 1.0 change to 180ohm/0805 FB

GIGABYTE			
Title GMCH-PWR			
Size Custom	Document Number	8I915P DUO PRO	
Date: 星期四, 八月 26, 2004	Sheet	14	of 41
			Rev 1.2

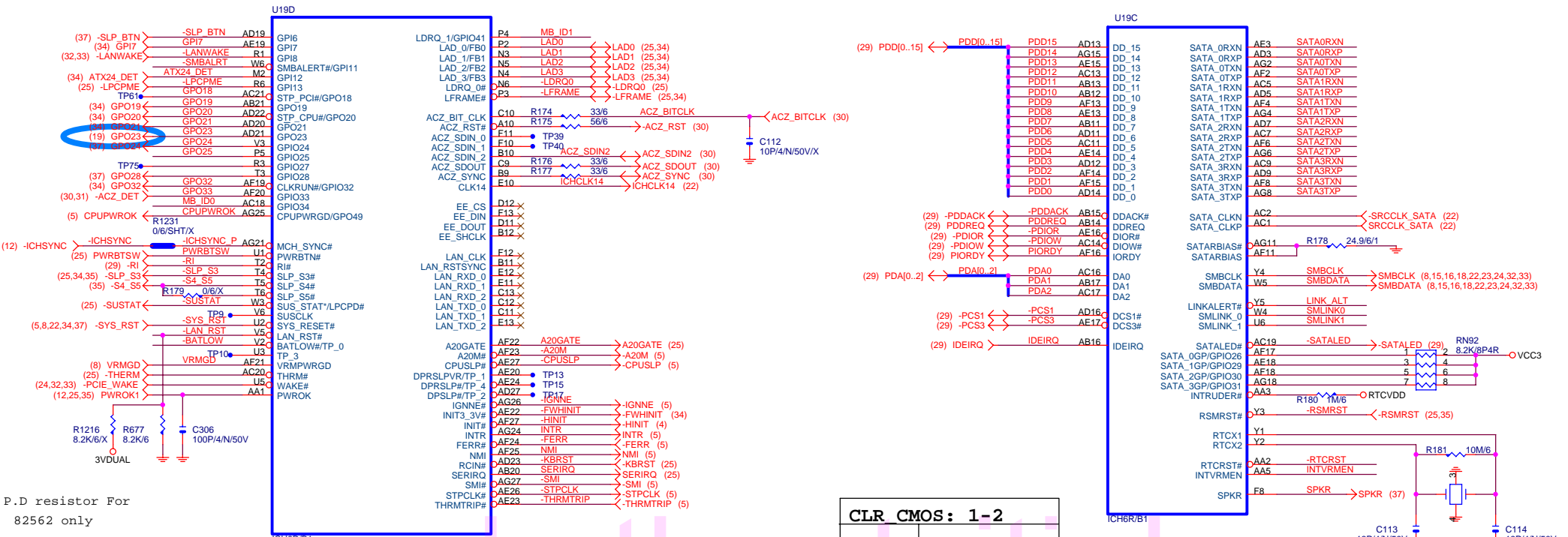




DMI Connection Note
 GMCH TX Pin Need Connect to ICH6 RX Pin
 ICH6 TX Pin Need Connect to GMCH RX Pin

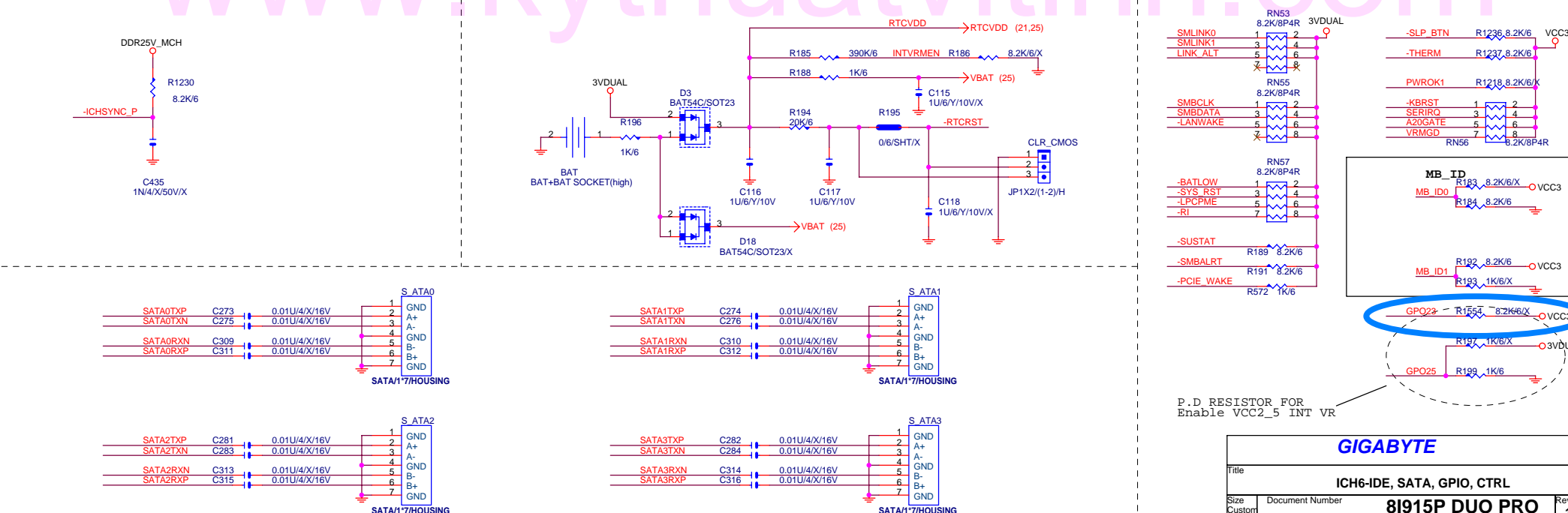


GIGABYTE			
Title			
ICH6-PCI, DMI, LAN, USB			
Size	Document Number	8I915P DUO PRO	
B		Rev	1.2
Date:	星期四, 八月 26, 2004	Sheet	19 of 41



P.D resistor For 82562 only

CLR CMOS: 1-2	
SHORT	CLEAR CMOS
OPEN	NORMAL

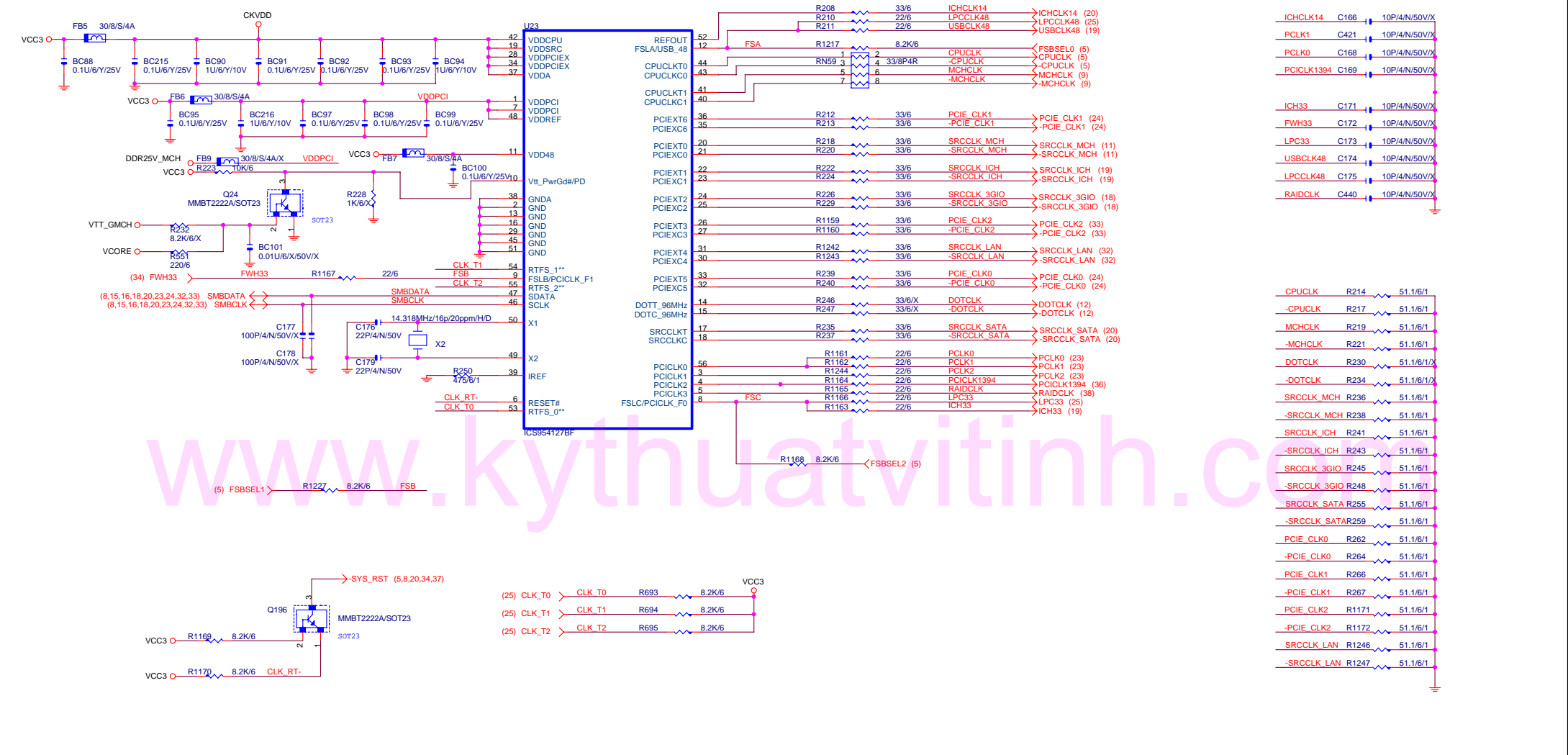


P.D RESISTOR FOR Enable VCC2_5 INT VR

GIGABYTE

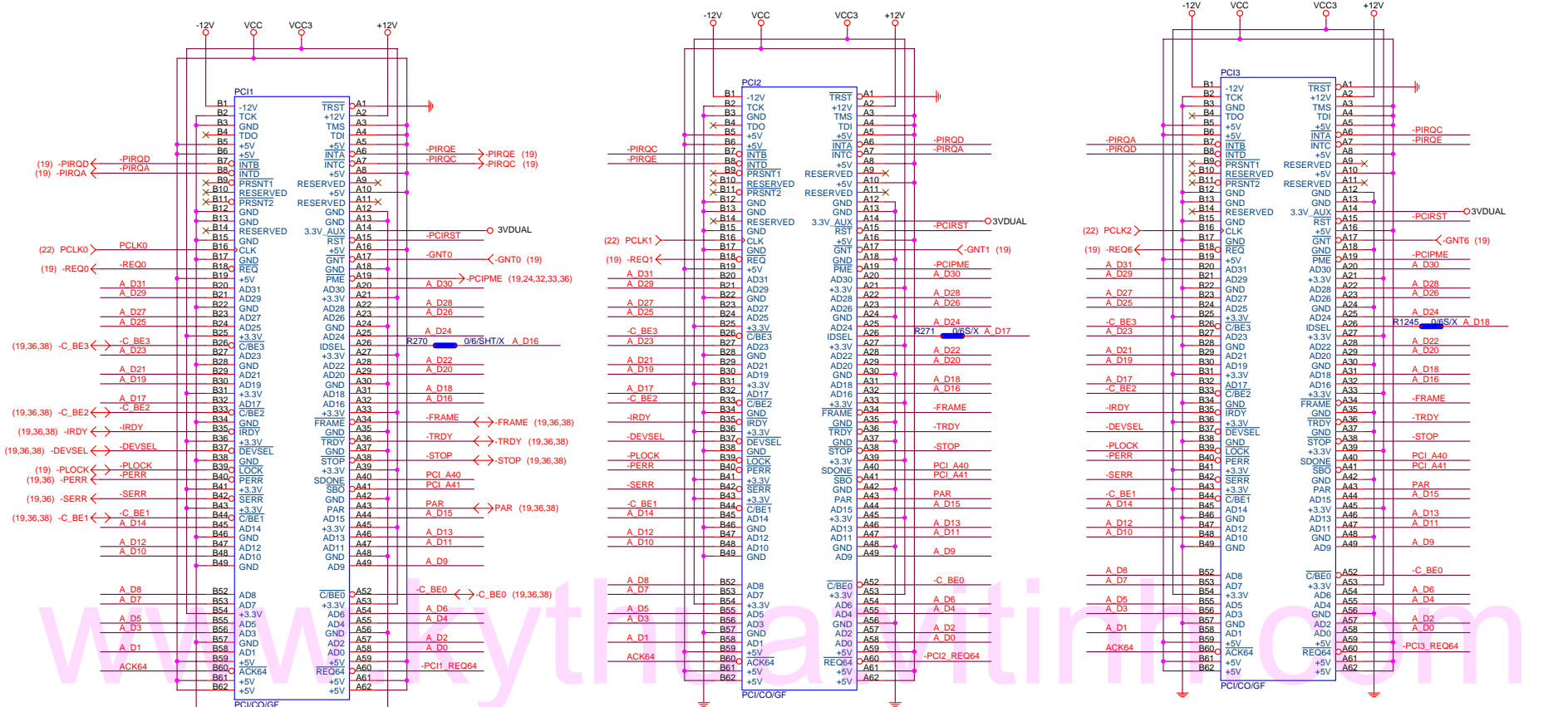
Title	ICH6-IDE, SATA, GPIO, CTRL	
Size	Document Number	81915P DUO PRO
Custom		Rev 1.2
Date:	星期四, 八月 26, 2004	Sheet 20 of 41

Need Change to X5R or X7R type

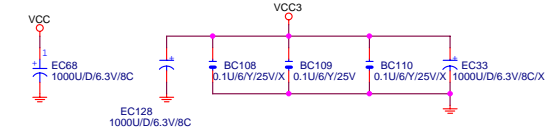
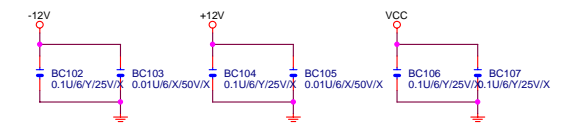
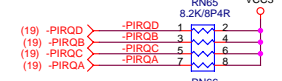
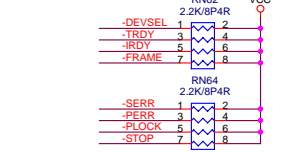
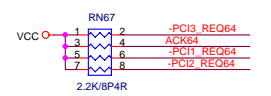
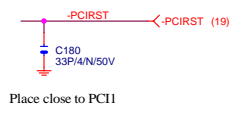
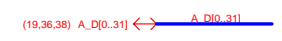


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GIGABYTE		
Title CK410M/GBT_CLK_GEN		
Size	Document Number	Rev
Custom	81915P DUO PRO	1.2
Date:	星期四, 八月 26, 2004	Sheet 22 of 41

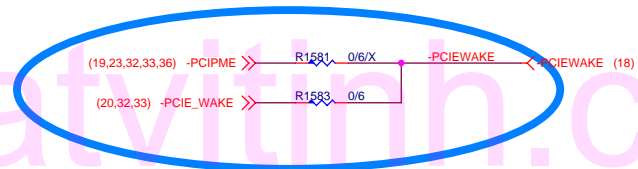
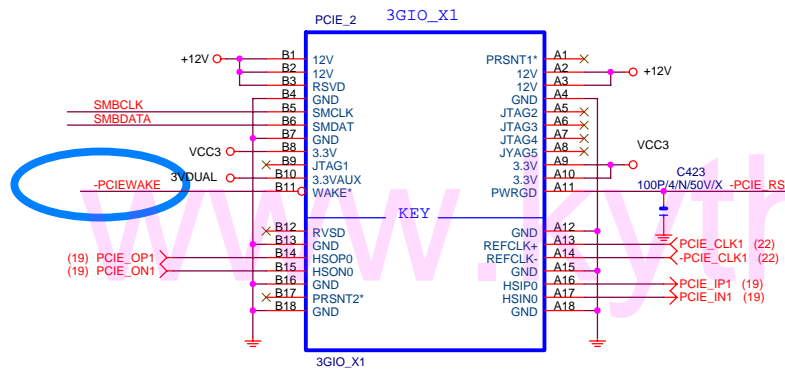
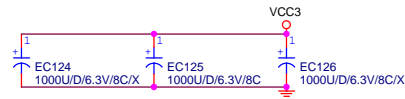
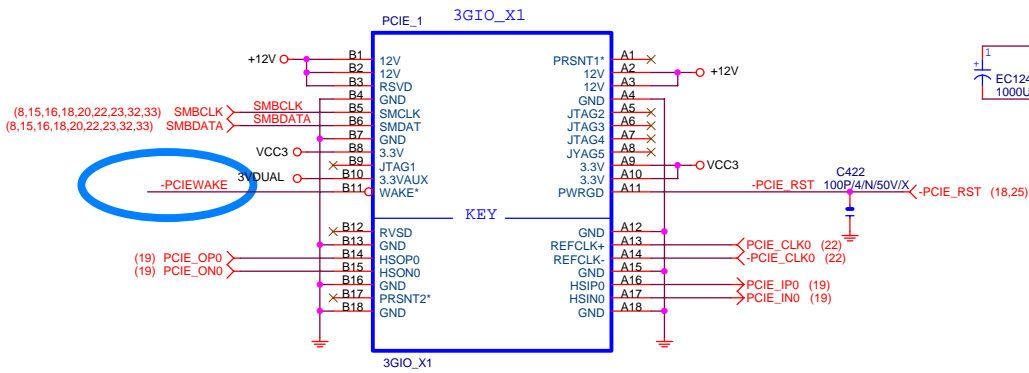


AD16 / -PIRQ (E-D-C-A) / -REQ0 / -GNT0



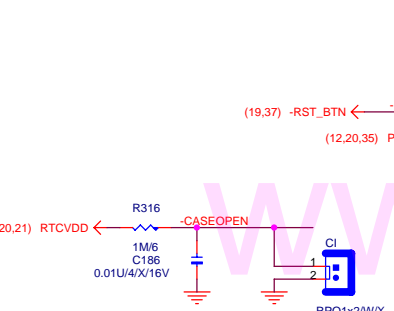
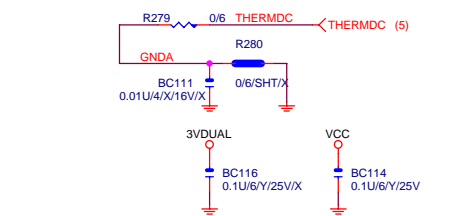
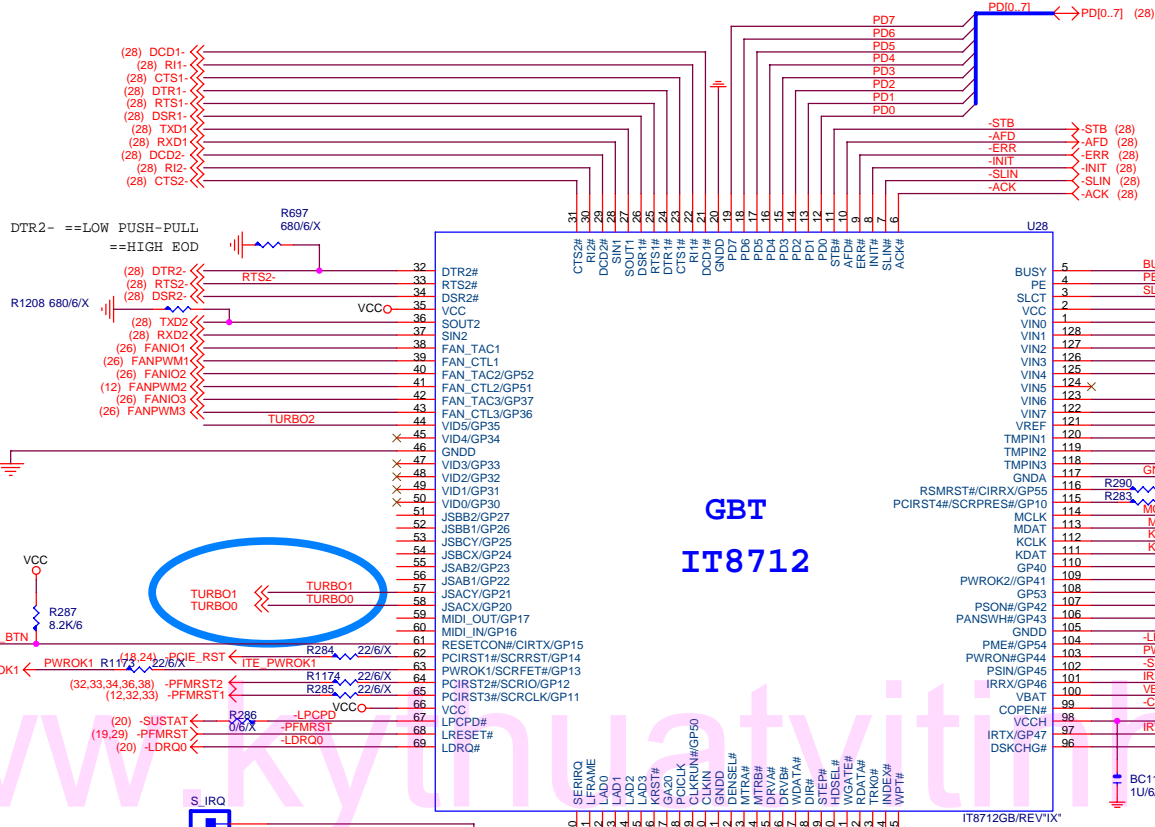
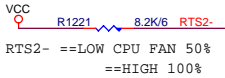
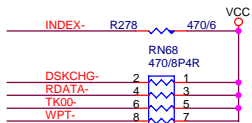
EC128 1000U/D/6.3V/8C

GIGABYTE		
PCI SLOT		
8I915P DUO PRO		
title	Document Number	Rev
		1.2
Date:	日期: 八月 26, 2004	Sheet 23 of 41

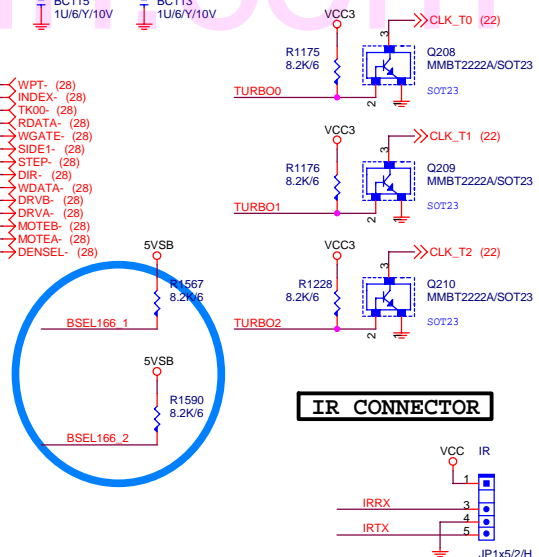
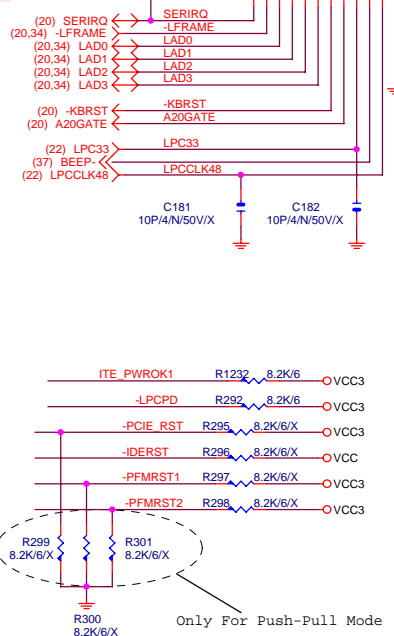
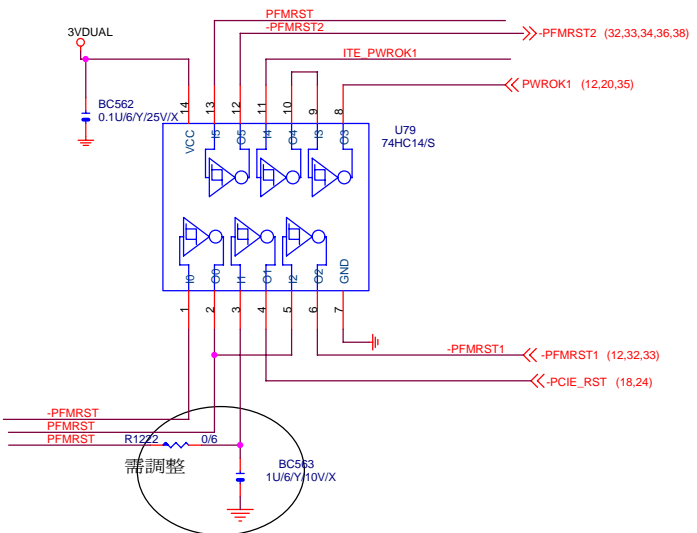


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Title			PCI E SLOT		
Size B			Document Number		Rev
			8I915P DUO PRO		1.2
Date: 星期四, 八月 26, 2004			Sheet 24 of 41		



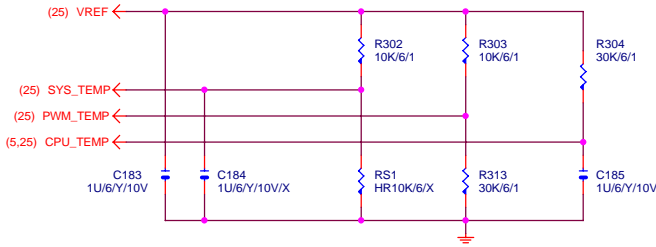
Case Open Circuits
CASE OPEN (N/A)



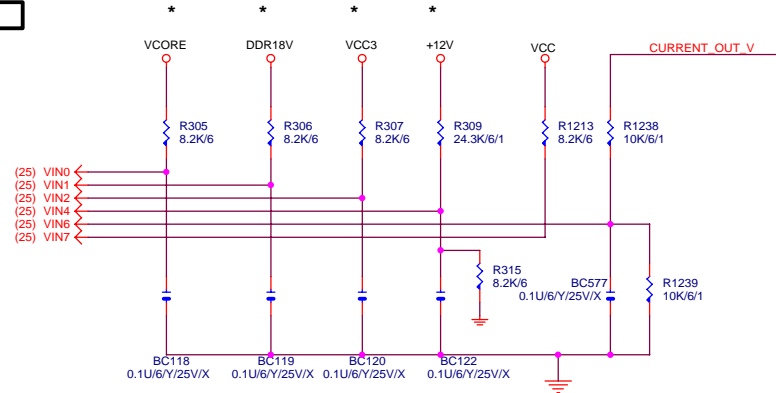
GIGABYTE

Title			LPC I/O
Size			81915P DUO PRO
Custom	Document Number	Rev	1.2
Date:	星期四, 八月 26, 2004	Sheet	25 of 41

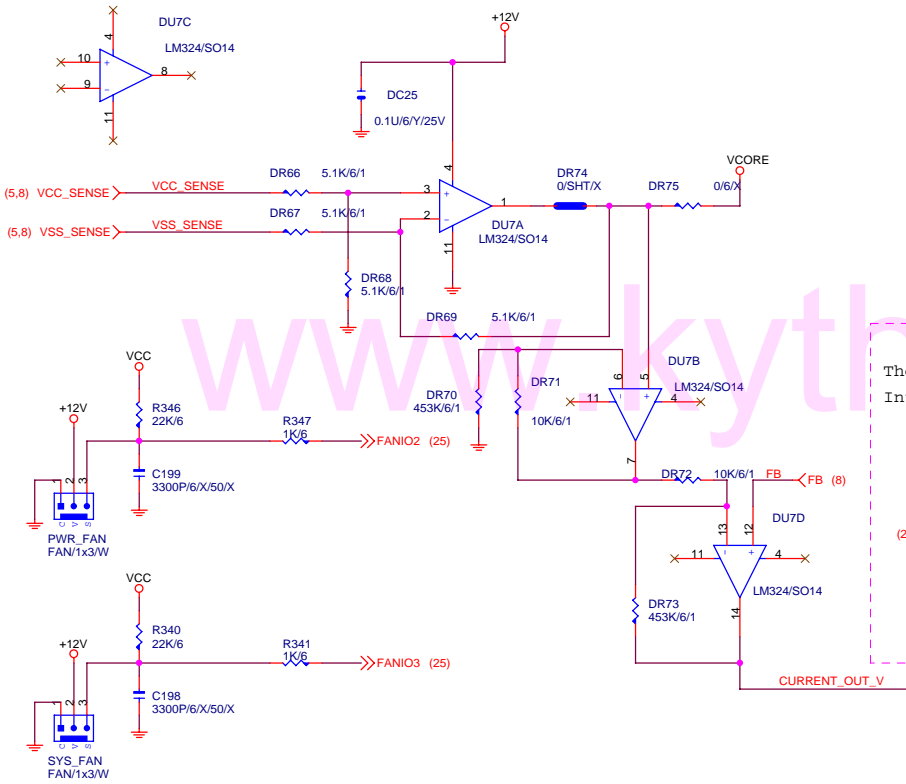
TEMP. SENSE



VOLTAGE SENSE



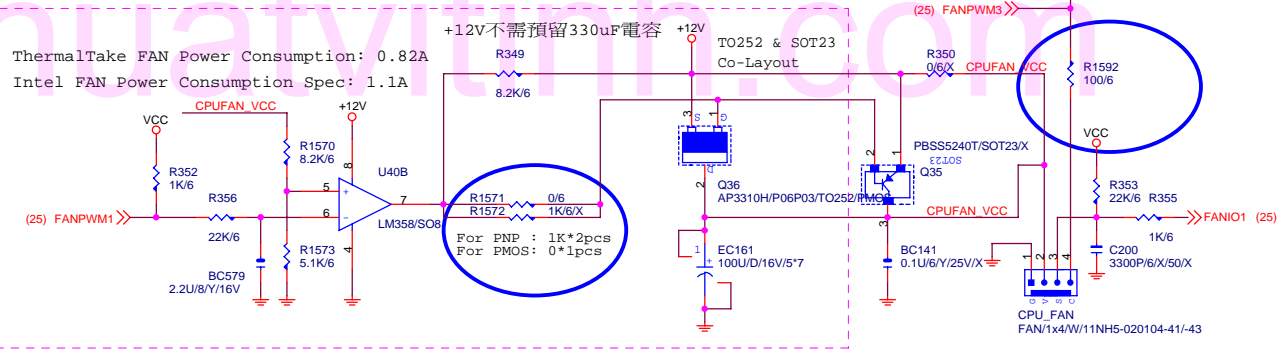
DUAL POWER



CPU/SYS FAN

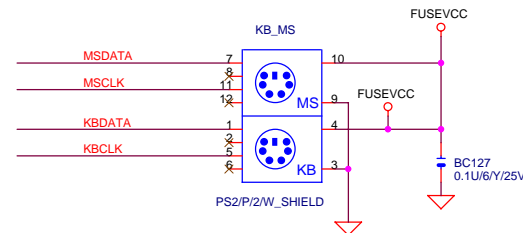
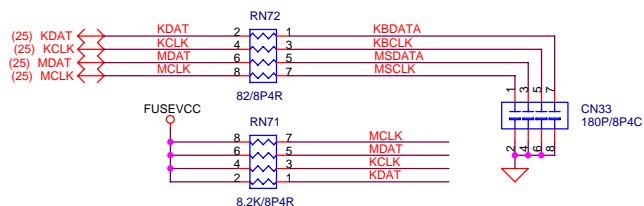
If use PBSS5240 lpcs : (non airflow) If use PBSS5240 lpcs : (with airflow)

CPUFAN_VCC=12V: Temp=40 deg	CPUFAN_VCC=12V: Temp=33 deg
CPUFAN_VCC=11V: Temp=82 deg	CPUFAN_VCC=11V: Temp=62 deg
CPUFAN_VCC=10V: Temp=70 deg	CPUFAN_VCC=10V: Temp=86 deg
CPUFAN_VCC= 9V: Temp=110 deg	CPUFAN_VCC= 9V: Temp=117 deg
CPUFAN_VCC= 8V: Temp>200 deg	CPUFAN_VCC= 8V: Temp>122 deg



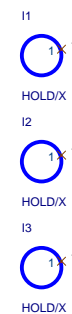
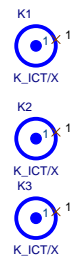
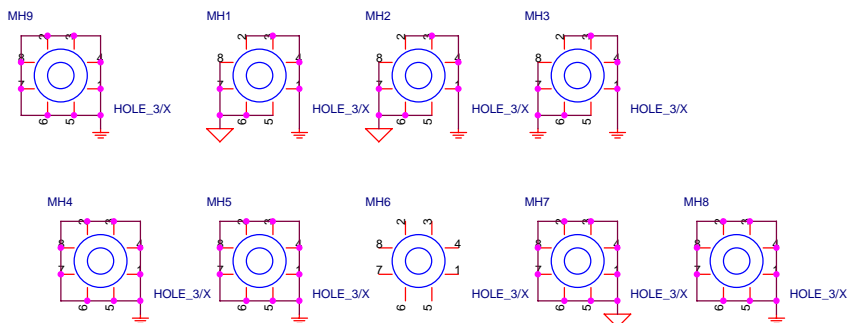
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Title HWM/FANCI/BIOS	
Size Custom	Document Number 8I915P DUO PRO
Date: 星期四, 八月 26, 2004	Sheet 26 of 41
	Rev 1.2

KB/MS



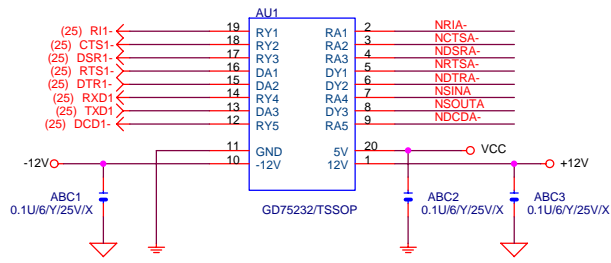
GAME PORT (N/A)

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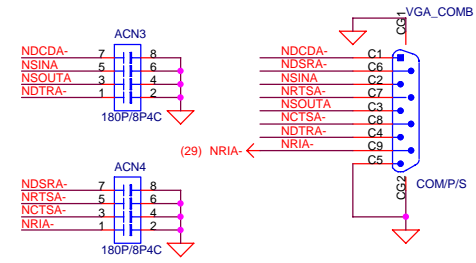
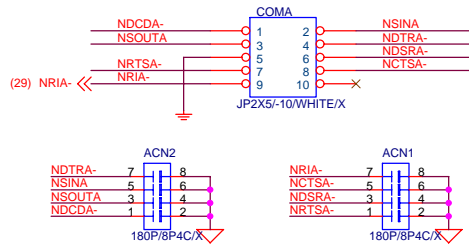


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Title		
PS/2 KB & MS		
Size	Document Number	Rev
B	8I915P DUO PRO	1.2
Date:	星期四, 八月 26, 2004	Sheet 27 of 41

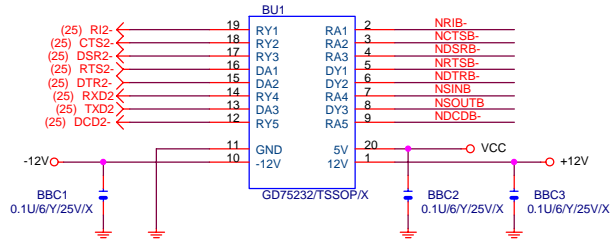
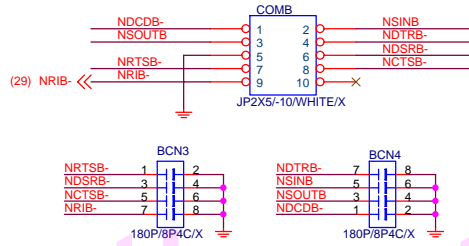
COMA / COMB



INTERNAL COMA



INTERNAL COMB

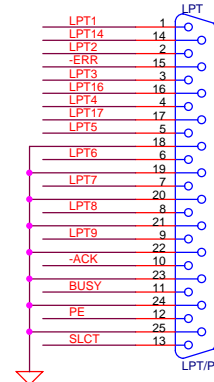
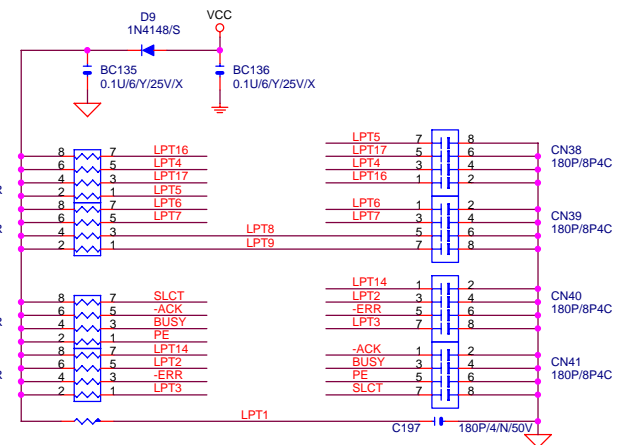
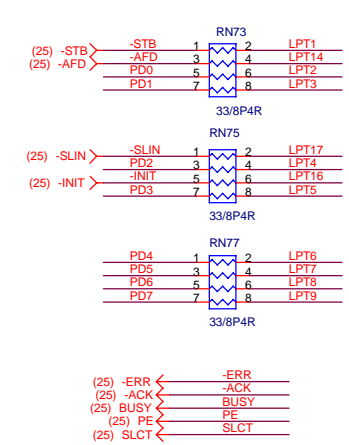


PLACE NEAR VGA_COM CONNECTOR

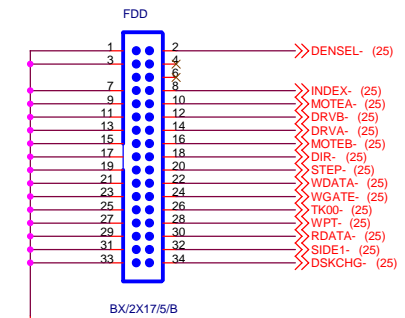
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LPT

(25) PD0..7] PDI0..7]

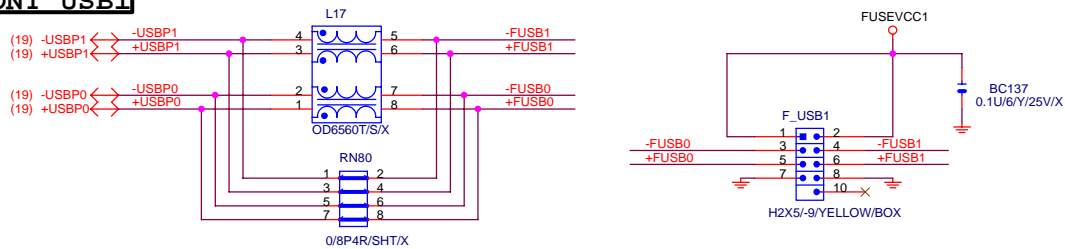


FLOPPY

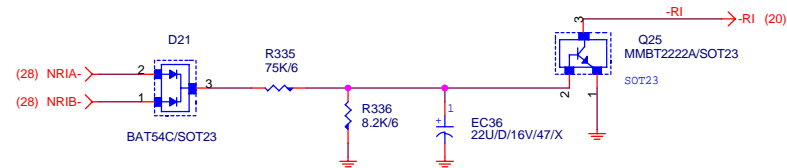


GIGABYTE		
Title COM, LPT, FDD		
Size B	Document Number 81915P DUO PRO	Rev 1.2
Date: 星期四, 八月 26, 2004	Sheet 28	of 41

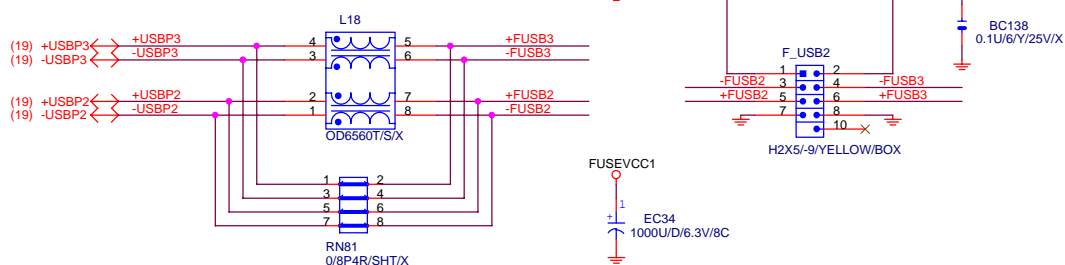
FRONT USB1



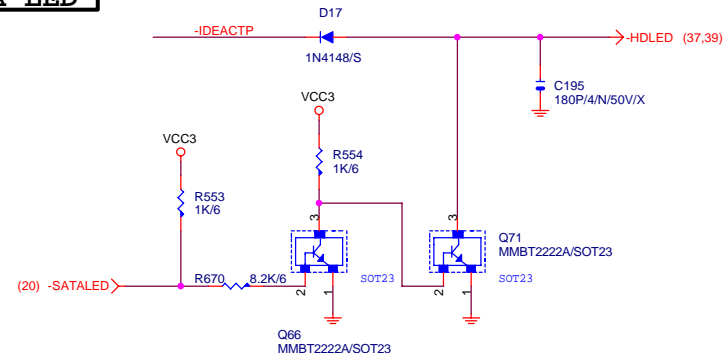
RING IN



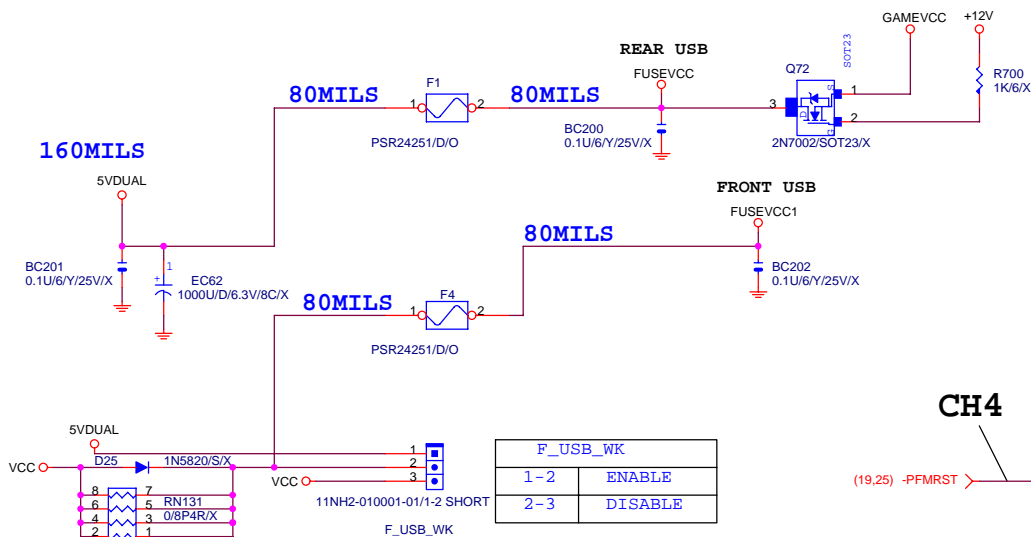
FRONT USB2



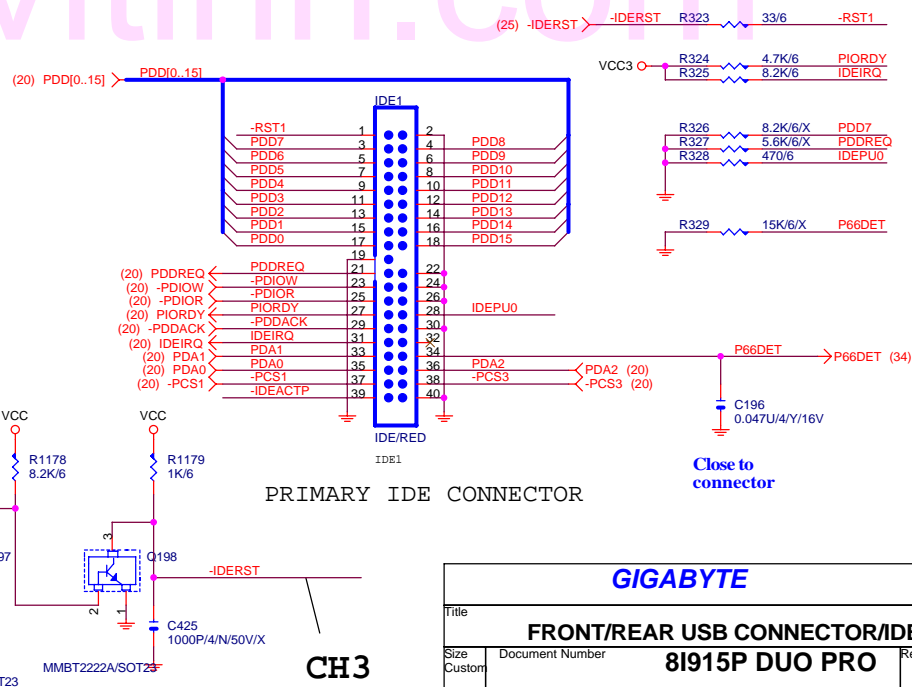
IDE/SATA LED



FUSEVCC, GAMEVCC



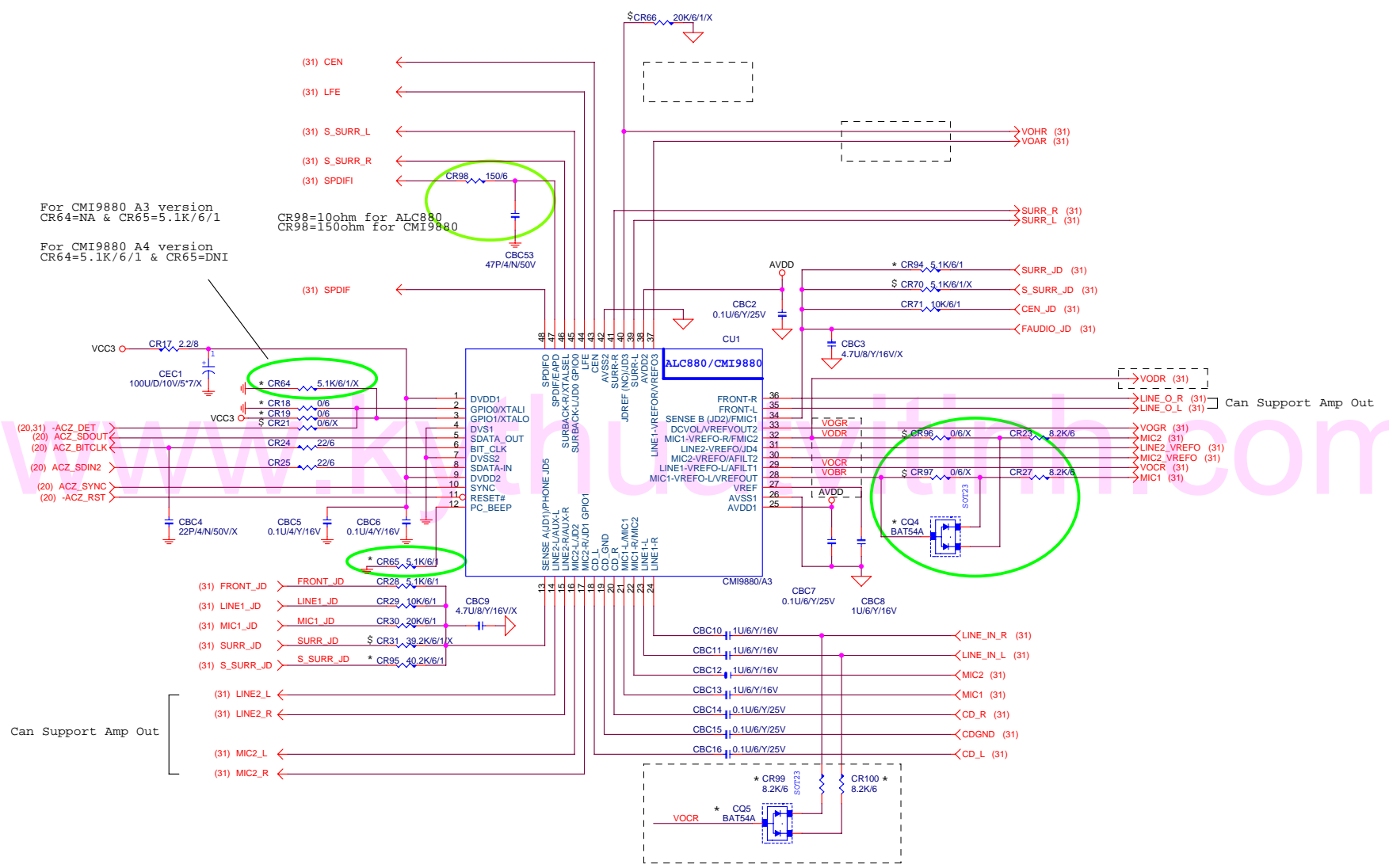
IDE



F_USB_WK	
1-2	ENABLE
2-3	DISABLE

GIGABYTE			
Title	FRONT/REAR USB CONNECTOR/IDE		
Size	Document Number	81915P DUO PRO	Rev 1.2
Custom			
Date:	星期四, 八月 26, 2004	Sheet	29 of 41

"\$" means for ALC880 only
 "*" means for CMI9880 only



For CMI9880 A3 version
 CR64=NA & CR65=5.1K/6/1
 For CMI9880 A4 version
 CR64=5.1K/6/1 & CR65=DNI

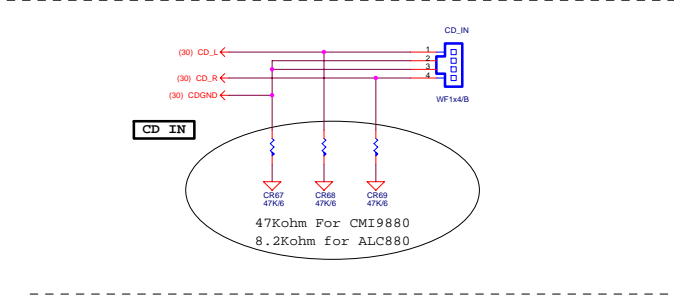
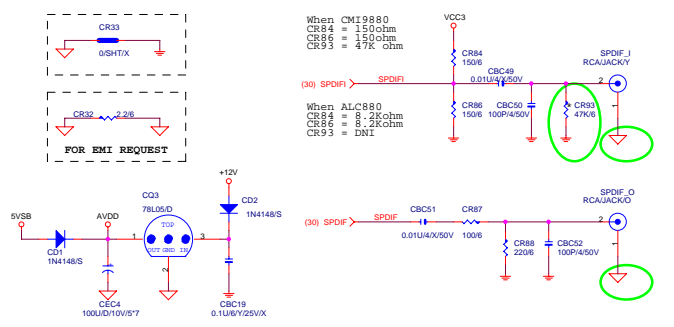
CR98=10ohm for ALC880
 CR98=150ohm for CMI9880

- (20,31) -ACZ_DET
- (20) ACZ_SDOUT
- (20) ACZ_BITCLK
- (20) ACZ_SYNC
- (20) -ACZ_RST

Can Support Amp Out

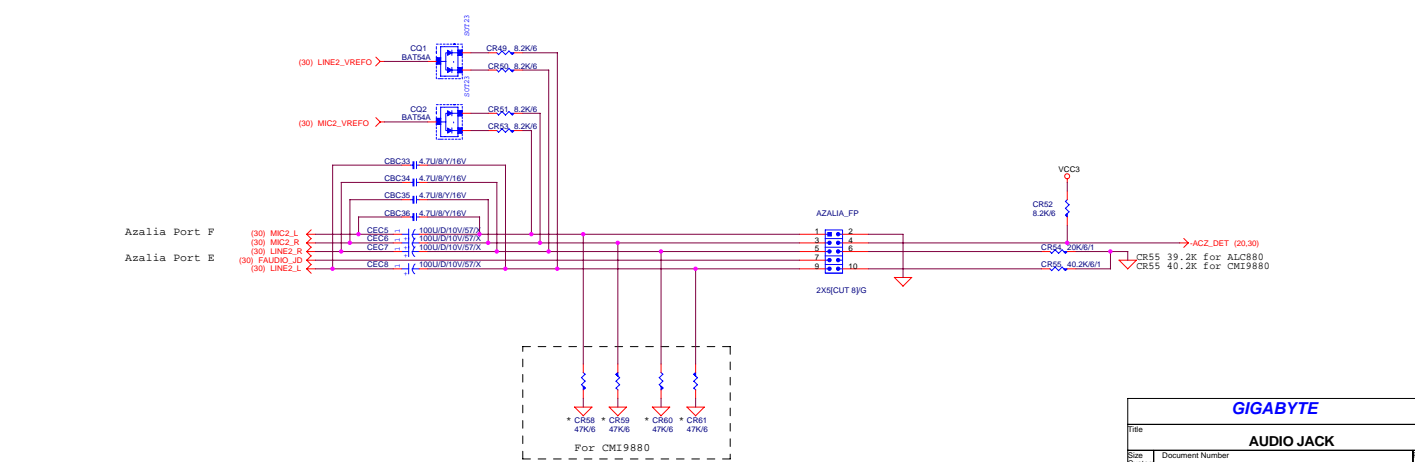
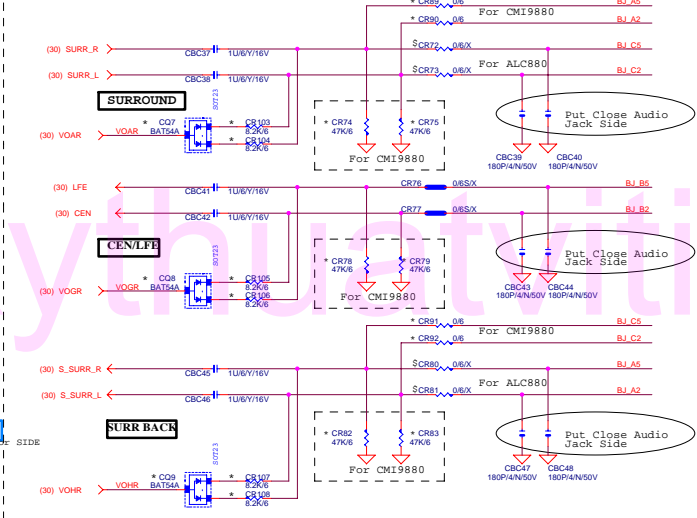
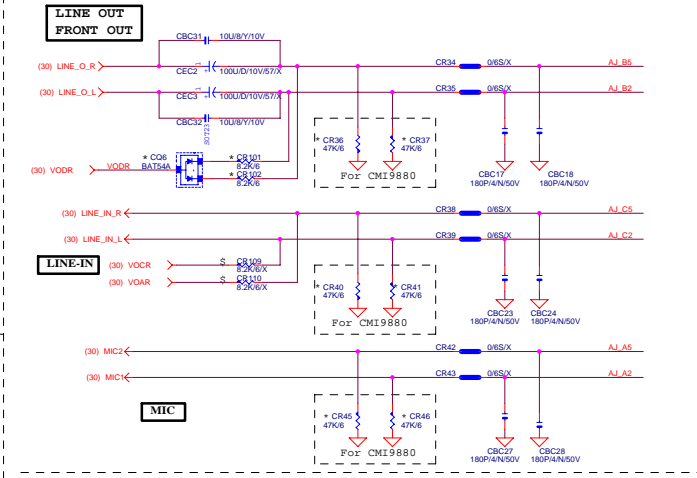
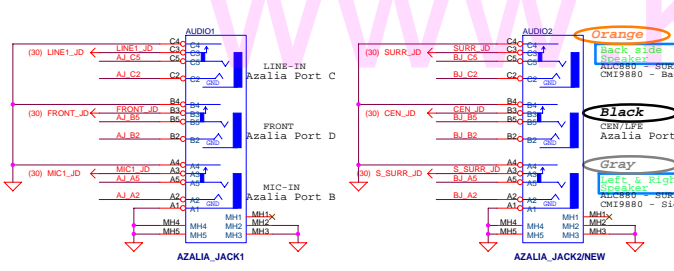
GIGABYTE

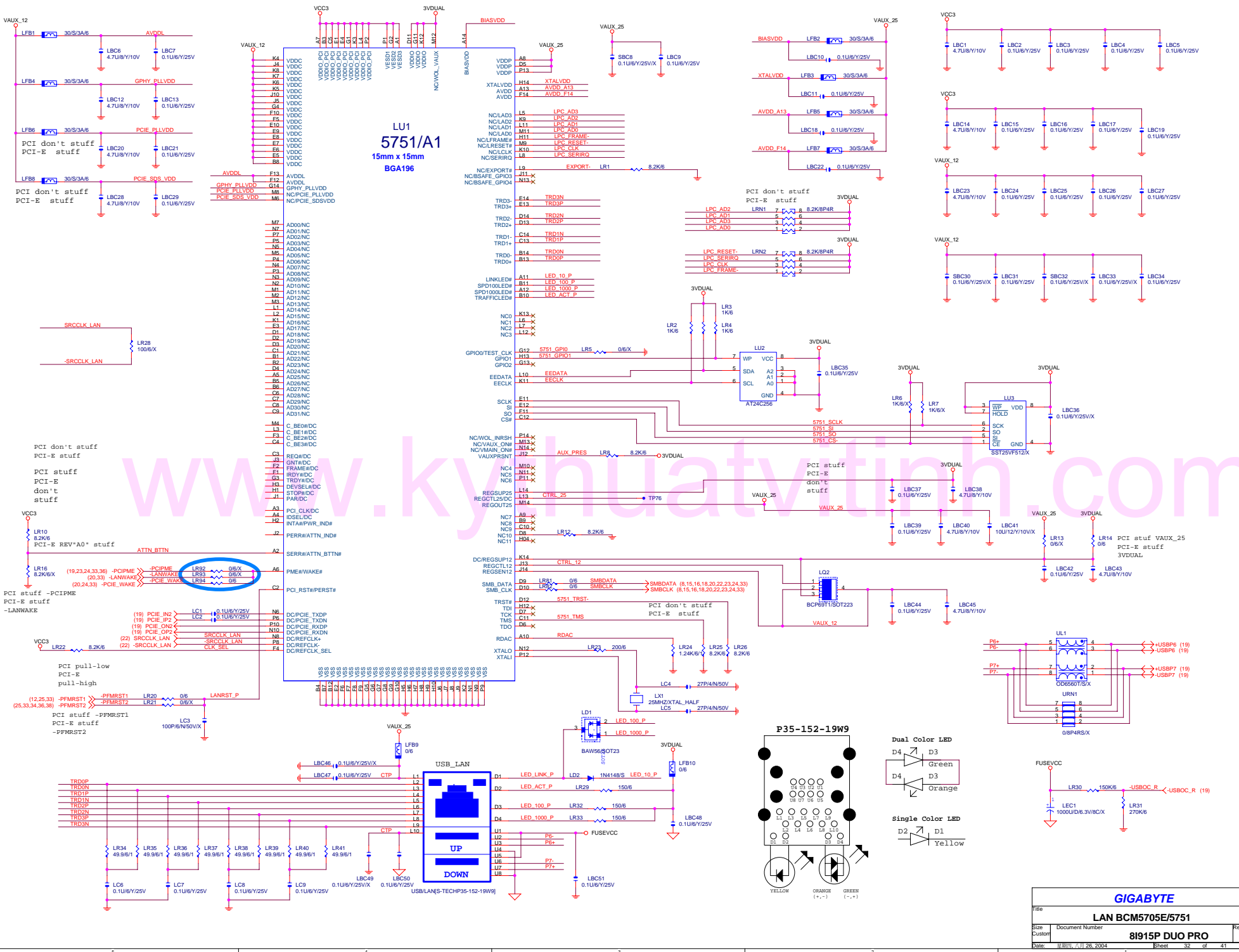
Title			
AC97 CMI9880			
Size	Document Number	81915P DUO PRO	
Custom		Rev	1.2
Date:	星期四, 八月 26, 2004	Sheet	30 of 41



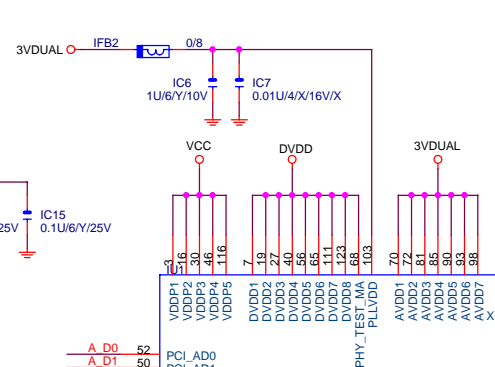
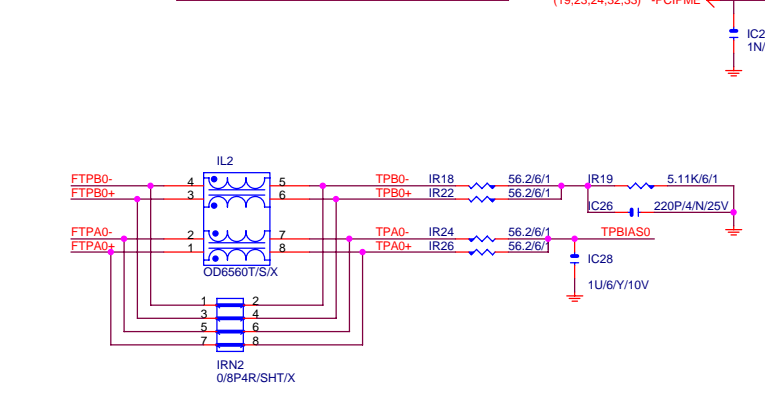
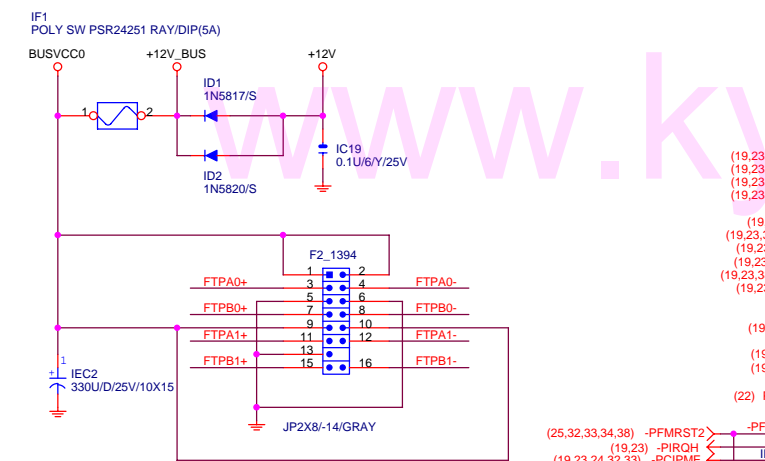
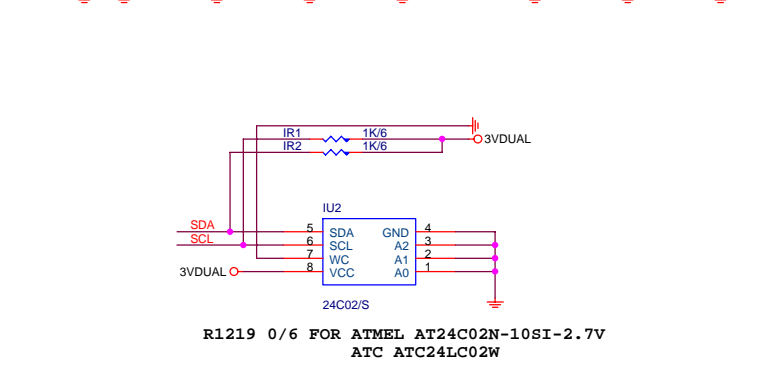
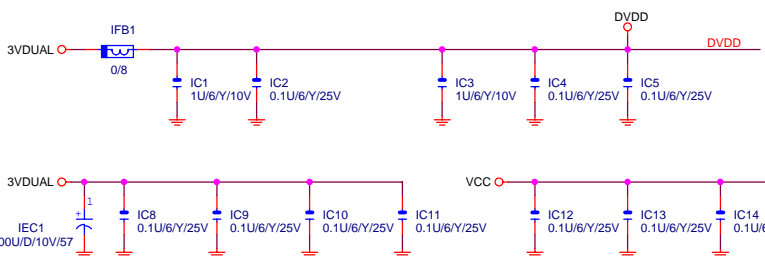
Azalia Jack CMI9880 Port A is Side SURROUND, Port H is Back SURROUND
 ALC880 Port A is SURROUND, Port H is SIDE

Normal --> pin4/pin3 open
 Plug jack --> pin4/pin3 close

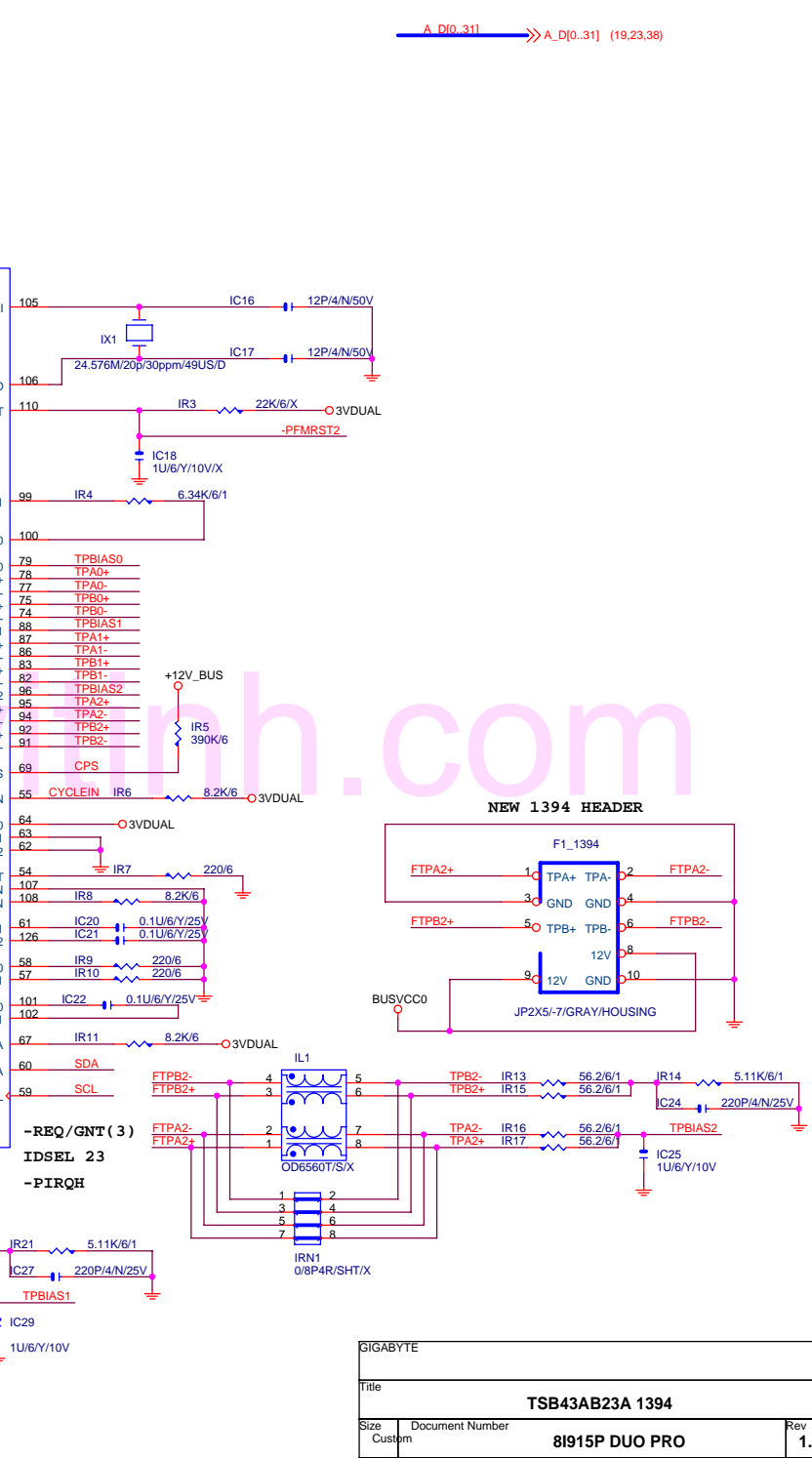




GIGABYTE			
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Sub	Document Number		Rev
Custom	81915P DUO PRO		1.2
Date	2004.11.26	Sheet	32 of 41

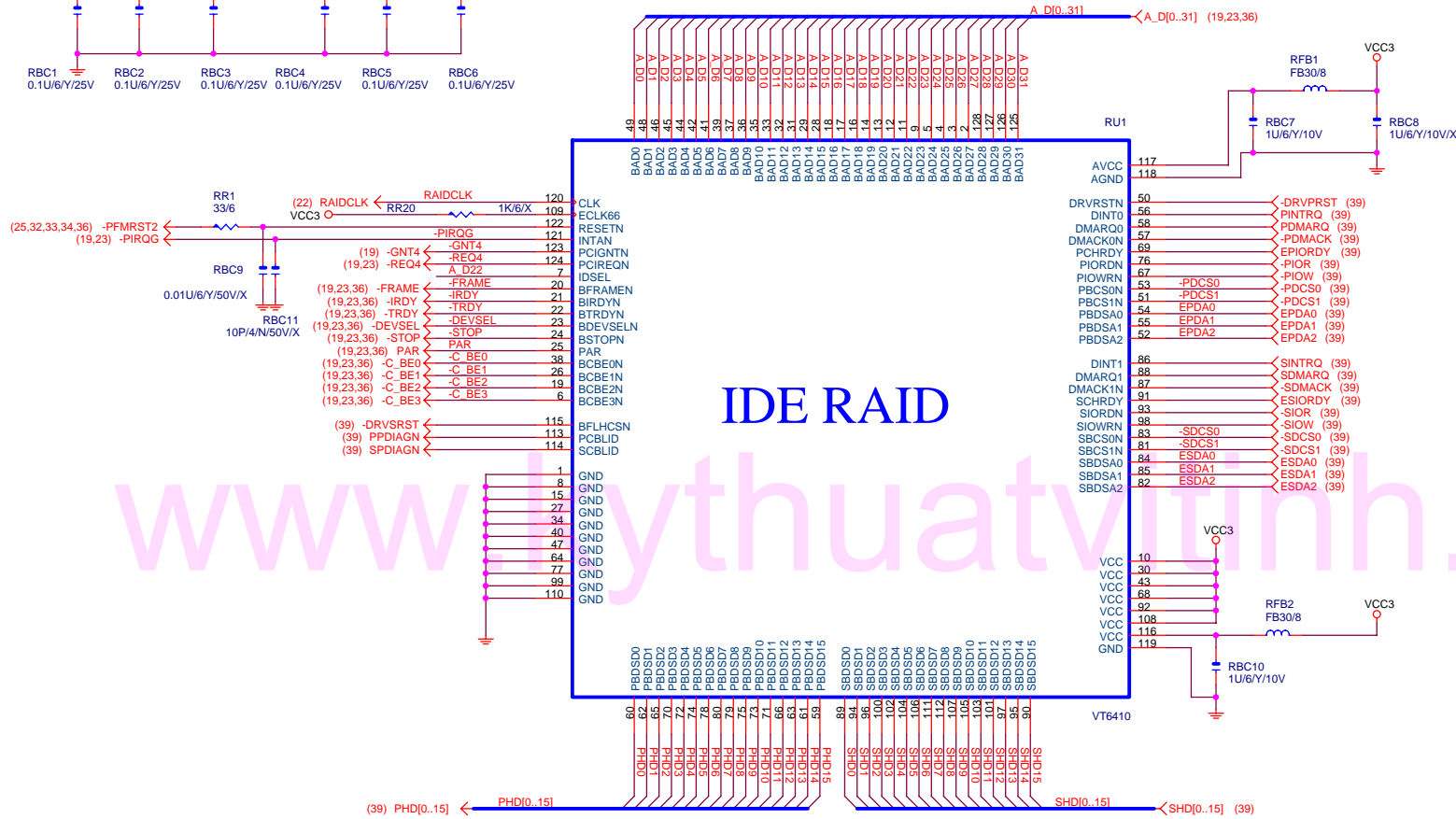
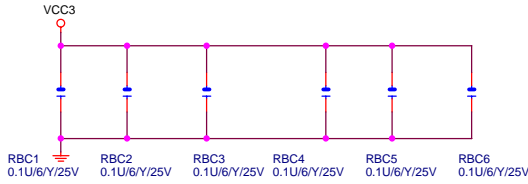


A_D0	52	PCI_AD0
A_D1	50	PCI_AD1
A_D2	49	PCI_AD2
A_D3	48	PCI_AD3
A_D4	47	PCI_AD4
A_D5	45	PCI_AD5
A_D6	44	PCI_AD6
A_D7	42	PCI_AD7
A_D8	39	PCI_AD8
A_D9	38	PCI_AD9
A_D10	37	PCI_AD10
A_D11	35	PCI_AD11
A_D12	34	PCI_AD12
A_D13	33	PCI_AD13
A_D14	31	PCI_AD14
A_D15	29	PCI_AD15
A_D16	14	PCI_AD16
A_D17	13	PCI_AD17
A_D18	11	PCI_AD18
A_D19	10	PCI_AD19
A_D20	9	PCI_AD20
A_D21	8	PCI_AD21
A_D22	6	PCI_AD22
A_D23	5	PCI_AD23
A_D24	128	PCI_AD24
A_D25	127	PCI_AD25
A_D26	125	PCI_AD26
A_D27	124	PCI_AD27
A_D28	122	PCI_AD28
A_D29	121	PCI_AD29
A_D30	120	PCI_AD30
A_D31	118	PCI_AD31
(19,23,38) -C_BE0	41	PCI_CBE0
(19,23,38) -C_BE1	28	PCI_CBE1
(19,23,38) -C_BE2	15	PCI_CBE2
(19,23,38) -C_BE3	2	PCI_CBE3
(19,23,38) PAR	26	PCI_PAR
(19,23,38) -FRAME	17	PCI_FRAME
(19,23,38) -IRDY	18	PCI_IRDY
(19,23,38) -TRDY	20	PCI_TRDY
(19,23,38) -DEVSEL	21	PCI_DEVSEL
(19,23,38) -STOP	22	PCI_STOP
(19,23) -REQ3	115	PCI_REQ
(19) -GNT3	114	PCI_GNT
(19,23) -PERR	24	PCI_PERR
(19,23) -SERR	25	PCI_SERR
(22) PCICLK1394	112	PCI_PCLK
(25,32,33,34,38) -PFMRST2	53	PCI_RST
(19,23) -PIRQH	109	PCI_INTA
(19,23,24,32,33) -PCIPME	117	PCI_PME
DGN01	12	PLLGND
DGN02	23	AGND1
DGN03	36	AGND2
DGN04	38	AGND3
DGN05	43	AGND4
DGN06	51	AGND5
DGN07	53	AGND6
DGN08	66	AGND7
DGN09	72	AGND8
DGN10	73	AGND9
PLLGND	104	AGND10
AGND1	71	AGND11
AGND2	72	AGND12
AGND3	73	AGND13
AGND4	74	AGND14
AGND5	75	AGND15
AGND6	76	AGND16
AGND7	77	AGND17
AGND8	78	AGND18
AGND9	79	AGND19
AGND10	80	AGND20
AGND11	81	AGND21
AGND12	82	AGND22
AGND13	83	AGND23
AGND14	84	AGND24
AGND15	85	AGND25
AGND16	86	AGND26
AGND17	87	AGND27
AGND18	88	AGND28
AGND19	89	AGND29
AGND20	90	AGND30
AGND21	91	AGND31
AGND22	92	AGND32
AGND23	93	AGND33
AGND24	94	AGND34
AGND25	95	AGND35
AGND26	96	AGND36
AGND27	97	AGND37
AGND28	98	AGND38
AGND29	99	AGND39
AGND30	100	AGND40
AGND31	101	AGND41
AGND32	102	AGND42
AGND33	103	AGND43
AGND34	104	AGND44
AGND35	105	AGND45
AGND36	106	AGND46
AGND37	107	AGND47
AGND38	108	AGND48
AGND39	109	AGND49
AGND40	110	AGND50
AGND41	111	AGND51
AGND42	112	AGND52
AGND43	113	AGND53
AGND44	114	AGND54
AGND45	115	AGND55
AGND46	116	AGND56
AGND47	117	AGND57
AGND48	118	AGND58
AGND49	119	AGND59
AGND50	120	AGND60
AGND51	121	AGND61
AGND52	122	AGND62
AGND53	123	AGND63
AGND54	124	AGND64
AGND55	125	AGND65
AGND56	126	AGND66
AGND57	127	AGND67
AGND58	128	AGND68
AGND59	129	AGND69
AGND60	130	AGND70
AGND61	131	AGND71
AGND62	132	AGND72
AGND63	133	AGND73
AGND64	134	AGND74
AGND65	135	AGND75
AGND66	136	AGND76
AGND67	137	AGND77
AGND68	138	AGND78
AGND69	139	AGND79
AGND70	140	AGND80
AGND71	141	AGND81
AGND72	142	AGND82
AGND73	143	AGND83
AGND74	144	AGND84
AGND75	145	AGND85
AGND76	146	AGND86
AGND77	147	AGND87
AGND78	148	AGND88
AGND79	149	AGND89
AGND80	150	AGND90
AGND81	151	AGND91
AGND82	152	AGND92
AGND83	153	AGND93
AGND84	154	AGND94
AGND85	155	AGND95
AGND86	156	AGND96
AGND87	157	AGND97
AGND88	158	AGND98
AGND89	159	AGND99
AGND90	160	AGND100



GIGABYTE		
Title		
TSB43AB23A 1394		
Size	Document Number	Rev
Custom	81915P DUO PRO	1.2
Date:	星期四, 六月 26, 2004	Sheet 36 of 41

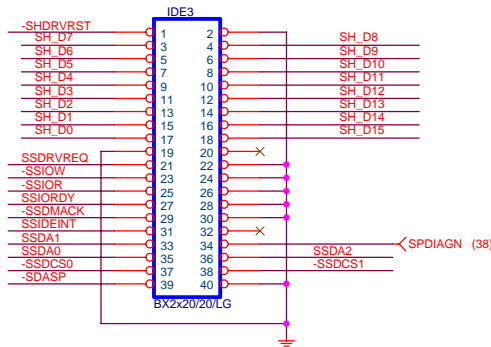
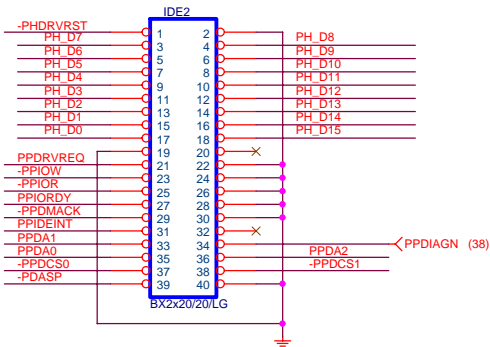
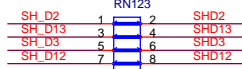
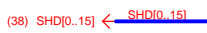
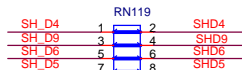
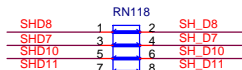
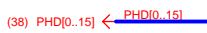
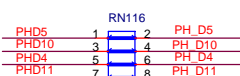
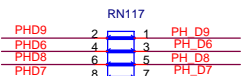
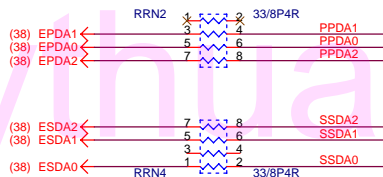
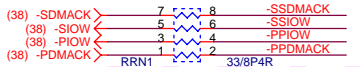
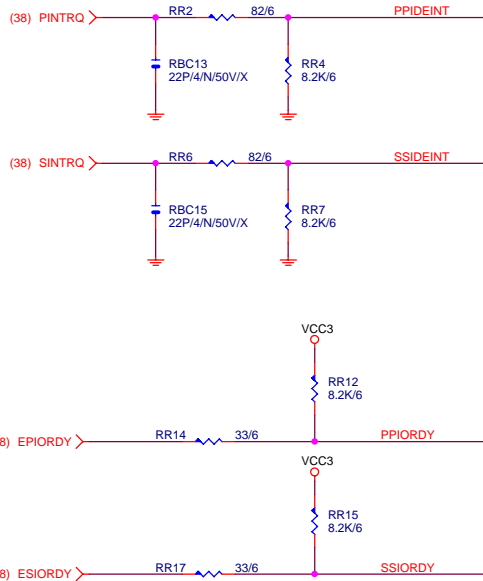
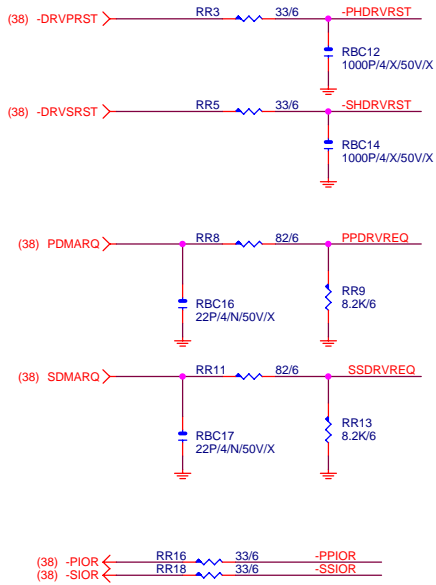
ALL INPUT PIN MUST HAVE 0.1 CAPACITOR



IDE RAID

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GIGABYTE		
Title ATA100/133 & IDE RAID		
Size	Document Number 81915P DUO PRO	Rev 1.2
Date:	星期日, 八月 26, 2004	Sheet 38 of 41



ICH6 GPIO Table:

NAME	PWR LANE	USAGE	NAME	PWR LANE	USAGE
GPI0	V5REF	M/B ID (-REQ6)	GPI41	VCC3	M/B ID
GPI1	V5REF	-REQ5	GPO48	VCC3	-GNT4
GPI2	V5REF	-PIRQE	GPO49	V-CPUIO	CPUPWOK
GPI3	V5REF	-PIRQF			
GPI4	V5REF	-PIRQG			
GPI5	V5REF	-PIRQH			
GPI6	VCC3	-SLP_BTN			
GPI7	VCC3	DUAL BIOS			
GPI8	3VDAUL	-LANWAKE			
GPI9	3VDAUL	-USBOC4			
GPI10	3VDAUL	-USBOC5			
GPI11	3VDAUL	-SMBALT			
GPI12	VCC3	ATX_DET			
GPI13	3VDAUL	-LPCPME			
GPI14	3VDAUL	-USBOC6			
GPI15	3VDAUL	-USBOC7			
GPO16	VCC3	CPU OV1 (-GNT6)			
GPO17	VCC3	-GNT5			
GPO18	VCC3	CPU OV2			
GPO19	VCC3	DUAL BIOS			
GPO20	VCC3	BIOS T-BLOCK			
GPO21	VCC3	DUAL BIOS			
GPO23	VCC3	DDR OV0			
GPI024	3VDAUL	GREEN_LED			
GPI025	3VDAUL	DDR OV1			
GPI26	VCC3	SATA_GP0			
GPI027	3VDAUL	+PWRLED			
GPI028	3VDAUL	-PWRLED			
GPI29	VCC3	SATA_GP1			
GPI30	VCC3	SATA_GP2			
GPI31	VCC3	SATA_GP3			
GPI032	VCC3	BIOS_WP			
GPI033	VCC3	AZALIA_DET			
GPI034	VCC3	M/B ID			
GPI40	V5REF	-REQ4			

PWROK/RESET Table:

ITE8712BHX PIN	NET NAME	TARGET
PIN62/-PCIRST1	-PCIE_RST	<ol style="list-style-type: none"> 1. PCI-E * 1 Slot1 2. PCI-E * 1 Slot2 3. PCI-E * 1 Slot3 4. PCI-E * 16 Slot
PIN64/-PCIRST2	-PFMRST2	<ol style="list-style-type: none"> 1. Onboard PCI Lan 2. Onboard 1394 Chip 3. OnBoard FWH
PIN65/-PCIRST3	-PFMRST1	<ol style="list-style-type: none"> 1. Onboard PCI-E Lan 2. Onboard SATA Chip 3. GMCH
PIN115/-PCIRST4	-PFMRST -IDERST	Reserved For IDE
PIN63/PWROK1	PWROK1	<ol style="list-style-type: none"> 1. GMCH 2. ICH6 3. 5VDUAL SWITCH 4. DPS CONTROL
PIN109/PWROK2	-THERM	<ol style="list-style-type: none"> 1. ICH6

GIGABYTE THCHNOLOGIES , INC.

Title			
GPIO/RESET TABLE			
Size	Document Number	8I915P DUO PRO	Rev
Custom			1.2
Date:	星期四, 八月 26, 2004	Sheet	41 of 41