

Model Name: 8I915P DUO

REV1.4

SHEET	TITLE
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B
06	P4_LGA775_C
07	P4_LGA775_D
08	VCORE POWER
09	GMCH-GRANTS DALE_HOST
10	GMCH-GARNTSDALE_DDR
11	GMCH-GRANTS DALE_PCI E, DMI
12	GMCH-GRANTS DALE_INT VGA
13	GMCH-GRANTS DALE_GND
14	GMCH-GRANTS DALE_PWR
15	DDR CHANNEL A
16	DDR CHANNEL B
17	DDR TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH6 PCI, USB, DMI, LAN
20	ICH6 IDE, GPIO, SATA, CTRL
21	ICH6 VCC, GND
22	CLK GEN

SHEET	TITLE
23	PCI SLOT
24	PCI EXPRESS*1 SLOT
25	ITE8712HX
26	HWMO/FAN/FWH BIOS
27	KB_MS/GAME
28	COM/LPT/FDD
29	(FRONT+REAR)USB/RING/IDE
30	AZALIA CODEC ALC880/CMI9880
31	AUDIO JACK
32	LAN BCM5705E/5751
33	LAN BCM5751
34	ATX POWER CONN.
35	ALL POWER
36	1394 TSB43AB23
37	FRONT PANEL/BZ
38	RAID VIA6410
39	RAID IDE CONNECTOR
40	GPIO TABLE
41	RESET TABLE

 COMPONENT SIDE
 (1 oz. Copper)
 VCC SIDE
 (1 oz. Copper)
 GND SIDE
 (1 oz. Copper)
 SOLDER SIDE
 (1 oz. Copper)

GIGABYTE		
Cover Sheet		
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BLOCK DIAGRAM

**INTEL Pentium4
LGA775**

VCCORE = 1.75V / SLEEP: 1.3V
VCC3

PAGE 4, 5, 6, 7

PWM/OTHER POWER

VCCORE = 1.75V (60-110MHZ) / SLEEP: 1.3V
5VSB=12V,+12V,VCC,VCC3,DUAL
VTT_DDR2_5VSTR

PAGE 8, 34, 35

CHANNEL A
DDRII DIMM X 1
DDR DIMM X 1

2_5VSTR = 2.5V(MEMORY,SUSPEND POWER)
VTT_DDR = 1.25V

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CHANNEL B
DDRII DIMM X 1
DDR DIMM X 1

2_5VSTR = 2.5V(MEMORY,SUSPEND POWER)
VTT_DDR = 1.25V

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**GMCH
GRANTS DALE**

VCCORE = 1.75V / SLEEP: 1.3V
2_5VSTR = 2.5V(MEMORY)
VDDQ = 1.5V (AGP POWER 4X, HUBLINK)

PAGE 9, 10, 11, 12, 13, 14

MAA0-14
MAA_CPC1-5
MAB_CPC1-5
MDD0-63
-DQSD0-7
DM0-7

GAD0-31
ADSTB0,ADSTB0-
ADSTB1,ADSTB1-
SBA0-7
SBSTB,SBSTB-
GCBE0-3-
ST0-2

CLOCK GENERATOR

CKVDD = 3.3V

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**PCI EXPRESS
BY 16 PORTS**

VCC3 = 1.5V (ASP POWER 4X)
VCC3 = 3.3V
+12V = 12V
3VDUAL = 3.3V
VCC = 5V

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USB PORTS 0~7


VCC = 5V
5VSB = 5V
5VUSB = 5V

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ICH6

VCC25 = 2.5V(I/O, MEMORY, VLINK)
3VDUAL = 3.3V(SUSPEND POWER)
VCC3 = 3.3V
RTCVDD = 3.3V

PAGE 19, 20, 21

HLO-10
CONTROL BUS  HUB LINK

IDE Primary

VCC = 5V

PAGE 29

SERIAL ATA

VCC = 5V

PAGE 20

FWH/HWMO

VCC = 5V
VCC3 = 3V

PAGE 26

LPC ITE8712HX

VCC = 5V
5VSB = 5V
VBAT = 3V

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I/O PORTS :
COMA COMB LPT PS2 IR FDD

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**AC97/Azalia
ALC880/CMI9880**

+12V = 12V
VCC3 = 3.3V
VCC = 5V
AVDD = 5V

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AUDIO PORTS : FRONT AUDIO
LIN_OUT LINE_IN MIC
TELE CD_IN AUX_IN

PAGE 31

FRONT PANEL/BZ

VCC = 5V
5VSB = 5V
+12V = 12V
PVCC = 5V

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**PCI SLOT
PCI EXPRESS SLOT**

+12 = 12V
+12 = 12V
VCC = 5V
VCC3 = 3V
3VDUAL = 3V

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LAN BCM5721/5751

PAGE 32, 33

1394 IT TSB43AB23

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RAID VIA6410

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AZALIA
LINK

PCI BUS

LPC BUS

Model Name: 8I915P DUO

Version: 1.4

Component value change history

2004/05/19

Data	Change Item	Reason
0317	ADD 2ND SOURCE	*** 更換 *** 新舊線路中 元件值不同 *** L10由 10H/8/180mA/S(10L12-07100B-01/10L12-041001-04)變為 10H/8/180mA/S(10L12-07100B-01/10L12-041001-04) L15由 10H/8/180mA/S(10L12-07100B-01/10L12-041001-04)變為 10H/8/180mA/S(10L12-07100B-01/10L12-041001-04) Q190由 SUD50N024-09P/APM2014N/IRLR3715/TO252變為 10IF4-083918-01R_10IF4-492409-01R_10IF4-098880-10R/TO252 Q191由 SUD50N024-09P/APM2014N/IRLR3715/TO252變為 10IF4-083918-01R_10IF4-492409-01R_10IF4-098880-10R/TO252 Q42由 SUD50N024-09P/TO252變為 10IF4-083918-01R_10IF4-492409-01R_10IF4-098880-10R/TO252 Q44由 SUD50N024-09P/TO252變為 10IF4-083918-01R_10IF4-492409-01R_10IF4-098880-10R/TO252
	LIB CHANGE	*** 更換 *** 新舊線路中 元件值不同 *** DL2由 0.6uH/D/30A/5018/3PW/(TAI/EMC)變為 0.6uH/TO-5018/40A/1P/[11LC5-40600C-D1] DL3由 0.6uH/D/30A/5018/3PW/(TAI/EMC)變為 0.6uH/TO-5018/40A/1P/[11LC5-40600C-D1] DL4由 0.6uH/D/30A/5018/3PW/(TAI/EMC)變為 0.6uH/TO-5018/40A/1P/[11LC5-40600C-D1] DL5由 0.6uH/D/30A/5018/3PW/(TAI/EMC)變為 0.6uH/TO-5018/40A/1P/[11LC5-40600C-D1] L1由 10UH/8/100mA/S(10L12-00100A-01/02/03)變為 10UH//120mA/8/S/[10L12-12100A-01_10L12-12100A-02_10L12-12100A-03] L2由 10UH/8/100mA/S(10L12-00100A-01/02/03)變為 10UH//120mA/8/S/[10L12-12100A-01_10L12-12100A-02_10L12-12100A-03]
0401	Q187 ADD 2ND SOURCE	10GL6-501085-01R
0407	N/B VERSION CHANGE TO C2	10HB1-032915-25

Circuit or PCB layout change for next version

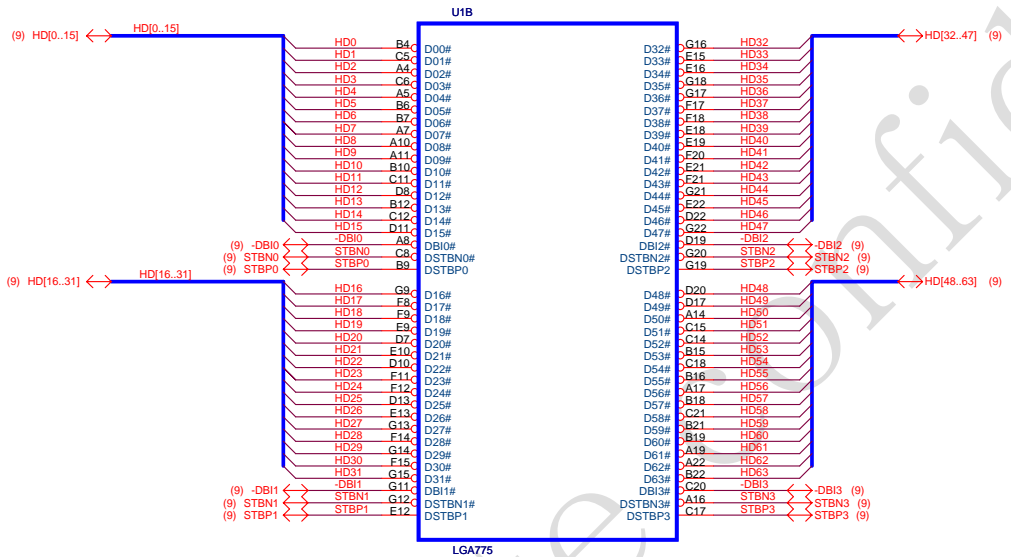
PAGE	Change Item	Reason
0317	LIB CHANGE	

GIGABYTE

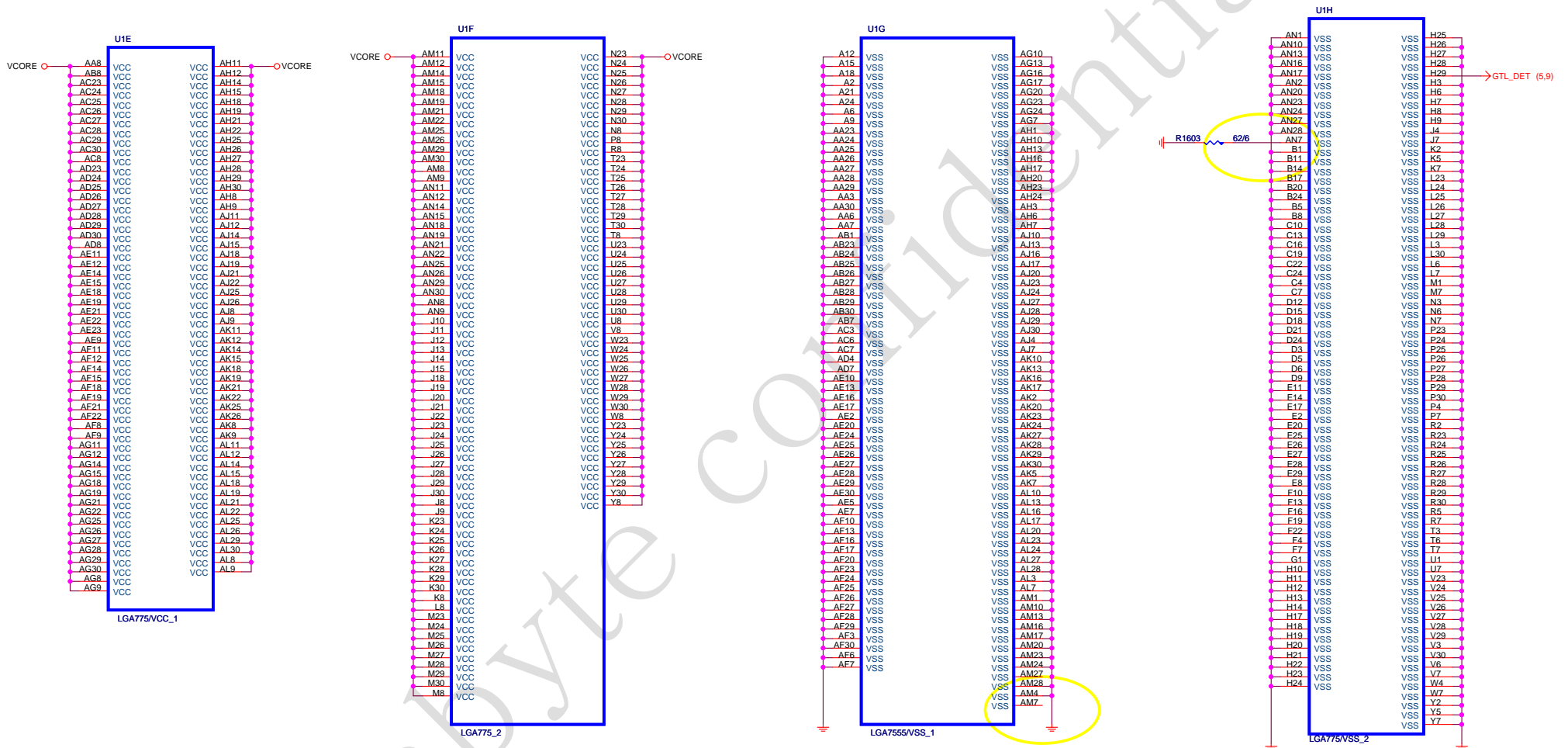
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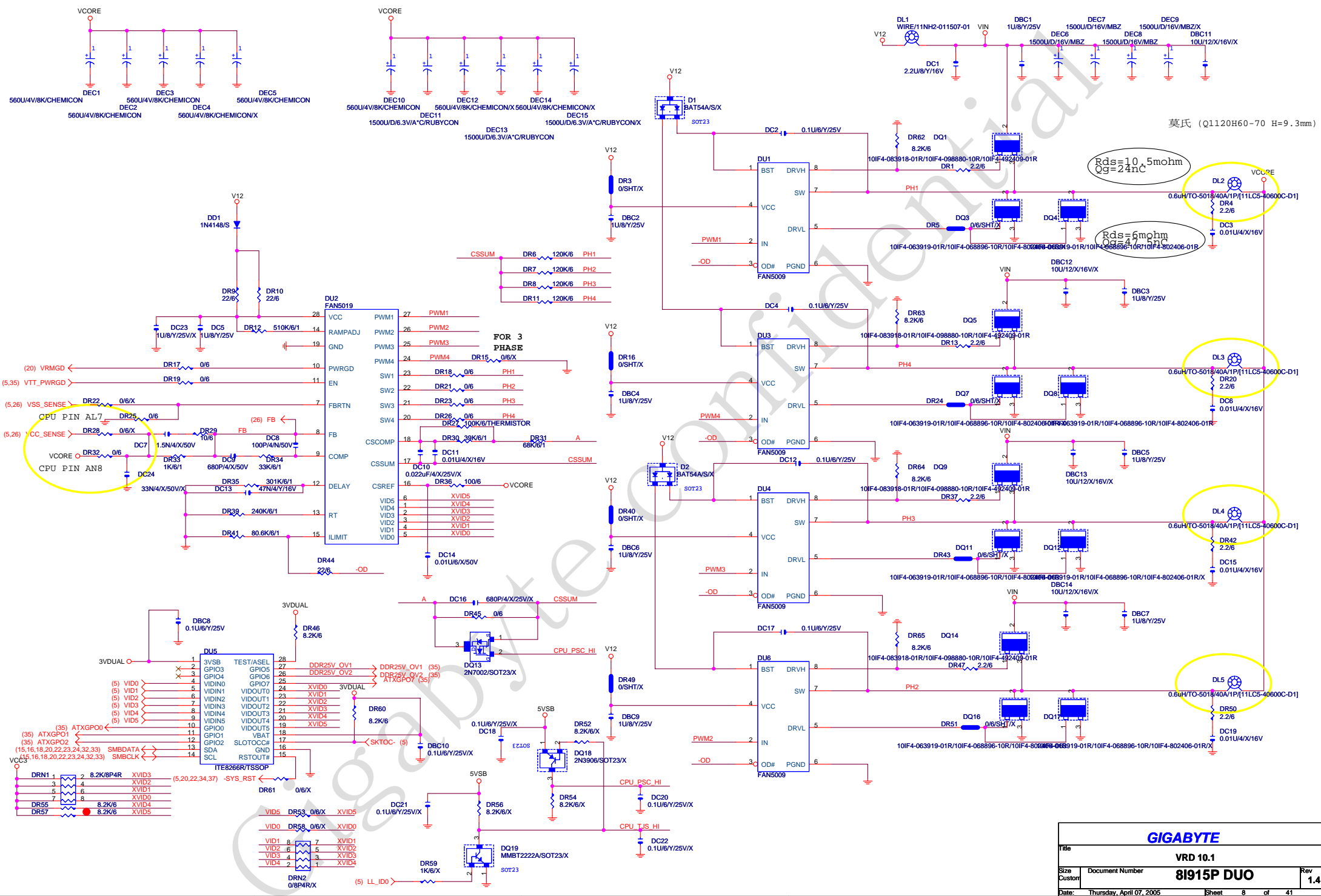
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莫氏 (Q1120H60-70 H=9.3mm)

$R_{ds} = 10.5 \text{ m}\Omega$
 $Q_g = 24 \text{ nC}$

$R_{ds} = 6 \text{ m}\Omega$
 $Q_g = 4.5 \text{ nC}$

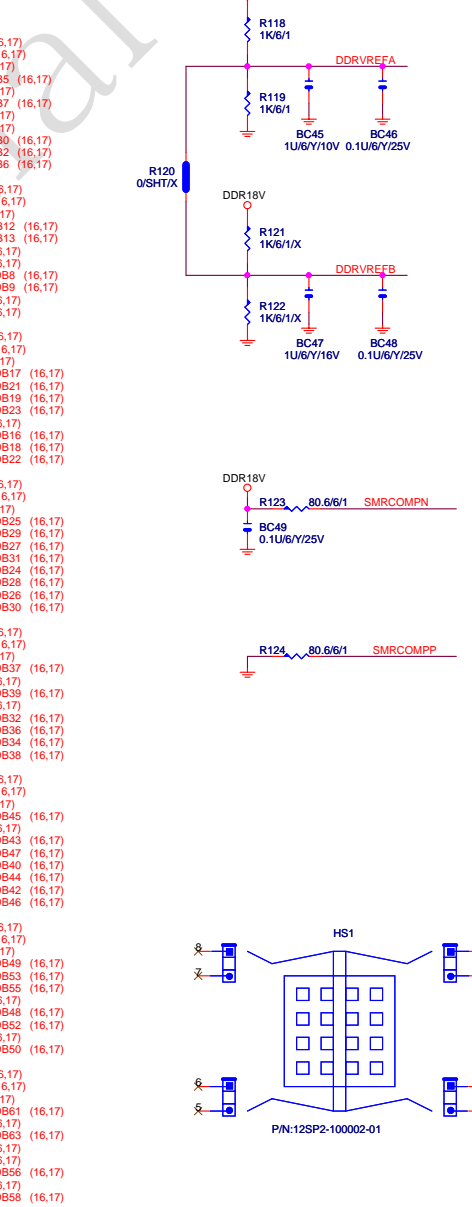
GIGABYTE		
VRD 10.1		
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USE	GRANTSDALE-PC2	GRANTSDALE-PC2
(15,17) MAA0_SBA01	AP26	SAMA0/SABA1
(15,17) MAA1	AL24	SAMA1/RSV
(15,17) MAA2	AP23	SAMA2/RSV
(15,17) MAA3	AP23	SAMA3/RSV
(15,17) MAA4	AP23	SAMA4/RSV
(15,17) MAA5_MAA1	AP22	SAMA5/SAMA10
(15,17) MAA6_MAA10	AN23	SAMA6/SAMA10
(15,17) MAA7_MAA3	AP21	SAMA7/SAMA3
(15,17) MAA8_MAA0	AN22	SAMA8/SAMA0
(15,17) MAA9_MAA2	AN21	SAMA9/SAMA2
(15,16,17) MAA10_SBA0	AM27	SAMA10/SBA0
(15,17) MAA11_MAA4	AM21	SAMA11/SAMA4
(15,17) MAA12_MAA6	AR20	SAMA12/SAMA6
(15,17) MAA13_SWEA	AP31	SAMA13/SAVE*
(15,17) SWEA_SBA00	AN28C	SAWE*/SABA0
(15,17) SCASA_SRSAS	AN29C	SACAS*/SARAS*
(15,16,17) SRSAS_SRSASB	AP27C	SARAS*/SBRAS*
(15,16,17) SBA00_SWEB	AR27	SABA0/SBWE*
(15,16,17) SBA01_SCASB	AN27	SABA1/SBCAS*
(15,16,17) SBA2_MAB10	AN20	SABA2/SBMA10
(15,17) CSA0	AR29C	SACS0*/RSV
(15,17) CSA1	AP32C	SACS1*/RSV
	AP32C	SACS2*/RSV
	AP31C	SACS3*/RSV
(15,17) CKEA0_MAA5	AP19	SACKE0/SAMA5
(15,16,17) CKEA1_MAB0	AM18	SACK1/SBMA0
(15,17) CKEA2_MAA8	AN18	SACK2/SAMA8
(16,17) CKEA3_SBA1	AR19	SACK3/SBA1
(15,17) MODT_A0	AP30	SAODT0/RSV
(15,17) MODT_A1	AN32	SAODT1/RSV
	AP29	SAODT2/RSV
	AP33	SAODT3/RSV
(15) DCLKA0_DCLKA3	AN26	SACK0/SACK3*
(15) DCLKA0_DCLKA3	AP25C	SACK0*/SACK3
(15) DCLKA1_DCLKA4	AM2	SACK1/SACK4
(15) DCLKA1_DCLKA4	AM3	SACK1*/SACK4*
(15) DCLKA2_DCLKA5	AC34	SACK2/SACK5*
(15) DCLKA2_DCLKA5	AC35C	SACK2*/SACK5
(15) DCLKA3_DCLKA0	AN25	SACK3/SACK0
(15) DCLKA3_DCLKA0	AM24C	SACK3*/SACK0
(15) DCLKA4_DCLKA1	AN3	SACK4/SACK1*
(15) DCLKA4_DCLKA1	AN2C	SACK4*/SACK1*
(15) DCLKA5_DCLKA2	AM3	SACK5/SACK2*
(15) DCLKA5_DCLKA2	AB34C	SACK5*/SACK2
(15,17) MAA13	AB33	RSV/SAMA13
	TP2	AH15C
	TP4	AE16C
		SM_XSLEWIN
		SM_SLEWIN0
		SM_SLEWOUT0
		SM_VREF0
		SC5
		0.1U6/Y/25VX

GRANTSDALE-PC2	GRANTSDALE-PC2	GRANTSDALE-PC2
SADQ00	AG1	DQS00 (15,17)
SADQ05*/RSV	AG2	DQS00 (15,17)
SADQ06	AF2	DM00 (15,17)
SADQ07	AE3	MD00 (15,17)
SADQ08	SAD01	MDA1 (15,17)
SADQ09/SADQ07	AJ2	MDA2_MDA7 (15,17)
SADQ10	SAD03	MDA3 (15,17)
SADQ11	SAD04	MDA4 (15,17)
SADQ12/SADQ08	AE1	MDA5 (15,17)
SADQ13/SADQ02	AG3	MDA6 (15,17)
	AH2	MDA7_MDA2 (15,17)
SADQ14	AL2	DQS01 (15,17)
SADQ15*/RSV	AL1	DQS01 (15,17)
SADQ16	SADM1	DM01 (15,17)
SADQ17/SADQ09	AK2	MDA8_MDA10 (15,17)
SADQ18/SADQ09	AK3	MDA9_MDA13 (15,17)
SADQ19	AN4	MDA10 (15,17)
SADQ20	AP4	MDA11 (15,17)
SADQ21/SADQ08	AJ1	MDA12_MDA8 (15,17)
SADQ22/SADQ18	AJ3	MDA13_MDA12 (15,17)
SADQ23	AP2	MDA14 (15,17)
SADQ24	AP3	MDA15 (15,17)
SADQ25	AP7	DQS02 (15,17)
SADQ26*/RSV	AR7	DQS02 (15,17)
SADQ27	AN7	DM02 (15,17)
SADQ28*/RSV	AR8	MDA16_MDA17 (15,17)
SADQ29	AP8	MDA18_MDA19 (15,17)
SADQ30/SADQ17	AP9	MDA19_MDA21 (15,17)
SADQ31/SADQ19	AN9	MDA20 (15,17)
SADQ32	AN5	MDA21_MDA16 (15,17)
SADQ33/SADQ11	AP5	MDA22_MDA18 (15,17)
SADQ34/SADQ18	AN8	MDA23_MDA22 (15,17)
SADQ35	AR8	DQS03 (15,17)
SADQ36*/RSV	AG17	DQS03 (15,17)
SADQ37	AH16	DM03 (15,17)
SADQ38/SADQ25	AL17	MDA24_MDA25 (15,17)
SADQ39/SADQ29	AJ17	MDA25_MDA29 (15,17)
SADQ40/SADQ27	AE19	MDA26_MDA27 (15,17)
SADQ41/SADQ31	AH18	MDA27_MDA31 (15,17)
SADQ42/SADQ24	AK16	MDA28_MDA24 (15,17)
SADQ43/SADQ28	AF16	MDA29_MDA28 (15,17)
SADQ44/SADQ26	AD17	MDA30_MDA26 (15,17)
SADQ45/SADQ30	AE19	MDA31_MDA30 (15,17)
SADQ46/SADQ38	AM30	DQS04 (15,17)
SADQ47	AK29	DQS04 (15,17)
SADQ48/SADQ33	AK27	MDA32_MDA33 (15,17)
SADQ49/SADQ37	AJ28	MDA33_MDA37 (15,17)
SADQ50/SADQ39	AK31	MDA34_MDA39 (15,17)
SADQ51/SADQ35	AK31	MDA35 (15,17)
SADQ52/SADQ32	AH27	MDA36_MDA32 (15,17)
SADQ53/SADQ36	AL27	MDA37_MDA36 (15,17)
SADQ54/SADQ34	AN30	MDA38_MDA34 (15,17)
SADQ55/SADQ38	AL30	MDA39_MDA38 (15,17)
SADQ56	AG35	DQS05 (15,17)
SADQ57*/RSV	AG33	DQS05 (15,17)
SADQ58	AG34	DM04 (15,17)
SADQ59/SADQ45	AH35	MDA40_MDA45 (15,17)
SADQ60	MDA41	(15,17)
SADQ61/SADQ46	AF33	MDA42_MDA46 (15,17)
SADQ62/SADQ47	AE34	MDA43_MDA47 (15,17)
SADQ63/SADQ40	AJ33	MDA44 (15,17)
SADQ64/SADQ42	AG32	MDA45_MDA40 (15,17)
SADQ65/SADQ43	AF34	MDA46_MDA42 (15,17)
SADQ66	AA34	DQS06 (15,17)
SADQ67*/RSV	AA35	DQS06 (15,17)
SADQ68	AA33	DM06 (15,17)
SADQ69/SADQ52	AD31	MDA48_MDA52 (15,17)
SADQ70/SADQ53	AD35	MDA49_MDA53 (15,17)
SADQ71	Y33	MDA50 (15,17)
SADQ72	W34	MDA51 (15,17)
SADQ73/SADQ48	AE35	MDA52_MDA48 (15,17)
SADQ74/SADQ49	AE34	MDA53_MDA49 (15,17)
SADQ75	AA32	MDA54 (15,17)
SADQ76	Y35	MDA55 (15,17)
SADQ77	U34	DQS07 (15,17)
SADQ78*/RSV	U35	DQS07 (15,17)
SADQ79	U33	DM07 (15,17)
SADQ80	V34	MDA56 (15,17)
SADQ81	R32	MDA57 (15,17)
SADQ82	R34	MDA58 (15,17)
SADQ83	W35	MDA59 (15,17)
SADQ84	W35	MDA60 (15,17)
SADQ85	W33	MDA61 (15,17)
SADQ86	T33	MDA62 (15,17)
SADQ87	T35	MDA63 (15,17)

GRANTSDALE-PC2	GRANTSDALE-PC2	GRANTSDALE-PC2
DQS00 (15,17)	AM15	SBM0/SAMA9
DM00 (15,17)	AR15	SBMA1/SBMA4
MD00 (15,17)	AN15	SBMA2/SBMA5
MDA1 (15,17)	AL15	SBMA3/SBMA7
MDA2_MDA7 (15,17)	AP14	SBMA4/SBMA8
MDA3 (15,17)	AM12	SBMA5/SBMA12
MDA4 (15,17)	AP13	SBMA6/SAMA12
MDA5 (15,17)	AL12	SBMA7/CKEA0
MDA6 (15,17)	AR13	SBMA8/SBMA9
MDA7_MDA2 (15,17)	AP15	SBMA9/SBMA11
DQS01 (15,17)	AP11	SBMA11/SACK2E
DQS01 (15,17)	AR11	SBMA12/CKEA3
DM01 (15,17)	AL33	SBMA13/SAC3*
MDA8_MDA10 (15,17)	AP17	SBWE*/SBMA6
MDA9_MDA13 (15,17)	AP18	SBCAS*/SBMA1
MDA10 (15,17)	AN17	SBRAS*/SBMA2
MDA11 (15,17)	AR16	SBBA0/SBMA3
MDA12_MDA8 (15,17)	AN16	SBBA1/SAMA7
MDA13_MDA12 (15,17)	AN11	SBBA2/SACK1E
MDA14 (15,17)	AN33	SBCS0*/SBCS2*
MDA15 (15,17)	AM34	SBCS1*/SACS0*
DQS02 (15,17)	AP34	SBCS2*/SBCS0*
DM02 (15,17)	AN30	SBCS3*/SBCS1*
MDA16_MDA17 (15,17)	AP10	SBCKE0/SBCKE2
MDA18_MDA19 (15,17)	AN10	SBCKE1/SBCKE0
MDA19_MDA23 (15,17)	AR9	SBCKE2/SBCKE3
MDA20 (15,17)	AM9	SBCKE3/SBCKE1
MDA21_MDA16 (15,17)	AM33	SBODT0/SBCS3
MDA22_MDA18 (15,17)	AL34	SBODT1/SACAS*
MDA23_MDA22 (15,17)	AL35	SBODT2/SACS1*
DQS03 (15,17)	AK34	SBODT3/SACS2*
DM03 (15,17)	AK36	
MDA24_MDA25 (15,17)	AH22	SBCK0
MDA25_MDA29 (15,17)	AG22	SBCK0*
MDA26_MDA27 (15,17)	AK9	SBCK1/SBCK4
MDA27_MDA31 (15,17)	AL9	SBCK1*/SBCK4*
MDA28_MDA24 (15,17)	AE26	SBCK2
MDA29_MDA28 (15,17)	AE2C	SBCK2*
MDA30_MDA26 (15,17)	AL23	SBCK3
MDA31_MDA30 (15,17)	AL22C	BLCK3*
MDA32 (15,17)	AJ11	SBCK4/SBCK1*
MDA33_MDA32 (15,17)	AL11C	SBCK4*/SBCK1*
MDA34_MDA39 (15,17)	AD28	SBCK5/SBCK5*
MDA35 (15,17)	AD29	SBCK5*/SBCK5*
MDA36_MDA32 (15,17)		
MDA37_MDA36 (15,17)		
MDA38_MDA34 (15,17)		
MDA39_MDA38 (15,17)		
DQS05 (15,17)		
DQS05 (15,17)		
DM04 (15,17)		
MDA40_MDA45 (15,17)		
MDA41 (15,17)		
MDA42_MDA46 (15,17)		
MDA43_MDA47 (15,17)		
MDA44 (15,17)		
MDA45_MDA40 (15,17)		
MDA46_MDA42 (15,17)		
MDA47_MDA43 (15,17)		
DQS06 (15,17)		
DQS06 (15,17)		
DM06 (15,17)		
MDA48_MDA52 (15,17)		
MDA49_MDA53 (15,17)		
MDA50 (15,17)		
MDA51 (15,17)		
MDA52_MDA48 (15,17)		
MDA53_MDA49 (15,17)		
MDA54 (15,17)		
MDA55 (15,17)		
DQS07 (15,17)		
DQS07 (15,17)		
DM07 (15,17)		
MDA56 (15,17)		
MDA57 (15,17)		
MDA58 (15,17)		
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MDA62 (15,17)		
MDA63 (15,17)		

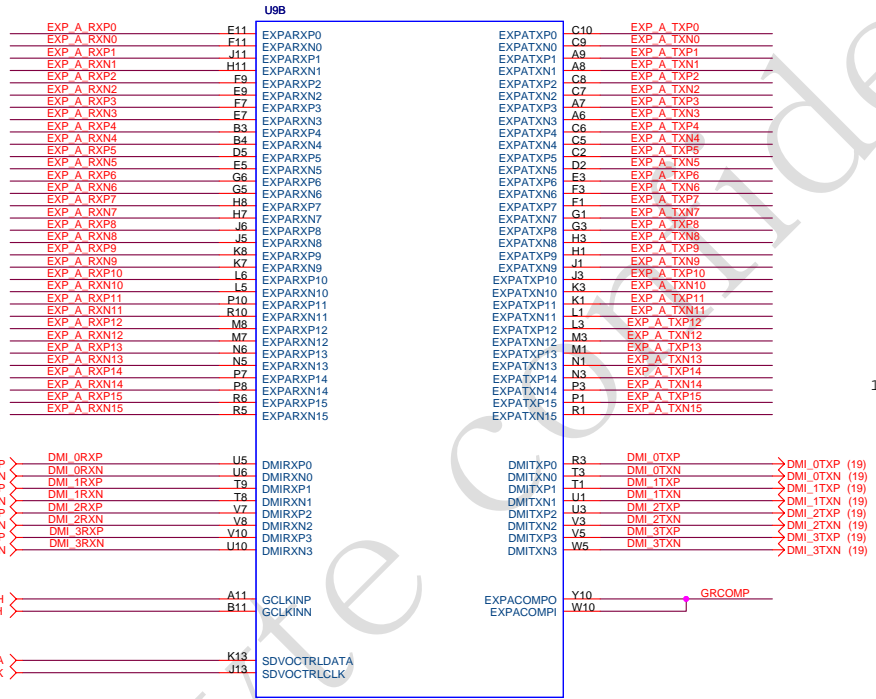
GRANTSDALE-PC2	GRANTSDALE-PC2	GRANTSDALE-PC2
SBDQ00	AK5	DOSB0 (16,17)
SBDQ07*/RSV	AL4	DOSB0 (16,17)
SBDM0	AJ5	DMB0 (16,17)
SBDQ0*/SBDQ5	AL6	DMB0_MDB5 (16,17)
SBDQ1	AH6	MDB1 (16,17)
SBDQ2/SBDQ7	AL7	MDB1_MDB7 (16,17)
SBDQ3	AN6	MDB3 (16,17)
SBDQ4	AG9	MDB4 (16,17)
SBDQ5/SBDQ0	AH7	MDB5_MDB0 (16,17)
SBDQ6/SBDQ2	AL5	MDB6_MDB2 (16,17)
SBDQ7/SBDQ6	AM5	MDB7_MDB6 (16,17)
SBDQ8	AK10	DOSB1 (16,17)
SBDQ9*/RSV	AH10	DOSB1 (16,17)
SBDM1	AJ8	DMB1 (16,17)
SBDQ8/SBDQ12	AL8	DMB8_MDB12 (16,17)
SBDQ9/SBDQ13	AL9	DMB9_MDB13 (16,17)
SBDQ10	AE11	MDB10 (16,17)
SBDQ11	AE11	MDB11 (16,17)
SBDQ12/SBDQ8	AJ7	MDB12_MDB8 (16,17)
SBDQ13/SBDQ9	AL7	MDB13_MDB9 (16,17)
SBDQ14	AG10	MDB14 (16,17)
SBDQ15	AG11	MDB15 (16,17)
SBDQ16	AK13	DOSB2 (16,17)
SBDQ17*/RSV	AL14	DOSB2 (16,17)
SBDM2	AH13	DMB2 (16,17)
SBDQ18/SBDQ17	AE13	DMB2_MDB7 (16,17)
SBDQ19/SBDQ17	AH12	MDB17_MDB21 (16,17)
SBDQ20/SBDQ19	AD14	MDB18_MDB19 (16,17)
SBDQ21/SBDQ23	AD15	MDB19_MDB23 (16,17)
SBDQ22/SBDQ16	AD12	MDB20 (16,17)
SBDQ23/SBDQ18	AE13	MDB21_MDB16 (16,17)
SBDQ24/SBDQ23	AG14	MDB22_MDB18 (16,17)
SBDQ25/SBDQ23	AF14	MDB23_MDB22 (16,17)
SBDQ26/SBDQ25	AF20	DOSB3 (16,17)
SBDQ27/SBDQ29	AK19	DMB3 (16,17)
SBDQ28/SBDQ27	AH19	DMB24_MDB25 (16,17)
SBDQ29/SBDQ31	AH21	MDB26_MDB27 (16,17)
SBDQ30/SBDQ31	AD21	MDB27_MDB31 (16,17)
SBDQ31/SBDQ30	AD18	MDB28_MDB24 (16,17)
SBDQ32/SBDQ28	AE22	MDB29_MDB28 (16,17)
SBDQ33/SBDQ30	AF22	MDB30_MDB26 (16,17)
SBDQ34	AH25	DOSB4 (16,17)
SBDQ35*/RSV	AG26	DOSB4 (16,17)
SBDQ36/SBDQ37	AG24	DMB4 (16,17)
SBDQ37	AF25	DMB32_MDB37 (16,17)
SBDQ38/SBDQ39	AL26	MDB33 (16,17)
SBDQ39/SBDQ38	AJ26	MDB34_MDB39 (16,17)
SBDQ40/SBDQ38	AF23	MDB35 (16,17)
SBDQ41/SBDQ36	AD23	MDB36_MDB32 (16,17)
SBDQ42/SBDQ36	AL25	MDB37_MDB36 (16,17)
SBDQ43/SBDQ34	AJ25	MDB38_MDB34 (16,17)
SBDQ44/SBDQ38	AH28	DOSB5 (16,17)
SBDQ45*/RSV	AH30	DOSB5 (16,17)
SBDQ46	AK32	DMB5 (16,17)
SBDQ47/SBDQ46	AJ31	DMB40_MDB45 (16,17)
SBDQ48/SBDQ49	AG31	DMB42_MDB43 (16,17)
SBDQ49/SBDQ53	AF28	MDB43_MDB47 (16,17)
SBDQ50/SBDQ55	AJ29	MDB44_MDB40 (16,17)
SBDQ51	AK33	MDB45_MDB44 (16,17)
SBDQ52/SBDQ48	AG30	MDB46_MDB42 (16,17)
SBDQ53/SBDQ52	AG27	MDB47_MDB46 (16,17)
SBDQ54	AB31	DOSB6 (16,17)
SBDQ55/SBDQ50	AC30	DOSB6 (16,17)
SBDQ56/SBDQ61	AD24	DMB6 (16,17)
SBDQ57	AE27	DMB48_MDB49 (16,17)
SBDQ58/SBDQ63	AC26	MDB50_MDB55 (16,17)
SBDQ59	SBDQ51	MDB51 (16,17)
SBDQ60	AE28	MDB52_MDB48 (16,17)
SBDQ61/SBDQ56	AE11	MDB53_MDB52 (16,17)
SBDQ62	AC28	MDB54 (16,17)
SBDQ63/SBDQ58	AB27	MDB55_MDB50 (16,17)
SBDQ64	W27	DOSB7 (16,17)
SBDQ65*/RSV	Y28	DOSB7 (16,17)
SBDQ66	W31	DMB7 (16,17)
SBDQ67	AA28	DMB56_MDB61 (16,17)
SBDQ68/SBDQ63	SBDQ57	MDB57 (16,17)
SBDQ69	W29	MDB58_MDB53 (16,17)
SBDQ70	V28	MDB59 (16,17)
SBDQ71/RSV	Y26	MDB60 (16,17)
SBDQ72/RSV	AA29	MDB61_MDB56 (16,17)
SBDQ73/SBDQ62	W26	MDB62 (16,17)
SBDQ74	U26	MDB63_MDB58 (16,17)



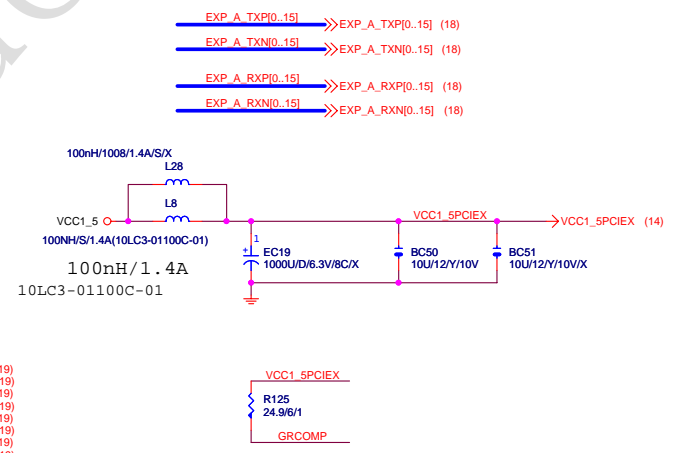
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Trace Width Need = 5mils
Trace Space Need > 10mils

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Trace Width Need = 5mils
Trace Space Need > 10mils

Trace Length Need < 1.5"
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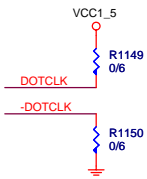


For DVO Function

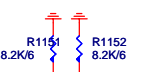


GIGABYTE			
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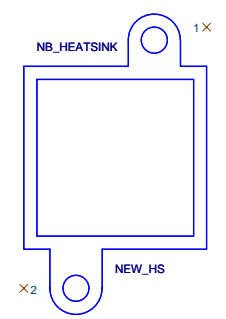
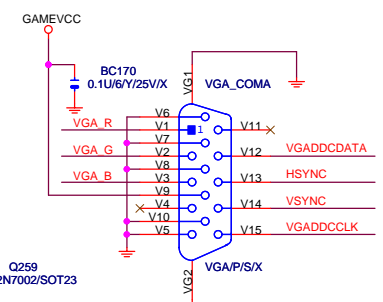
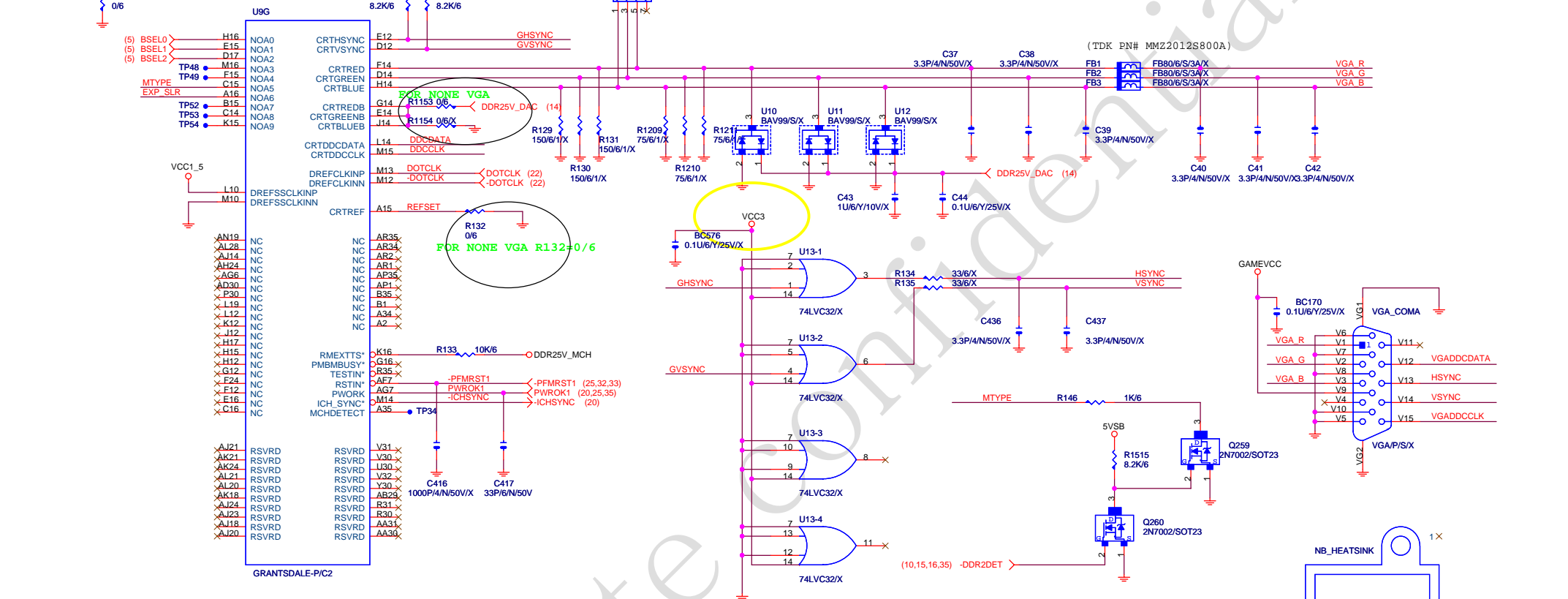
FOR NONE VGA



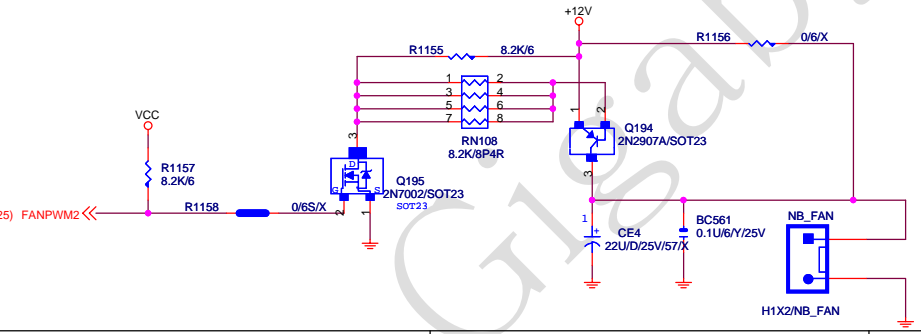
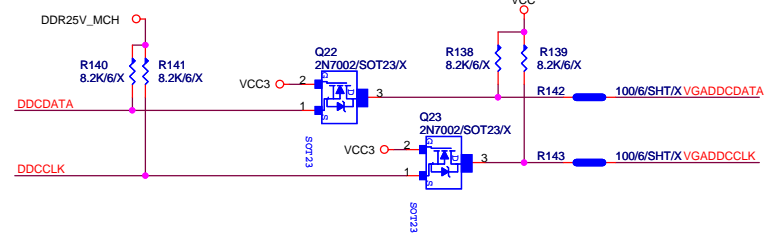
FOR NONE VGA



FOR NONE VGA



When -DDR2DET=0, MTYPE --> 0
 When -DDR2DET=1, MTYPE --> 1



GIGABYTE			
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U9H		
A10	VSS_1	VSS_101
A18	VSS_2	VSS_102
AA26	VSS_3	VSS_103
A3	VSS_4	VSS_104
A30	VSS_5	VSS_105
A33	VSS_6	VSS_106
A5	VSS_7	VSS_107
AA1	VSS_8	VSS_108
AA10	VSS_9	VSS_109
AA2	VSS_14	VSS_110
AA26	VSS_16	VSS_111
AA27	VSS_17	VSS_112
AA3	VSS_18	VSS_113
AA4	VSS_19	VSS_114
AA5	VSS_20	VSS_115
AA6	VSS_21	VSS_116
AA7	VSS_22	VSS_117
AA8	VSS_23	VSS_118
AA9	VSS_24	VSS_119
AB28	VSS_26	VSS_120
AB32	VSS_28	VSS_121
AB35	VSS_29	VSS_122
AC27	VSS_31	VSS_123
AC29	VSS_32	VSS_124
AC31	VSS_33	VSS_125
AC32	VSS_34	VSS_126
AD14	VSS_35	VSS_127
AD13	VSS_36	VSS_128
AD16	VSS_37	VSS_129
AD19	VSS_38	VSS_130
AD22	VSS_39	VSS_131
AD26	VSS_41	VSS_132
AD27	VSS_42	VSS_133
AD34	VSS_43	VSS_134
AE12	VSS_44	VSS_135
AE14	VSS_45	VSS_136
AE15	VSS_46	VSS_137
AE17	VSS_47	VSS_138
AE18	VSS_48	VSS_139
AE20	VSS_49	VSS_140
AE21	VSS_50	VSS_141
AE23	VSS_51	VSS_142
AE24	VSS_52	VSS_143
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AE30	VSS_54	VSS_145
AE32	VSS_55	VSS_146
AE4	VSS_56	VSS_147
AE6	VSS_57	VSS_148
AE9	VSS_58	VSS_149
AF1	VSS_59	VSS_150
AF10	VSS_60	VSS_151
AF12	VSS_61	VSS_152
AF15	VSS_62	VSS_153
AF18	VSS_63	VSS_154
AF21	VSS_64	VSS_155
AF26	VSS_65	VSS_156
AF29	VSS_66	VSS_157
AF30	VSS_67	VSS_158
AF31	VSS_68	VSS_159
AF32	VSS_69	VSS_160
AF35	VSS_70	VSS_161
AF4	VSS_71	VSS_162
AF6	VSS_72	VSS_163
AF8	VSS_73	VSS_164
AG12	VSS_74	VSS_165
AG13	VSS_75	VSS_166
AG15	VSS_76	VSS_167
AG16	VSS_77	VSS_168
AG18	VSS_78	VSS_169
AG19	VSS_79	VSS_170
AG21	VSS_80	VSS_171
AG22	VSS_81	VSS_172
AG25	VSS_82	VSS_173
AG28	VSS_83	VSS_174
AG29	VSS_84	VSS_175
AG5	VSS_85	VSS_176
AH1	VSS_86	VSS_177
AH11	VSS_87	VSS_178
AH14	VSS_88	VSS_179
AH17	VSS_89	VSS_180
AH20	VSS_90	VSS_181
AH23	VSS_91	VSS_182
AH26	VSS_92	VSS_183
AH29	VSS_93	VSS_184
AH32	VSS_94	VSS_185
AH34	VSS_95	VSS_186
AH5	VSS_96	VSS_187
AH6	VSS_97	VSS_188
AH8	VSS_98	VSS_189
AJ10	VSS_99	VSS_190
AJ13	VSS_100	VSS_191

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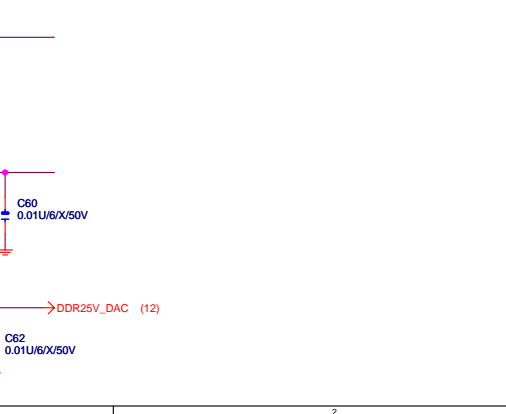
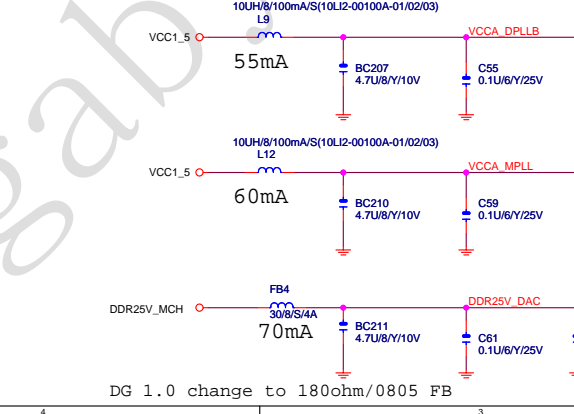
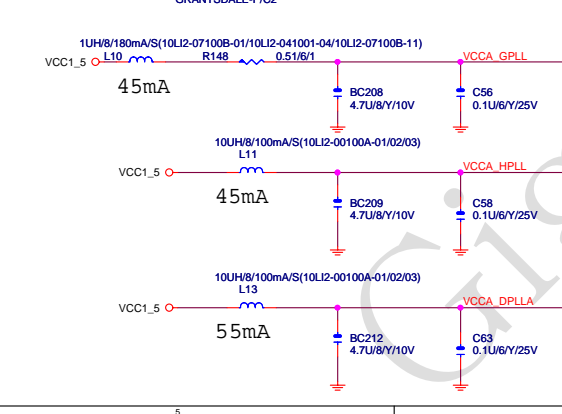
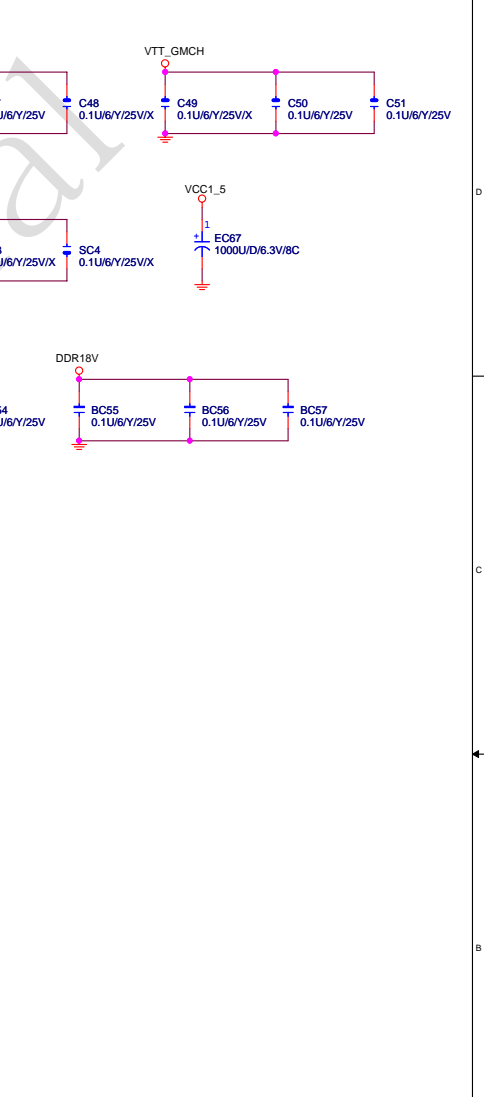
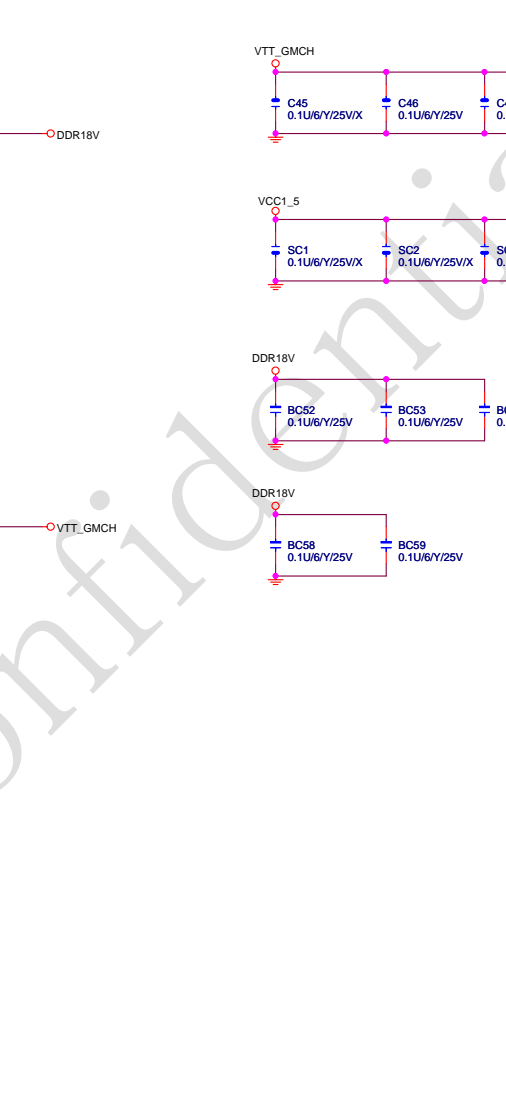
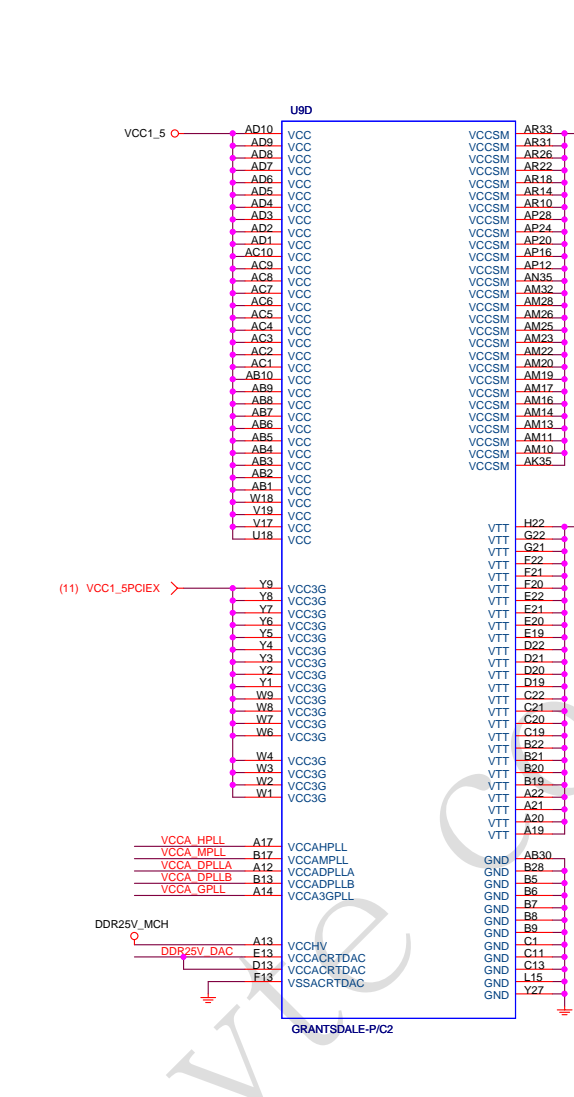
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F25	VSS_202	VSS_302
F29	VSS_203	VSS_305
F30	VSS_204	VSS_307
F32	VSS_205	VSS_308
F35	VSS_206	VSS_309
F4	VSS_207	VSS_310
F5	VSS_208	VSS_311
F6	VSS_209	VSS_312
F8	VSS_210	VSS_313
G10	VSS_211	VSS_317
G11	VSS_212	VSS_320
G15	VSS_213	VSS_321
G17	VSS_215	VSS_322
G19	VSS_216	VSS_323
G2	VSS_217	VSS_324
G20	VSS_218	VSS_325
G23	VSS_219	VSS_326
G26	VSS_220	VSS_327
G27	VSS_221	VSS_328
G28	VSS_222	VSS_332
G4	VSS_223	VSS_332
G7	VSS_224	VSS_335
G8	VSS_225	VSS_336
G9	VSS_226	VSS_337
H10	VSS_227	VSS_338
H13	VSS_228	VSS_339
H2	VSS_229	VSS_340
H21	VSS_230	VSS_341
H24	VSS_231	VSS_344
H25	VSS_232	VSS_344
H27	VSS_233	VSS_344
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H32	VSS_235	VSS_348
H34	VSS_236	VSS_349
H4	VSS_237	VSS_350
H5	VSS_238	VSS_351
H6	VSS_239	VSS_352
H8	VSS_240	VSS_353
J10	VSS_241	VSS_354
J15	VSS_242	VSS_357
J16	VSS_243	VSS_357
J17	VSS_244	VSS_358
J18	VSS_245	VSS_359
J2	VSS_246	VSS_359
J20	VSS_247	VSS_363
J23	VSS_248	VSS_364
J30	VSS_249	VSS_365
J4	VSS_250	VSS_365
J7	VSS_251	VSS_366
J8	VSS_252	VSS_367
J9	VSS_253	VSS_368
K10	VSS_254	VSS_369
K11	VSS_255	VSS_370
K14	VSS_256	VSS_371
K2	VSS_257	VSS_374
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K6	VSS_267	VSS_375
K9	VSS_268	VSS_375
L11	VSS_269	VSS_375
L13	VSS_270	VSS_375
L16	VSS_271	VSS_375
L17	VSS_272	VSS_375
L18	VSS_273	VSS_375
L2	VSS_274	VSS_375
L20	VSS_275	VSS_375
L21	VSS_276	VSS_375
L22	VSS_277	VSS_375
L24	VSS_278	VSS_375
L27	VSS_279	VSS_375
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L4	VSS_282	VSS_375
L7	VSS_283	VSS_375
L8	VSS_284	VSS_375
E18	VSS_285	VSS_375
L9	VSS_286	VSS_375
M10	VSS_287	VSS_375
M17	VSS_288	VSS_375
E29	VSS_289	VSS_375
M20	VSS_290	VSS_375
M24	VSS_291	VSS_375
M25	VSS_292	VSS_375
M27	VSS_293	VSS_375
M29	VSS_294	VSS_375
M34	VSS_295	VSS_375
M4	VSS_296	VSS_375
M5	VSS_297	VSS_375
M6	VSS_298	VSS_375
M9	VSS_299	VSS_375
M9	VSS_300	VSS_375

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N10		
N2	VSS_305	VSS_305
N28	VSS_307	VSS_307
N30	VSS_308	VSS_308
N32	VSS_309	VSS_309
N4	VSS_310	VSS_310
N7	VSS_311	VSS_311
N8	VSS_312	VSS_312
N9	VSS_313	VSS_313
P2	VSS_317	VSS_317
P29	VSS_320	VSS_320
P31	VSS_321	VSS_321
P32	VSS_322	VSS_322
P35	VSS_323	VSS_323
P4	VSS_324	VSS_324
P5	VSS_325	VSS_325
P6	VSS_326	VSS_326
P9	VSS_327	VSS_327
R2	VSS_332	VSS_332
R26	VSS_335	VSS_335
R27	VSS_336	VSS_336
R4	VSS_337	VSS_337
R7	VSS_338	VSS_338
R8	VSS_339	VSS_339
R9	VSS_340	VSS_340
T10	VSS_341	VSS_341
T2	VSS_344	VSS_344
T26	VSS_347	VSS_347
T30	VSS_348	VSS_348
T32	VSS_349	VSS_349
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T4	VSS_351	VSS_351
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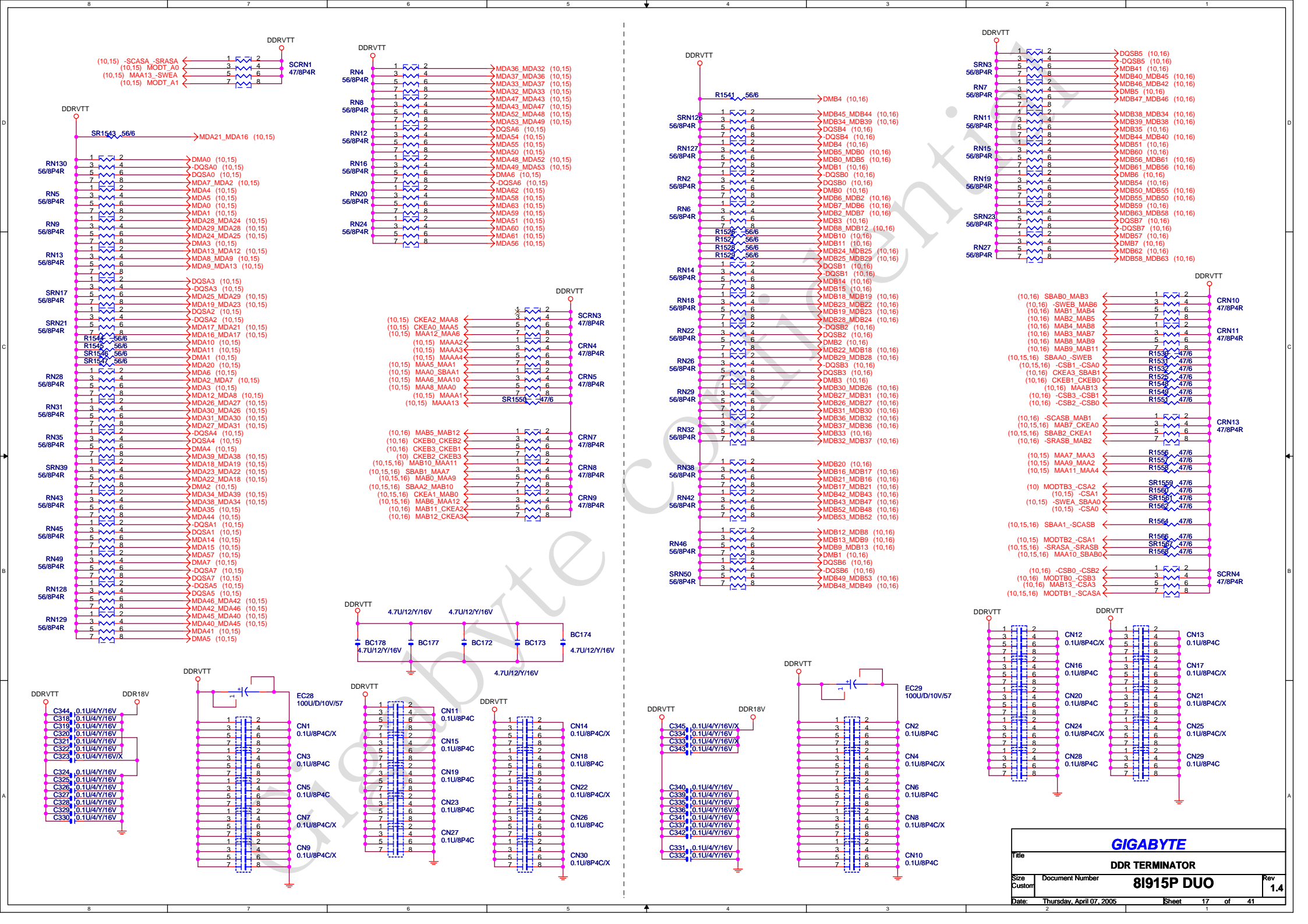
GIGABYTE

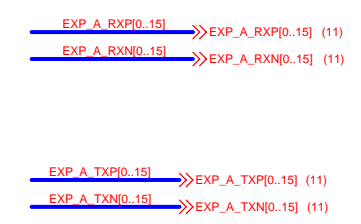
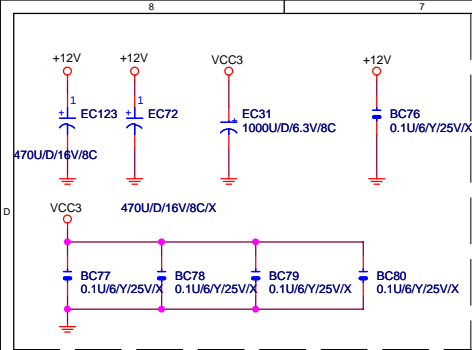
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GMCH-GND			
Size	Document Number	8I915P DUO	Rev
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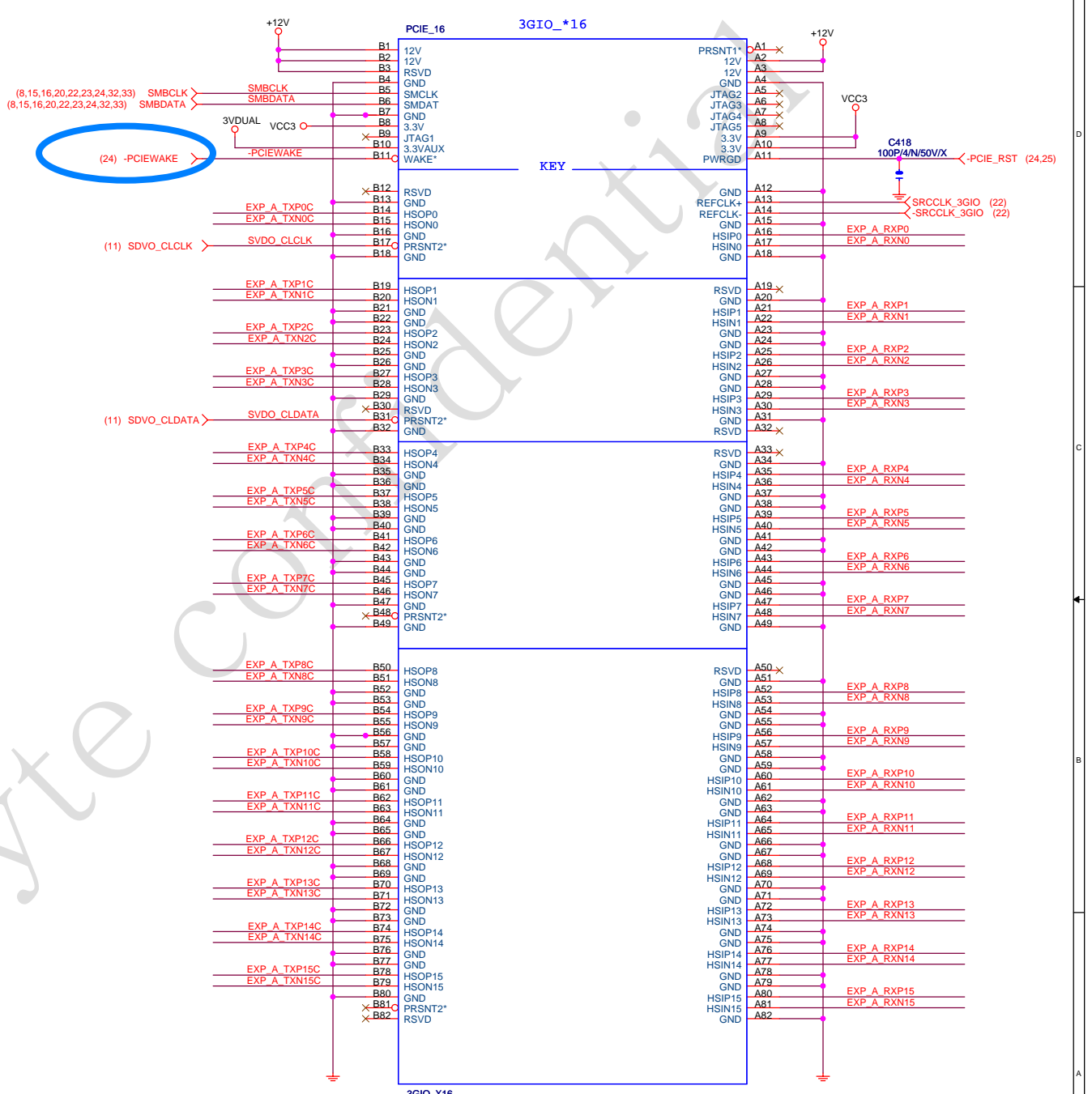
DG 1.0 change to 180ohm/0805 FB

GIGABYTE			
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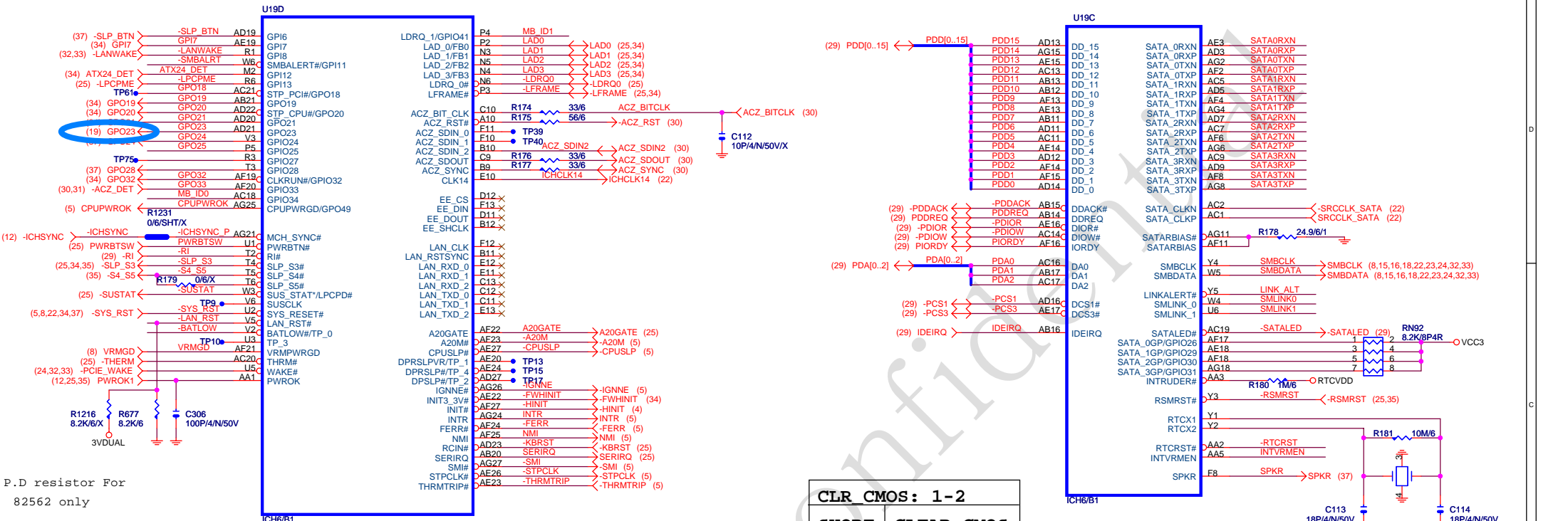




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EXP_A_TXN0	C71	0.1U/6/Y/25V	EXP_A_TXN0C
EXP_A_TXP1	C72	0.1U/6/Y/25V	EXP_A_TXP1C
EXP_A_TXN1	C73	0.1U/6/Y/25V	EXP_A_TXN1C
EXP_A_TXP2	C74	0.1U/6/Y/25V	EXP_A_TXP2C
EXP_A_TXN2	C75	0.1U/6/Y/25V	EXP_A_TXN2C
EXP_A_TXP3	C76	0.1U/6/Y/25V	EXP_A_TXP3C
EXP_A_TXN3	C77	0.1U/6/Y/25V	EXP_A_TXN3C
EXP_A_TXP4	C78	0.1U/6/Y/25V	EXP_A_TXP4C
EXP_A_TXN4	C79	0.1U/6/Y/25V	EXP_A_TXN4C
EXP_A_TXP5	C80	0.1U/6/Y/25V	EXP_A_TXP5C
EXP_A_TXN5	C81	0.1U/6/Y/25V	EXP_A_TXN5C
EXP_A_TXP6	C82	0.1U/6/Y/25V	EXP_A_TXP6C
EXP_A_TXN6	C83	0.1U/6/Y/25V	EXP_A_TXN6C
EXP_A_TXP7	C84	0.1U/6/Y/25V	EXP_A_TXP7C
EXP_A_TXN7	C85	0.1U/6/Y/25V	EXP_A_TXN7C
EXP_A_TXP8	C86	0.1U/6/Y/25V	EXP_A_TXP8C
EXP_A_TXN8	C87	0.1U/6/Y/25V	EXP_A_TXN8C
EXP_A_TXP9	C88	0.1U/6/Y/25V	EXP_A_TXP9C
EXP_A_TXN9	C89	0.1U/6/Y/25V	EXP_A_TXN9C
EXP_A_TXP10	C90	0.1U/6/Y/25V	EXP_A_TXP10C
EXP_A_TXN10	C91	0.1U/6/Y/25V	EXP_A_TXN10C
EXP_A_TXP11	C92	0.1U/6/Y/25V	EXP_A_TXP11C
EXP_A_TXN11	C93	0.1U/6/Y/25V	EXP_A_TXN11C
EXP_A_TXP12	C94	0.1U/6/Y/25V	EXP_A_TXP12C
EXP_A_TXN12	C95	0.1U/6/Y/25V	EXP_A_TXN12C
EXP_A_TXP13	C96	0.1U/6/Y/25V	EXP_A_TXP13C
EXP_A_TXN13	C97	0.1U/6/Y/25V	EXP_A_TXN13C
EXP_A_TXP14	C98	0.1U/6/Y/25V	EXP_A_TXP14C
EXP_A_TXN14	C99	0.1U/6/Y/25V	EXP_A_TXN14C
EXP_A_TXP15	C100	0.1U/6/Y/25V	EXP_A_TXP15C
EXP_A_TXN15	C101	0.1U/6/Y/25V	EXP_A_TXN15C

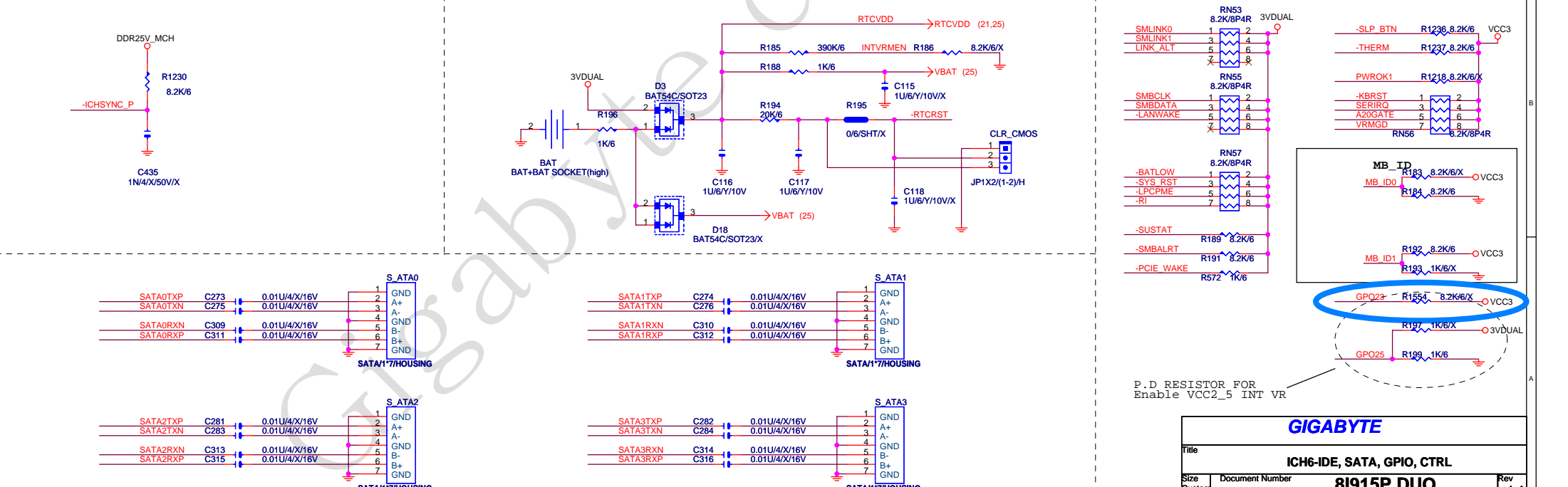


Gigabyte



P.D resistor For
82562 only

CLR CMOS: 1-2	
SHORT	CLEAR CMOS
OPEN	NORMAL

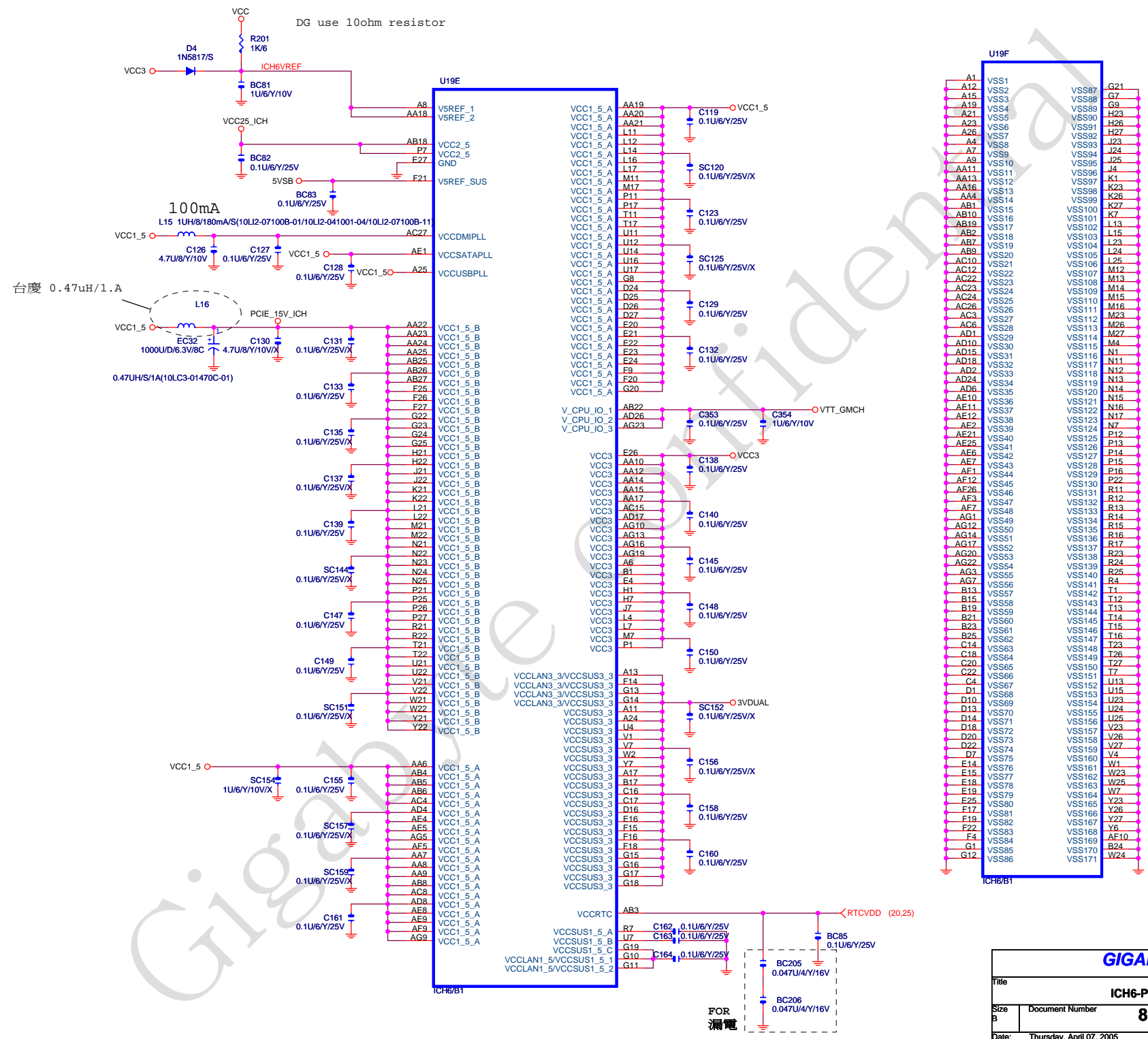


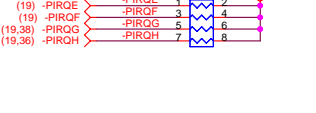
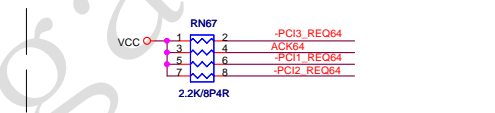
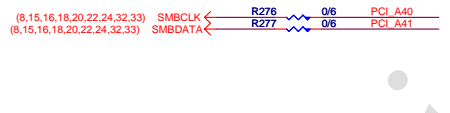
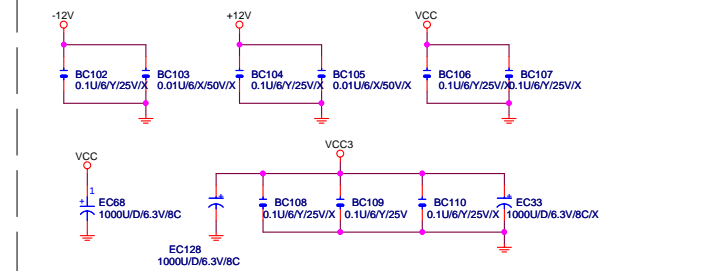
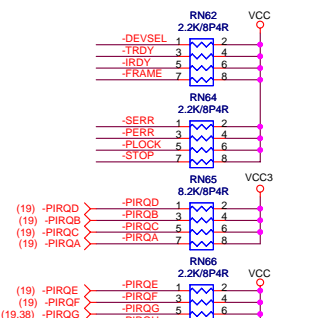
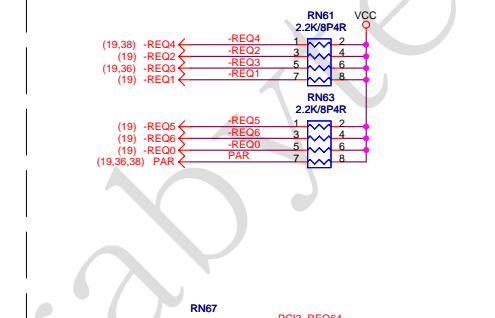
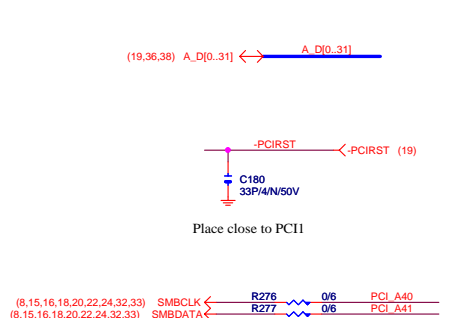
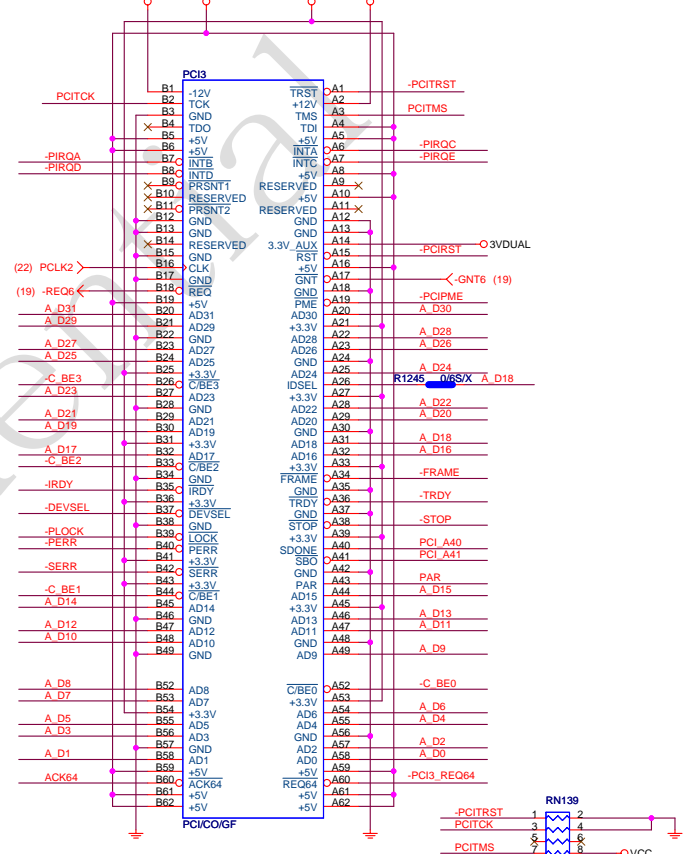
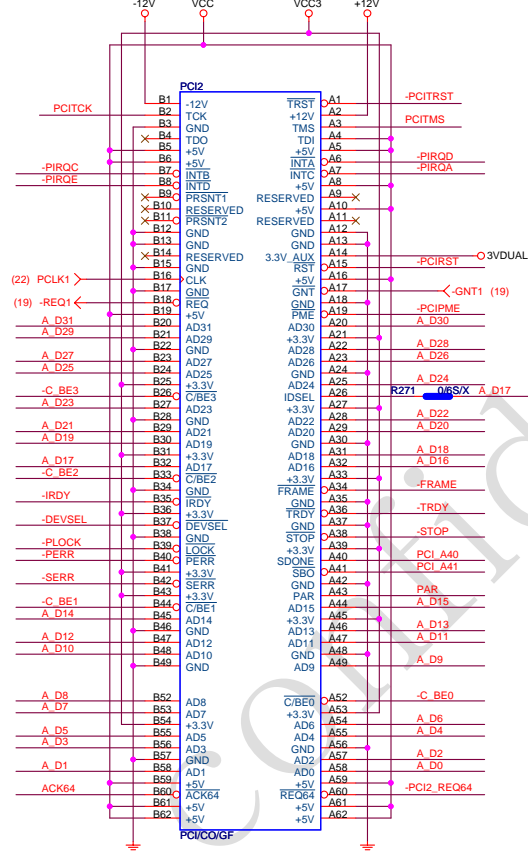
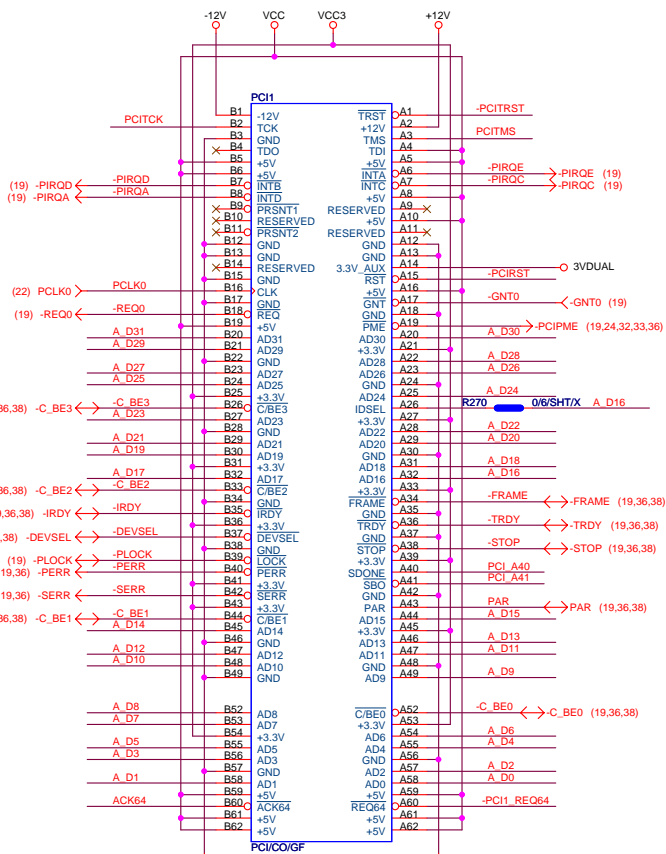
Need Change to X5R or X7R type

GIGABYTE

Title **ICH6-IDE, SATA, GPIO, CTRL**

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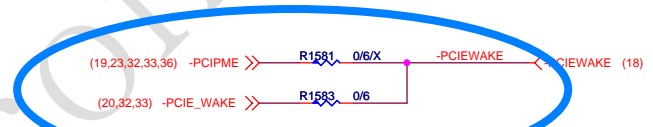
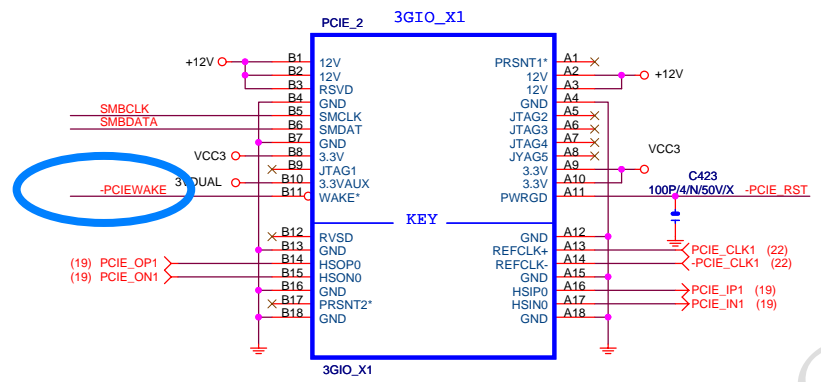
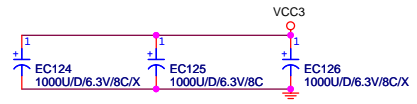
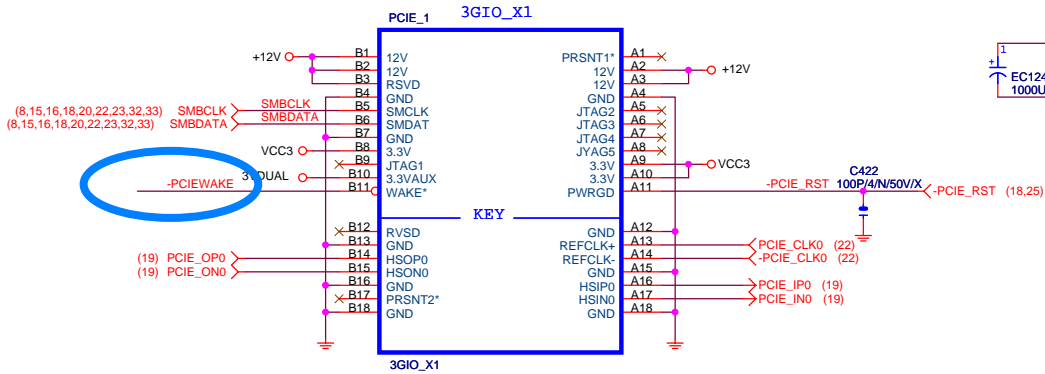


GIGABYTE

Title: **PCI SLOT**

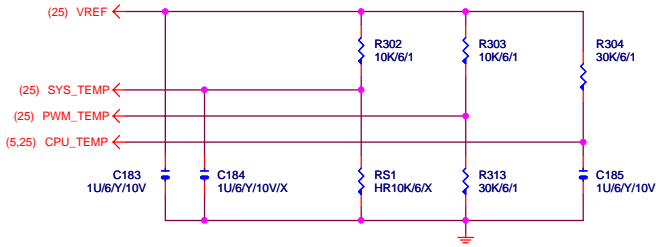
Size: Document Number **8I915P DUO** Rev **1.4**

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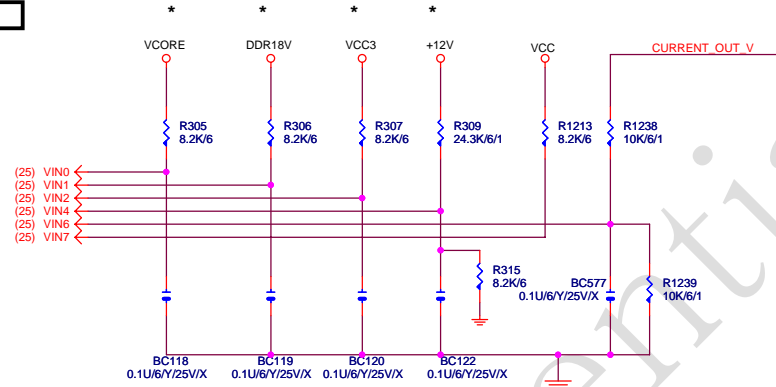


Gigabyte Confidential

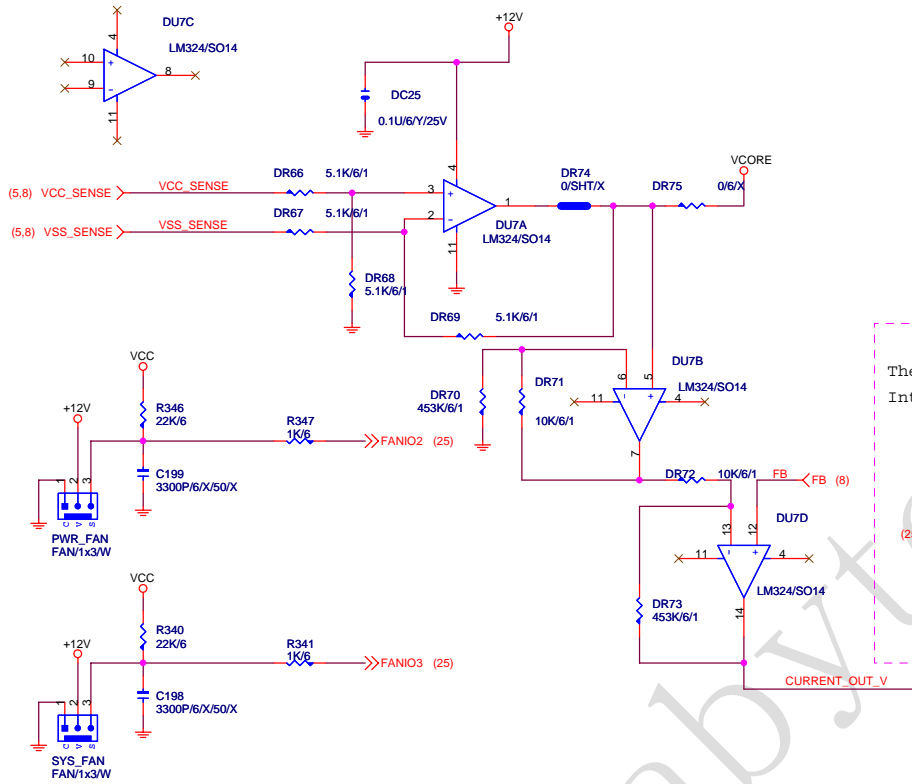
TEMP. SENSE



VOLTAGE SENSE



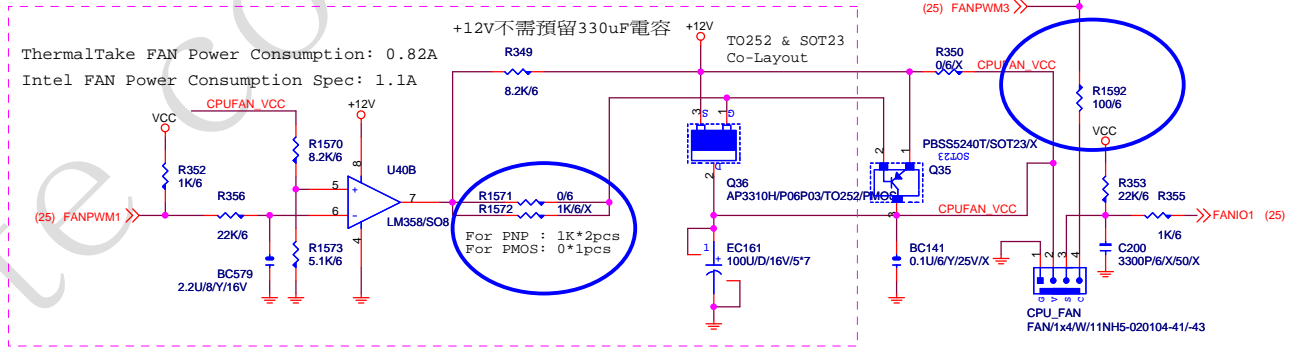
DUAL POWER



CPU/SYS FAN

If use PBSS5240 lpcs : (non airflow) If use PBSS5240 lpcs : (with airflow)

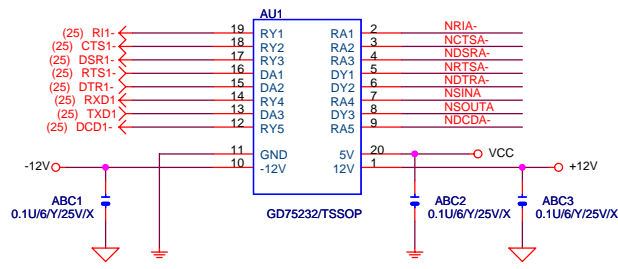
CPUFAN_VCC=12V: Temp=40 deg	CPUFAN_VCC=12V: Temp=33 deg
CPUFAN_VCC=11V: Temp=82 deg	CPUFAN_VCC=11V: Temp=62 deg
CPUFAN_VCC=10V: Temp=70 deg	CPUFAN_VCC=10V: Temp=86 deg
CPUFAN_VCC= 9V: Temp=110 deg	CPUFAN_VCC= 9V: Temp=117 deg
CPUFAN_VCC= 8V: Temp>200 deg	CPUFAN_VCC= 8V: Temp>122 deg



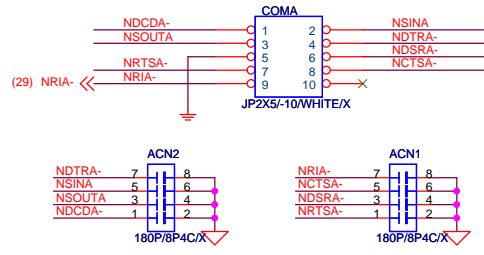
GIGABYTE

Title		
HWM/FAN/C/BIOS		
Size	Document Number	Rev
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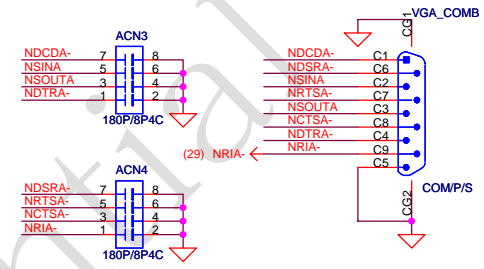
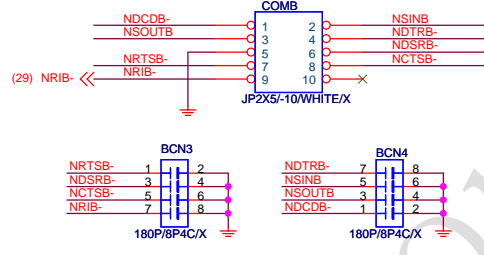
COMA / COMB



INTERNAL COMA

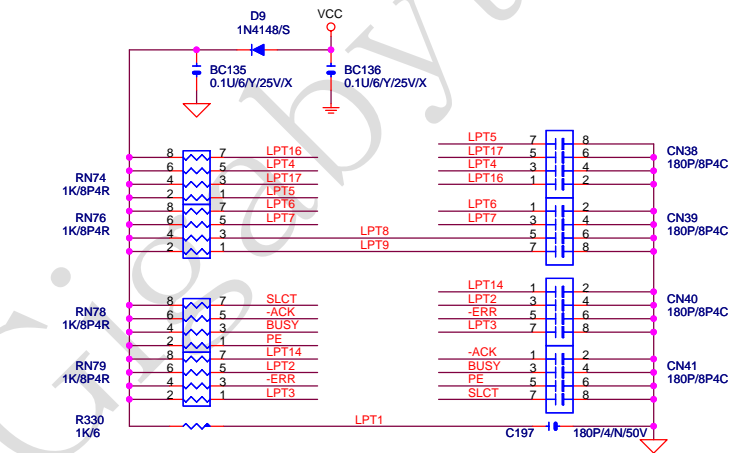
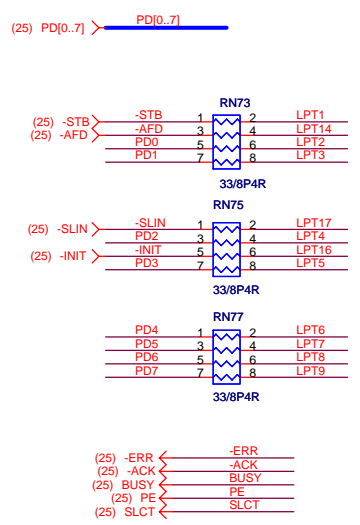


INTERNAL COMB

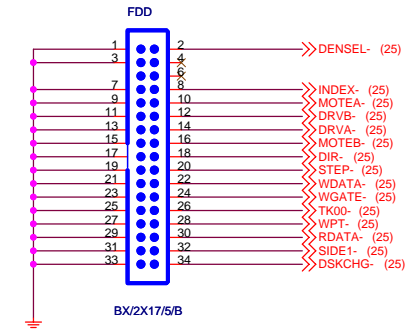


PLACE NEAR VGA_COM CONNECTOR

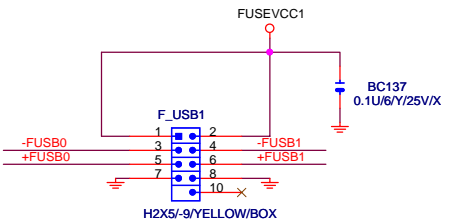
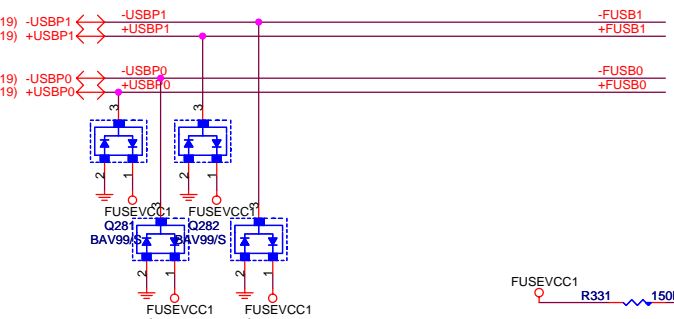
LPT



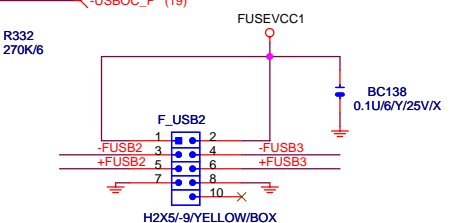
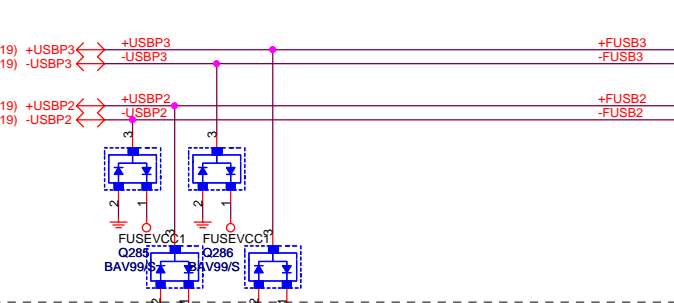
FLOPPY



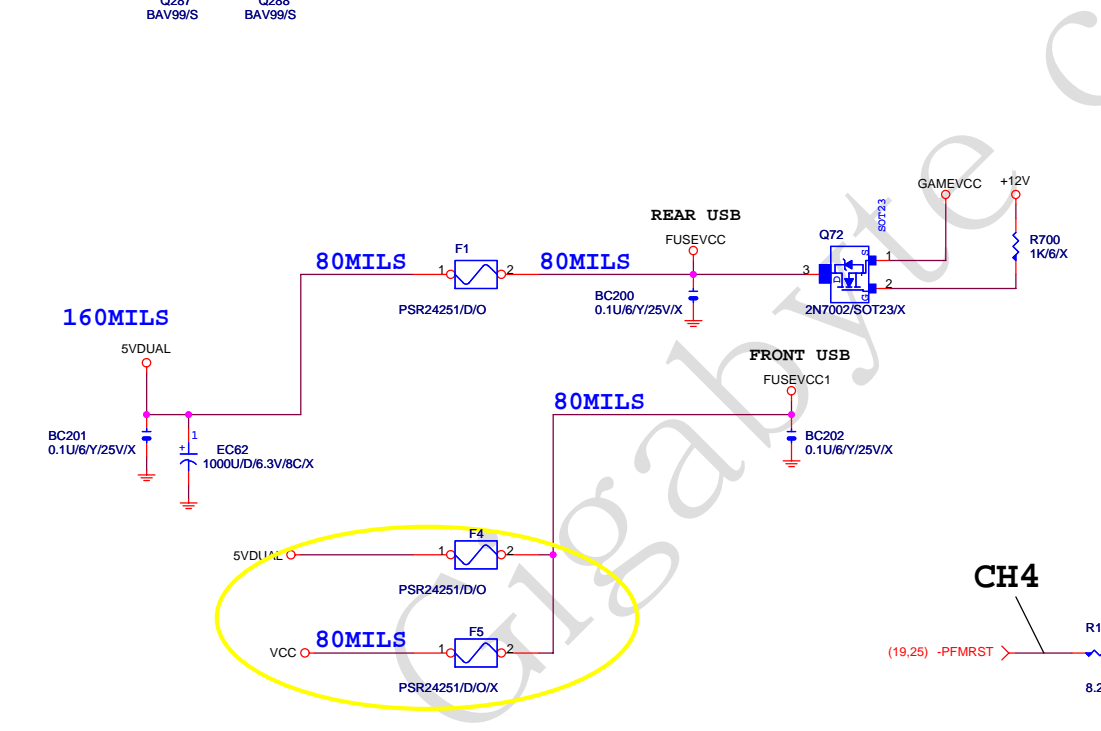
FRONT USB1



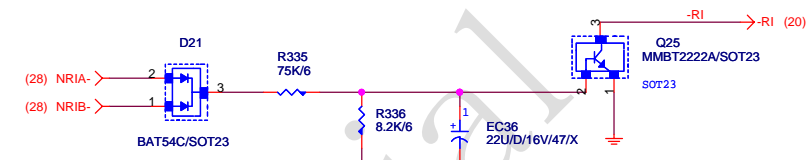
FRONT USB2



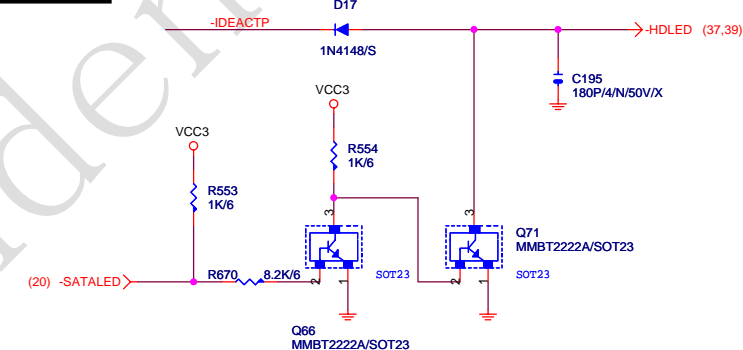
FUSEVCC, GAMEVCC



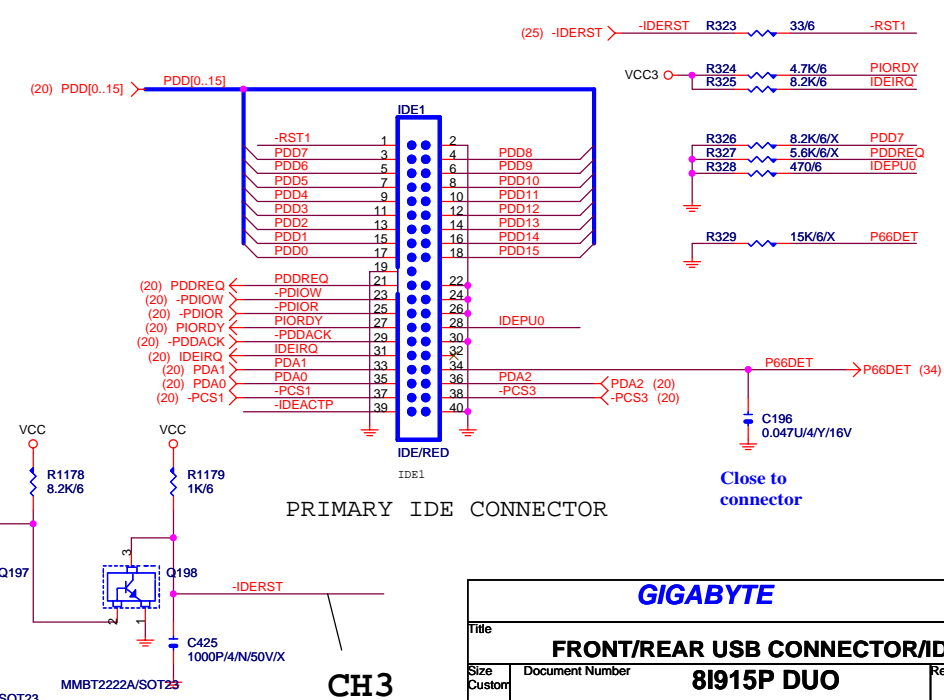
RING IN



IDE/SATA LED

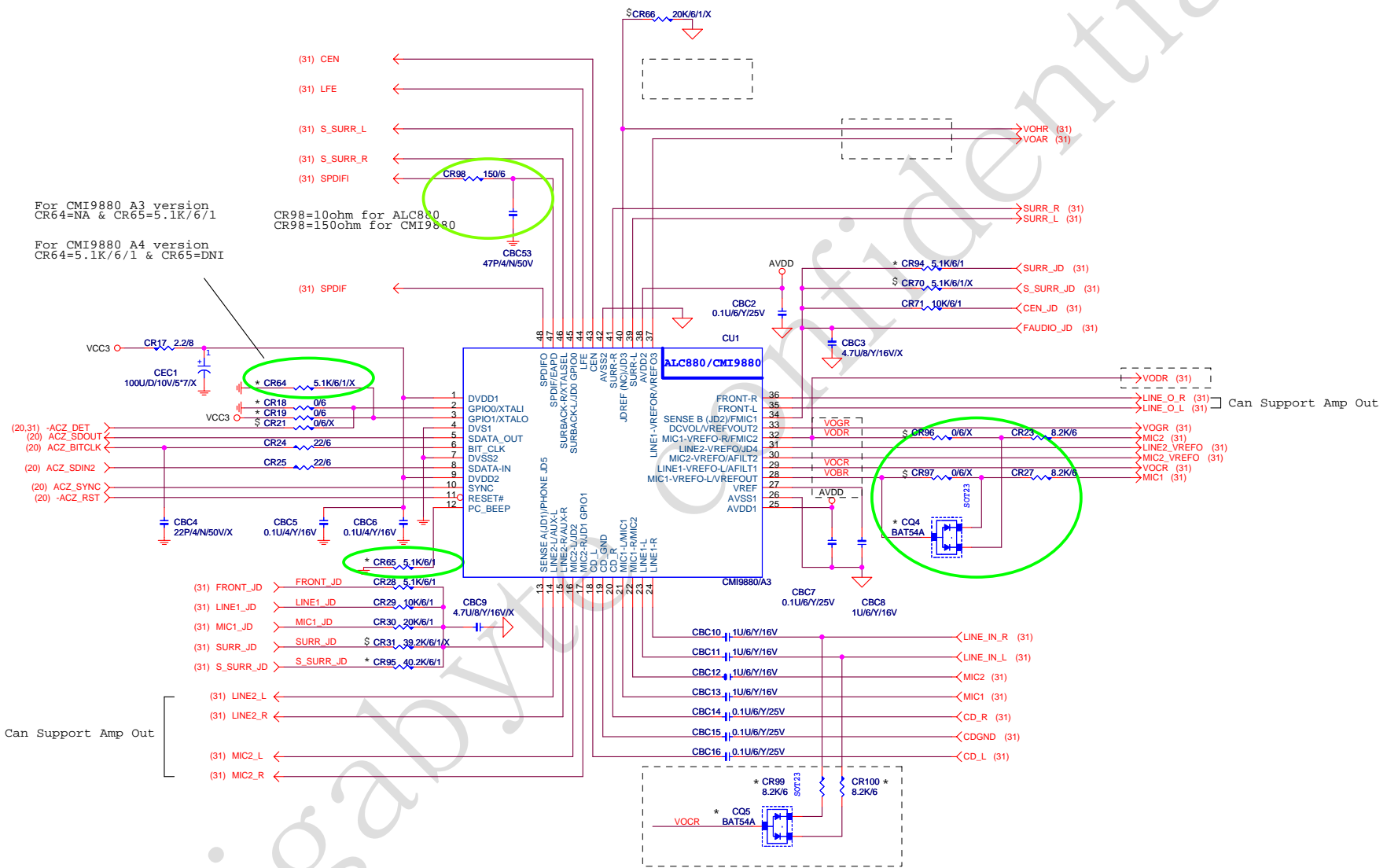


IDE



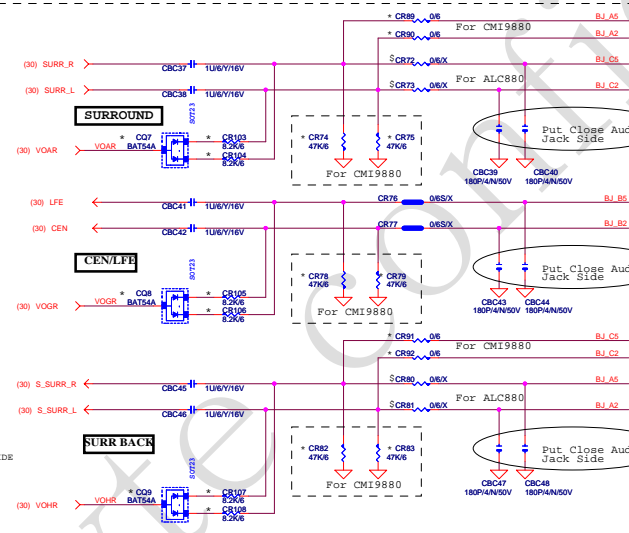
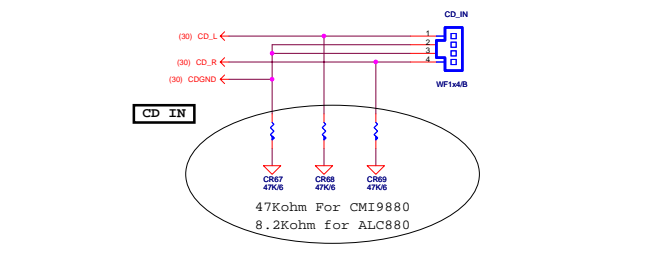
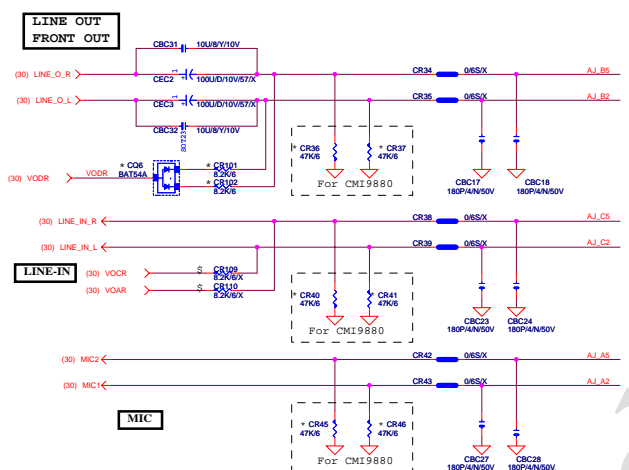
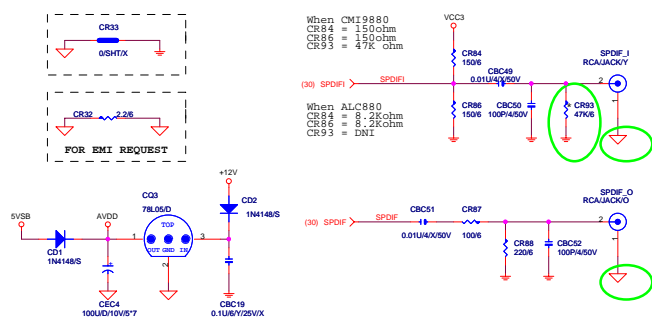
GIGABYTE			
FRONT/REAR USB CONNECTOR/IDE			
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"\$" means for ALC880 only
 "*" means for CMI9880 only



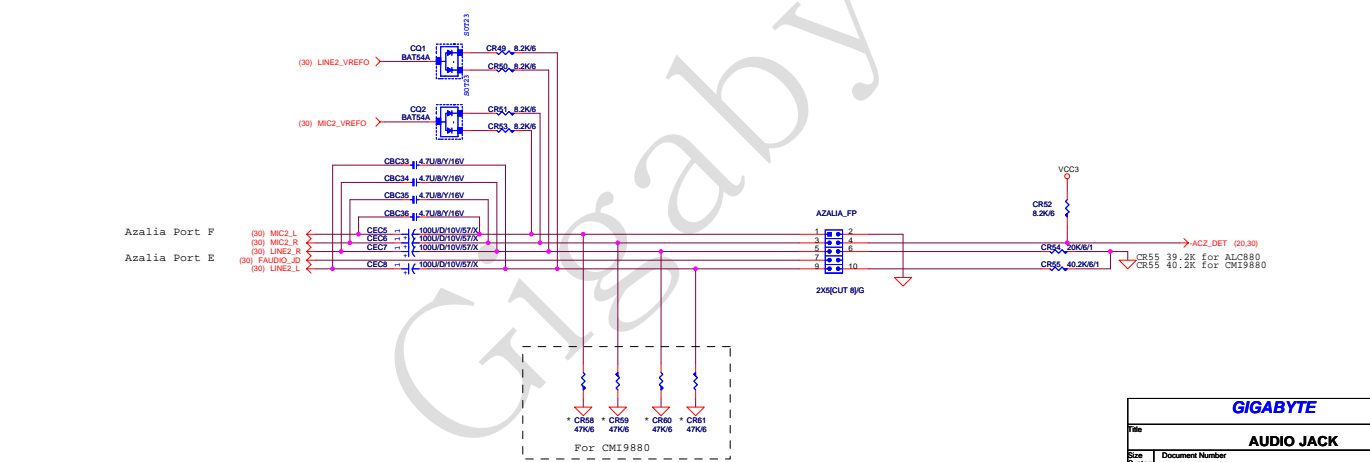
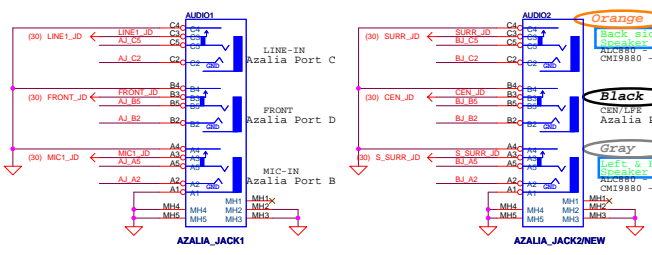
GIGABYTE

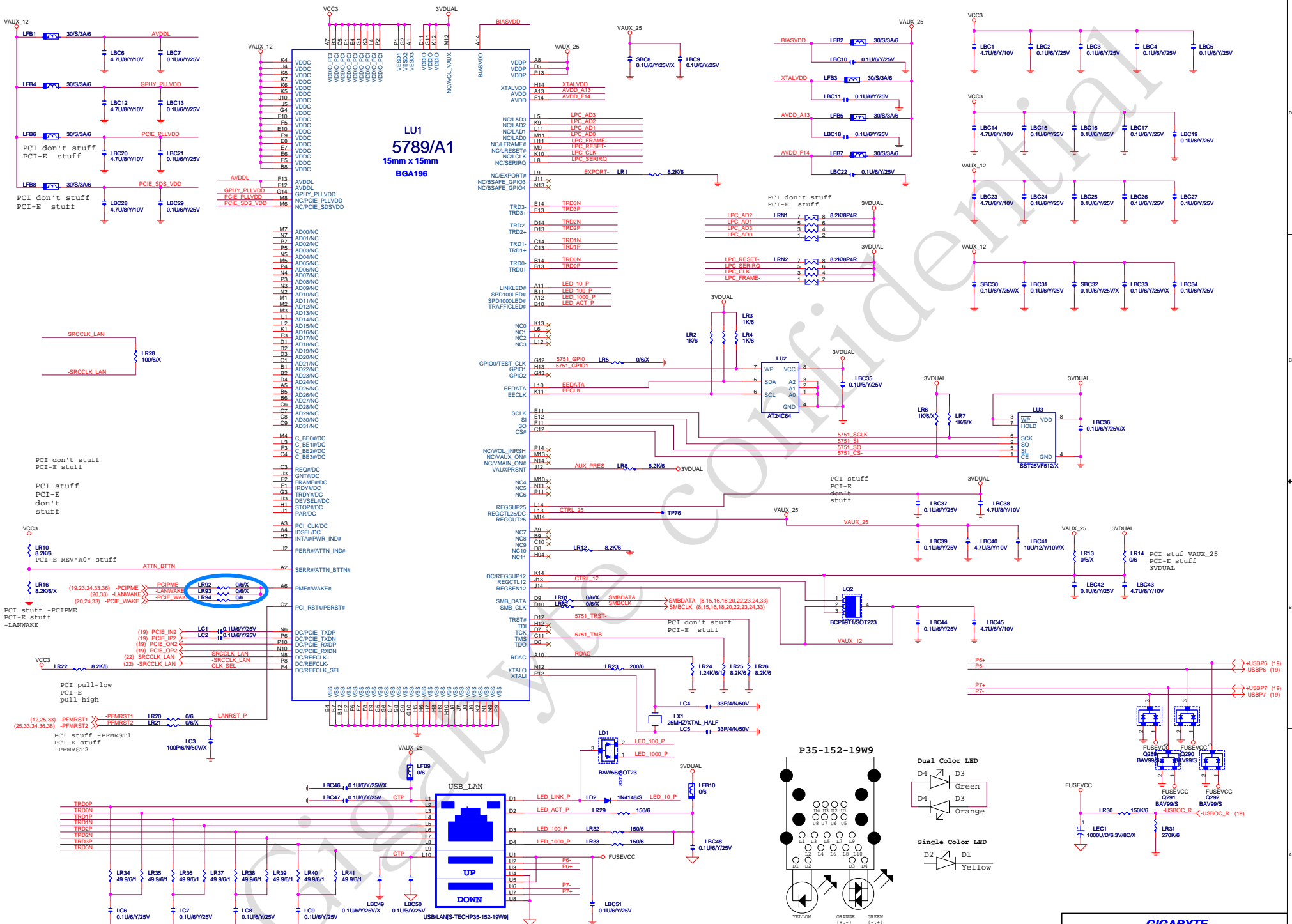
Title		
AC97 CMI9880		
Size	Document Number	Rev
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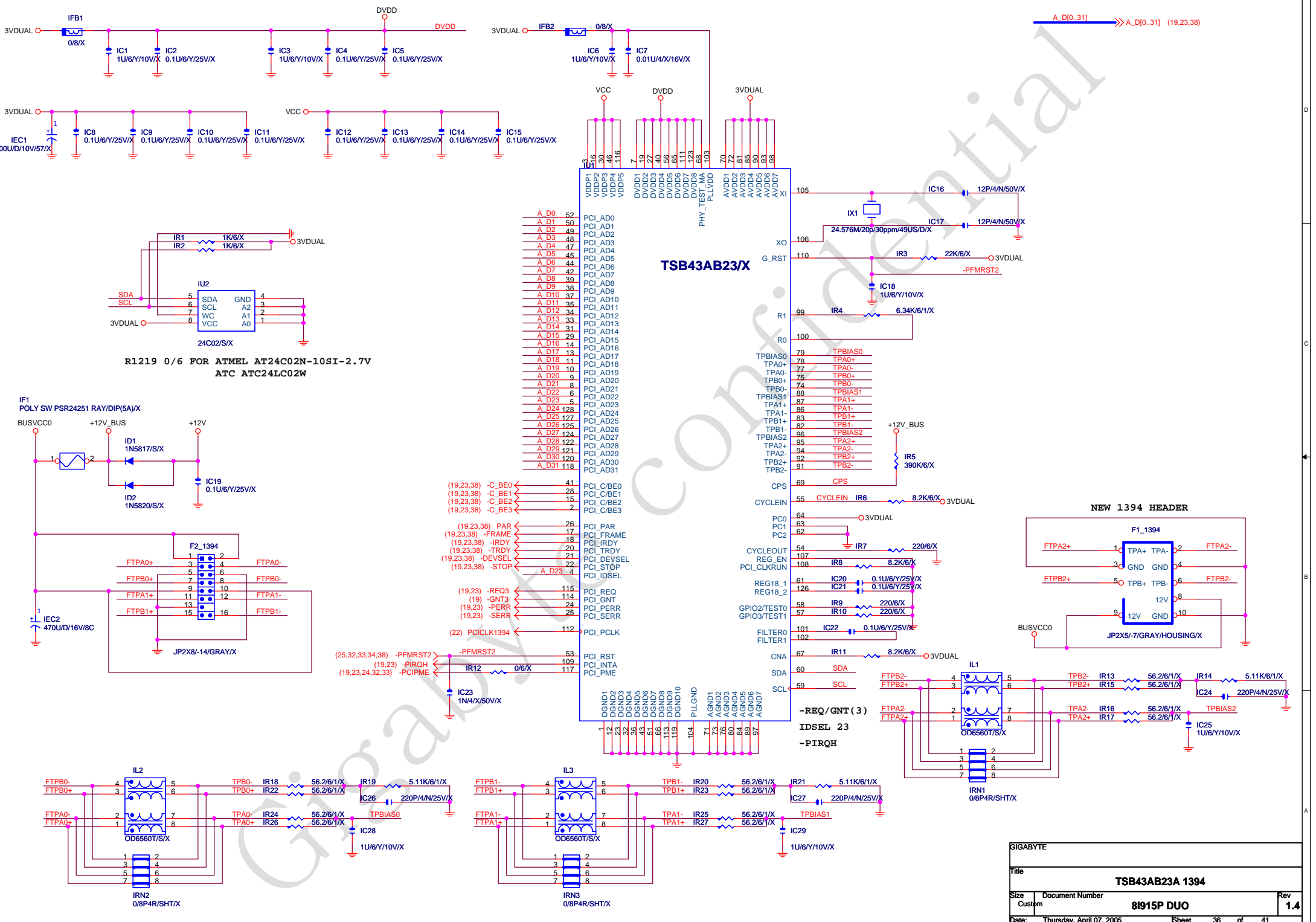


Azalia Jack CMI9880 Port A is Side SURROUND, Port H is Back SURROUND
ALC880 Port A is SURROUND, Port H is SIDE

Normal --> pin4/pin3 open
Plug jack --> pin4/pin3 close







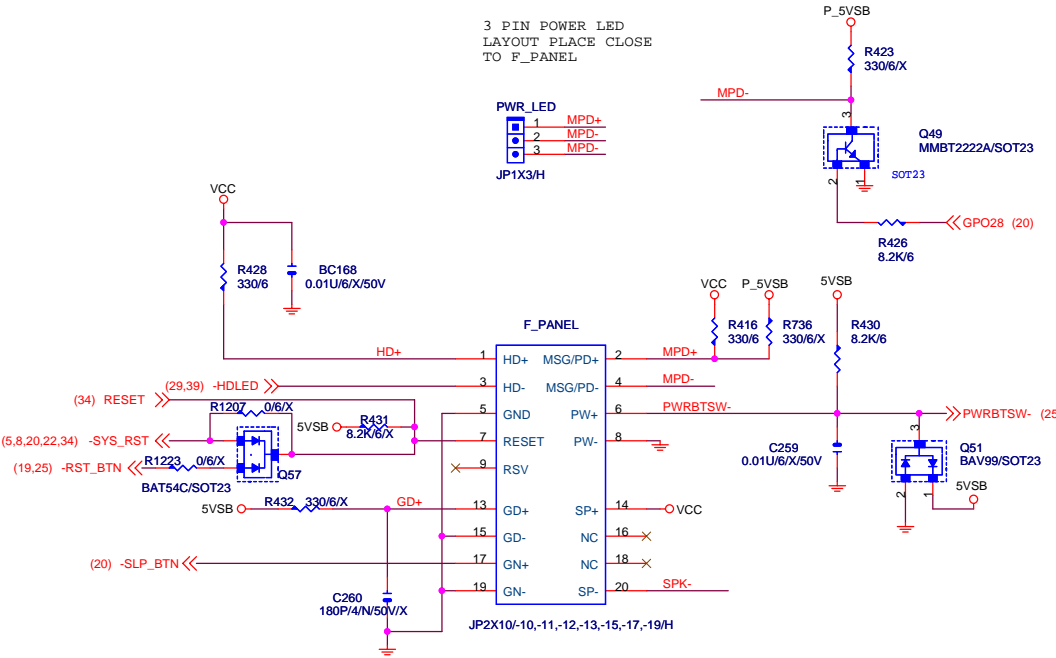
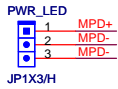
A D[0..31] >> A_D[0..31] (19,23,38)

TSB43AB23/X

A D0	52	PCL_AD0
A D1	50	PCL_AD1
A D2	49	PCL_AD2
A D3	48	PCL_AD3
A D4	47	PCL_AD4
A D5	45	PCL_AD5
A D6	44	PCL_AD6
A D7	42	PCL_AD7
A D8	39	PCL_AD8
A D9	38	PCL_AD9
A D10	37	PCL_AD10
A D11	35	PCL_AD11
A D12	34	PCL_AD12
A D13	33	PCL_AD13
A D14	31	PCL_AD14
A D15	29	PCL_AD15
A D16	14	PCL_AD16
A D17	13	PCL_AD17
A D18	11	PCL_AD18
A D19	10	PCL_AD19
A D20	9	PCL_AD20
A D21	8	PCL_AD21
A D22	6	PCL_AD22
A D23	5	PCL_AD23
A D24	128	PCL_AD24
A D25	127	PCL_AD25
A D26	125	PCL_AD26
A D27	124	PCL_AD27
A D28	122	PCL_AD28
A D29	121	PCL_AD29
A D30	120	PCL_AD30
A D31	118	PCL_AD31
(19,23,38) -C_BE0	41	PCL_C/BE0
(19,23,38) -C_BE1	28	PCL_C/BE1
(19,23,38) -C_BE2	15	PCL_C/BE2
(19,23,38) -C_BE3	2	PCL_C/BE3
(19,23,38) PAR	26	PCL_PAR
(19,23,38) -FRAME	17	PCL_FRAME
(19,23,38) -IRDY	18	PCL_IRDY
(19,23,38) -TRDY	20	PCL_TRDY
(19,23,38) -DEVSEL	21	PCL_DEVSEL
(19,23,38) -STOP	22	PCL_STOP
(19,23,38) -REQ3	115	PCL_REQ
(19) -GNT3	114	PCL_GNT
(19,23) -PERR	24	PCL_PERR
(19,23) -SERR	25	PCL_SERR
(22) PCLCLK1394	112	PCL_PCLK
(25,32,33,34,38) -PFMRST2	53	PCL_RST
(19,23) -PIRQH	109	PCL_INTA
(19,23,24,32,33) -PCI_PME	117	PCL_PME
DGND1	12	PLLGND
DGND2	23	AGND1
DGND3	36	AGND2
DGND4	38	AGND3
DGND5	43	AGND4
DGND6	44	AGND5
DGND7	45	AGND6
DGND8	46	AGND7
DGND9	68	
DGND10	113	
PLLGND	104	
AGND1	71	
AGND2	73	
AGND3	74	
AGND4	80	
AGND5	81	
AGND6	89	
AGND7	91	

INTEL FRONT PANEL

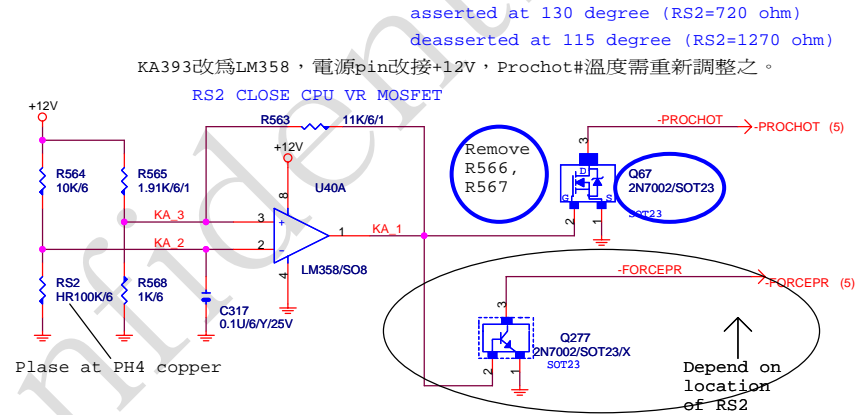
3 PIN POWER LED
LAYOUT PLACE CLOSE
TO F_PANEL



PROCESSOR HOT

(N/A)

如果要用2N7002需注意OP output
Hi時的電壓是否遠大於2V。



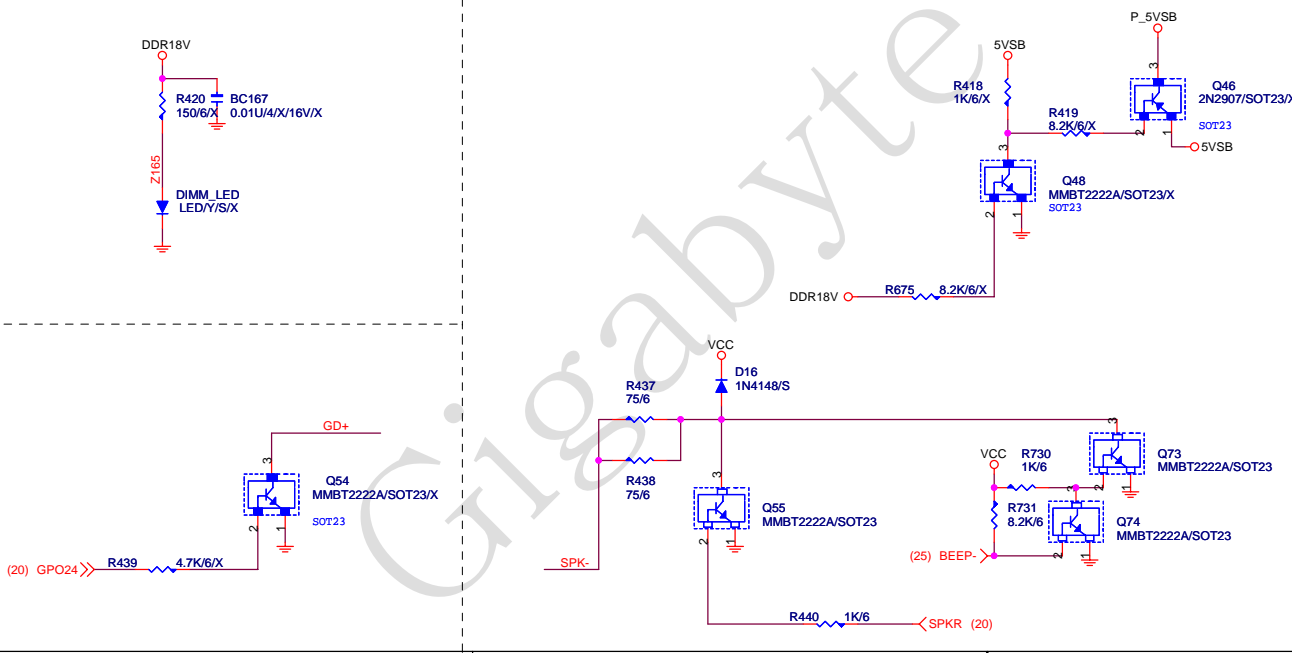
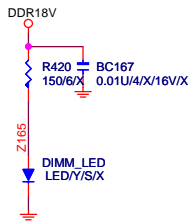
asserted at 130 degree (RS2=720 ohm)
deasserted at 115 degree (RS2=1270 ohm)

KA393改為LM358，電源pin改接+12V，Prochot#溫度需重新調整之。

RS2 CLOSE CPU VR MOSFET

Plase at PH4 copper

Depend on
location
of RS2



States for green LED NO1 GPO22 only S1 PROGRAMMING LOW

LED States	ACPI States	GPO22
ON	S1, S3	0
OFF	S0, S5	1

(GPO22 DEFAULT HIGH, main power)

States for a single-color power LED

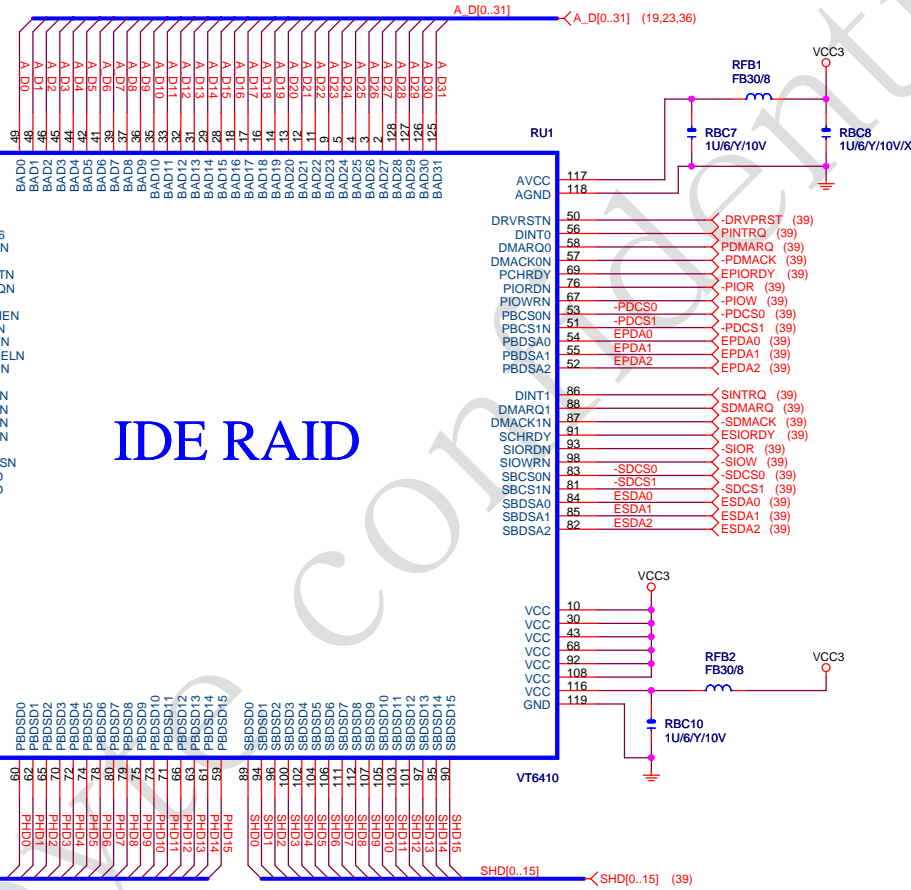
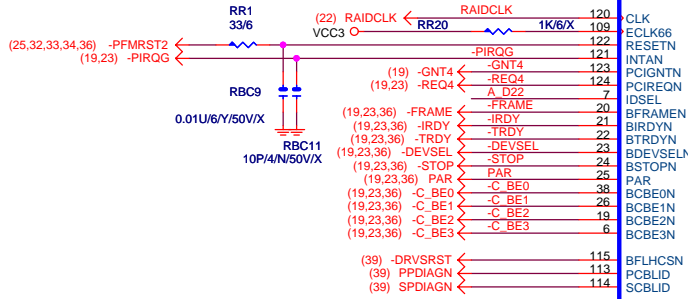
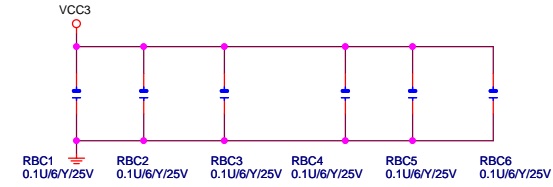
LED States	ACPI States	GPO25	GPO27	GPO24
OFF	S1, S3, S5	1	1	NO1
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	B	1

LED States	ACPI States	GPO25	GPO27	GPO22
OFF	S5	1	1	X
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	B	1
Steady Yellow	S1, S3	1	0	NO1
Blinking Yellow	S1, S3(message waiting)	1	B	NO1

GIGABYTE

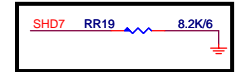
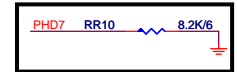
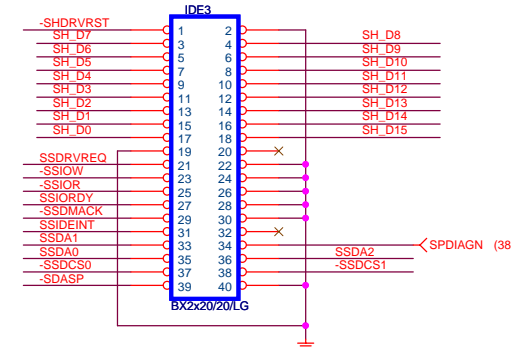
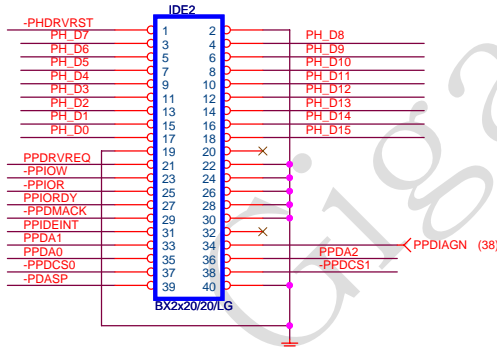
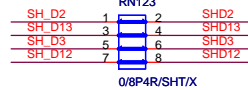
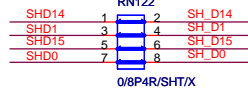
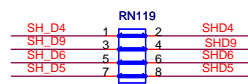
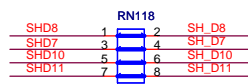
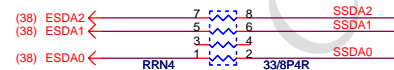
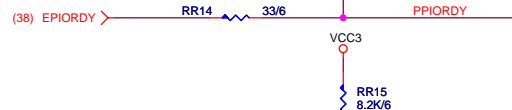
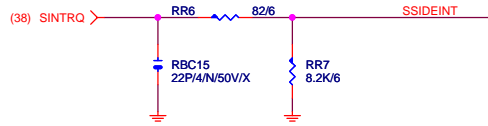
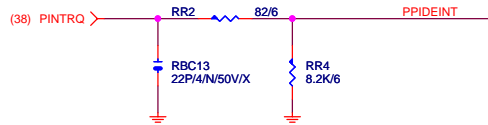
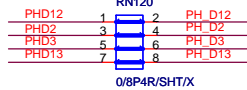
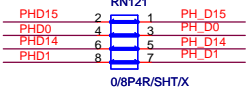
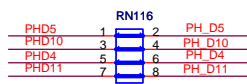
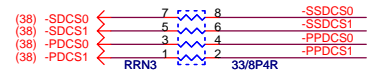
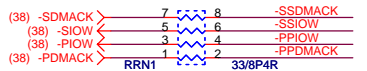
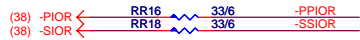
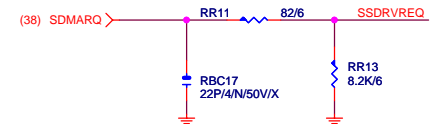
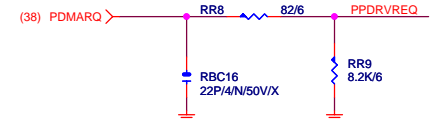
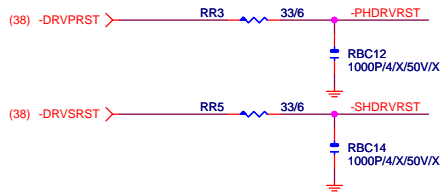
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ALL INPUT PIN MUST HAVE 0.1 CAPACITOR



IDE RAID

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ICH6 GPIO Table:

NAME	PWR LANE	USAGE	NAME	PWR LANE	USAGE
GPI0	V5REF	M/B ID (-REQ6)	GPI41	VCC3	M/B ID
GPI1	V5REF	-REQ5	GPO48	VCC3	-GNT4
GPI2	V5REF	-PIRQE	GPO49	V-CPUIO	CPUPWOK
GPI3	V5REF	-PIRQF			
GPI4	V5REF	-PIRQG			
GPI5	V5REF	-PIRQH			
GPI6	VCC3	-SLP BTN			
GPI7	VCC3	DUAL BIOS			
GPI8	3VDAUL	-LANWAKE			
GPI9	3VDAUL	-USBOC4			
GPI10	3VDAUL	-USBOC5			
GPI11	3VDAUL	-SMBALT			
GPI12	VCC3	ATX DET			
GPI13	3VDAUL	-LPCPME			
GPI14	3VDAUL	-USBOC6			
GPI15	3VDAUL	-USBOC7			
GPO16	VCC3	CPU OV1 (-GNT6)			
GPO17	VCC3	-GNT5			
GPO18	VCC3	CPU OV2			
GPO19	VCC3	DUAL BIOS			
GPO20	VCC3	BIOS T-BLOCK			
GPO21	VCC3	DUAL BIOS			
GPO23	VCC3	DDR OV0			
GPIO24	3VDAUL	GREEN LED			
GPIO25	3VDAUL	DDR OV1			
GPI26	VCC3	SATA GP0			
GPIO27	3VDAUL	+PWRLED			
GPIO28	3VDAUL	-PWRLED			
GPI29	VCC3	SATA GP1			
GPI30	VCC3	SATA GP2			
GPI31	VCC3	SATA GP3			
GPIO32	VCC3	BIOS WP			
GPIO33	VCC3	AZALIA DET			
GPIO34	VCC3	M/B ID			
GPI40	V5REF	-REQ4			

PWROK/RESET Table:

ITE8712BHX PIN	NET NAME	TARGET
PIN62/-PCIRST1	-PCIE_RST	1. PCI-E * 1 Slot1 2. PCI-E * 1 Slot2 3. PCI-E * 1 Slot3 4. PCI-E * 16 Slot
PIN64/-PCIRST2	-PFMRST2	1. Onboard PCI Lan 2. Onboard 1394 Chip 3. OnBoard FWH
PIN65/-PCIRST3	-PFMRST1	1. Onboard PCI-E Lan 2. Onboard SATA Chip 3. GMCH
PIN115/-PCIRST4	-PFMRST_ -IDERST	Reserved For IDE
PIN63/PWROK1	PWROK1	1. GMCH 2. ICH6 3. 5VDUAL SWITCH 4. DPS CONTROL
PIN109/PWROK2	-THERM	1. ICH6

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