Model Name: 8I915ME-GV

Revision 1.02

DATE:05/02-1

SHEET	TITLE
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B
06	P4_LGA775_C
07	P4_LGA775_D
08	VCORE POWER
09	GMCH-GRANTSDALE_HOST
10	GMCH-GARNTSDALE_DDR
11	GMCH-GRANTSDALE_PCI E, DMI
12	GMCH-GRANTSDALE_INT VGA
13	GMCH-GRANTSDALE_GND
14	GMCH-GRANTSDALE_PWR
15	DDR CHANNEL A
16	DDR CHANNEL B
17	DDR TERMINATION
18	FAN & WOL
19	ICH6 PCI, USB, DMI, LAN
20	ICH6 IDE, GPIO, SATA, CTRL
21	ICH6 VCC, GND
22	CLK GEN

SHEET	TITLE
23	PCI SLOT 1,2
24	PCI SLOT 3
25	ITE8712HX
26	HWMO/FAN/FWH BIOS
27	KB_MS
28	COM/LPT/FDD
29	(FRONT+REAR)USB/RING/IDE
30	AC97 CODEC ALC655
31	AUDIO JACK
32	LAN RTL8110S/8100C
33	ATX POWER CONN.
34	ALL POWER
35	FRONT PANEL/BZ

COMPONENT SIDE
(1 oz. Copper)
VCC SIDE
(1 oz. Copper)
GND SIDE
(1 oz. Copper)
SOLDER SIDE
(1 oz. Copper)

GIGABYTE

Title: Cover Sheet

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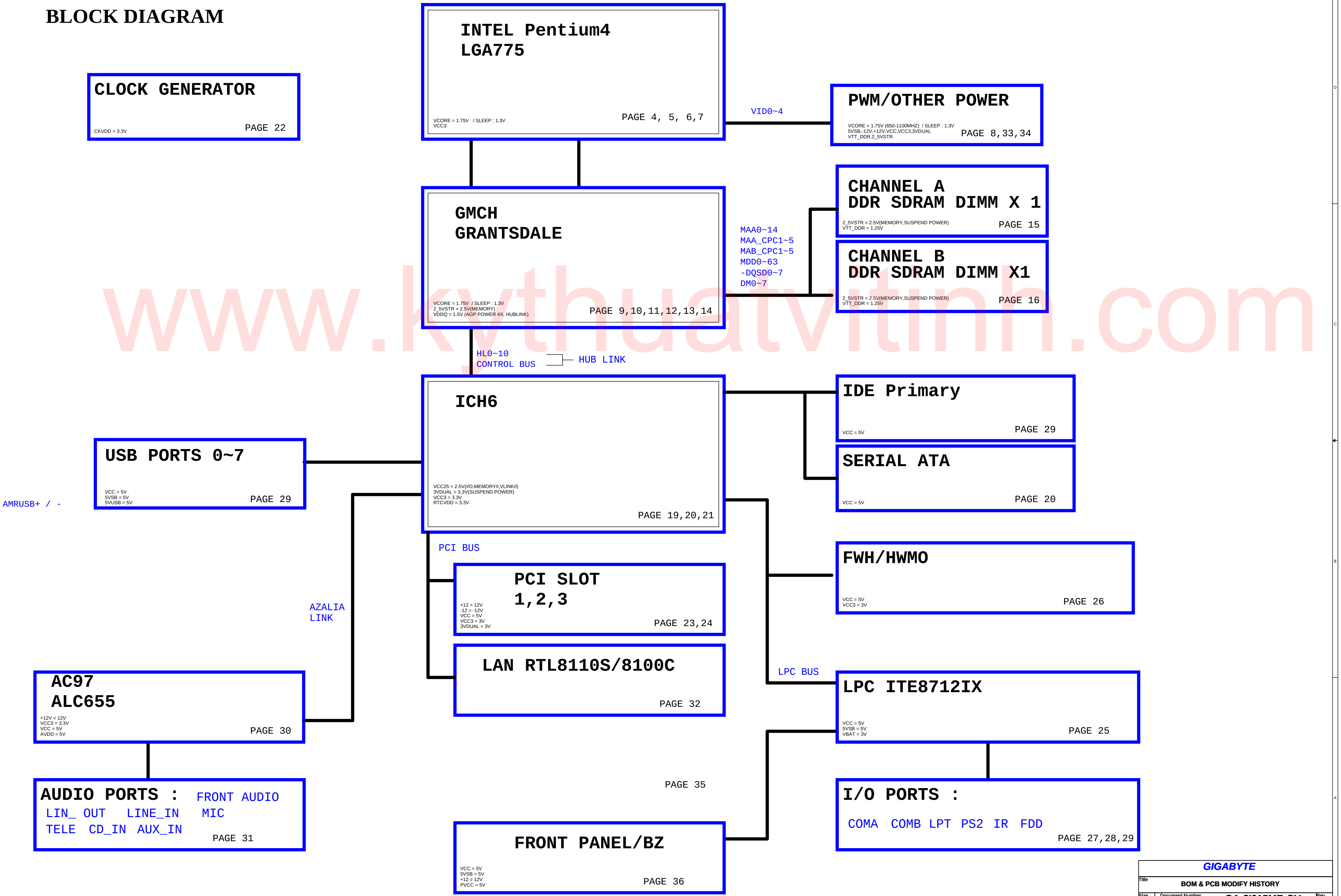
BLOCK DIAGRAM



GIGABYTE			
Title	BOM & PCB MODIFY HISTORY		
Size Custom	Document Number	GA-8I915ME-GV	Rev 1.02
Date:	Monday, May 30, 2005	Sheet 2 of 36	

Model Name: 8I915ME-GV

Version: 1.02

Component value change history

2005/01/10

Data	Change Item	Reason
02/15	FROM 8IGV775-CH1 cost down rev:1.0	
03/25	rev:1.02 for EMI & noise &F10 power issue	
04/19	FROM 8I915ME-CH1 FOR DIST GV	
05/02 BOM:1.02B	FOR PVT	
05/30 BOM:1.02C	FOR Lite Power inrush current	

Circuit or PCB layout change for next version

PAGE	Change Item	Reason

GIGABYTE

Title	BOM & PCB MODIFY HISTORY	
Size: Custom	Document Number: GA-8I915ME-GV	Rev: 1.02
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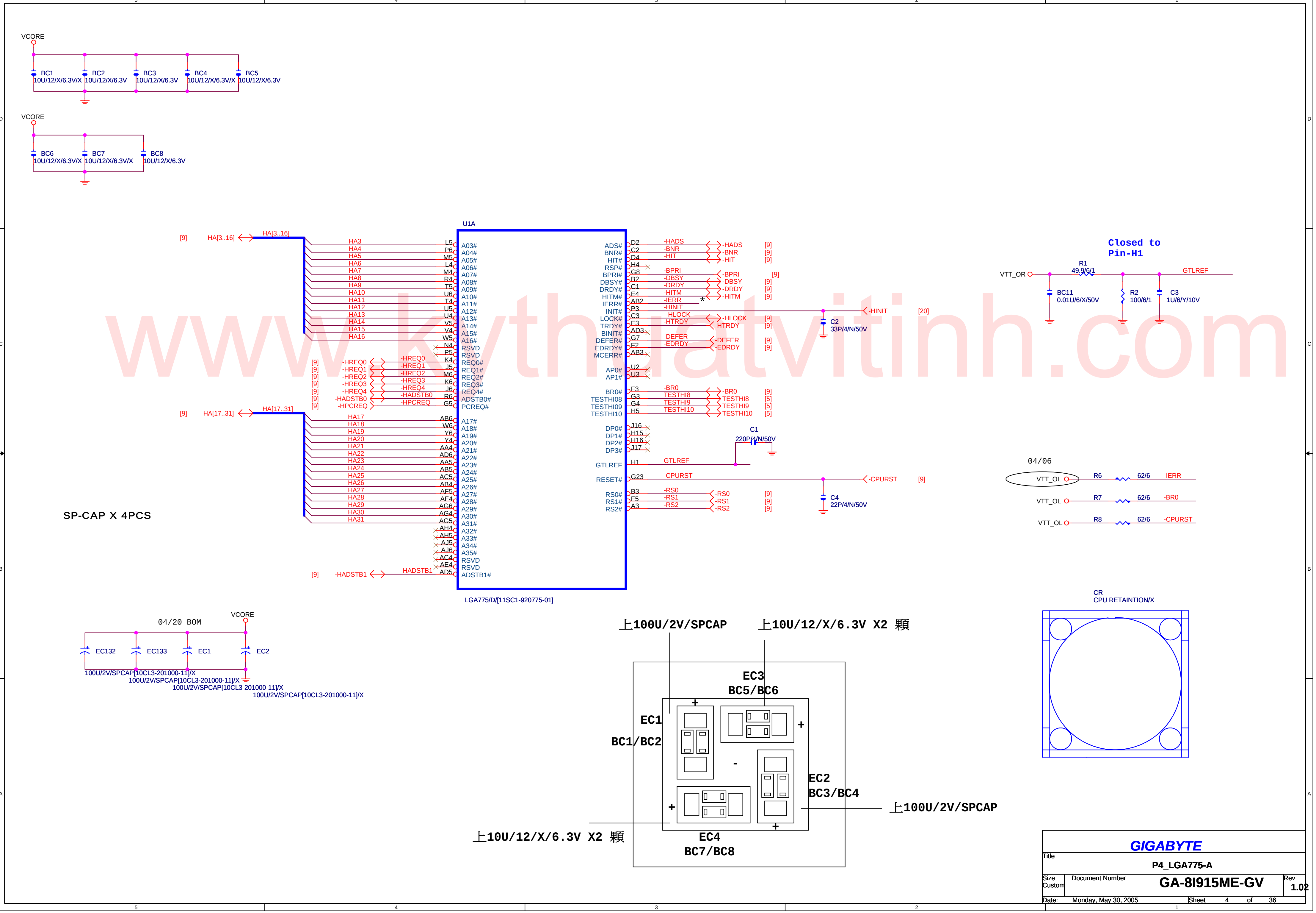

VCORE
BC1 10U/12/X/6.3V/X
BC2 10U/12/X/6.3V
BC3 10U/12/X/6.3V
BC4 10U/12/X/6.3V/X
BC5 10U/12/X/6.3V
BC6 10U/12/X/6.3V/X
BC7 10U/12/X/6.3V/X
BC8 10U/12/X/6.3V
U1A
[9] HA[3..16] HA[3..16]
HA3 HA4 HA5 HA6 HA7 HA8 HA9 HA10 HA11 HA12 HA13 HA14 HA15 HA16
L5 P6 M5 L4 M4 R4 T5 U6 T4 U5 U4 V5 V4 W5 N4 P5 K4 J5 M6 K6 J6 R6 G5
A03# A04# A05# A06# A07# A08# A09# A10# A11# A12# A13# A14# A15# A16# RSVD RSVD REQ0# REQ1# REQ2# REQ3# REQ4# ADSTB0# PCREQ#
[9] -HREQ0 [9] -HREQ1 [9] -HREQ2 [9] -HREQ3 [9] -HREQ4 [9] -HADSTB0 [9] -HPCREQ
[9] HA[17..31] HA[17..31]
HA17 HA18 HA19 HA20 HA21 HA22 HA23 HA24 HA25 HA26 HA27 HA28 HA29 HA30 HA31
AB6 W6 Y6 Y4 AA4 AD6 AA5 AB5 AC5 AB4 AF5 AF4 AG6 AG4 AG5 AH4 AH5 AJ5 AJ6 AC4 AE4 AD5
A17# A18# A19# A20# A21# A22# A23# A24# A25# A26# A27# A28# A29# A30# A31# A32# A33# A34# A35# RSVD RSVD ADSTB1#
[9] -HADSTB1
LGA775/D/[11SC1-920775-01]
ADS# BNR# HIT# RSP# BPRI# DBSY# DRDY# HITM# IERR# INIT# LOCK# TRDY# BINIT# DEFER# EDRDY# MCERR#
D2 C2 D4 H4 G8 B2 C1 E4 AB2 P3 C3 E3 AD3 G7 F2 AB3
-HADS -BNR -HIT -BPRI -DBSY -DRDY -HITM -IERR -HINIT -HLOCK -HTRDY -DEFER -EDRDY
[9] [9] [9] [9] [9] [9] [9] [9] [9] [9] [9]
-HINIT [20]
C2 33P/4/N/50V
AP0# AP1# U2 U3
BR0# TESTHI08 TESTHI09 TESTHI10
F3 G3 G4 H5
-BR0 [9] TESTHI8 [5] TESTHI9 [5] TESTHI10 [5]
DP0# DP1# DP2# DP3# J16 H15 H16 J17
C1 220P/4/N/50V
GTLREF H1 GTLREF
RESET# G23 -CPURST -CPURST [9]
RS0# RS1# RS2# B3 F5 A3 -RS0 -RS1 -RS2 [9] [9] [9]
C4 22P/4/N/50V
Closed to
Pin-H1
VTT_OR R1 49.9/6/1 GTLREF
BC11 0.01U/6/X/50V
R2 100/6/1
C3 1U/6/Y/10V
04/06
VTT_OL R6 62/6 -IERR
VTT_OL R7 62/6 -BR0
VTT_OL R8 62/6 -CPURST
SP-CAP X 4PCS
04/20 BOM
VCORE
EC132 EC133 EC1 EC2
100U/2V/SPCAP[10CL3-201000-11]/X
100U/2V/SPCAP[10CL3-201000-11]/X
100U/2V/SPCAP[10CL3-201000-11]/X
100U/2V/SPCAP[10CL3-201000-11]/X
CR
CPU RETAINTION/X
上100U/2V/SPCAP
上10U/12/X/6.3V X2 顆
EC3
BC5/BC6
EC1
BC1/BC2
EC2
BC3/BC4
上100U/2V/SPCAP
上10U/12/X/6.3V X2 顆
EC4
BC7/BC8
GIGABYTE
Title
P4_LGA775-A
Size Custom
Document Number
GA-8I915ME-GV
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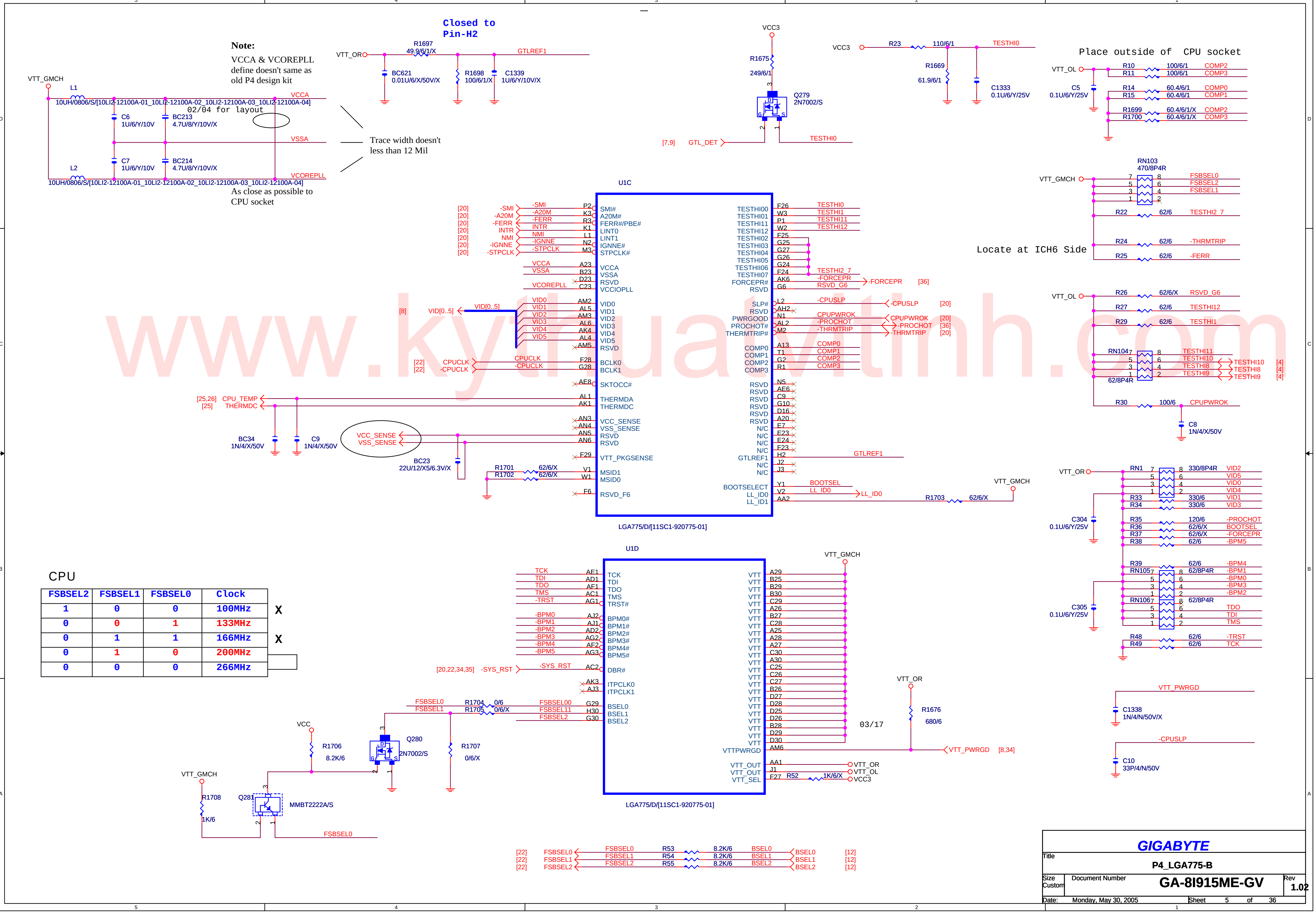

CPU

FSBSEL2	FSBSEL1	FSBSEL0	Clock	
1	0	0	100MHz	X
0	0	1	133MHz	
0	1	1	166MHz	X
0	1	0	200MHz	
0	0	0	266MHz	

GIGABYTE

Title: P4_LGA775-B

Size: Custom | Document Number: GA-8I915ME-GV | Rev: 1.02

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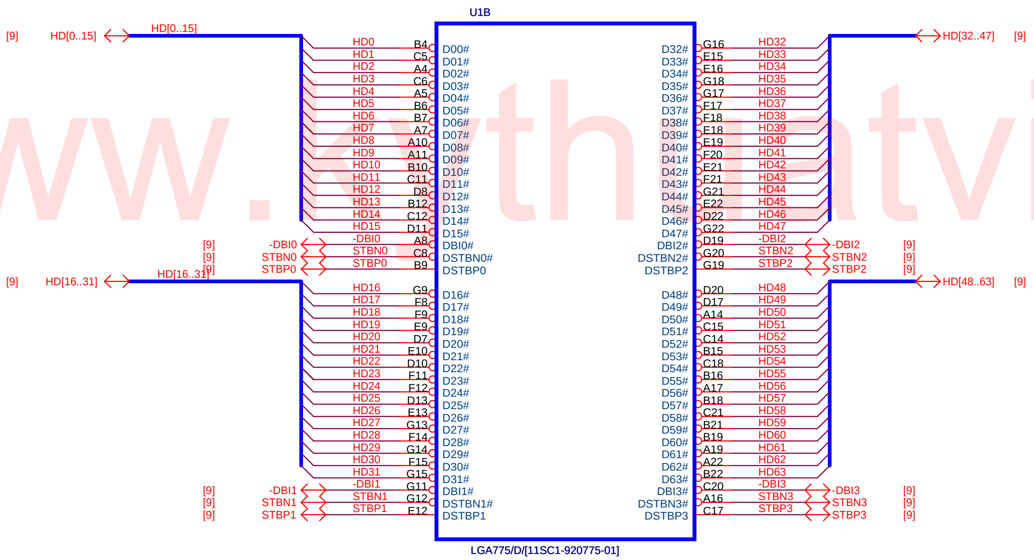
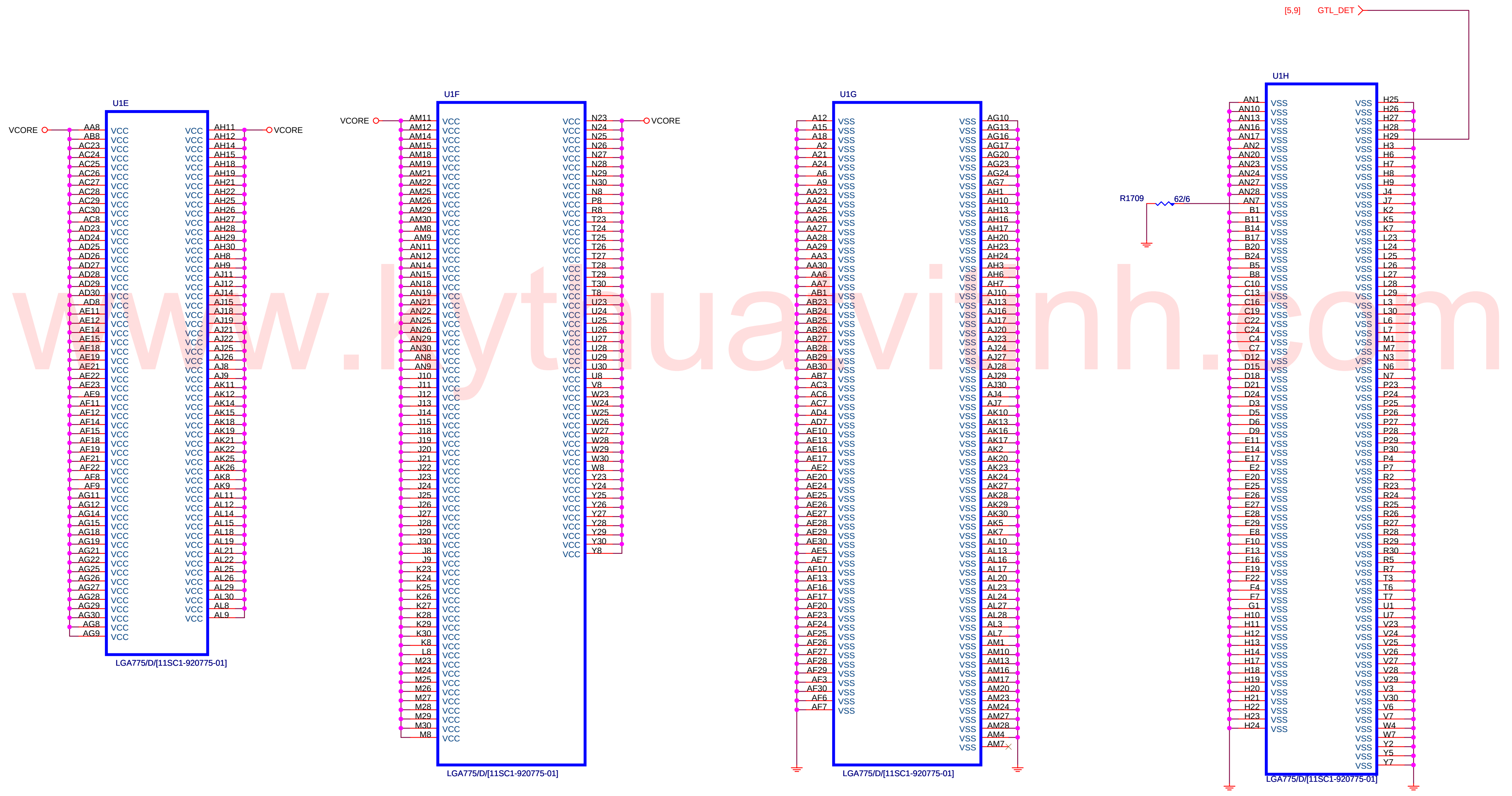


GIGABYTE

Title	P4_LGA775-C	
Size Custom	Document Number GA-8I915ME-GV	Rev 1.02
Date: Monday, May 30, 2005	Sheet 6 of 36	



GIGABYTE

Title: P4_LGA775-D

Size	Document Number	Rev
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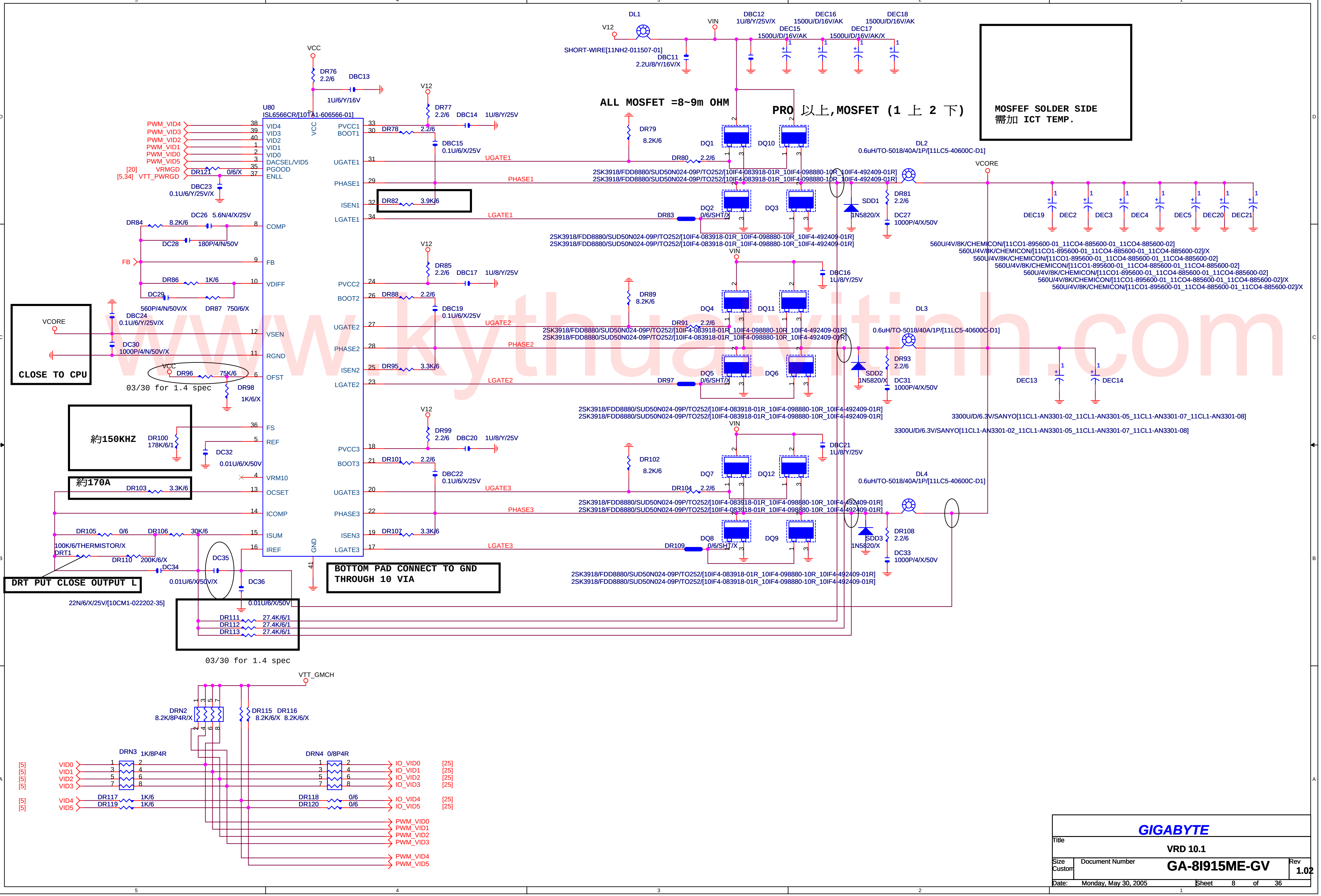

GIGABYTE	
Title	VRD 10.1
Size Custom	Document Number GA-8I915ME-GV
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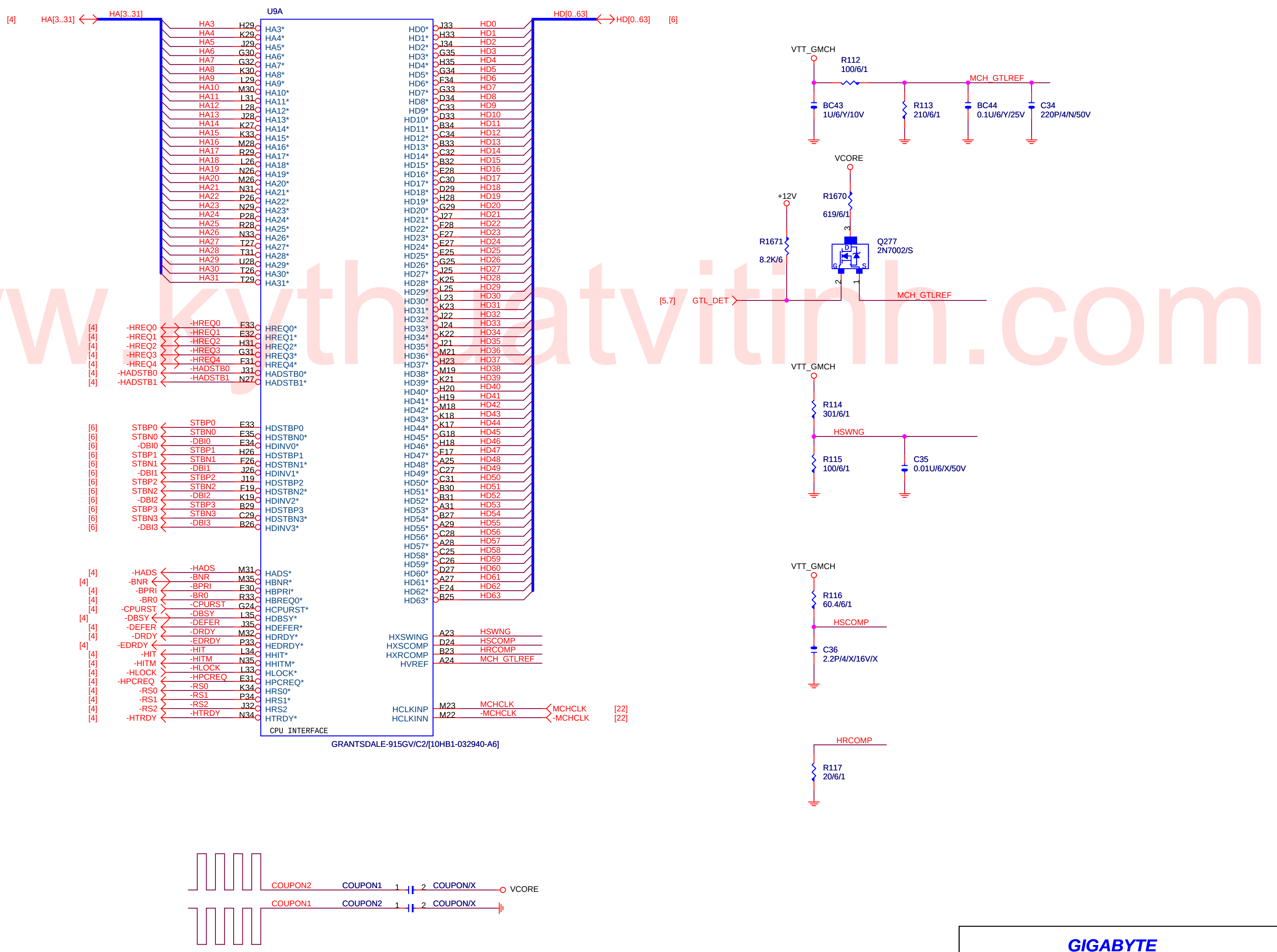
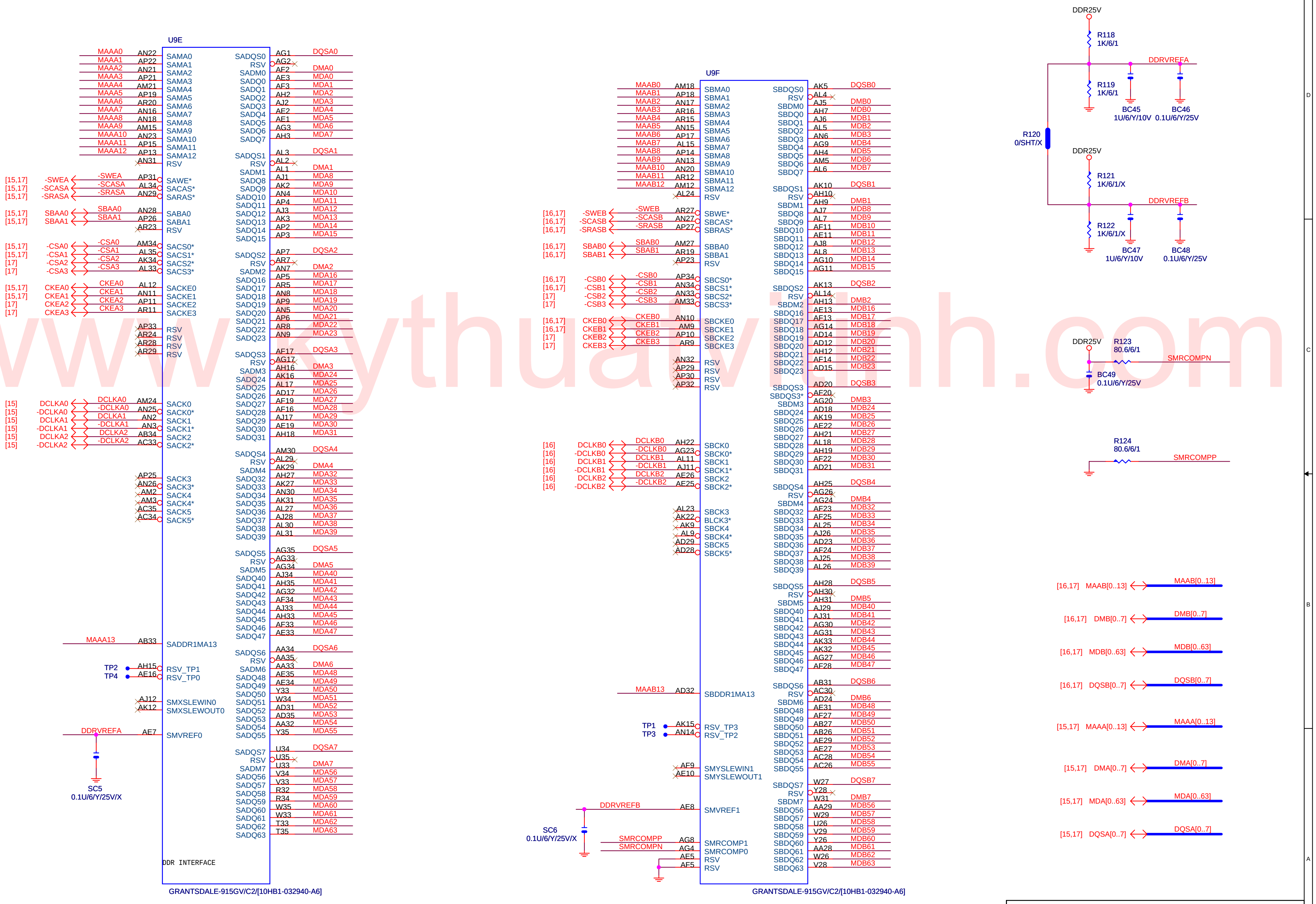


GIGABYTE			
Title	GMCH-HOST		
Size Custom	Document Number	GA-8I915ME-GV	Rev 1.02
Date:	Monday, May 30, 2005	Sheet 9 of 36	

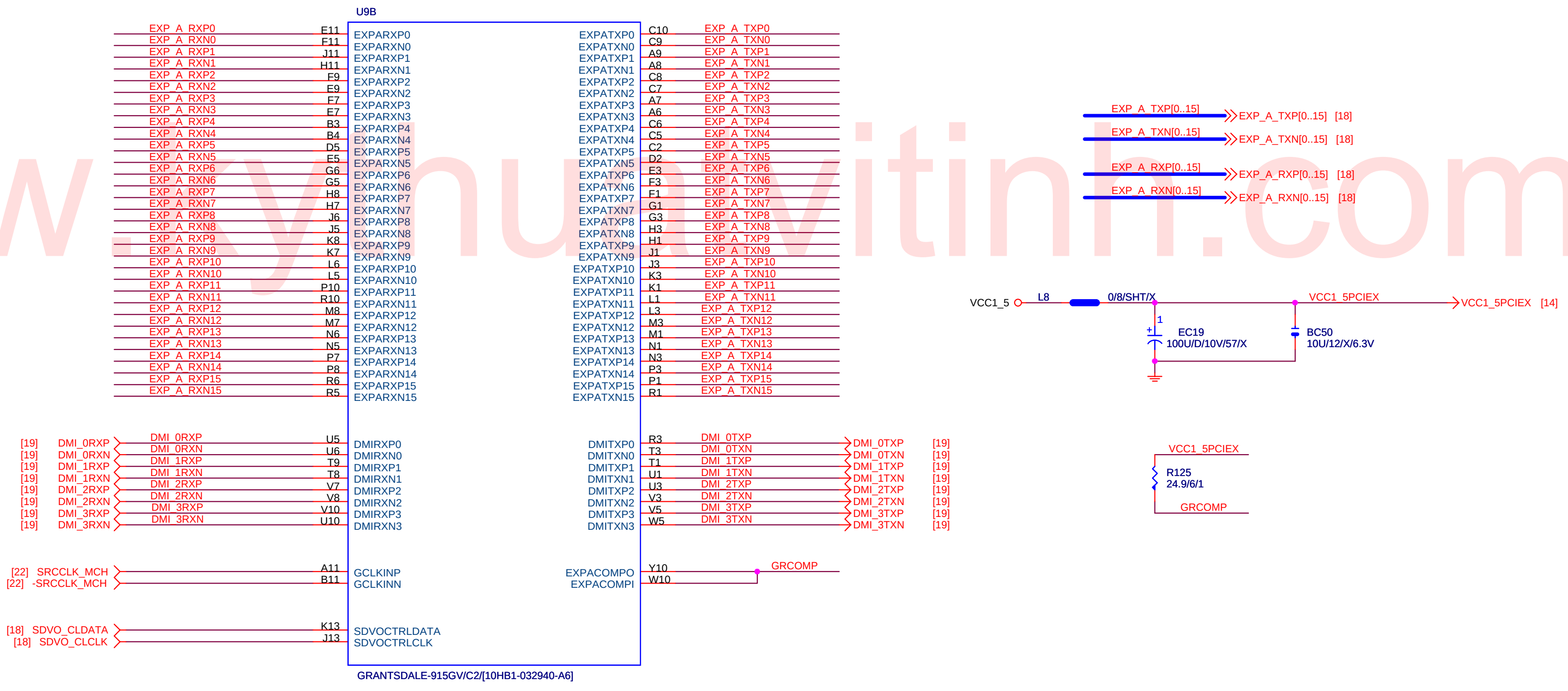


GIGABYTE

Title: GMCH-DDR

Size: Custom | Document Number: GA-8I915ME-GV | Rev 1.02

Date: Monday, May 30, 2005 | Sheet 10 of 36

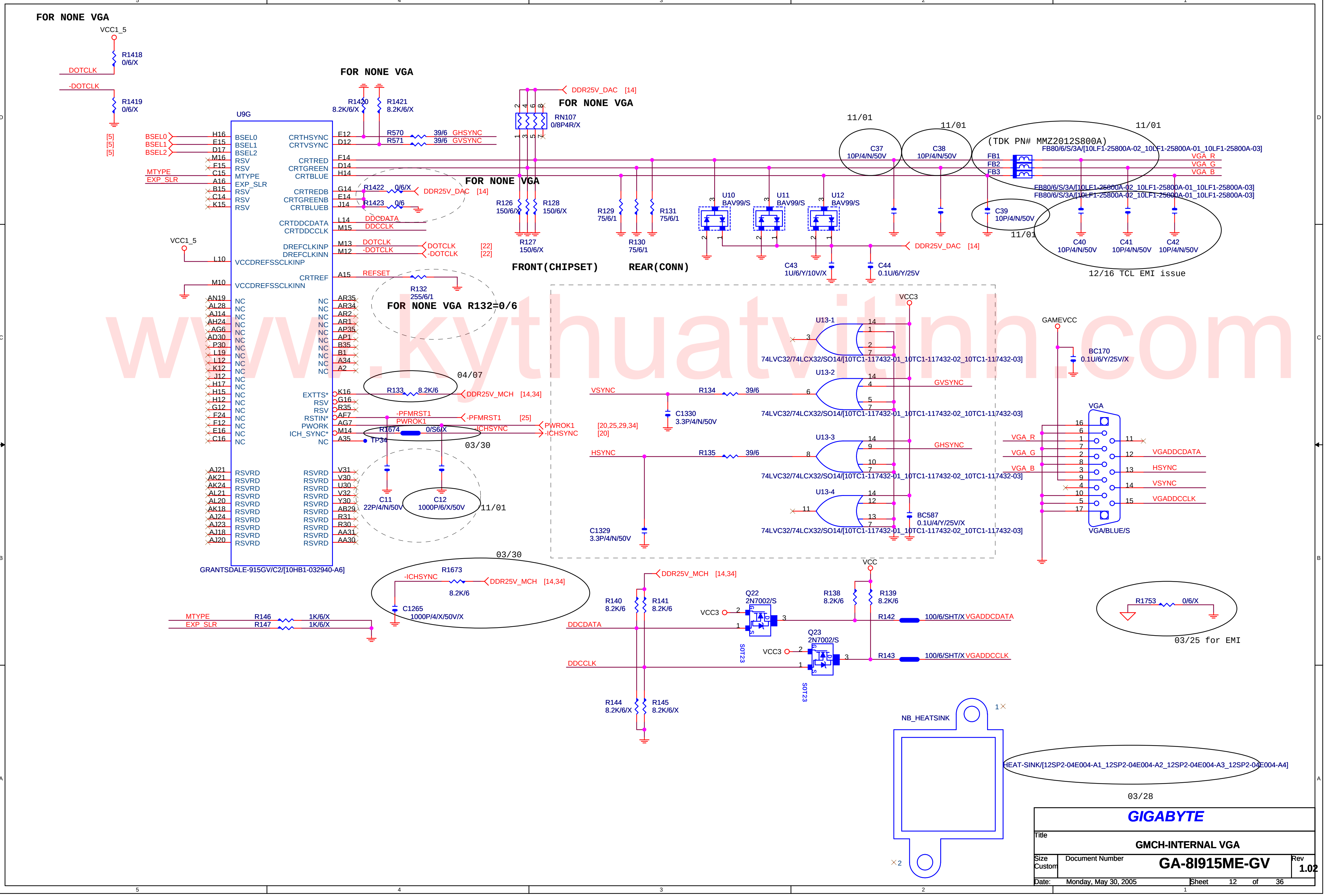


GIGABYTE

Title: GMCH-PCI E & DMI

Size	Document Number	Rev
Custom	GA-8I915ME-GV	1.02

Date: Monday, May 30, 2005 Sheet 11 of 36

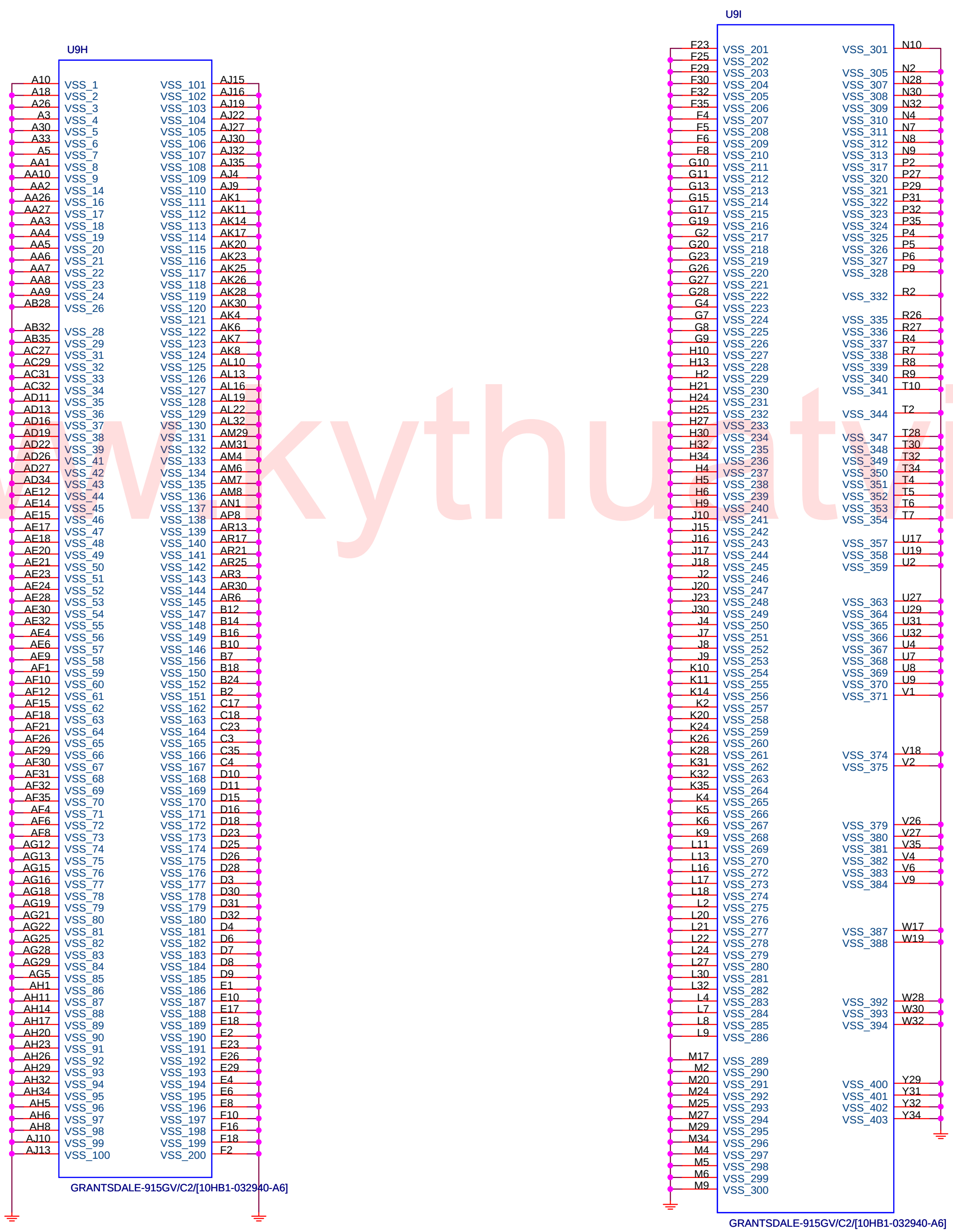


GIGABYTE

Title: GMCH-INTERNAL VGA

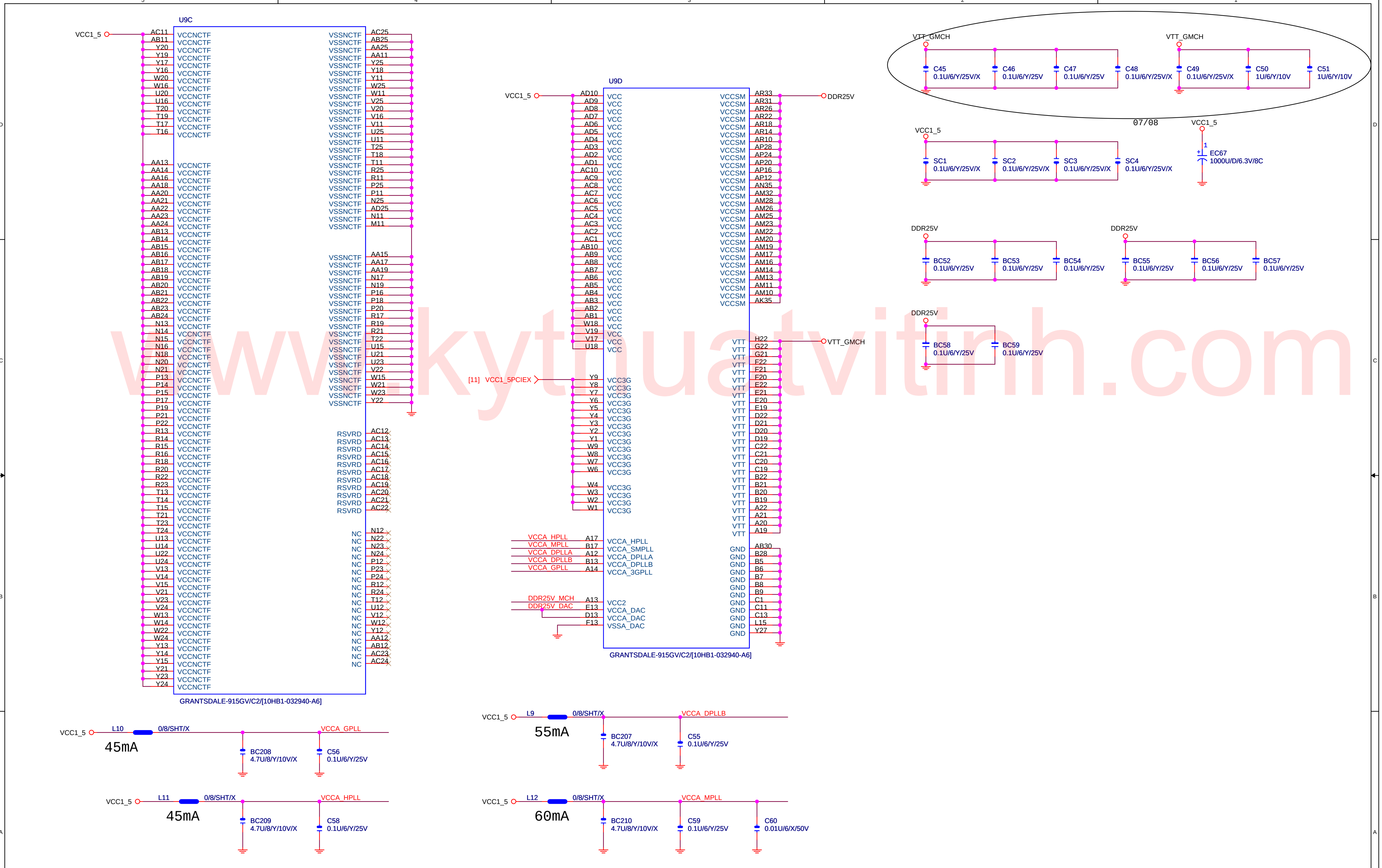
Size: Custom | Document Number: GA-8I915ME-GV | Rev: 1.02

Date: Monday, May 30, 2005 | Sheet 12 of 36



GIGABYTE

Title	GMCH-GND	
Size: Custom	Document Number: GA-8I915ME-GV	Rev: 1.02
Date: Monday, May 30, 2005	Sheet 13 of 36	

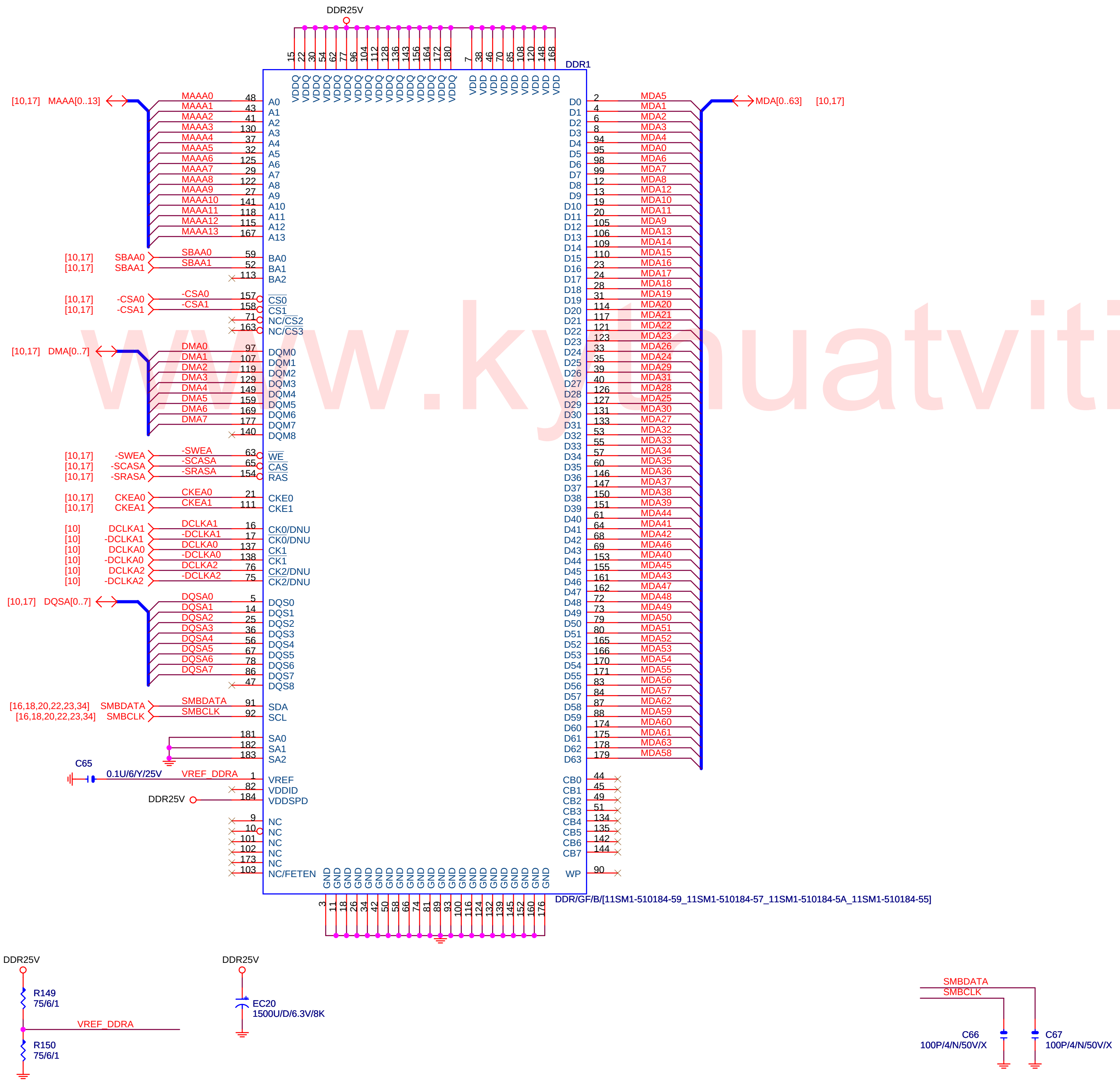


GIGABYTE

Title: GMCH-PWR

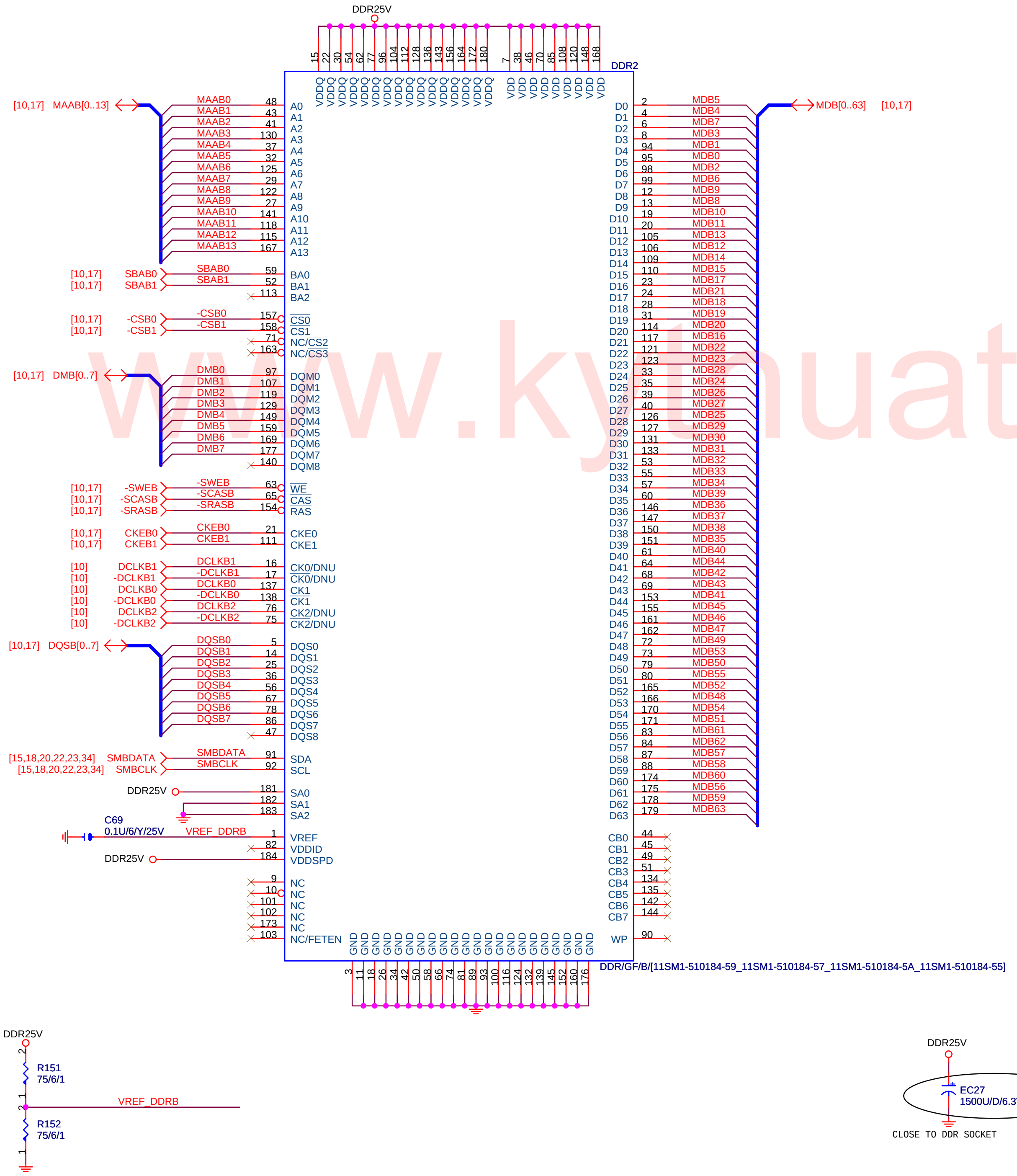
Size: Custom | Document Number: GA-8I915ME-GV | Rev: 1.02

Date: Monday, May 30, 2005 | Sheet 14 of 36



GIGABYTE

Title	DDR CHANNEL A	
Size B	Document Number GA-8I915ME-GV	Rev 1.02
Date: Monday, May 30, 2005	Sheet 15 of 36	

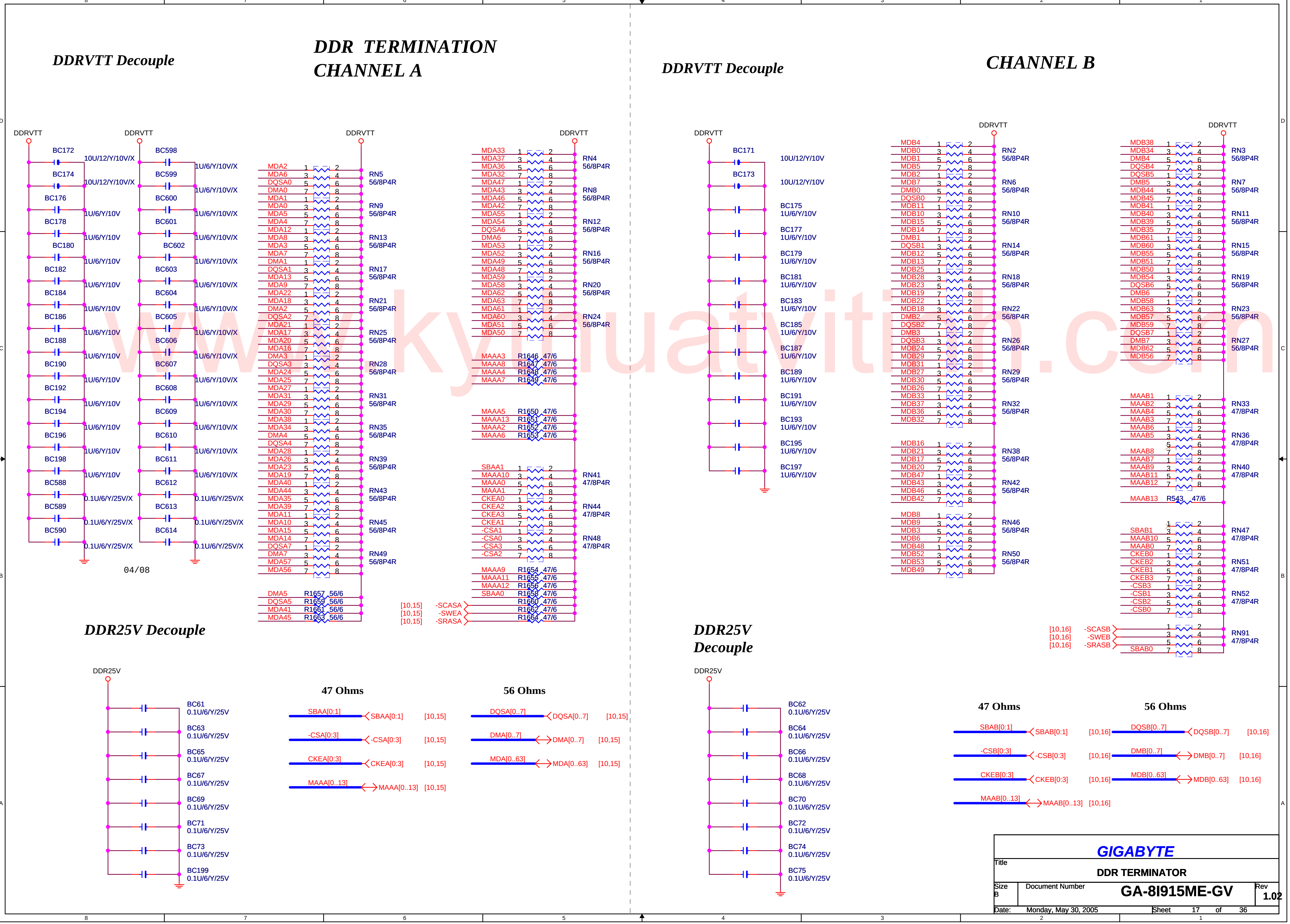


GIGABYTE

Title: DDR CHANNEL B

Size	Document Number	Rev
B	GA-8I915ME-GV	1.02

Date: Monday, May 30, 2005 Sheet 16 of 36


DDR TERMINATION
CHANNEL A
DDRVTT Decouple
DDRVTT Decouple
CHANNEL B
DDR25V Decouple
DDR25V
Decouple
47 Ohms
56 Ohms
47 Ohms
56 Ohms
04/08
GIGABYTE
Title
DDR TERMINATOR
Size
B
Document Number
GA-8I915ME-GV
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Date: Monday, May 30, 2005
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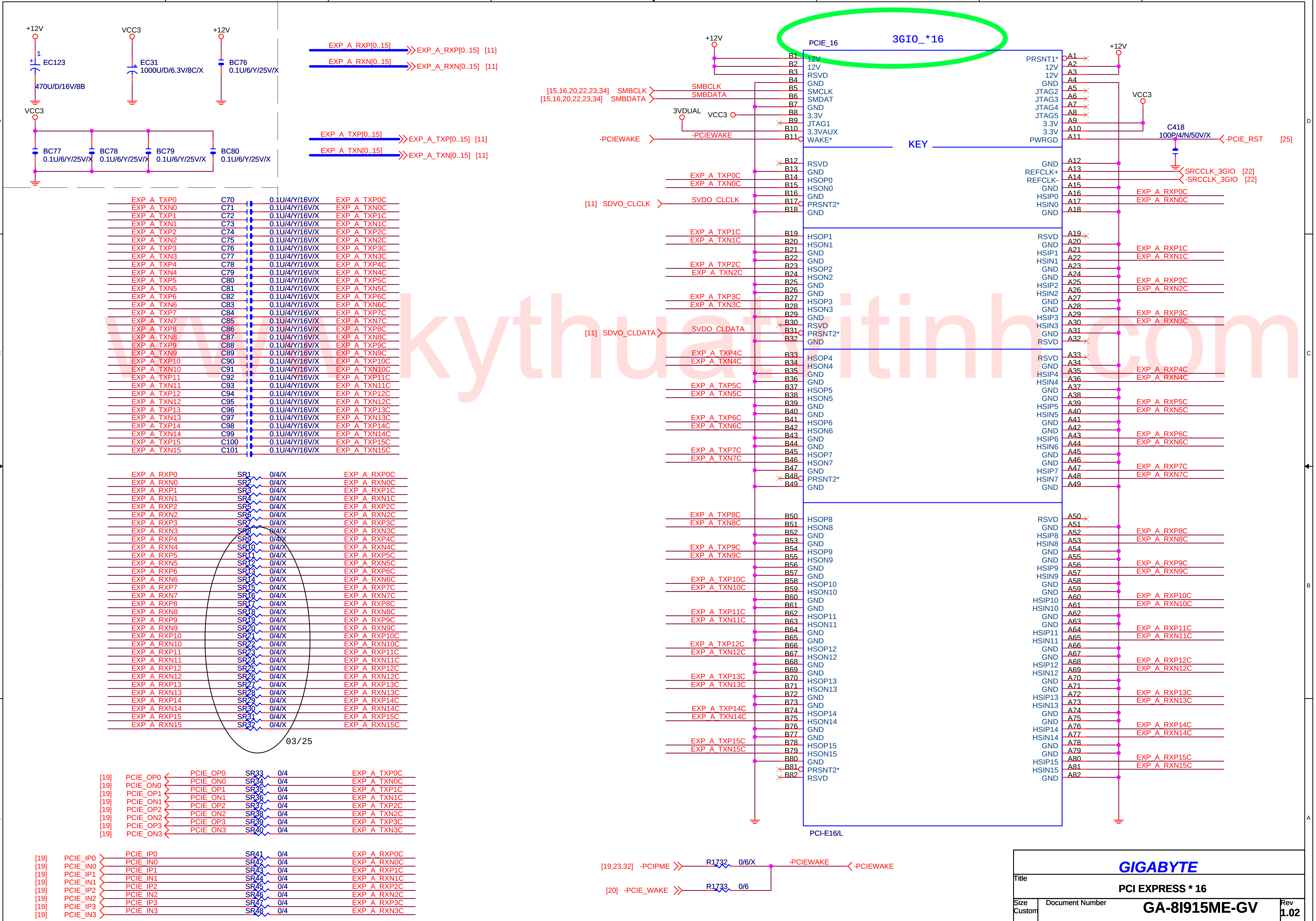

GIGABYTE

Title: PCI EXPRESS * 16

Size	Document Number	Rev
Custom	GA-8I915ME-GV	1.02

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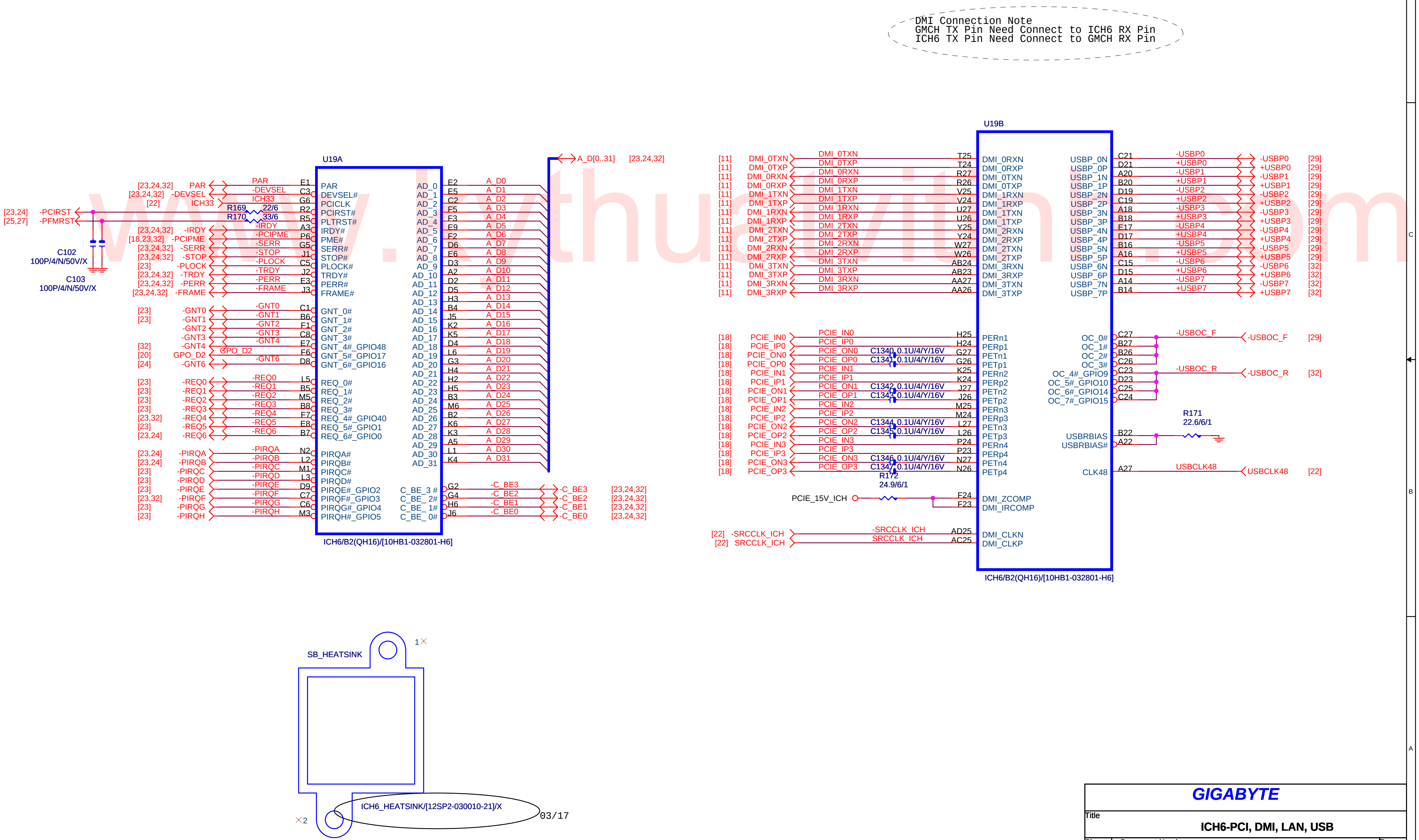

GIGABYTE

Title: ICH6-PCI, DMI, LAN, USB

Size	Document Number	Rev
B	GA-8I915ME-GV	1.02

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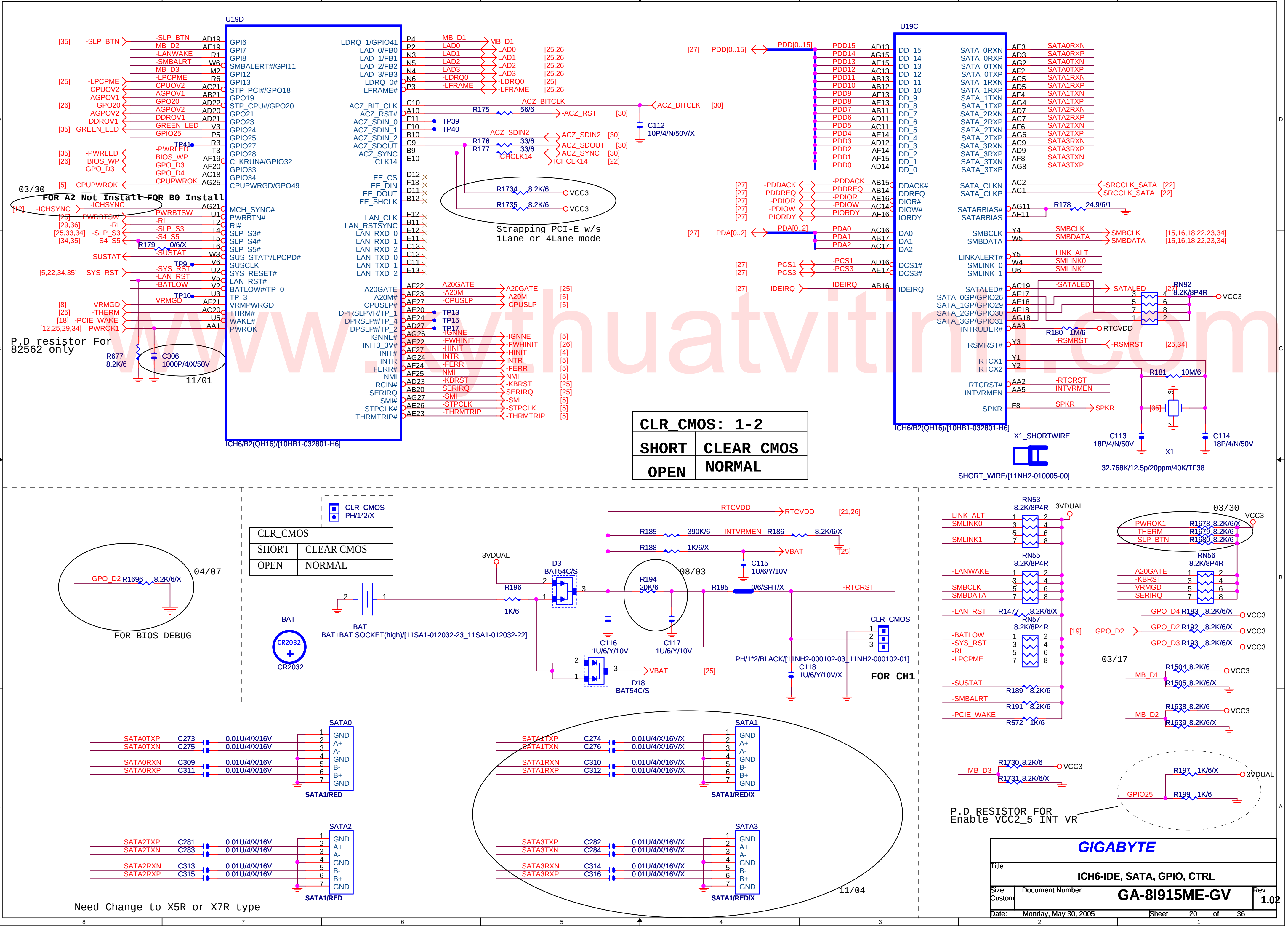


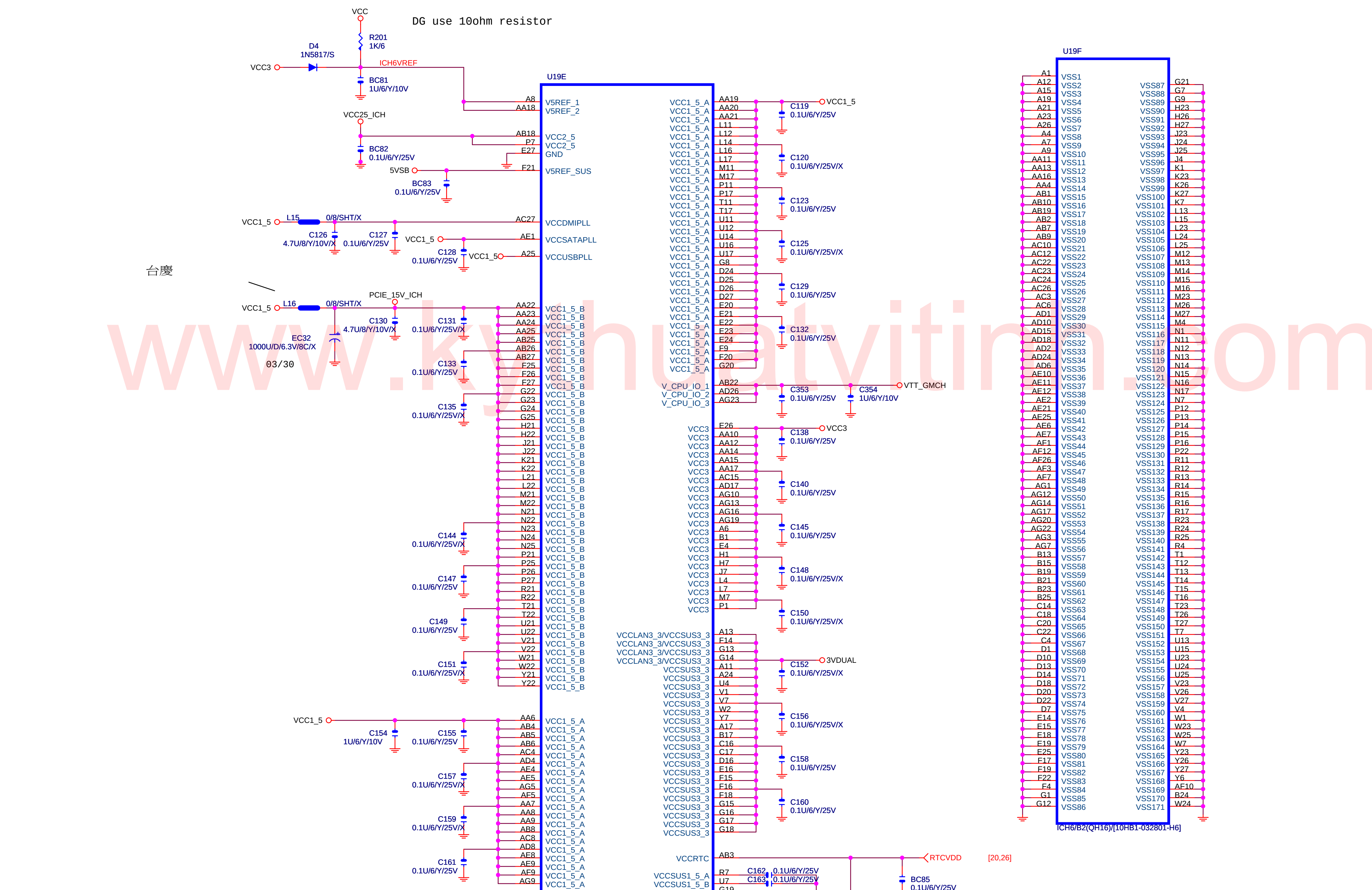
CLR_CMOS	
SHORT	CLEAR CMOS
OPEN	NORMAL

GIGABYTE

Title: ICH6-IDE, SATA, GPIO, CTRL

Size: Custom | Document Number: GA-8I915ME-GV | Rev: 1.02

Date: Monday, May 30, 2005 | Sheet 20 of 36



GIGABYTE

Title	ICH6-PWR & GND	
Size B	Document Number GA-8I915ME-GV	Rev 1.02
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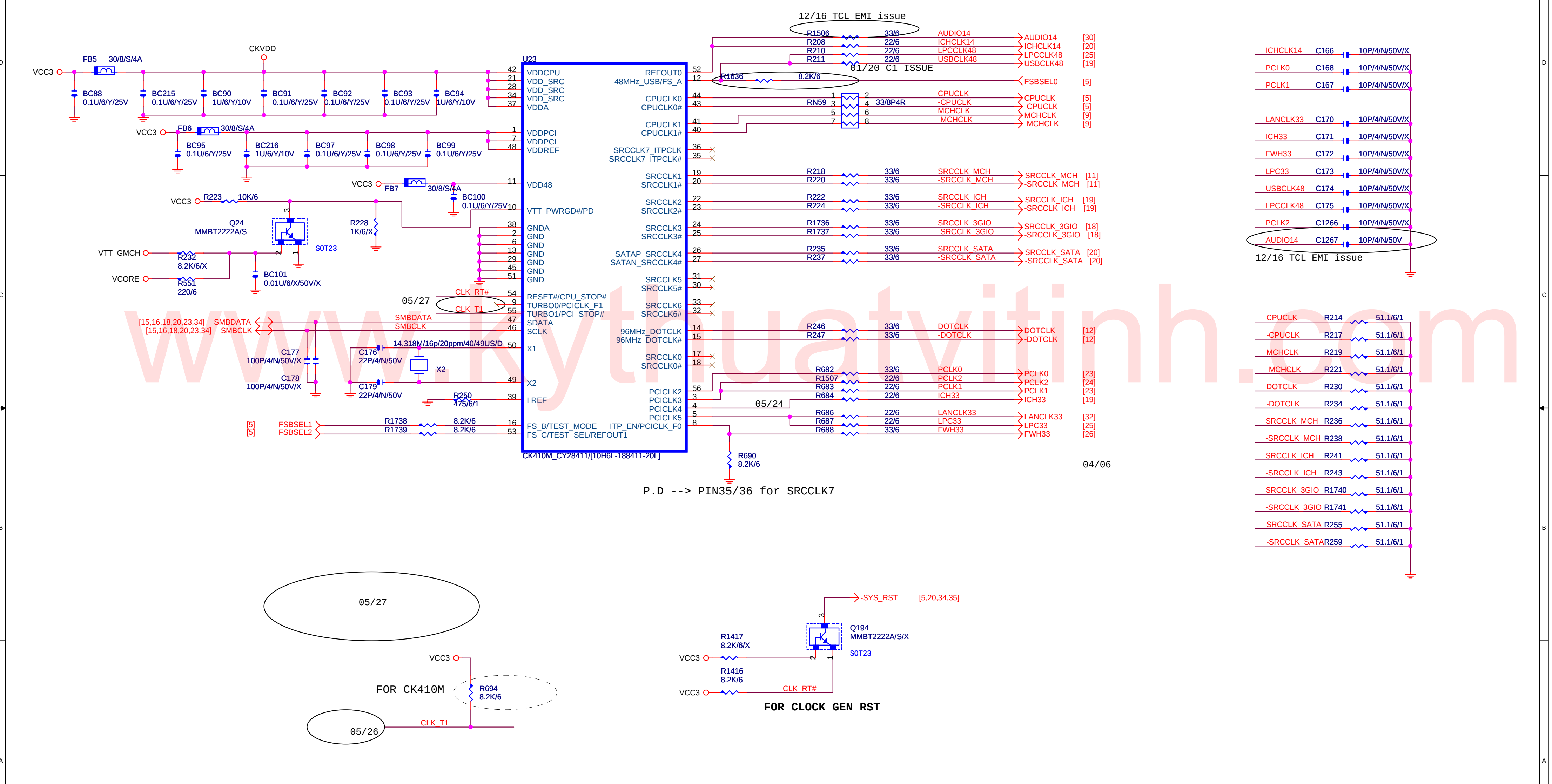

GIGABYTE

Title: **CK410M/GBT_CLK GEN**

Size	Document Number	Rev
Custom	GA-8I915ME-GV	1.02

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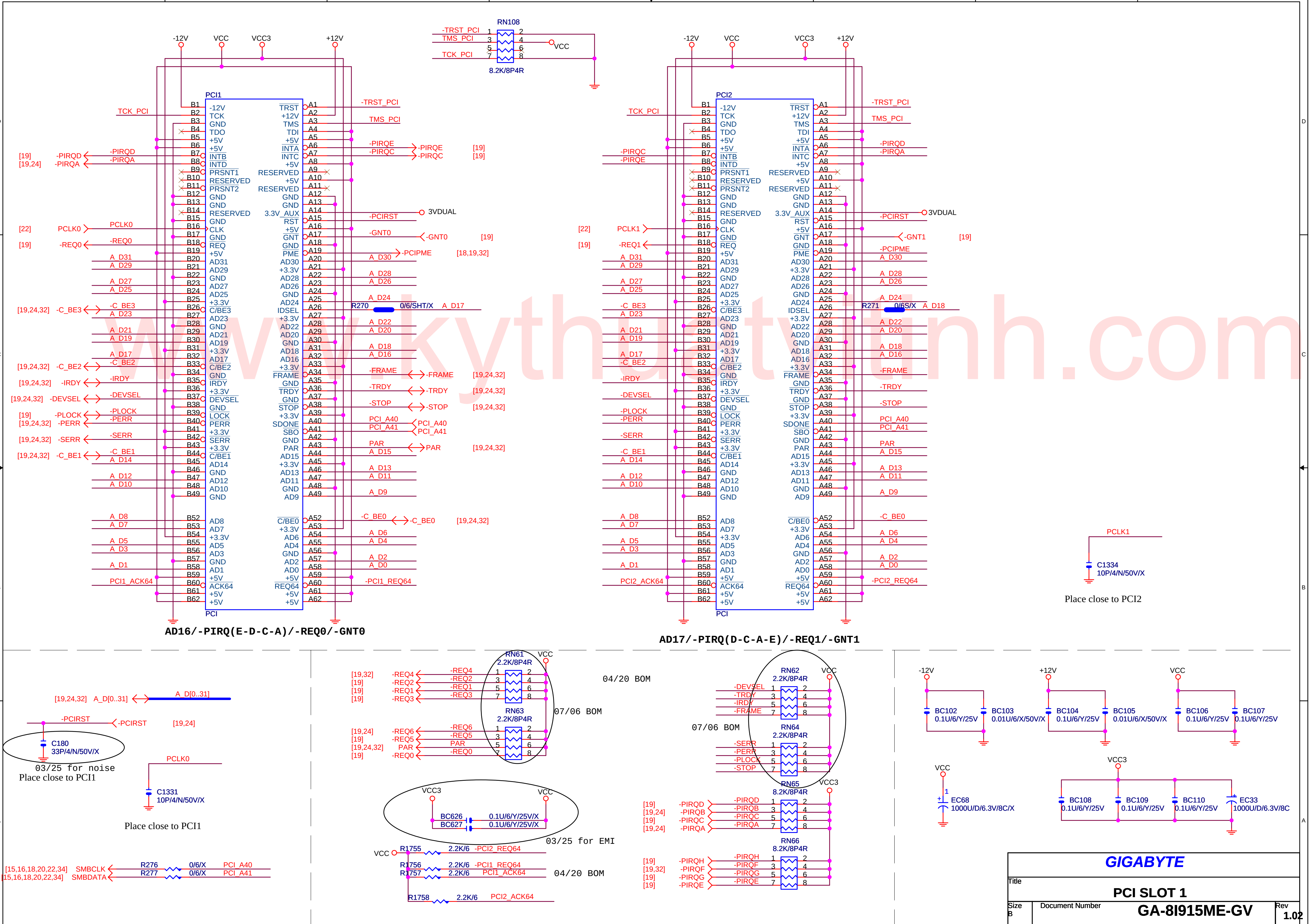

GIGABYTE

Title
PCI SLOT 1

Size	Document Number	Rev
B	GA-8I915ME-GV	1.02

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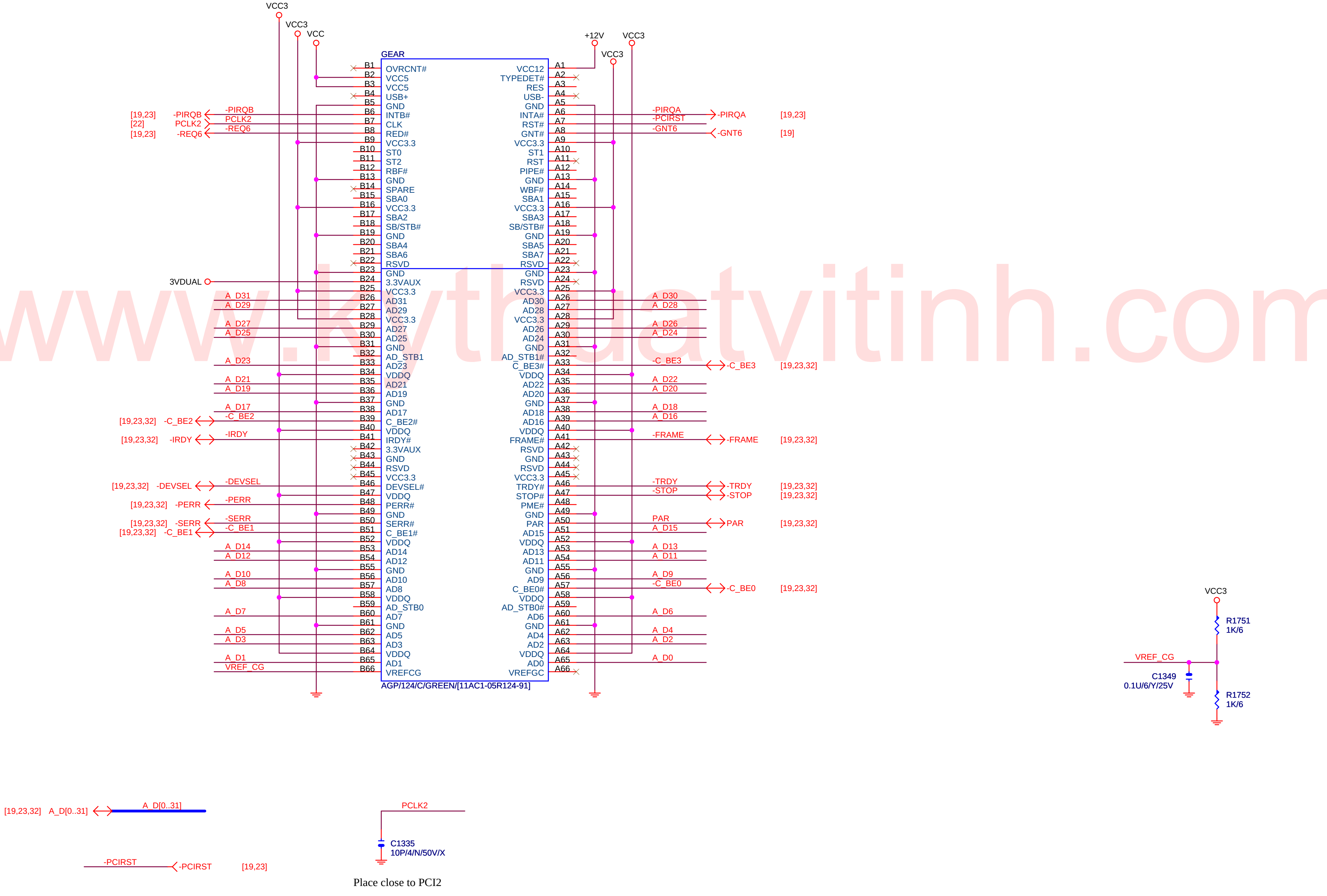

GIGABYTE

Title: PCI SLOT 3

Size	Document Number	Rev
B	GA-8I915ME-GV	1.02

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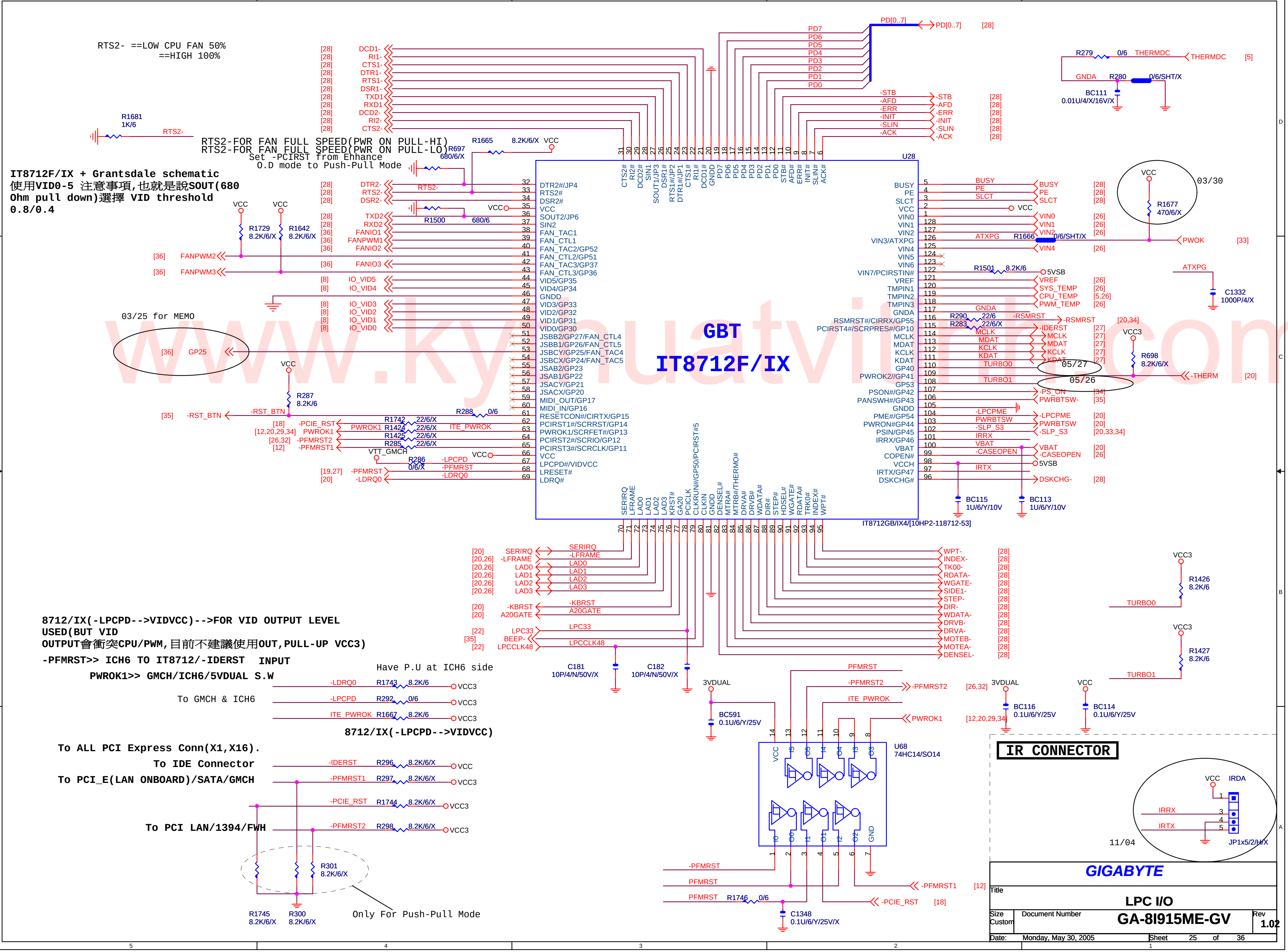

RTS2- ==LOW CPU FAN 50%
==HIGH 100%
RTS2-FOR FAN FULL SPEED(PWR ON PULL-HI)
RTS2-FOR FAN FULL SPEED(PWR ON PULL-LO)
Set -PCIRST from Enhance
O.D mode to Push-Pull Mode
IT8712F/IX + Grantsdale schematic
使用VID0-5 注意事項,也就是說SOUT(680
Ohm pull down)選擇 VID threshold
0.8/0.4
03/25 for MEMO
GBT
IT8712F/IX
U28
IT8712GB/IX4/[10HP2-118712-53]
03/30
05/27
05/26
8712/IX(-LPCPD-->VIDVCC)-->FOR VID OUTPUT LEVEL
USED(BUT VID
OUTPUT會衝突CPU/PWM,目前不建議使用OUT,PULL-UP VCC3)
-PFMRST>> ICH6 TO IT8712/-IDERST INPUT
PWROK1>> GMCH/ICH6/5VDUAL S.W
Have P.U at ICH6 side
To GMCH & ICH6
8712/IX(-LPCPD-->VIDVCC)
To ALL PCI Express Conn(X1,X16).
To IDE Connector
To PCI_E(LAN ONBOARD)/SATA/GMCH
To PCI LAN/1394/FWH
Only For Push-Pull Mode
U68
74HC14/SO14
IR CONNECTOR
11/04
JP1x5/2/H/X
GIGABYTE
Title
LPC I/O
Size
Custom
Document Number
GA-8I915ME-GV
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1.02
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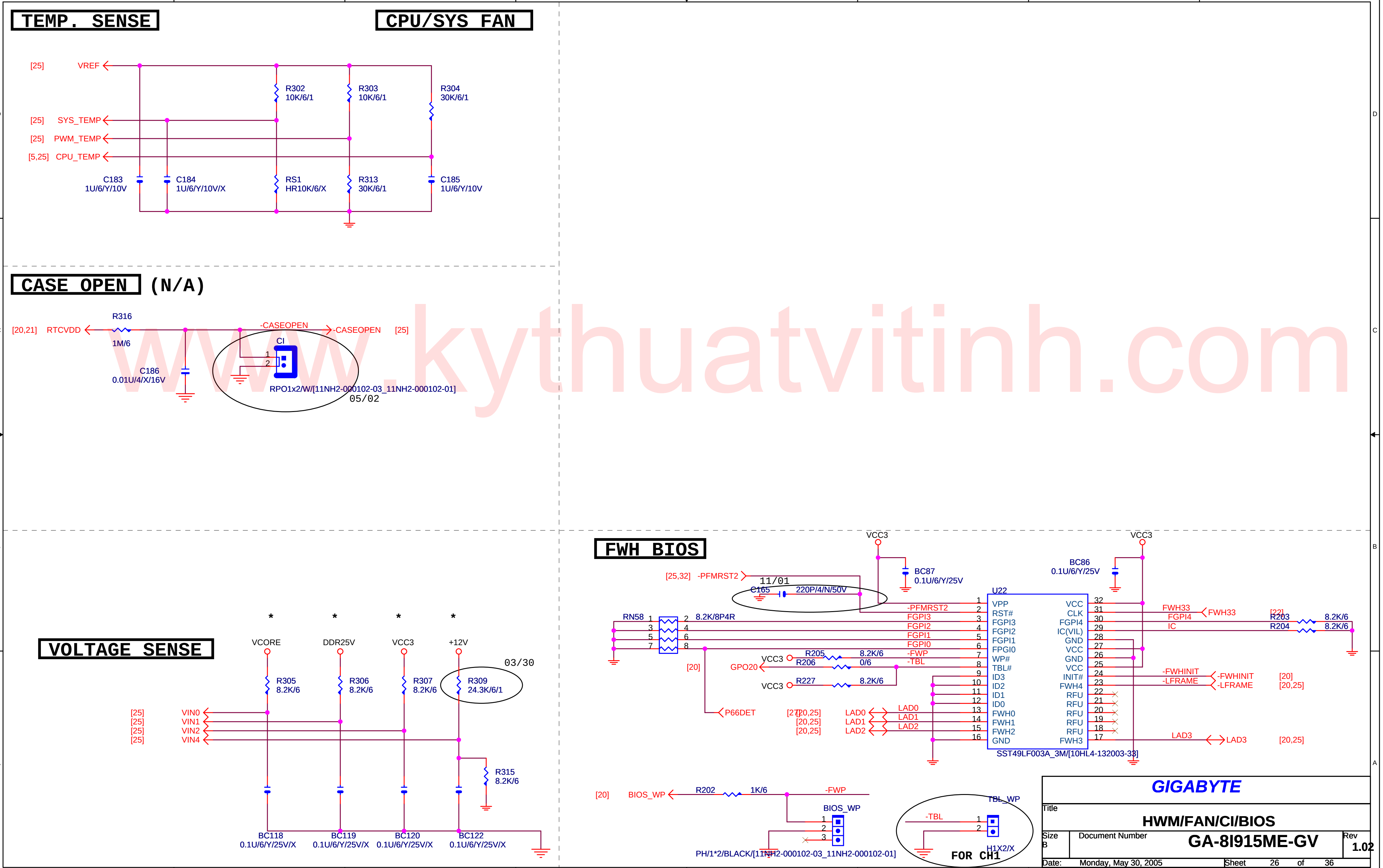

GIGABYTE

Title: **HWM/FAN/CI/BIOS**

Size	Document Number	Rev
B	GA-8I915ME-GV	1.02

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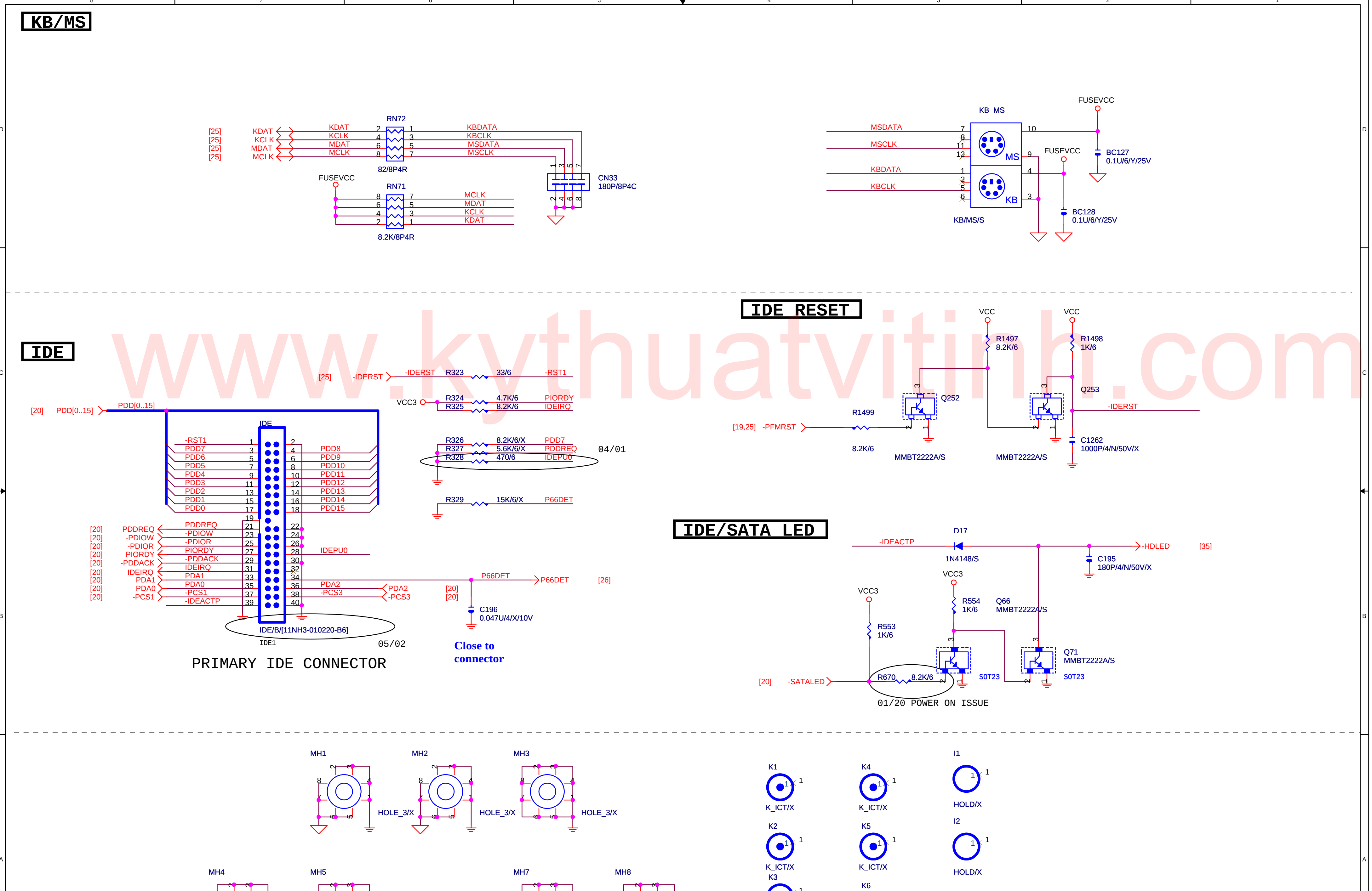
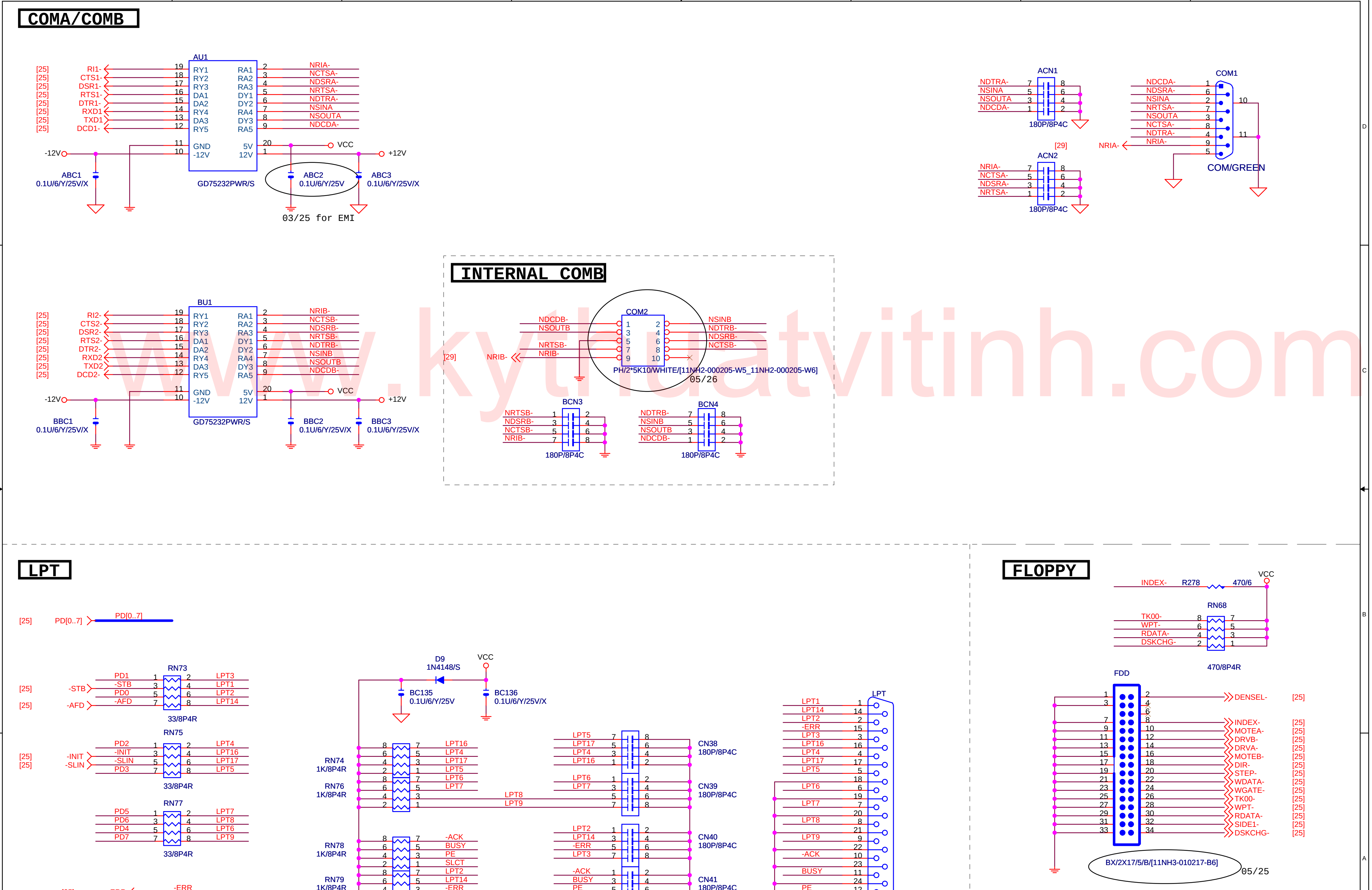


GIGABYTE

Title: PS/2 KB & MS

Size	Document Number	Rev
B	GA-8I915ME-GV	1.02

Date: Monday, May 30, 2005 — Sheet 27 of 36



GIGABYTE

Title: COM, LPT, FDD

Size: B | Document Number: GA-8I915ME-GV | Rev: 1.02

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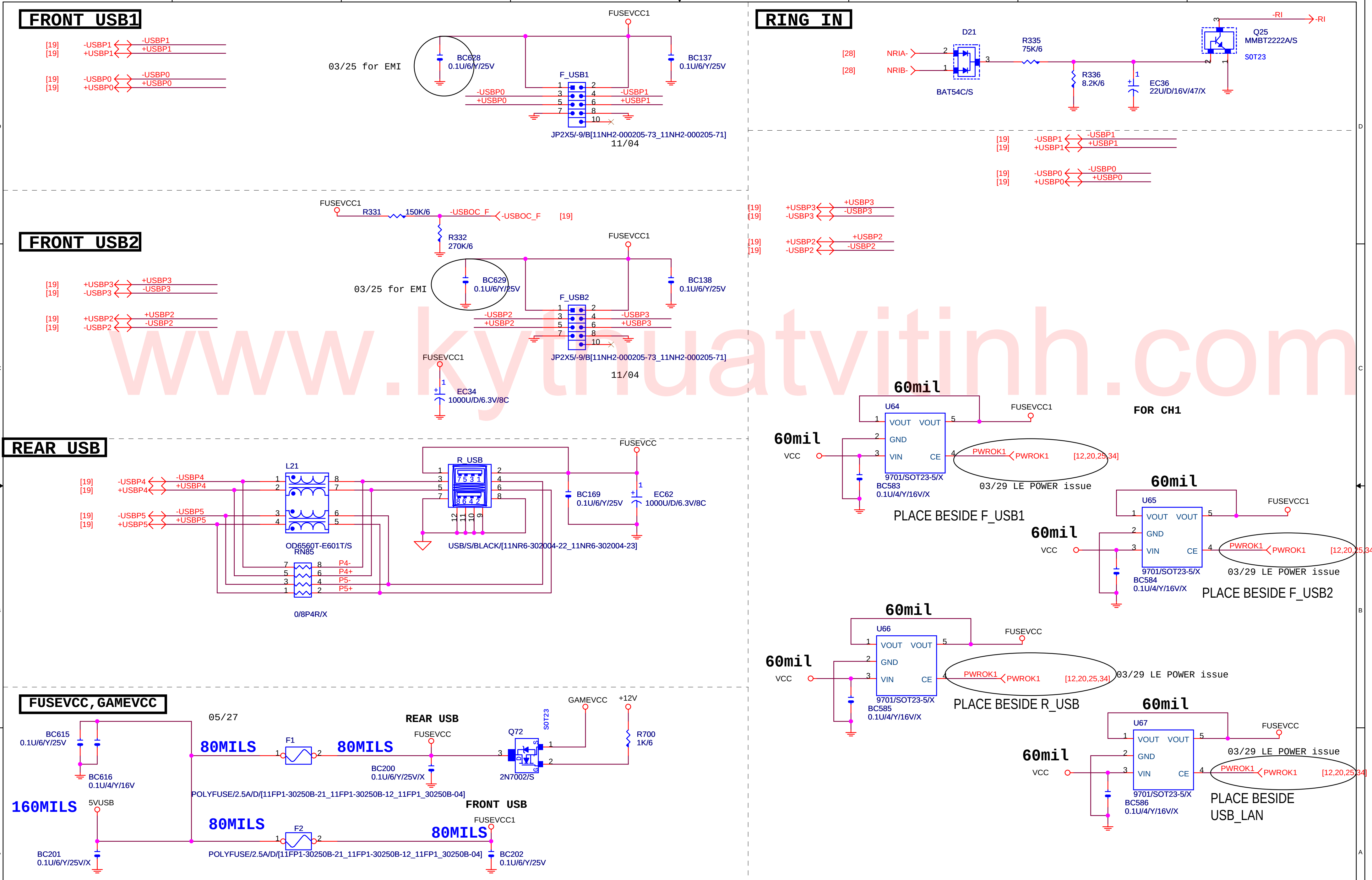

GIGABYTE

Title: **FRONT/REAR USB CONNECTOR/IDE**

Size	Document Number	Rev
Custom	GA-8I915ME-GV	1.02

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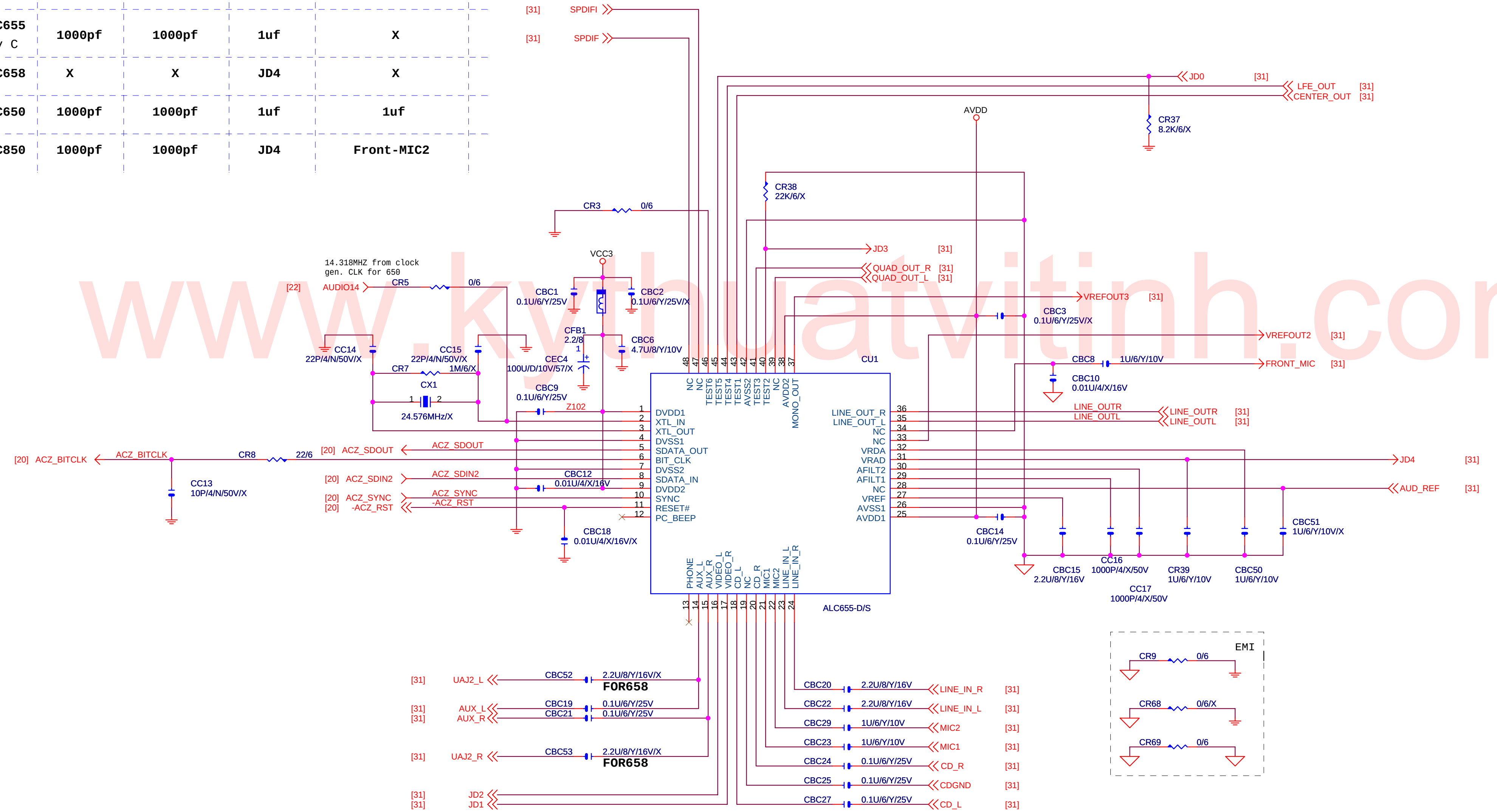
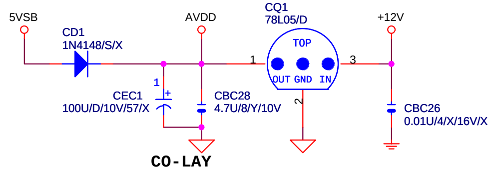
Filter Cap design:

	Pin-29	Pin-30	Pin-31	Pin-34
ALC655 Rev D	1000pf	1000pf	1uf	Front-MIC2
ALC655 Rev C	1000pf	1000pf	1uf	X
ALC658	X	X	JD4	X
ALC650	1000pf	1000pf	1uf	1uf
ALC850	1000pf	1000pf	JD4	Front-MIC2

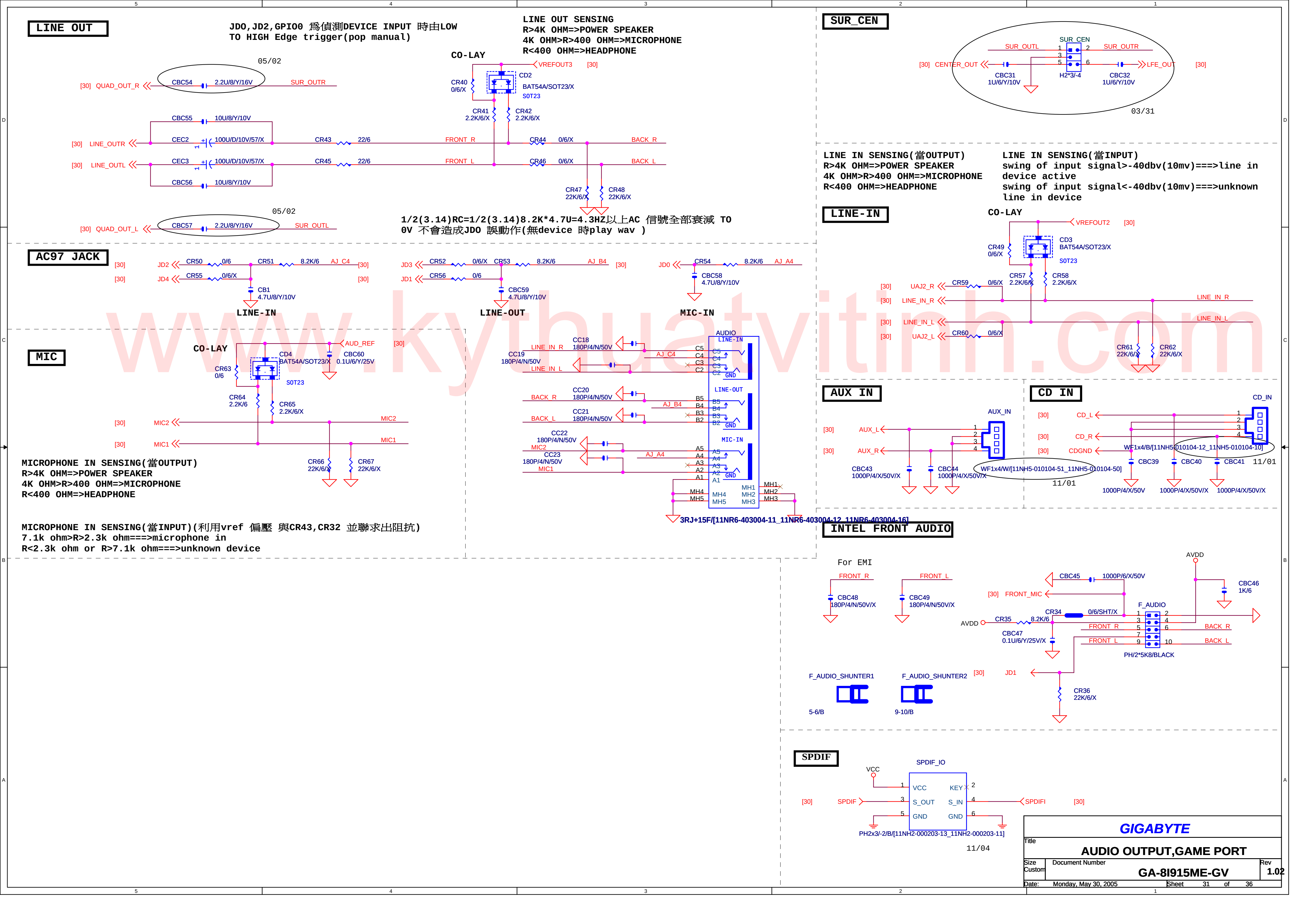




Arrangement of Jack detection Pin:

	Pin-45(JD0)	Pin-17(JD1)	Pin-16(JD2)	Pin-40(JD3)	Pin-31(JD4)	Pin-13(JD5)
ALC655	for MIC-IN	for FRONT-OUT	for LINE-IN			
ALC658	for MIC-IN	for UAJ1	for UAJ2	for FRONT-OUT Exernal pull high is needed	for LINE-IN Exernal pull high is needed	
ALC850	for MIC-IN	for Front Pannel OUT	for Front Pannel IN	for FRONT-OUT	for LINE-IN	for SurrBack Out

GIGABYTE	
Title	ALC655/658/850
Size Custom	Document Number GA-8I915ME-GV
Rev	1.02
Date: Monday, May 30, 2005	Sheet 30 of 36


LINE OUT
JDO,JD2,GPIO0 爲偵測DEVICE INPUT 時由LOW
TO HIGH Edge trigger(pop manual)
LINE OUT SENSING
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE
CO-LAY
05/02
QUAD_OUT_R
CBC54 2.2U/8/Y/16V
SUR_OUTR
VREFOUT3
CR40 0/6/X
CD2 BAT54A/SOT23/X
SOT23
CR41 2.2K/6/X
CR42 2.2K/6/X
CBC55 10U/8/Y/10V
LINE_OUTR
CEC2 100U/D/10V/57/X
CR43 22/6
FRONT_R
CR44 0/6/X
BACK_R
LINE_OUTL
CEC3 100U/D/10V/57/X
CR45 22/6
FRONT_L
CR46 0/6/X
BACK_L
CBC56 10U/8/Y/10V
CR47 22K/6/X
CR48 22K/6/X
QUAD_OUT_L
CBC57 2.2U/8/Y/16V
SUR_OUTL
1/2(3.14)RC=1/2(3.14)8.2K*4.7U=4.3HZ以上AC 信號全部衰減 TO
0V 不會造成JDO 誤動作(無device 時play wav)
SUR_CEN
SUR_OUTL SUR_CEN SUR_OUTR
CENTER_OUT LFE_OUT
CBC31 1U/6/Y/10V
H2*3/-4
CBC32 1U/6/Y/10V
03/31
LINE IN SENSING(當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE
LINE IN SENSING(當INPUT)
swing of input signal>-40dbv(10mv)===>line in
device active
swing of input signal<-40dbv(10mv)===>unknown
line in device
LINE-IN
CO-LAY
VREFOUT2
CR49 0/6/X
CD3 BAT54A/SOT23/X
SOT23
UAJ2_R CR59 0/6/X
CR57 2.2K/6/X
CR58 2.2K/6/X
LINE_IN_R
LINE_IN_L
UAJ2_L CR60 0/6/X
CR61 22K/6/X
CR62 22K/6/X
AC97 JACK
JD2 CR50 0/6 CR51 8.2K/6 AJ_C4
JD4 CR55 0/6/X
CB1 4.7U/8/Y/10V
LINE-IN
JD3 CR52 0/6/X CR53 8.2K/6 AJ_B4
JD1 CR56 0/6
CBC59 4.7U/8/Y/10V
LINE-OUT
JD0 CR54 8.2K/6 AJ_A4
CBC58 4.7U/8/Y/10V
MIC-IN
MIC
CO-LAY
CR63 0/6
CD4 BAT54A/SOT23/X
SOT23
AUD_REF
CBC60 0.1U/6/Y/25V
CR64 2.2K/6
CR65 2.2K/6/X
MIC2
MIC1
CR66 22K/6/X
CR67 22K/6/X
MICROPHONE IN SENSING(當OUTPUT)
R>4K OHM=>POWER SPEAKER
4K OHM>R>400 OHM=>MICROPHONE
R<400 OHM=>HEADPHONE
MICROPHONE IN SENSING(當INPUT)(利用vref 偏壓 與CR43,CR32 並聯求出阻抗)
7.1k ohm>R>2.3k ohm===>microphone in
R<2.3k ohm or R>7.1k ohm===>unknown device
CC18 180P/4/N/50V
CC19 180P/4/N/50V
CC20 180P/4/N/50V
CC21 180P/4/N/50V
CC22 180P/4/N/50V
CC23 180P/4/N/50V
AUDIO
LINE-IN
LINE-OUT
MIC-IN
3RJ+15F/[11NR6-403004-11_11NR6-403004-12_11NR6-403004-16]
AUX IN
AUX_IN
AUX_L
AUX_R
CBC43 1000P/4/X/50V/X
CBC44 1000P/4/X/50V/X
WF1x4/W/[11NH5-010104-51_11NH5-010104-50]
11/01
CD IN
CD_IN
CD_L
CD_R
CDGND
WF1x4/B/[11NH5-010104-12_11NH5-010104-10]
CBC39 1000P/4/X/50V
CBC40 1000P/4/X/50V/X
CBC41 1000P/4/X/50V/X
11/01
INTEL FRONT AUDIO
For EMI
FRONT_R
CBC48 180P/4/N/50V/X
FRONT_L
CBC49 180P/4/N/50V/X
AVDD
CBC45 1000P/6/X/50V
CBC46 1K/6
FRONT_MIC
CR34 0/6/SHT/X
F_AUDIO
CR35 8.2K/6
CBC47 0.1U/6/Y/25V/X
PH/2*5K8/BLACK
F_AUDIO_SHUNTER1 5-6/B
F_AUDIO_SHUNTER2 9-10/B
JD1
CR36 22K/6/X
SPDIF
SPDIF_IO
VCC
VCC KEY
S_OUT S_IN
GND GND
SPDIFI
PH2x3/-2/B/[11NH2-000203-13_11NH2-000203-11]
11/04
GIGABYTE
Title
AUDIO OUTPUT,GAME PORT
Size Custom
Document Number
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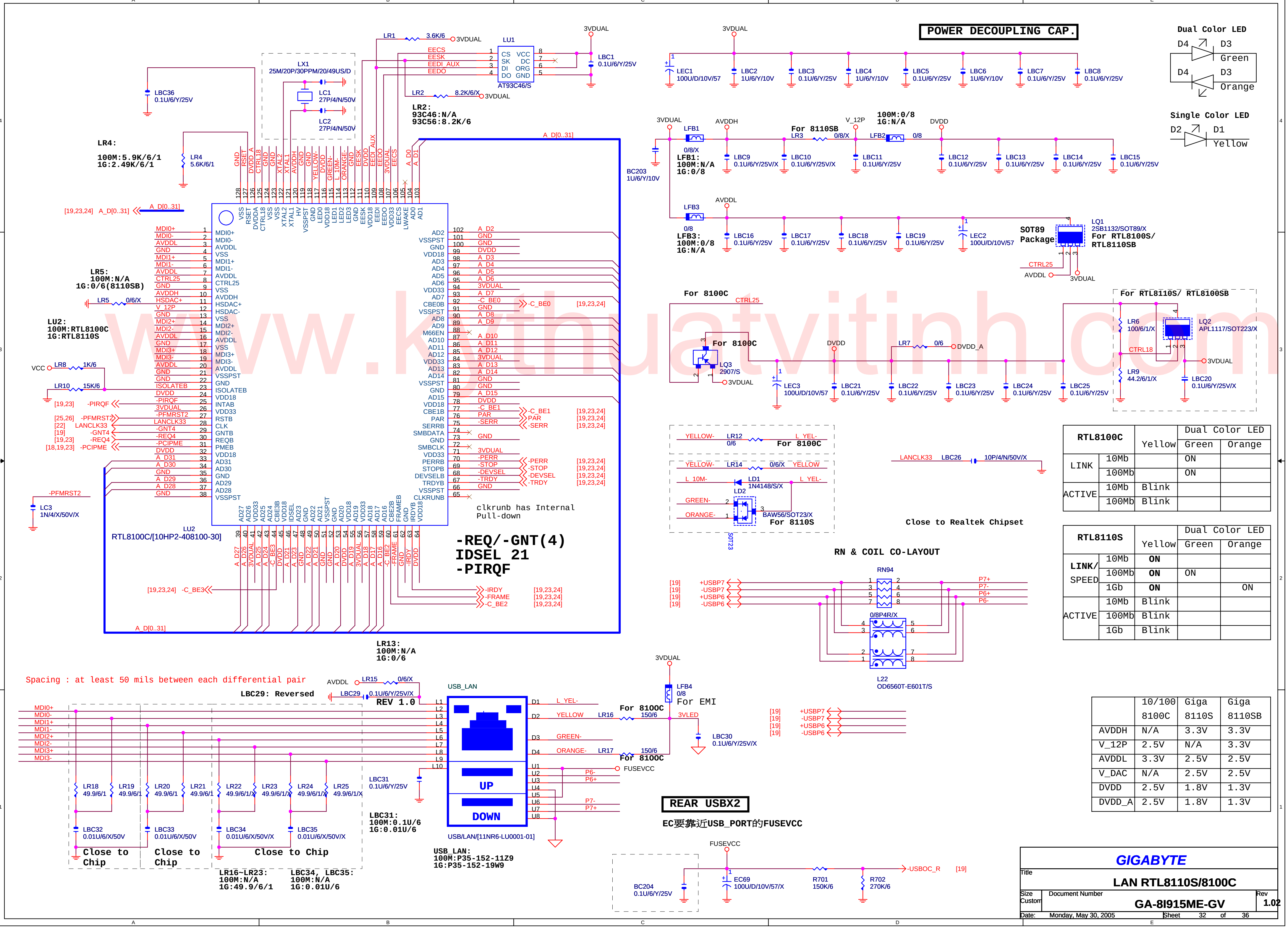

RTL8100C		Yellow	Dual Color LED Green	Orange
LINK	10Mb		ON	
	100Mb		ON	
ACTIVE	10Mb	Blink		
	100Mb	Blink		

RTL8110S		Yellow	Dual Color LED Green	Orange
LINK/SPEED	10Mb	ON		
	100Mb	ON	ON	
	1Gb	ON		ON
ACTIVE	10Mb	Blink		
	100Mb	Blink		
	1Gb	Blink		

	10/100 8100C	Giga 8110S	Giga 8110SB
AVDDH	N/A	3.3V	3.3V
V_12P	2.5V	N/A	3.3V
AVDDL	3.3V	2.5V	2.5V
V_DAC	N/A	2.5V	2.5V
DVDD	2.5V	1.8V	1.3V
DVDD_A	2.5V	1.8V	1.3V

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Title: LAN RTL8110S/8100C

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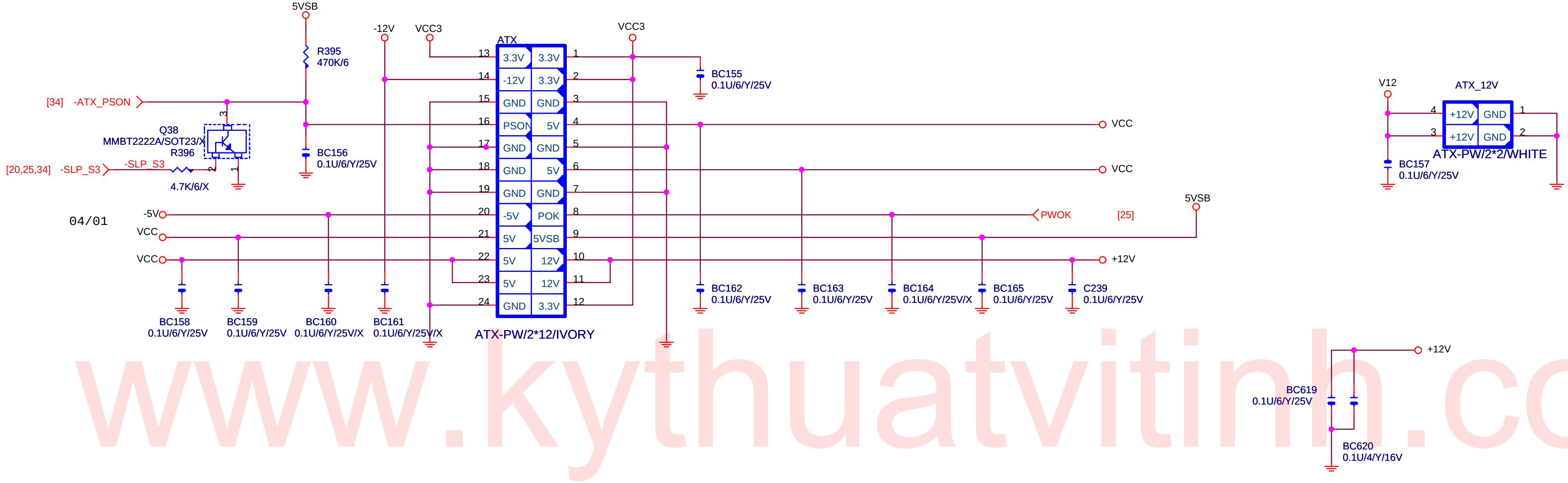
ATX POWER CONNECTOR



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Title: ATX POWER CONNECTOR & TPM

Size: B | Document Number: GA-8I915ME-GV | Rev: 1.02

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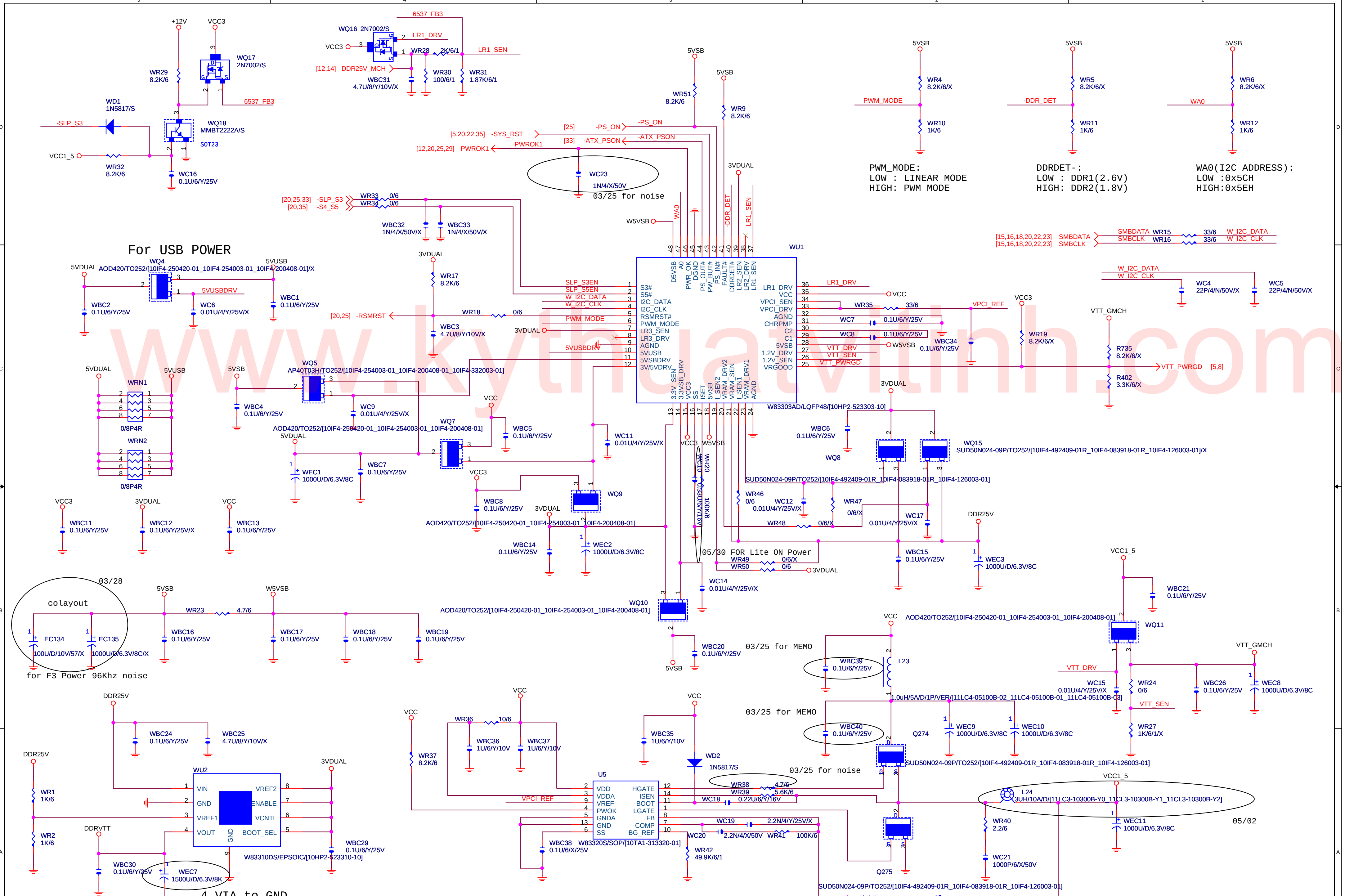

GIGABYTE

Title: DDR POWER

Size: Custom | Document Number: GA-8I915ME-GV | Rev: 1.02

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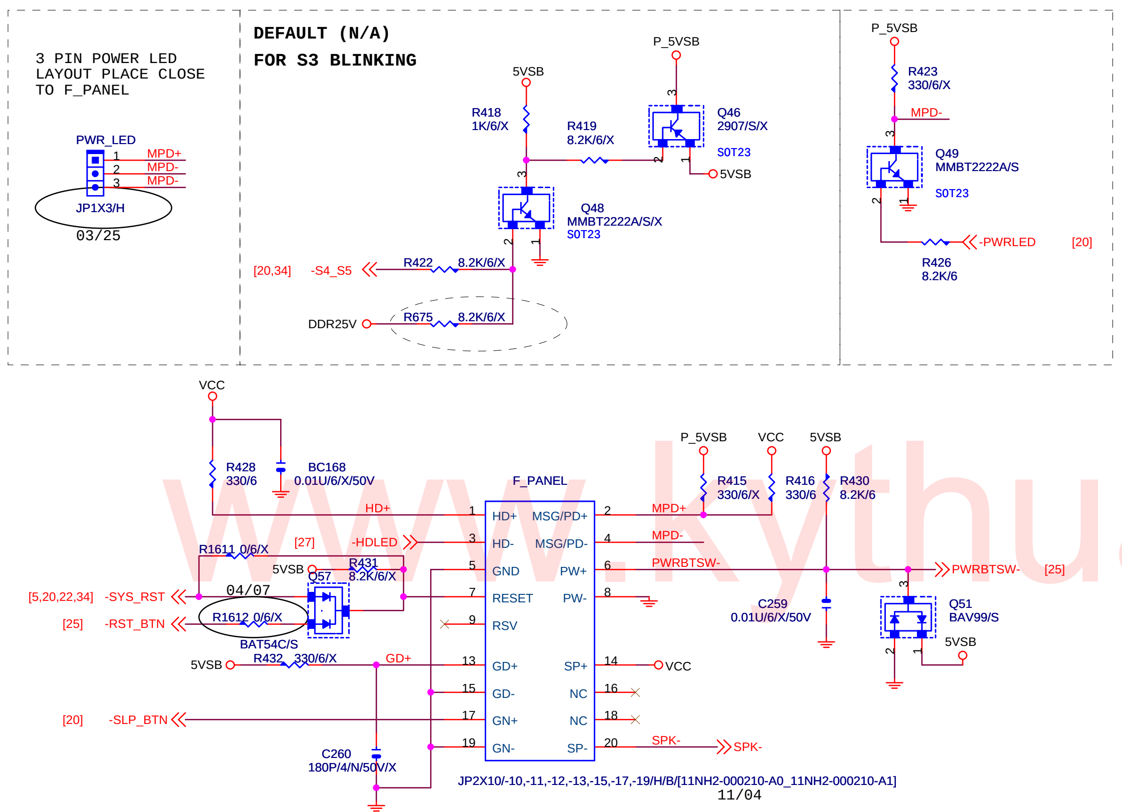
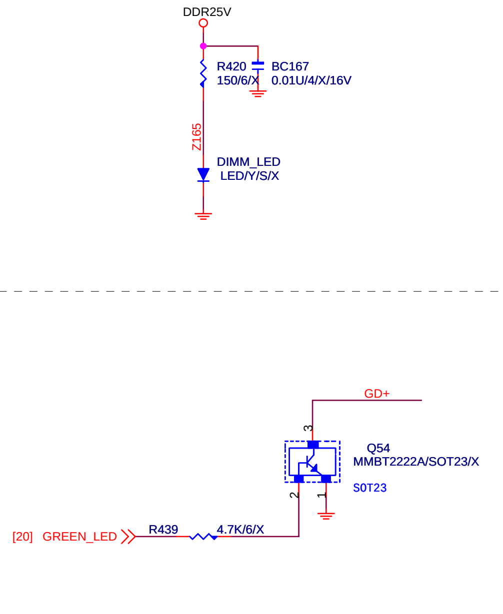
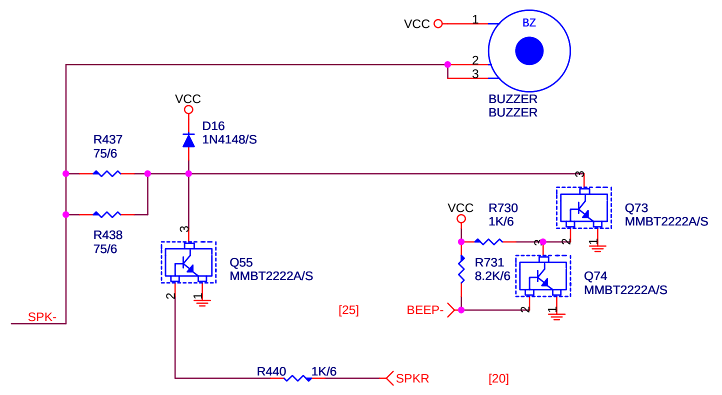
INTEL FRONT PANEL





BUZZER



States for green LED **NO1 GPO22 only S1 PROGRAMMING LOW**

LED States	ACPI States	GPO28
ON	S1,S3	0
OFF	S0,S5	1

(GPO22 DEFAULT HIGH, main power)

States for a single-color power LED

LED States	ACPI States	GPO25	GPO27	GPO24
OFF	S1,S3,S5	1	1	NO1
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	B	1

LED States	ACPI States	GPO25	GPO27	GPO22
OFF	S5	1	1	X
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	B	1
Steady Yellow	S1,S3	1	0	NO1
Blinking Yellow	S1,S3(message waiting)	1	B	NO1

GIGABYTE

Title	FRONT PANEL
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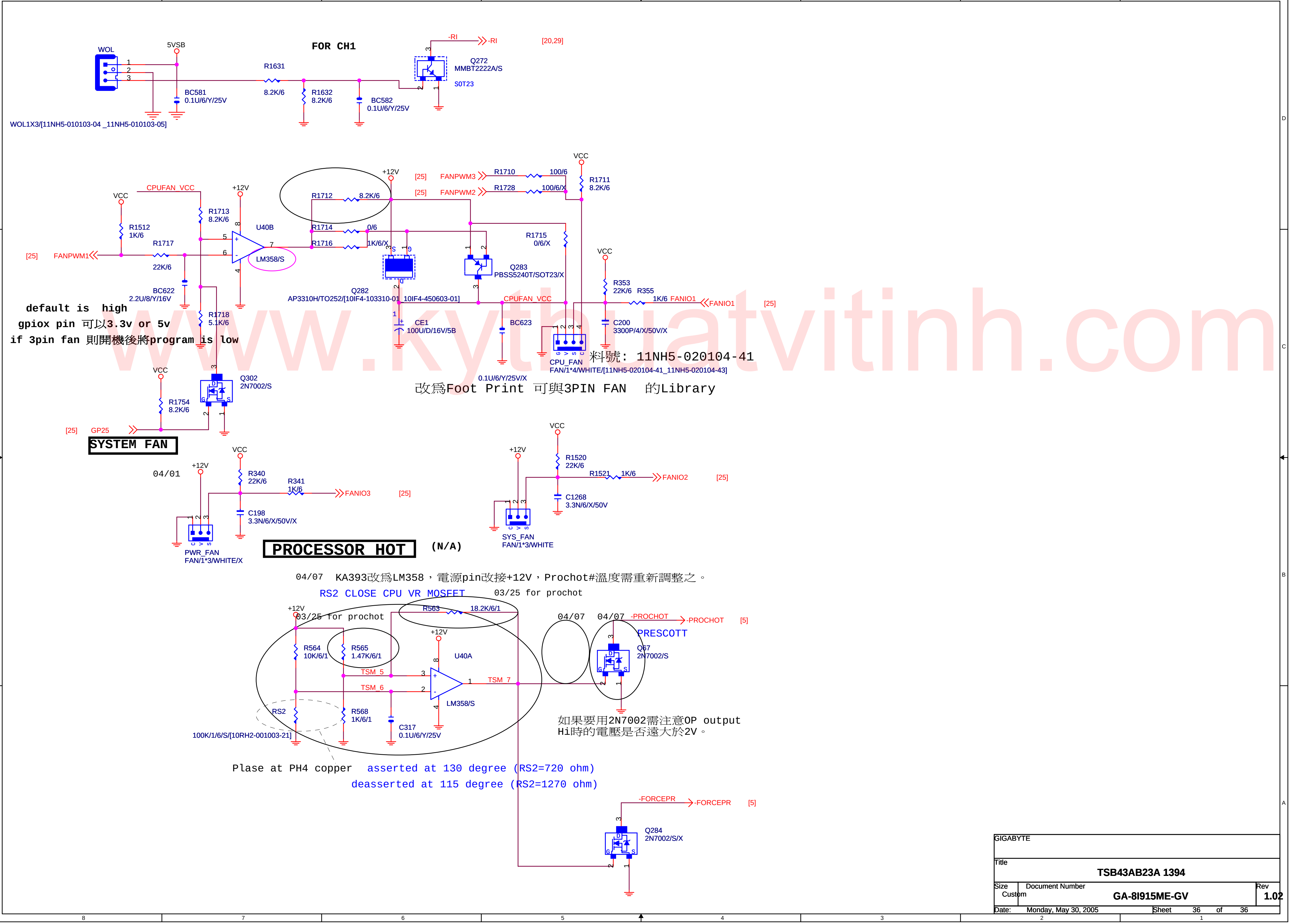

FOR CH1
WOL
5VSB
R1631
8.2K/6
R1632
8.2K/6
BC581
0.1U/6/Y/25V
BC582
0.1U/6/Y/25V
WOL1X3/[11NH5-010103-04 _11NH5-010103-05]
-RI
[20,29]
Q272
MMBT2222A/S
S0T23
CPUFAN_VCC
VCC
+12V
R1512
1K/6
R1713
8.2K/6
R1717
22K/6
[25]
FANPWM1
U40B
LM358/S
BC622
2.2U/8/Y/16V
R1718
5.1K/6
R1712
8.2K/6
R1714
0/6
R1716
1K/6/X
FANPWM3
FANPWM2
R1710
100/6
R1728
100/6/X
R1711
8.2K/6
R1715
0/6/X
Q283
PBSS5240T/SOT23/X
Q282
AP3310H/TO252/[10IF4-103310-01_10IF4-450603-01]
CE1
100U/D/16V/5B
BC623
0.1U/6/Y/25V/X
R353
22K/6
R355
1K/6
FANIO1
C200
3300P/4/X/50V/X
CPU_FAN
FAN/1*4/WHITE/[11NH5-020104-41_11NH5-020104-43]
料號: 11NH5-020104-41
改爲Foot Print 可與3PIN FAN 的Library
default is high
gpiox pin 可以3.3v or 5v
if 3pin fan 則開機後將program is low
Q302
2N7002/S
R1754
8.2K/6
GP25
SYSTEM FAN
04/01
R340
22K/6
R341
1K/6
FANIO3
C198
3.3N/6/X/50V/X
PWR_FAN
FAN/1*3/WHITE/X
R1520
22K/6
R1521
1K/6
FANIO2
C1268
3.3N/6/X/50V
SYS_FAN
FAN/1*3/WHITE
PROCESSOR HOT
(N/A)
04/07 KA393改爲LM358，電源pin改接+12V，Prochot#溫度需重新調整之。
RS2 CLOSE CPU VR MOSFET
03/25 for prochot
R563
18.2K/6/1
R564
10K/6/1
R565
1.47K/6/1
TSM_5
TSM_6
TSM_7
U40A
LM358/S
RS2
100K/1/6/S/[10RH2-001003-21]
R568
1K/6/1
C317
0.1U/6/Y/25V
04/07
-PROCHOT
[5]
PRESCOTT
Q67
2N7002/S
如果要用2N7002需注意OP output
Hi時的電壓是否遠大於2V。
Plase at PH4 copper
asserted at 130 degree (RS2=720 ohm)
deasserted at 115 degree (RS2=1270 ohm)
-FORCEPR
Q284
2N7002/S/X
GIGABYTE
Title
TSB43AB23A 1394
Size
Custom
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