

Model Name: 8I915G-MF

Revision 1.0



SHEET TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B
06	P4_LGA775_C
07	P4_LGA775_D
08	VCORE POWER
09	GMCH-GRANTSDALE_HOST
10	GMCH-GARNTSDALE_DDR
11	GMCH-GRANTSDALE_PCI E, DMI
12	GMCH-GRANTSDALE_INT VGA
13	GMCH-GRANTSDALE_GND
14	GMCH-GRANTSDALE_PWR
15	DDR CHANNEL A
16	DDR CHANNEL B
17	DDR TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH6 PCI, USB, DMI, LAN
20	ICH6 IDE, GPIO, SATA, CTRL
21	ICH6 VCC, GND
22	CLK GEN

SHEET TITLE

23	PCI SLOT 1
24	PCI EXPRESS*1 SLOT 1,2
25	ITE8712HX
26	HWMO/FAN/FWH BIOS
27	KB_MS/GAME
28	COM/LPT/FDD
29	(FRONT+REAR)USB/RING/IDE
30	AZALIA CODEC ALC880/CMI9880
31	AUDIO JACK
32	LAN RTL8110S/8100C
33	ATX POWER CONN.
34	ALL POWER
35	1394 TSB43AB23
36	FRONT PANEL/BZ

PCB VANDER: 育富(TB), 智恩(TA), 鴻達

	COMPONENT SIDE (1 oz. Copper)
	VCC SIDE (1 oz. Copper)
	GND SIDE (1 oz. Copper)
	SOLDER SIDE (1 oz. Copper)

<b>GIGABYTE</b>		
Title <b>Cover Sheet</b>		
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# BLOCK DIAGRAM

**INTEL Pentium4 LGA775**

V<sub>CORE</sub> = 1.75V / SLEEP: 1.3V  
V<sub>CC3</sub>

PAGE 4, 5, 6, 7

**CLOCK GENERATOR**

CKVDD = 3.3V

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**PWM/OTHER POWER**

V<sub>CORE</sub> = 1.75V (650-1100MHz) / SLEEP: 1.3V  
5V<sub>SB</sub> = 12V, V<sub>CC</sub>, V<sub>CC3</sub>, 3V<sub>DUAL</sub>  
V<sub>TT</sub>, DDR2, 2V<sub>STR</sub>

PAGE 8, 33, 34

**GMCH GRANTS DALE**

V<sub>CORE</sub> = 1.75V / SLEEP: 1.3V  
2.5V<sub>STR</sub> = 2.5V (MEMORY, SUSPEND POWER)  
V<sub>DDQ</sub> = 1.5V (AGP POWER 4X, HUBLINK)

PAGE 9, 10, 11, 12, 13, 14

**CHANNEL A DDR SDRAM DIMM X 2**

2.5V<sub>STR</sub> = 2.5V (MEMORY, SUSPEND POWER)  
V<sub>TT</sub>, DDR = 1.25V

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**CHANNEL B DDR SDRAM DIMM X 2**

2.5V<sub>STR</sub> = 2.5V (MEMORY, SUSPEND POWER)  
V<sub>TT</sub>, DDR = 1.25V

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**PCI EXPRESS BY 16 PORTS**


V<sub>DDQ</sub> = 1.5V (AGP POWER 4X)  
V<sub>CC3</sub> = 3.3V  
+12V = 12V  
3V<sub>DUAL</sub> = 3.3V  
V<sub>CC</sub> = 5V

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GAD0-31  
ADSTB0, ADSTB0-  
ADSTB1, ADSTB1-  
SBA0-7  
SBSTB, SBSTB-  
GCBE0-3-  
ST0-2

MAA0-14  
MAA\_CPC1-5  
MAB\_CPC1-5  
MDD0-63  
-DQSD0-7  
DM0-7

AGPUSB+ / -

HL0-10  
CONTROL BUS  HUB LINK

**ICH6**

V<sub>CC25</sub> = 2.5V (IO, MEMORY, VLINK)  
3V<sub>DUAL</sub> = 3.3V (SUSPEND POWER)  
V<sub>CC3</sub> = 3.3V  
RTVDD = 3.3V

PAGE 19, 20, 21

**IDE Primary**

V<sub>CC</sub> = 5V

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**SERIAL ATA**

V<sub>CC</sub> = 5V

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**USB PORTS 0~7**

V<sub>CC</sub> = 5V  
5V<sub>SB</sub> = 5V  
5V<sub>USB</sub> = 5V

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AMRUSB+ / -

AZALIA LINK

**PCI BUS**

**PCI SLOT 1 PCI EXPRESS BY 1, 2 SLOT**

+12 = 12V  
+12 = 12V  
V<sub>CC</sub> = 5V  
V<sub>CC3</sub> = 3V  
3V<sub>DUAL</sub> = 3V

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**FWH/HWMO**

V<sub>CC</sub> = 5V  
V<sub>CC3</sub> = 3V

PAGE 26

**LAN RTL8110S/8100C**

PAGE 32

LPC BUS

**LPC ITE8712HX**

V<sub>CC</sub> = 5V  
5V<sub>SB</sub> = 5V  
VBAT = 3V

PAGE 25

**1394 IT TSB43AB23**

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**FRONT PANEL/BZ**

V<sub>CC</sub> = 5V  
5V<sub>SB</sub> = 5V  
+12 = 12V  
PV<sub>CC</sub> = 5V

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**I/O PORTS :**

COMA COMB LPT PS2 IR FDD

PAGE 27, 28, 29

**AC97/Azalia ALC880**

+12V = 12V  
V<sub>CC3</sub> = 3.3V  
V<sub>CC</sub> = 5V  
AVDD = 5V

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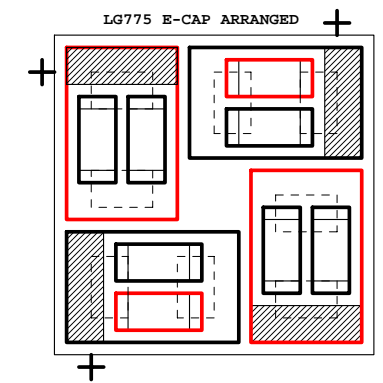
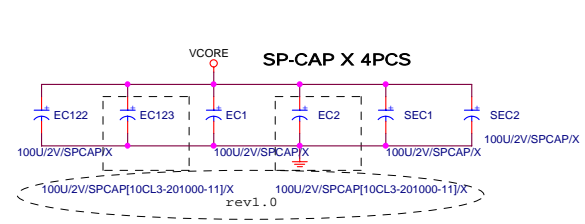
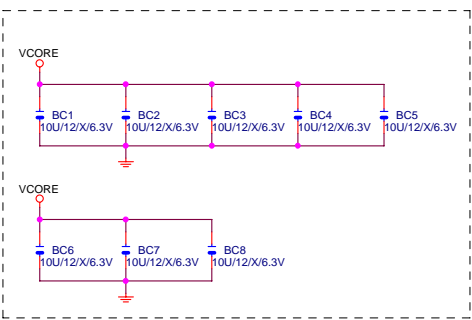
**AUDIO PORTS : FRONT AUDIO**

LIN\_OUT LINE\_IN MIC  
TELE CD\_IN AUX\_IN

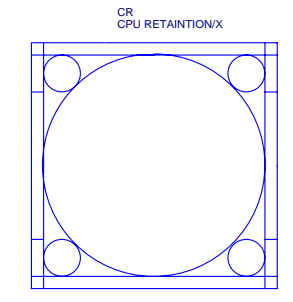
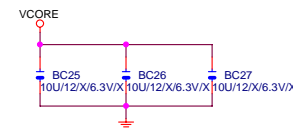
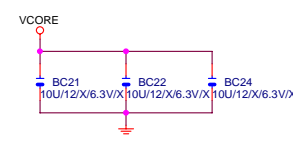
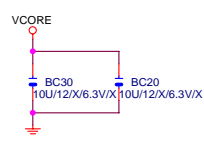
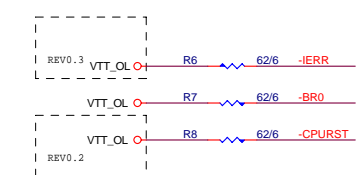
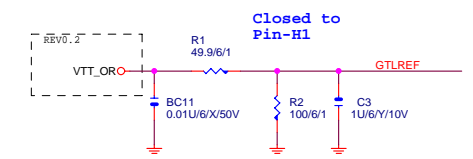
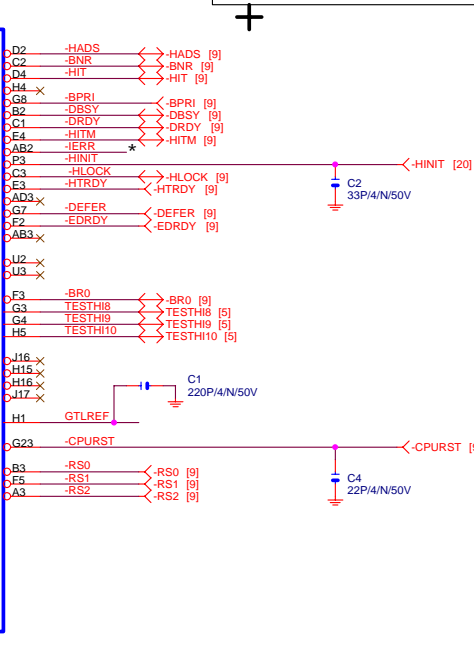
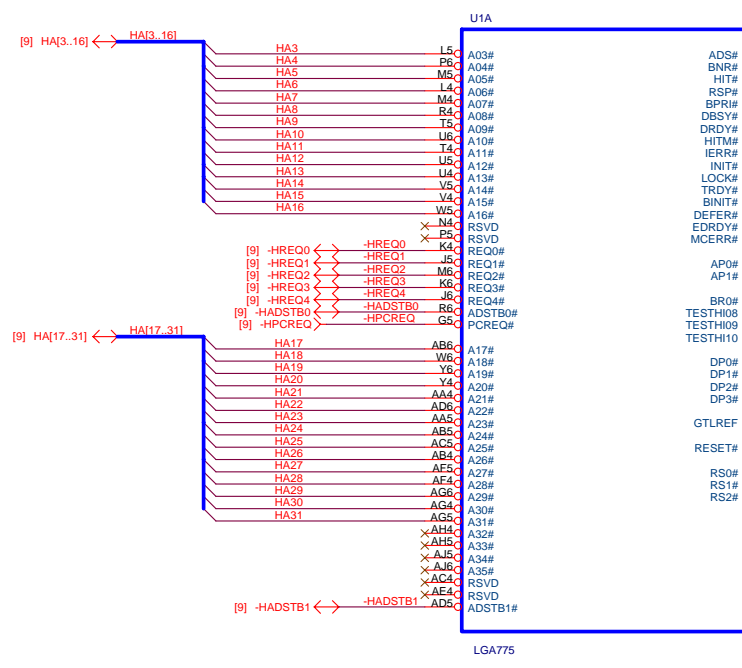
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<b>GIGABYTE</b>			
BOM & PCB MODIFY HISTORY			
File			
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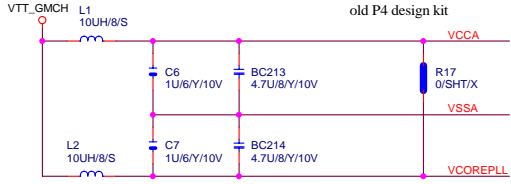


BOM上SP-CPx2+10uFxx2,外部电容不留



<b>GIGABYTE</b>			
Title <b>P4_LGA775-A</b>			
Size Custom	Document Number <b>81915G-MF</b>	Rev <b>1.0</b>	
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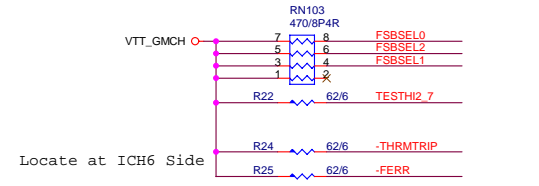
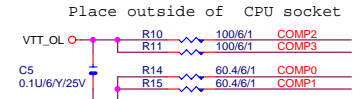
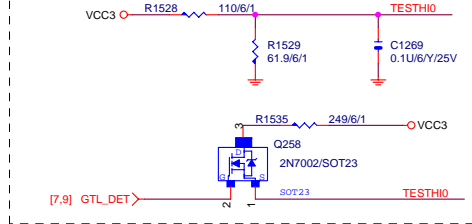
**Note:**  
VCCA & VCOREPLL  
define doesn't same as  
old P4 design kit



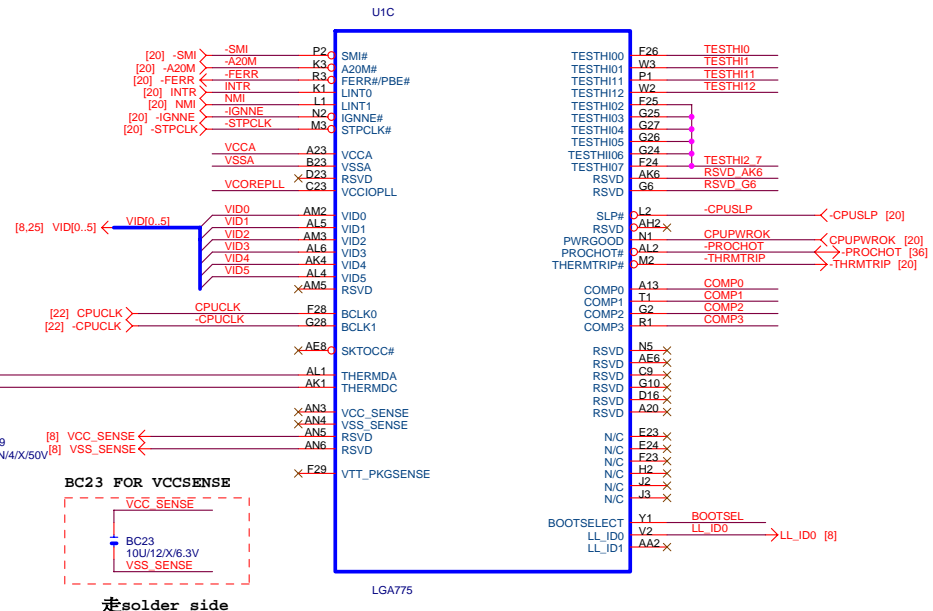
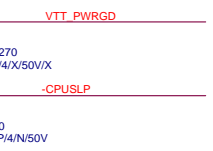
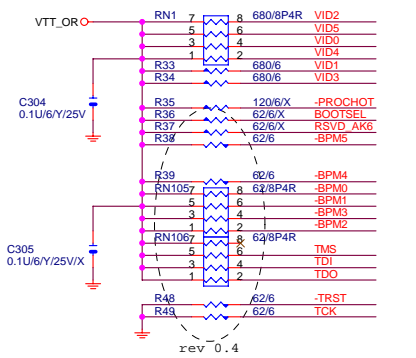
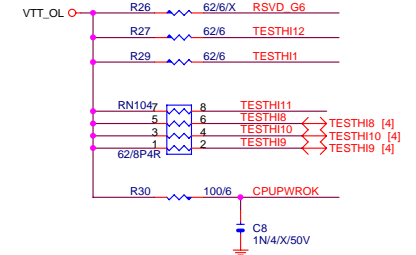
Trace width doesn't  
less than 12 Mil

As close as possible to  
CPU socket

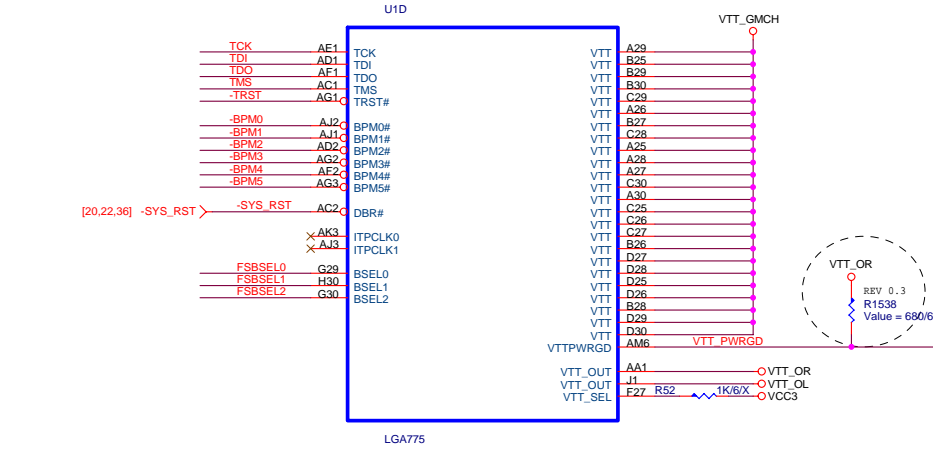
REV0.2



Locate at ICH6 Side



走solder side



**GIGABYTE**

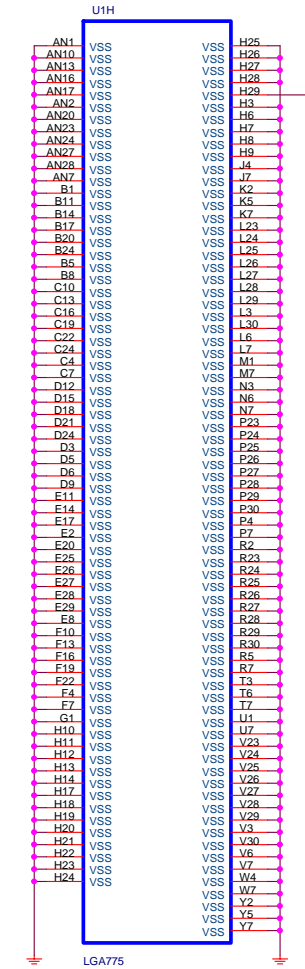
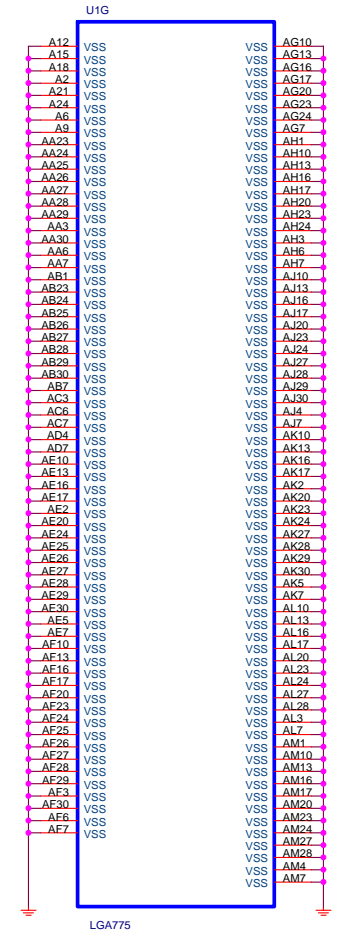
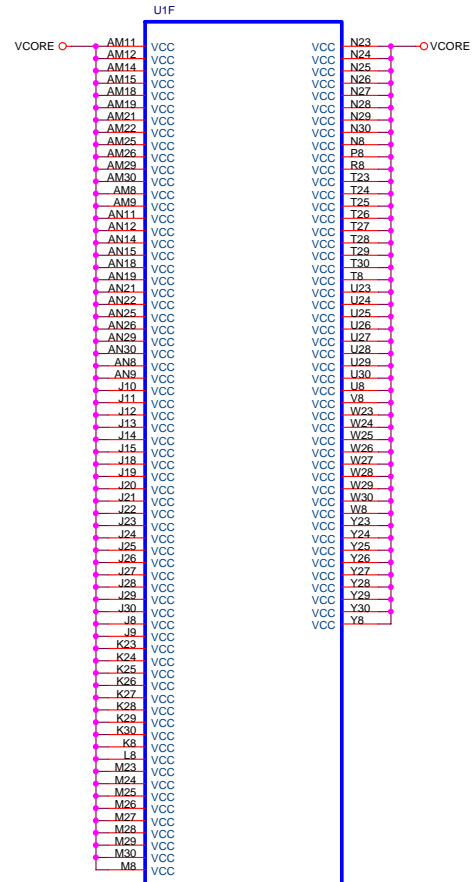
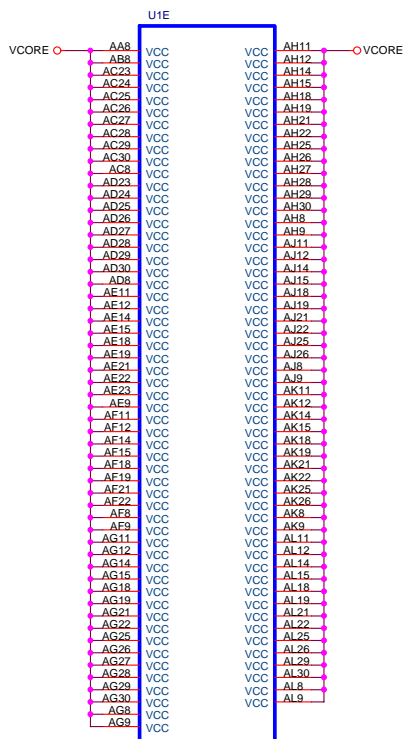
**P4\_LGA775-B**

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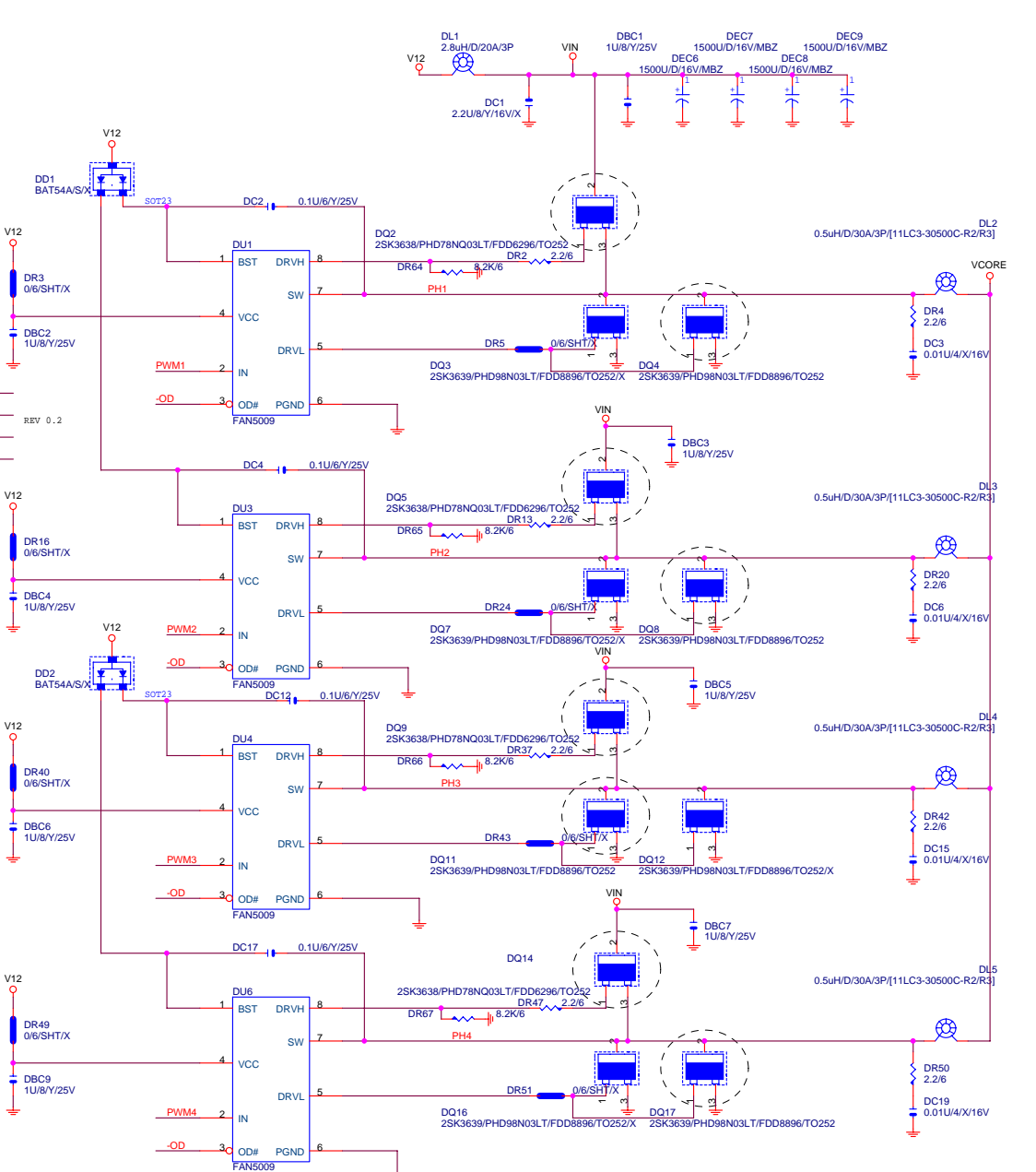
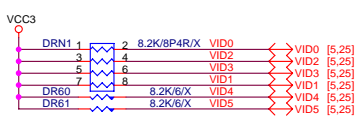
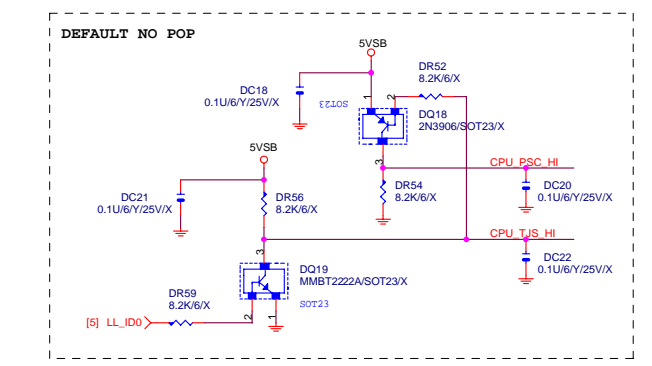
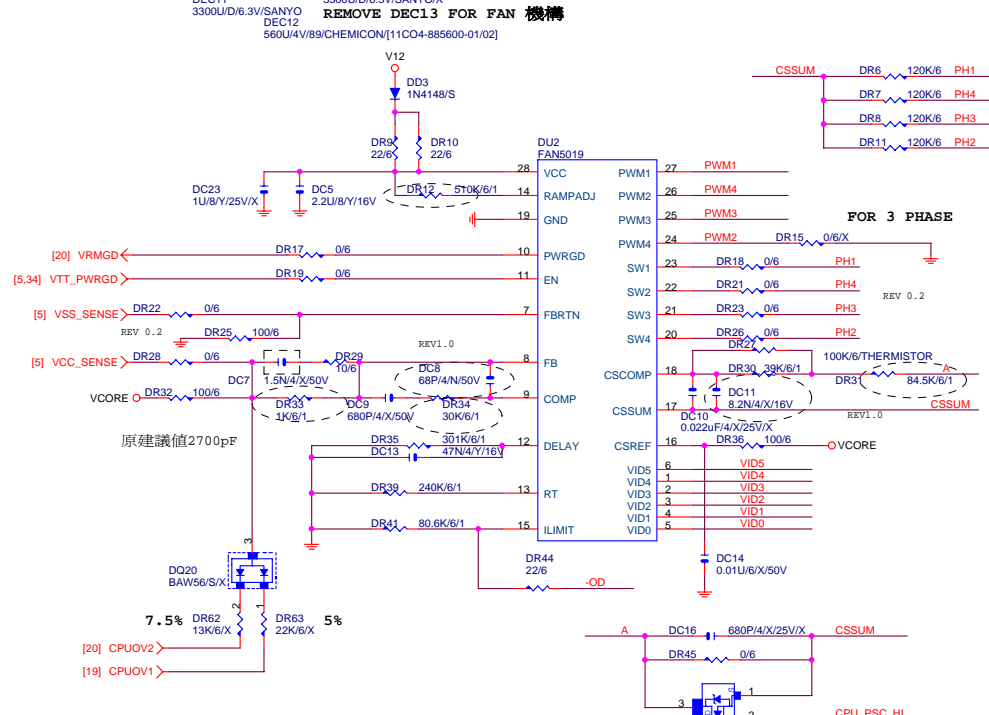
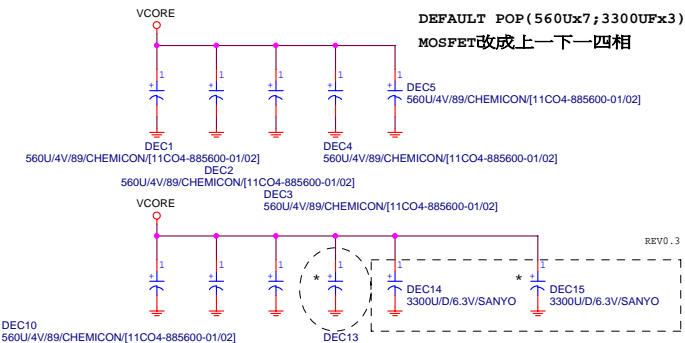
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Title		
P4_LGA775-C		
Size	Document Number	Rev
Custom	<b>8I915G-MF</b>	<b>1.0</b>
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REV0.2  
[5.9] GTL\_DET ←



DEFAULT POP (560Ux7; 3300UFx3)

MOSFET改成上一下四相

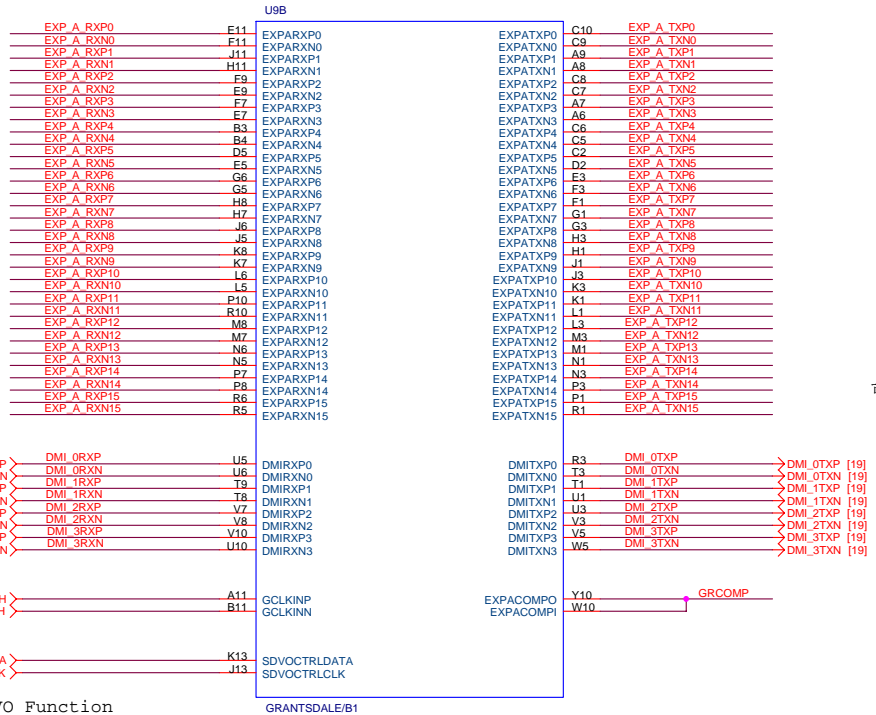


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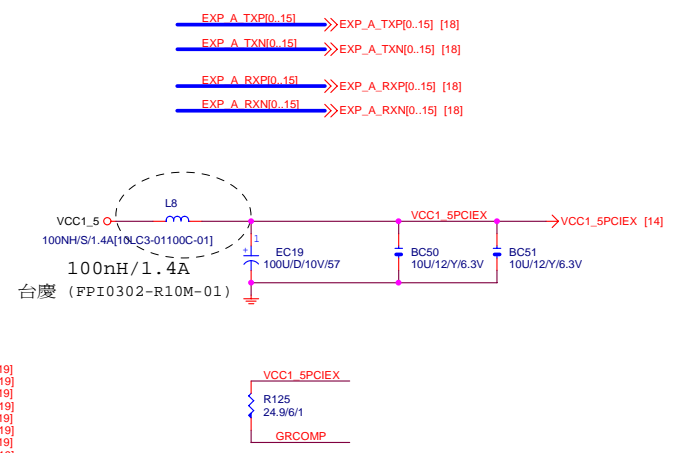
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Title <b>VRD 10.1</b>		
Size Custom	Document Number <b>81915G-MF</b>	Rev <b>1.0</b>
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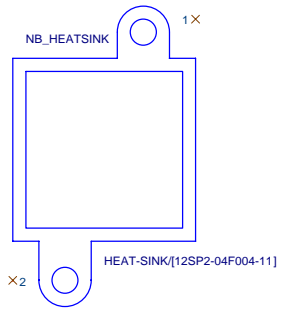
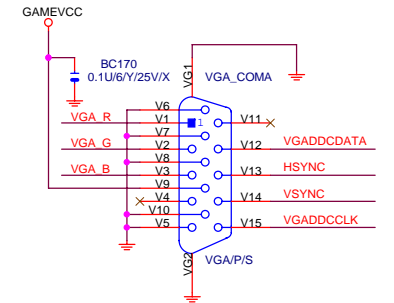
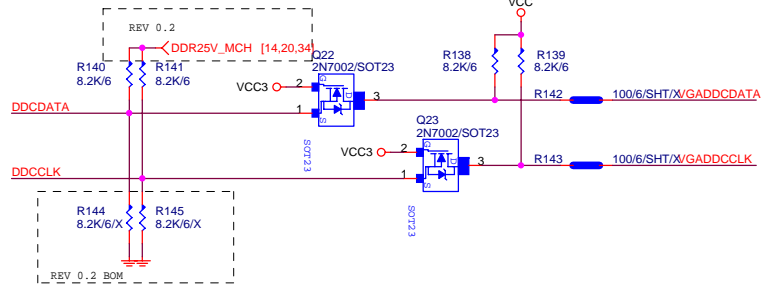
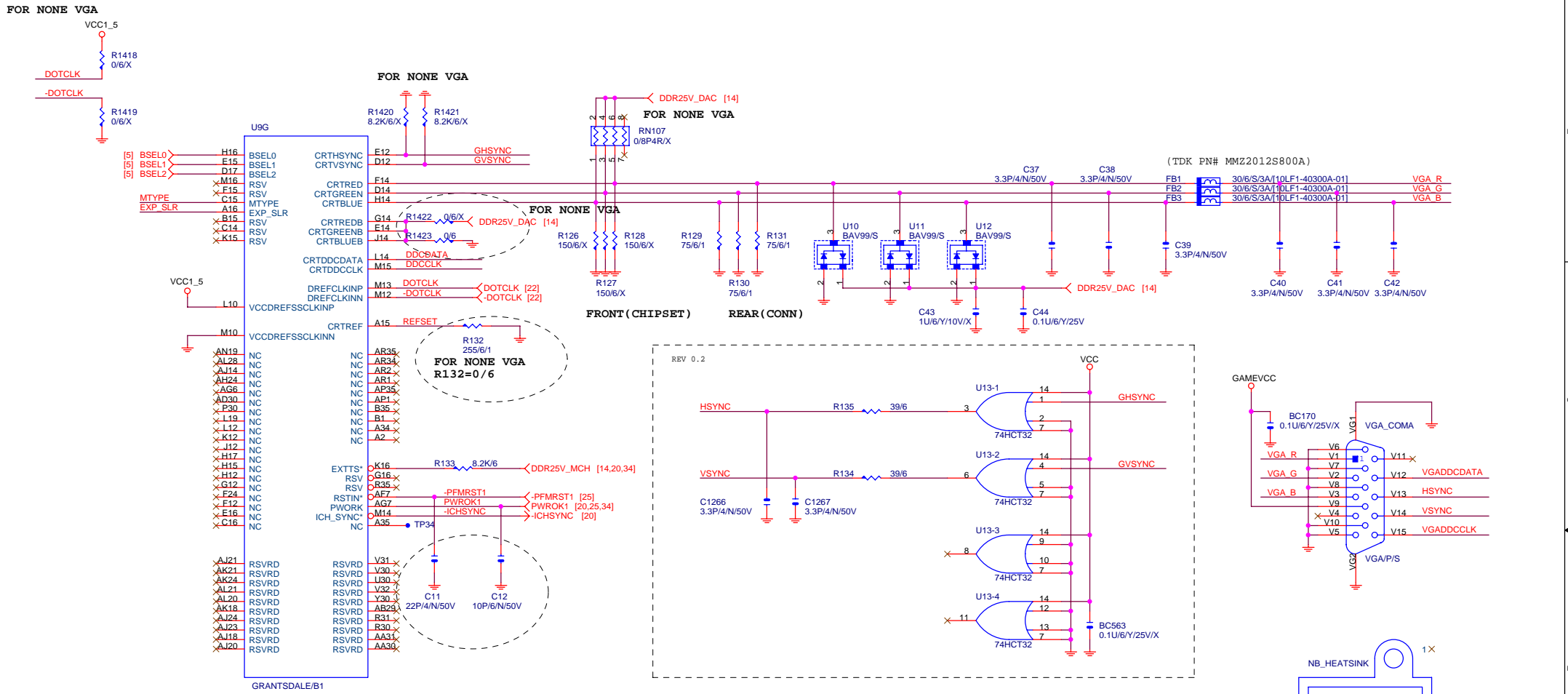




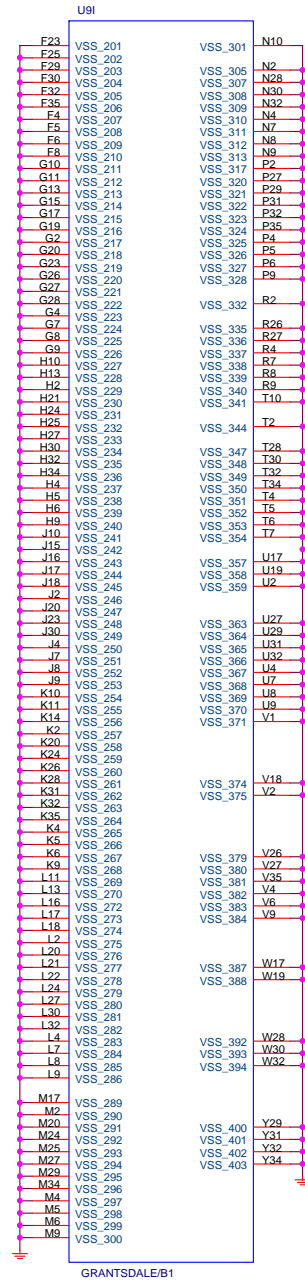
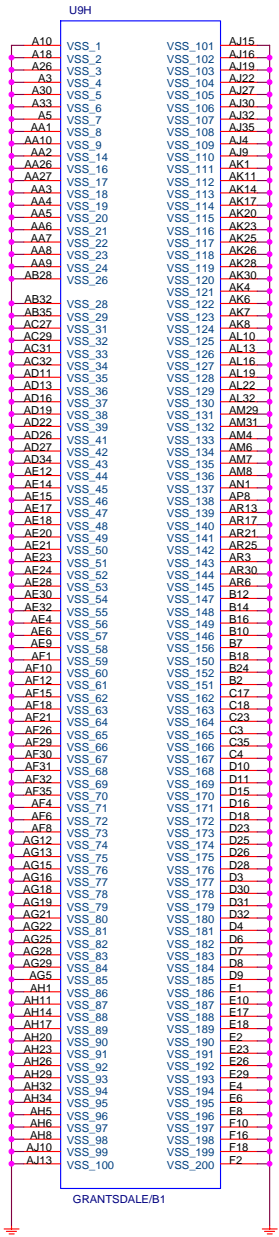
For DVO Function



<b>GIGABYTE</b>		
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Size	Document Number	Rev
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<b>GIGABYTE</b>		
Title	<b>GMCH-INTERNAL VGA</b>	
Size	Document Number	<b>8I915G-MF</b>
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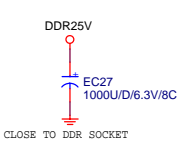
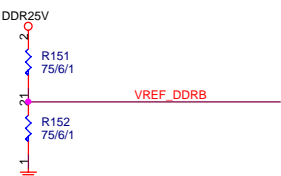
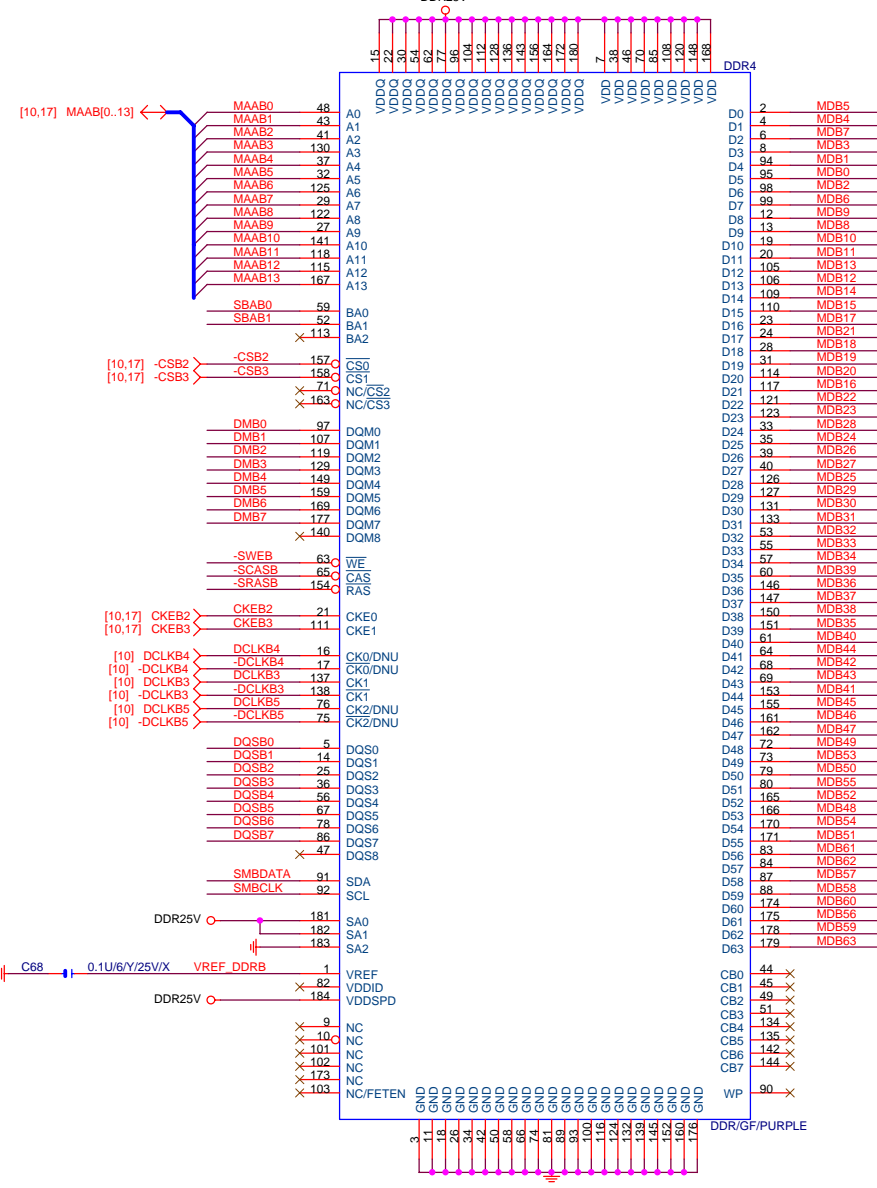
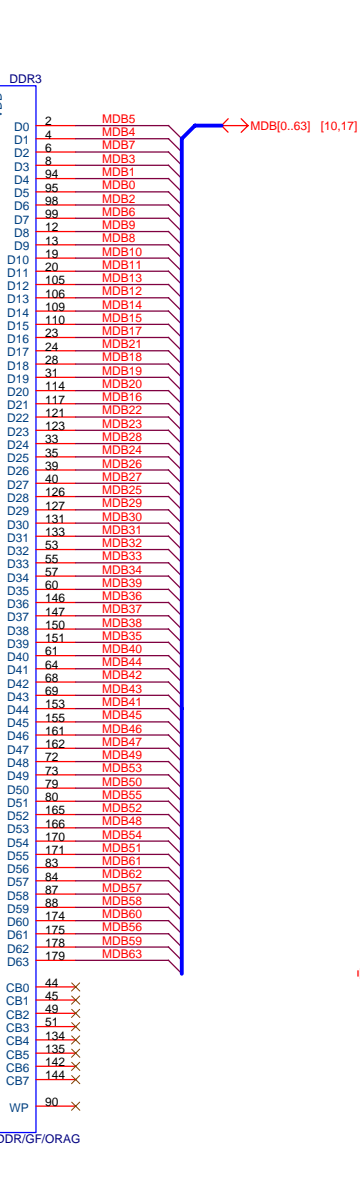
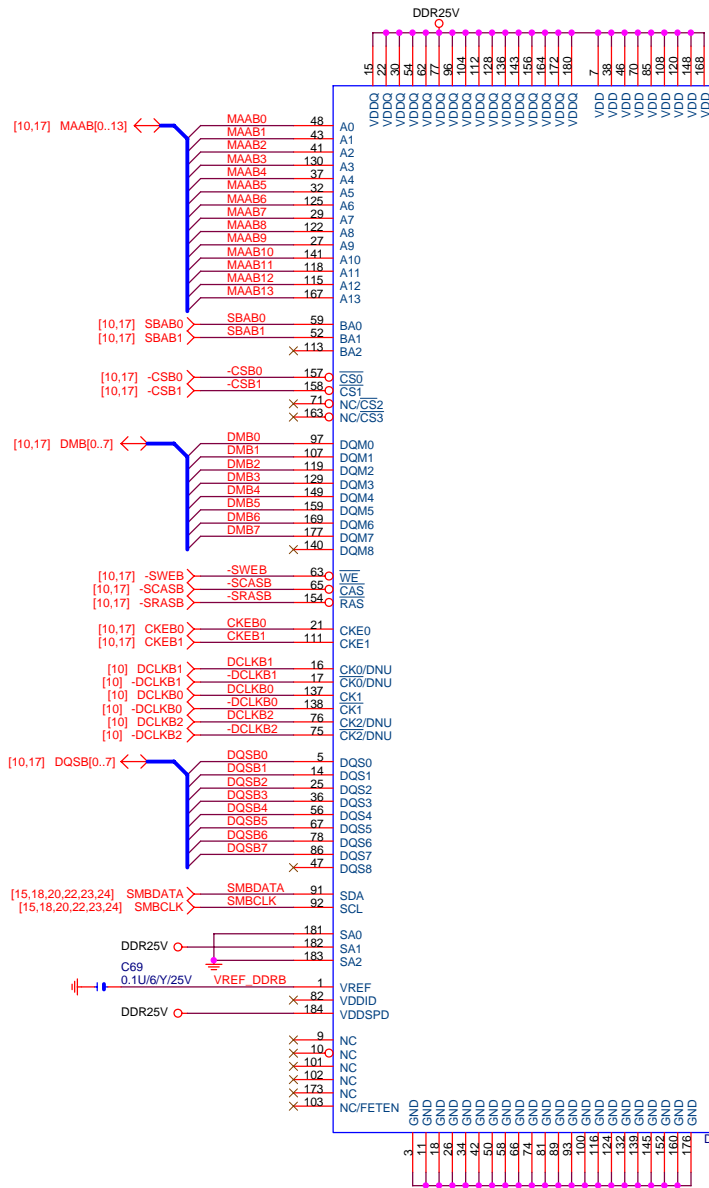


**GIGABYTE**

Title			GMCH-GND		
Size	Document Number	81915G-MF			Rev
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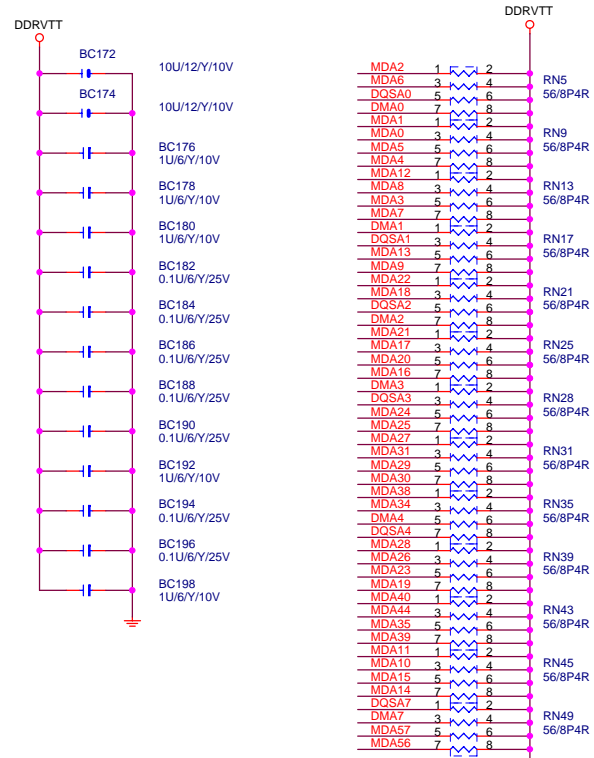




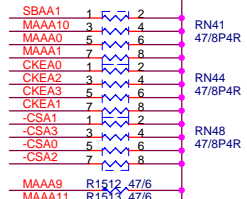
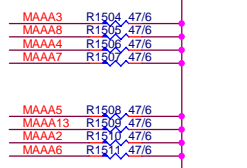
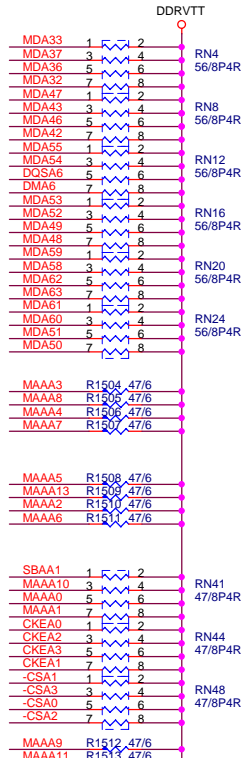
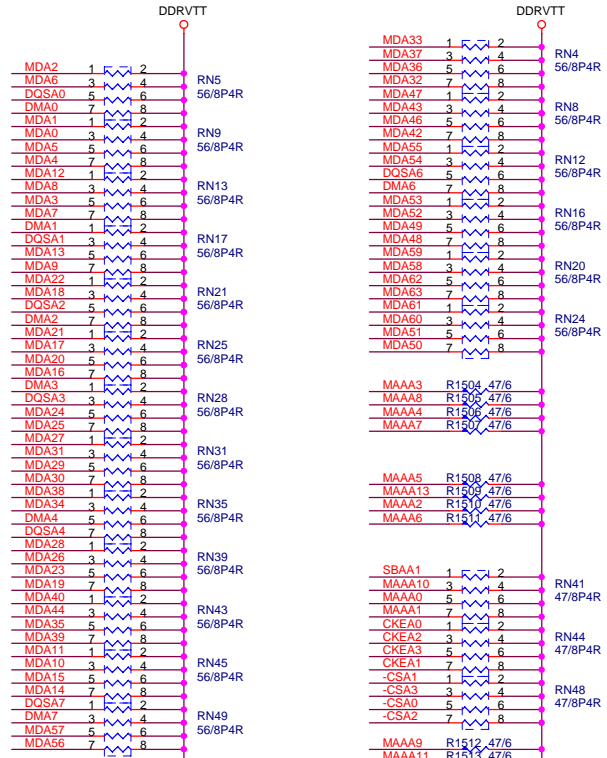


<b>GIGABYTE</b>		
<b>DDR CHANNEL B</b>		
Size B	Document Number	Rev 1.0
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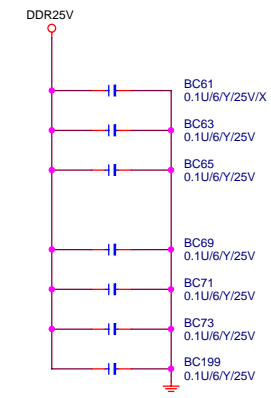
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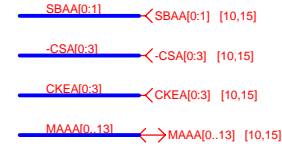
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CHANNEL A



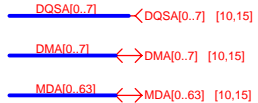
DDR25V Decouple



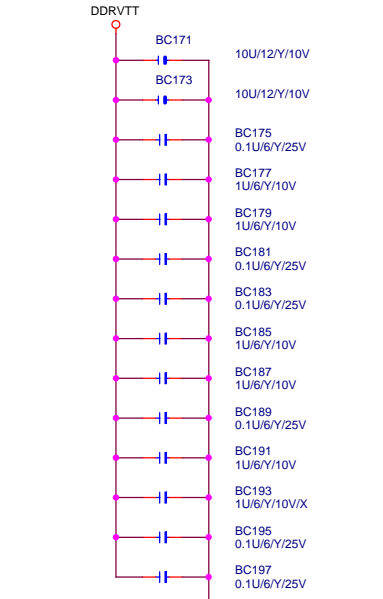
47 Ohms



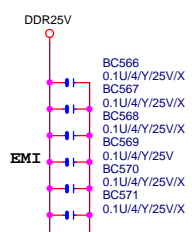
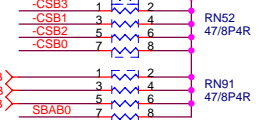
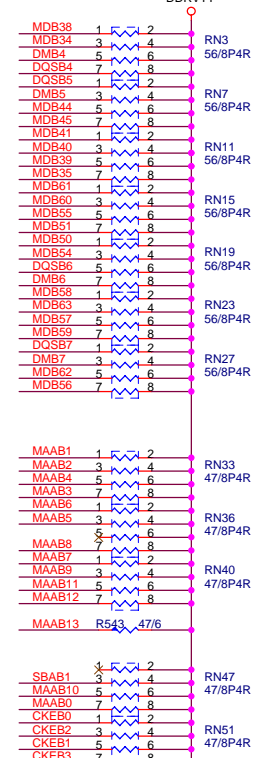
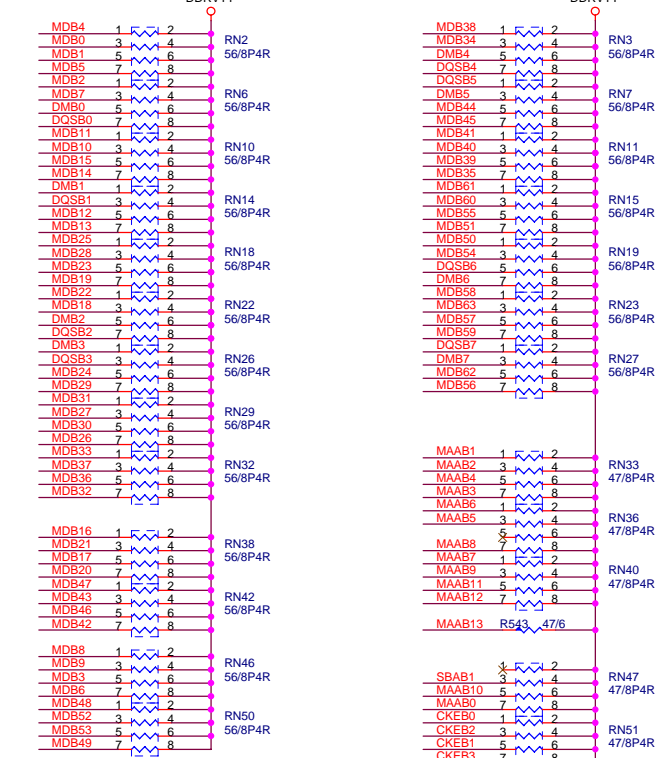
56 Ohms



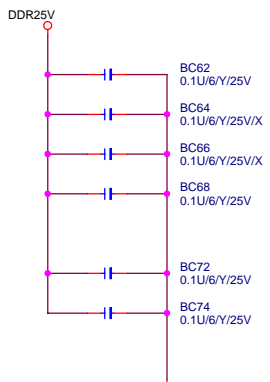
DDRVTT Decouple



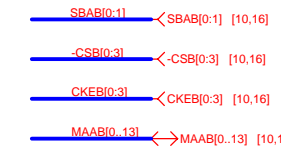
DDRVTT Decouple



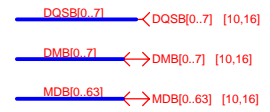
DDR25V Decouple



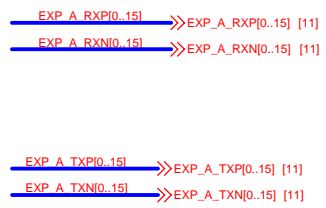
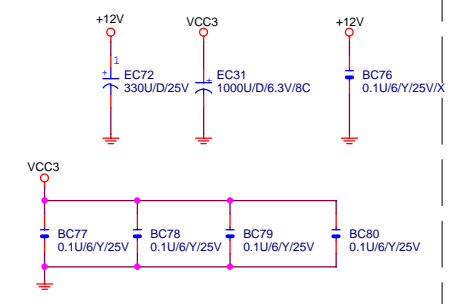
47 Ohms



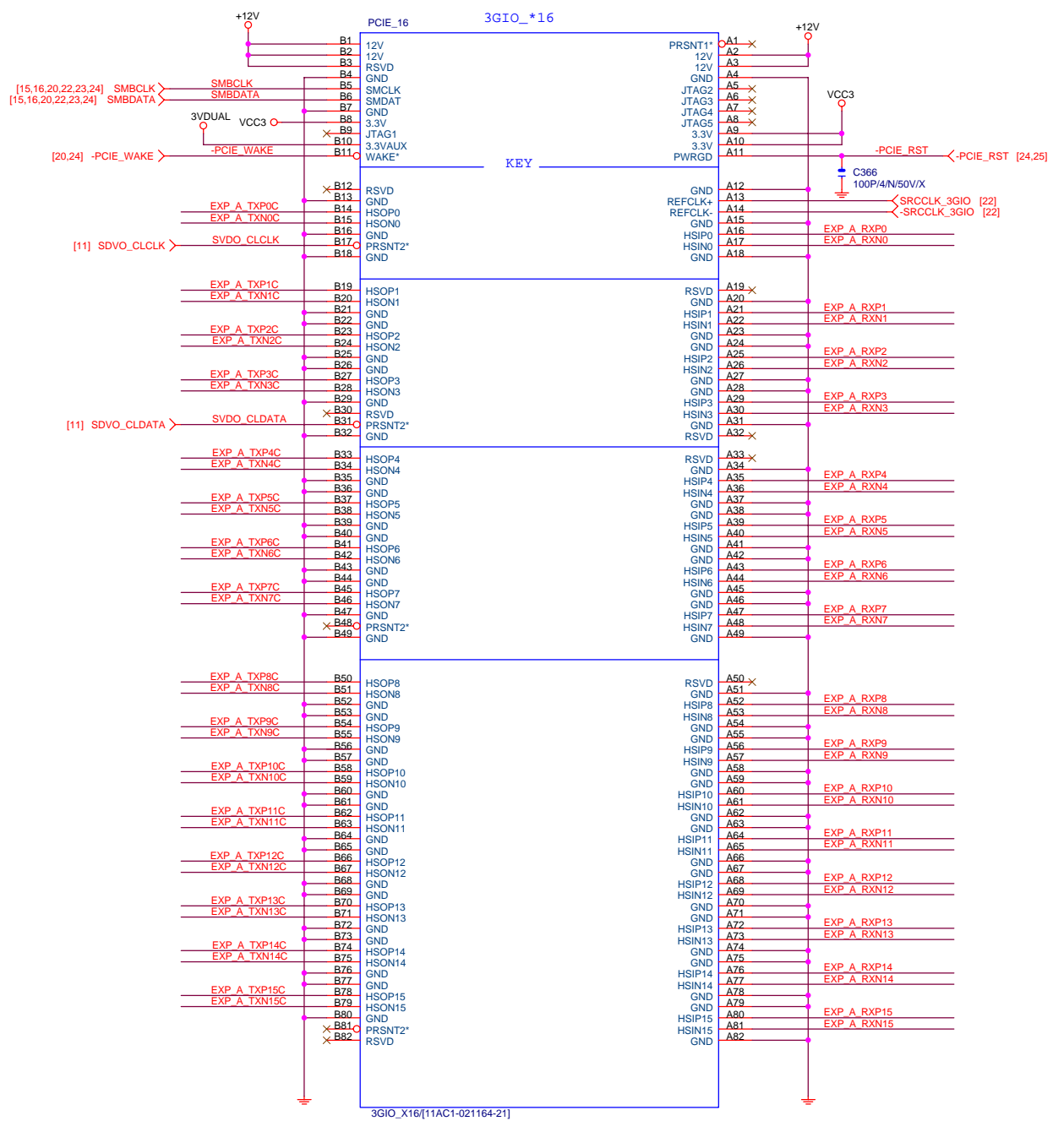
56 Ohms



Title			<b>GIGABYTE</b>		
Document Number			<b>DDR TERMINATOR</b>		
Size B			<b>8I915G-MF</b>		
Date:	星期三, 六月 09, 2004	Sheet	17	of	37
			Rev <b>1.0</b>		

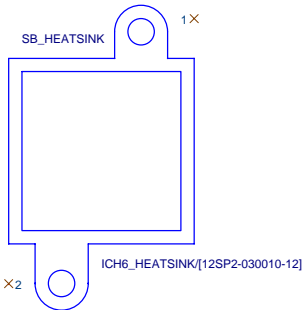
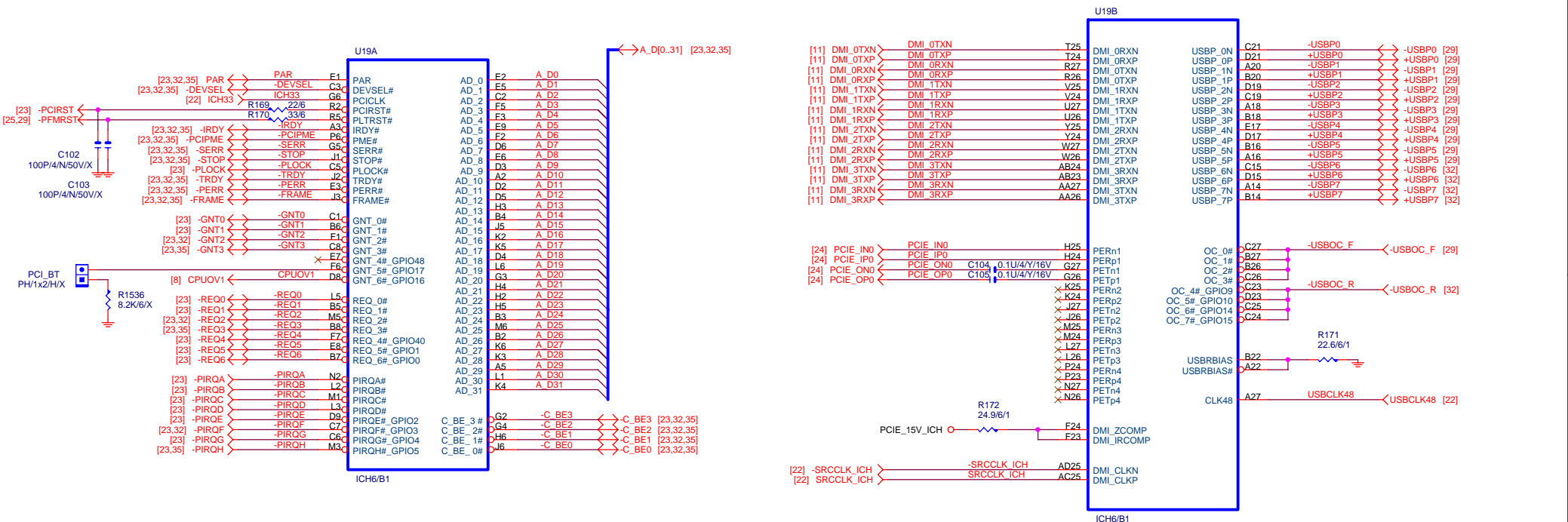


EXP_A_TXP0	C70	0.1U/6Y/25V	EXP_A_TXP0C
EXP_A_TXN0	C71	0.1U/6Y/25V	EXP_A_TXN0C
EXP_A_TXP1	C72	0.1U/6Y/25V	EXP_A_TXP1C
EXP_A_TXN1	C73	0.1U/6Y/25V	EXP_A_TXN1C
EXP_A_TXP2	C74	0.1U/6Y/25V	EXP_A_TXP2C
EXP_A_TXN2	C75	0.1U/6Y/25V	EXP_A_TXN2C
EXP_A_TXP3	C76	0.1U/6Y/25V	EXP_A_TXP3C
EXP_A_TXN3	C77	0.1U/6Y/25V	EXP_A_TXN3C
EXP_A_TXP4	C78	0.1U/6Y/25V	EXP_A_TXP4C
EXP_A_TXN4	C79	0.1U/6Y/25V	EXP_A_TXN4C
EXP_A_TXP5	C80	0.1U/6Y/25V	EXP_A_TXP5C
EXP_A_TXN5	C81	0.1U/6Y/25V	EXP_A_TXN5C
EXP_A_TXP6	C82	0.1U/6Y/25V	EXP_A_TXP6C
EXP_A_TXN6	C83	0.1U/6Y/25V	EXP_A_TXN6C
EXP_A_TXP7	C84	0.1U/6Y/25V	EXP_A_TXP7C
EXP_A_TXN7	C85	0.1U/6Y/25V	EXP_A_TXN7C
EXP_A_TXP8	C86	0.1U/6Y/25V	EXP_A_TXP8C
EXP_A_TXN8	C87	0.1U/6Y/25V	EXP_A_TXN8C
EXP_A_TXP9	C88	0.1U/6Y/25V	EXP_A_TXP9C
EXP_A_TXN9	C89	0.1U/6Y/25V	EXP_A_TXN9C
EXP_A_TXP10	C90	0.1U/6Y/25V	EXP_A_TXP10C
EXP_A_TXN10	C91	0.1U/6Y/25V	EXP_A_TXN10C
EXP_A_TXP11	C92	0.1U/6Y/25V	EXP_A_TXP11C
EXP_A_TXN11	C93	0.1U/6Y/25V	EXP_A_TXN11C
EXP_A_TXP12	C94	0.1U/6Y/25V	EXP_A_TXP12C
EXP_A_TXN12	C95	0.1U/6Y/25V	EXP_A_TXN12C
EXP_A_TXP13	C96	0.1U/6Y/25V	EXP_A_TXP13C
EXP_A_TXN13	C97	0.1U/6Y/25V	EXP_A_TXN13C
EXP_A_TXP14	C98	0.1U/6Y/25V	EXP_A_TXP14C
EXP_A_TXN14	C99	0.1U/6Y/25V	EXP_A_TXN14C
EXP_A_TXP15	C100	0.1U/6Y/25V	EXP_A_TXP15C
EXP_A_TXN15	C101	0.1U/6Y/25V	EXP_A_TXN15C

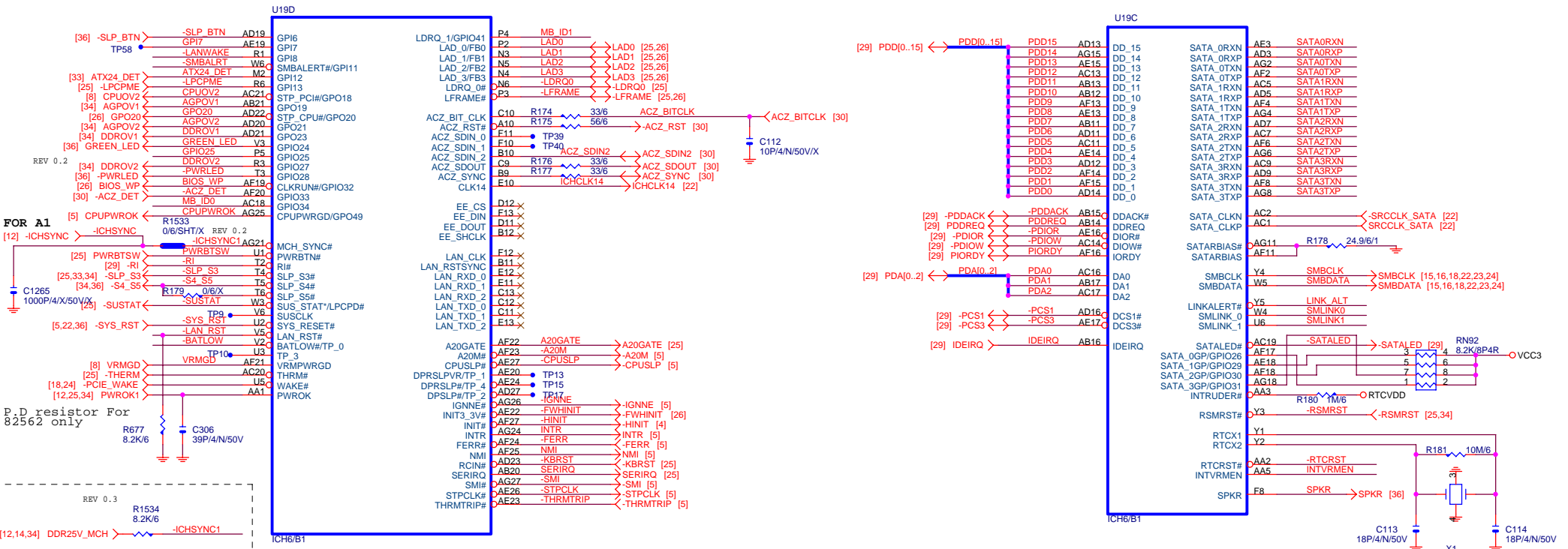


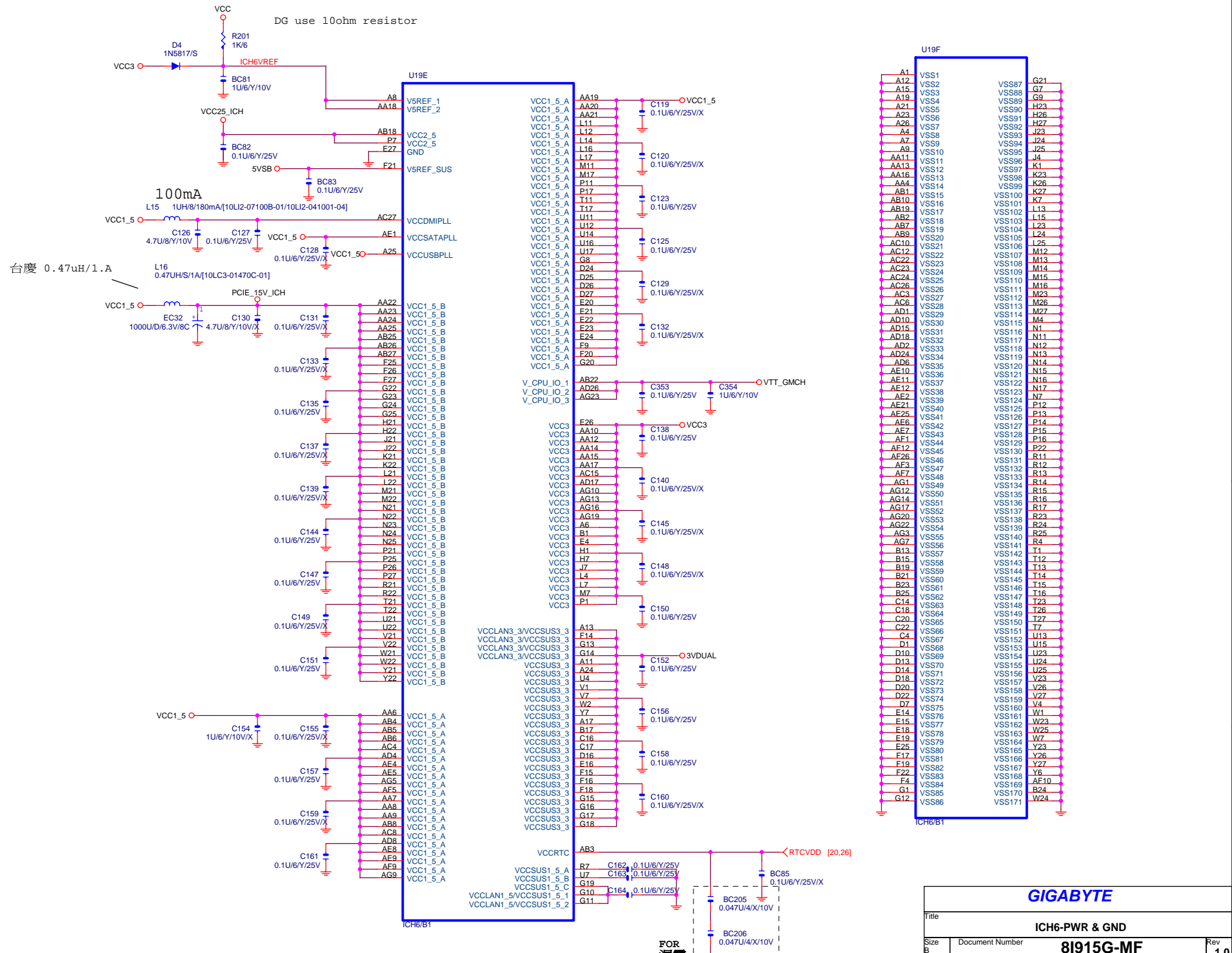
<b>GIGABYTE</b>		
PCI EXPRESS * 16		
Title	Document Number	Rev
	<b>8I915G-MF</b>	<b>1.0</b>
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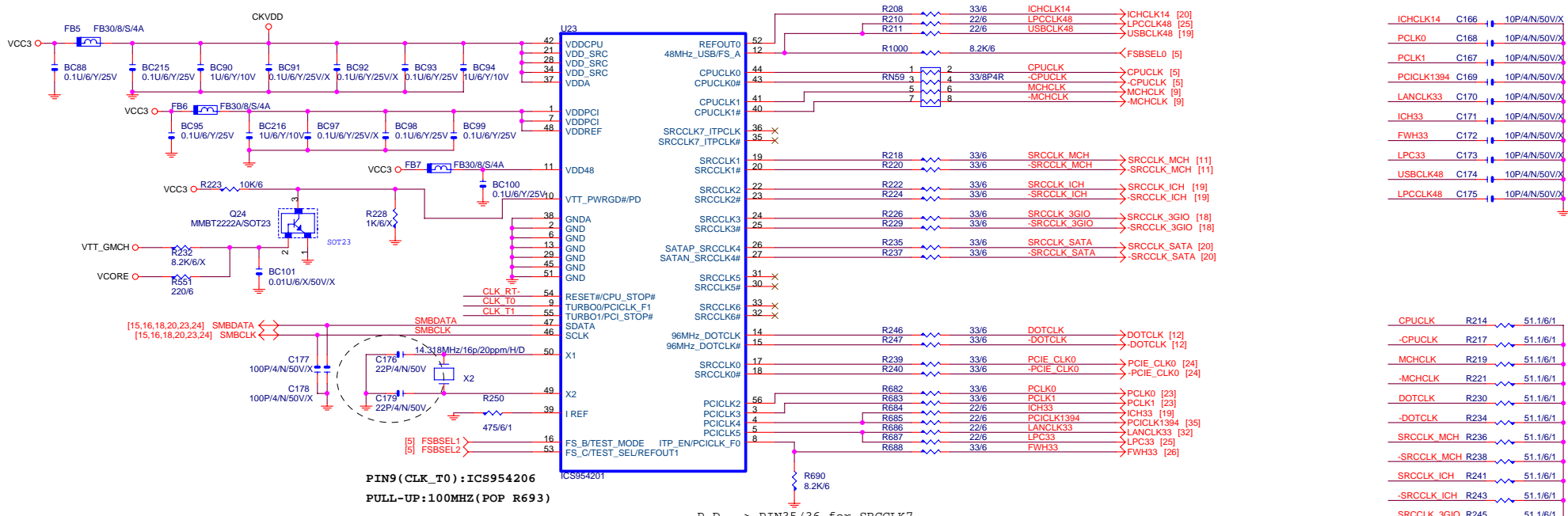
DMI Connection Note  
 GMCH TX Pin Need Connect to ICH6 RX Pin  
 ICH6 TX Pin Need Connect to GMCH RX Pin



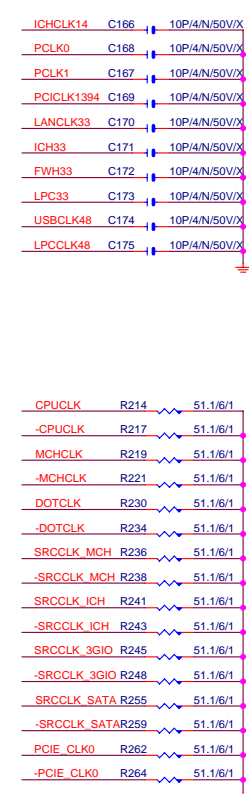
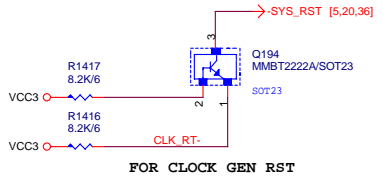
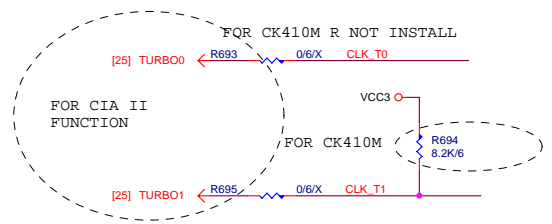
<b>GIGABYTE</b>		
Title		
<b>ICH6-PCI, DMI, LAN, USB</b>		
Size	Document Number	Rev
B	<b>81915G-MF</b>	<b>1.0</b>
Date:	星期三, 六月 09, 2004	Sheet 19 of 37





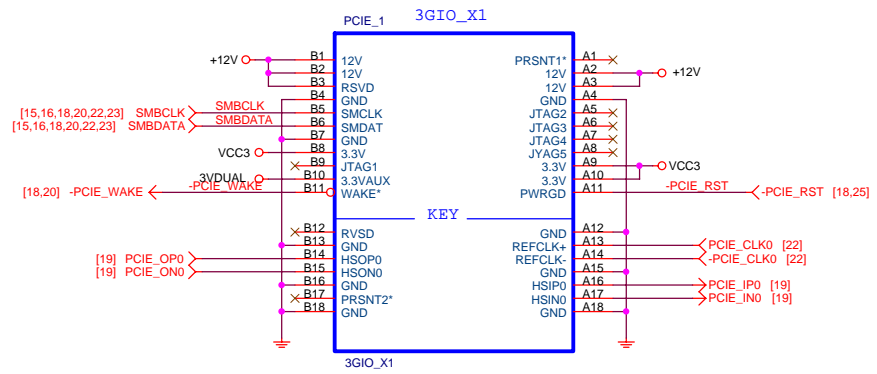


**PIN9 (CLK\_T0) : ICS954206**  
**PULL-UP: 100MHZ (POP R693)**



<b>GIGABYTE</b>		
Title <b>CK410M/GBT_CLK_GEN</b>		
Size Custom	Document Number <b>81915G-MF</b>	Rev <b>1.0</b>
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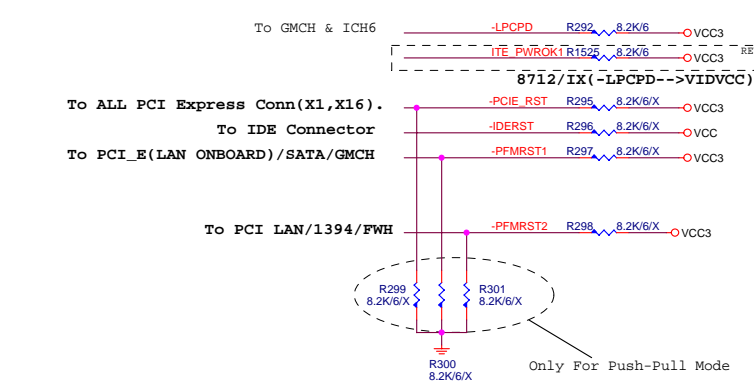


<b>GIGABYTE</b>		
Title		
<b>PCI E SLOT 1</b>		
Size	Document Number	Rev
B	<b>8I915G-MF</b>	<b>1.0</b>
Date:	星期三, 六月 09, 2004	Sheet 24 of 37

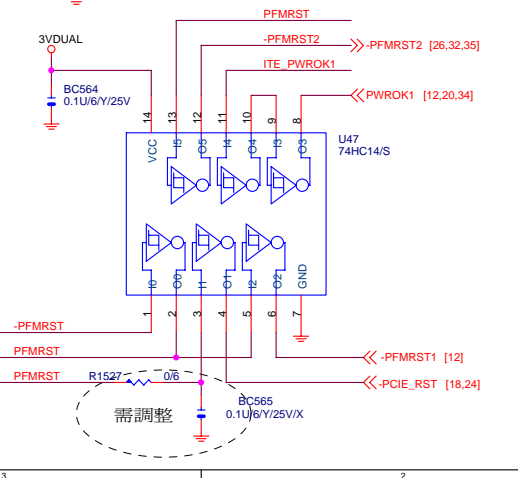
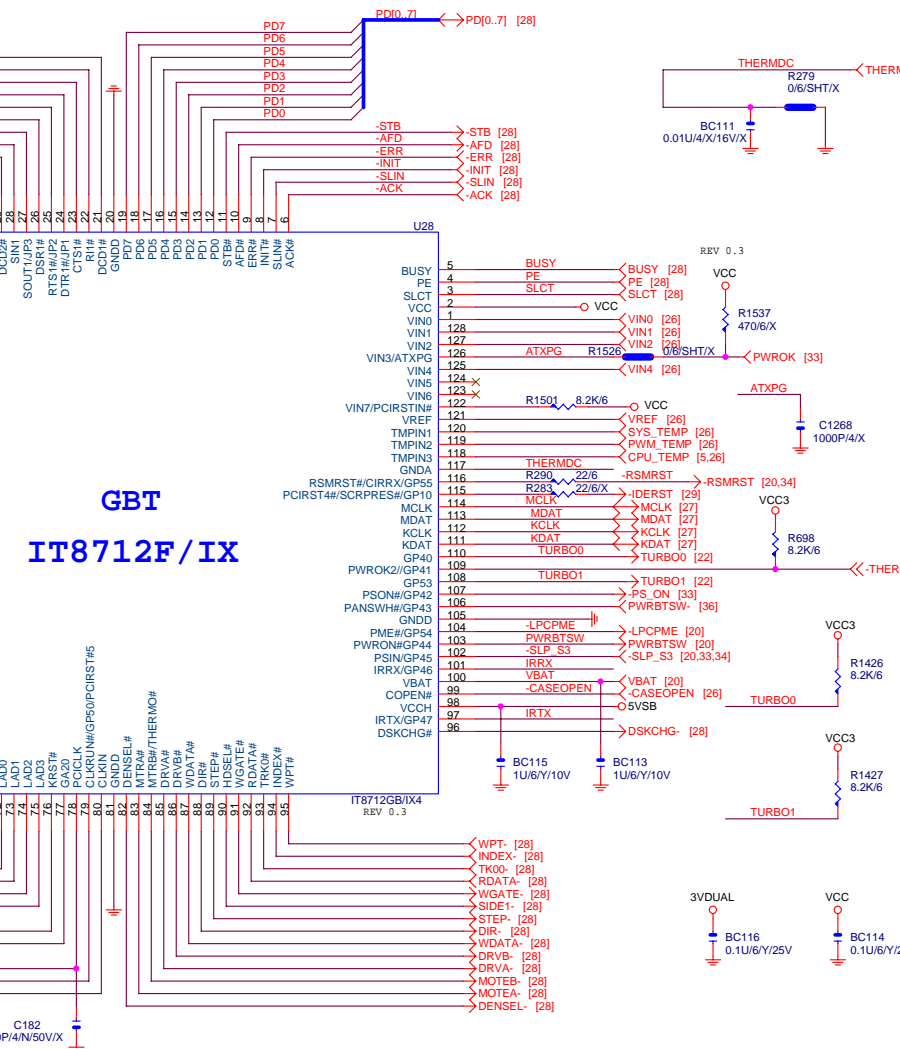
IT8712F/IX + Grantsdale schematic  
 使用VID0-5 注意事項,也就是說SOUT(680 Ohm pull down)選擇 VID threshold 0.8/0.4

8712/IX(-LPCPD-->VIDVCC)-->FOR VID OUTPUT LEVEL USED(BUT VID OUTPUT會衝突CPU/PWM,目前不建議使用OUT,PULL-UP VCC3)

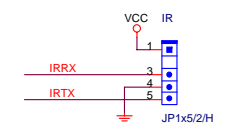
-PFMRST--> ICH6 TO IT8712/-IDERST INPUT  
 PWROK1--> GMCH/ICH6/5VDUAL S.W



Have P.U at ICH6 side  
 To GMCH & ICH6  
 To ALL PCI Express Conn(X1,X16).  
 To IDE Connector  
 To PCI\_E(LAN ONBOARD)/SATA/GMCH  
 To PCI LAN/1394/FWH



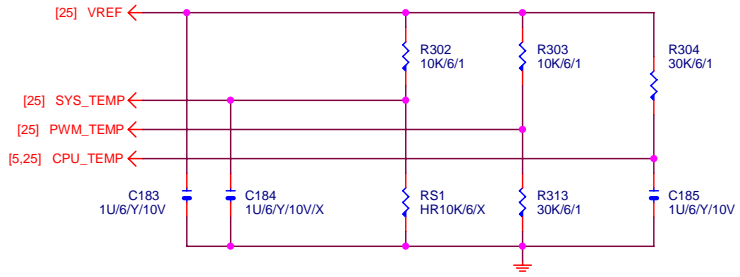
**IR CONNECTOR**



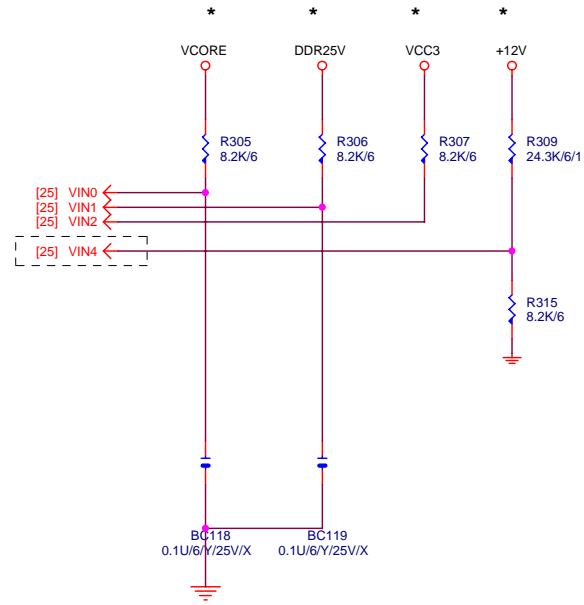
**GIGABYTE**

Title			LPC I/O
Document Number			81915G-MF
Size	Custom	Rev	1.0
Date:	日期: 六月 09, 2004	Sheet	25 of 37

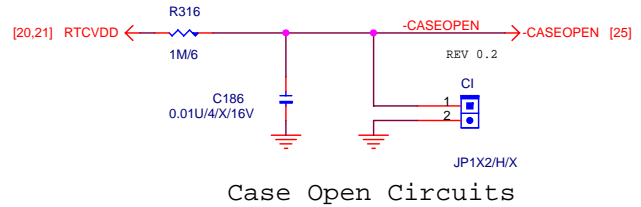
### TEMP. SENSE



### VOLTAGE SENSE

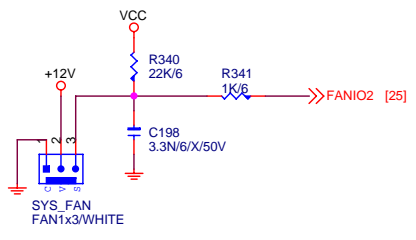


### CASE OPEN (N/A)

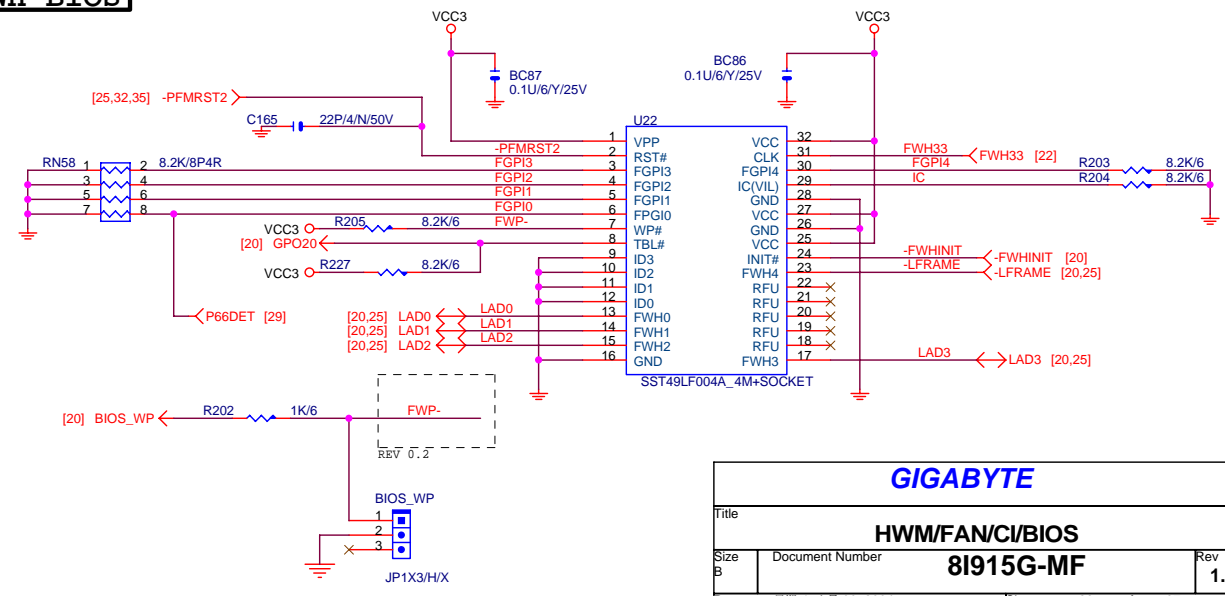


Case Open Circuits

### SYS FAN



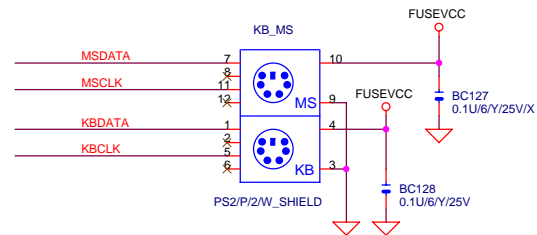
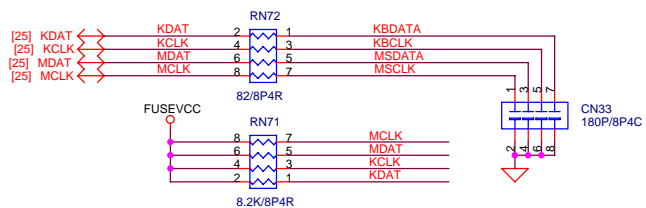
### FWH BIOS



**GIGABYTE**

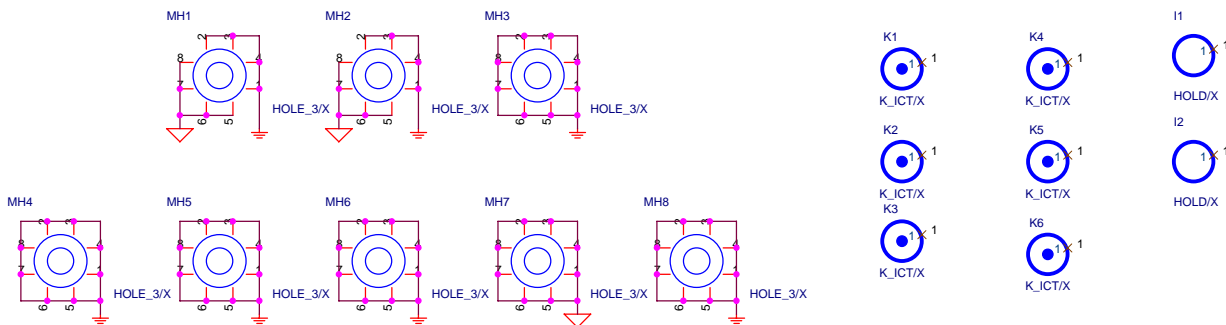
Title		
<b>HWM/FAN/CI/BIOS</b>		
Size	Document Number	Rev
B	<b>8I915G-MF</b>	<b>1.0</b>
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**KB/MS**



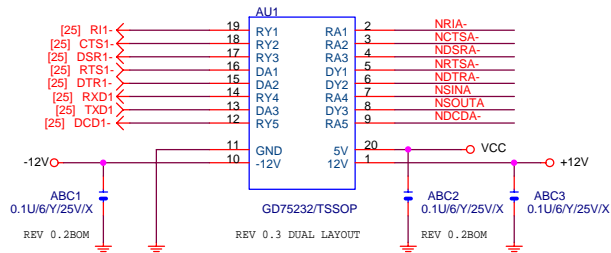
**GAME PORT (N/A)**

rev 0.4

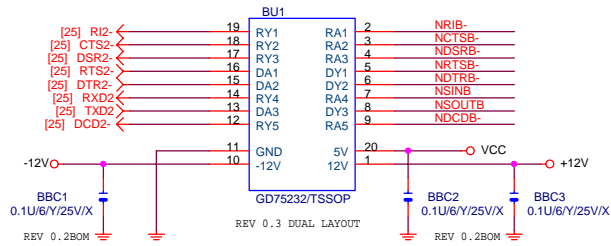


<b>GIGABYTE</b>		
Title		
<b>PS/2 KB &amp; MS</b>		
Size	Document Number	Rev
B	<b>8I915G-MF</b>	<b>1.0</b>
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### COMA / COMB

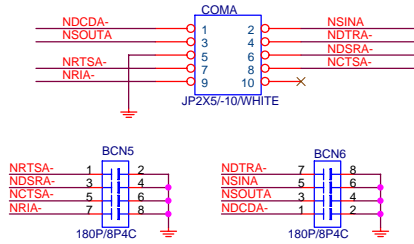


Foot Print Change



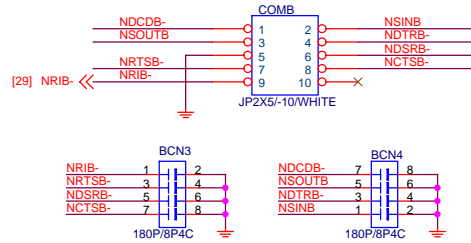
### INTERNAL COMA

rev 0.4



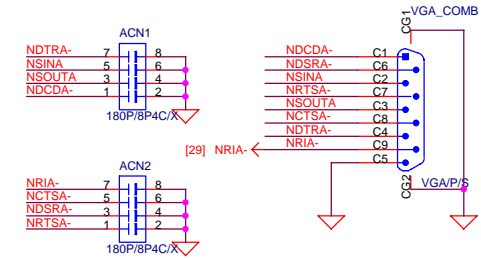
### INTERNAL COMB

rev 0.4



### REAR COMA

rev 0.4



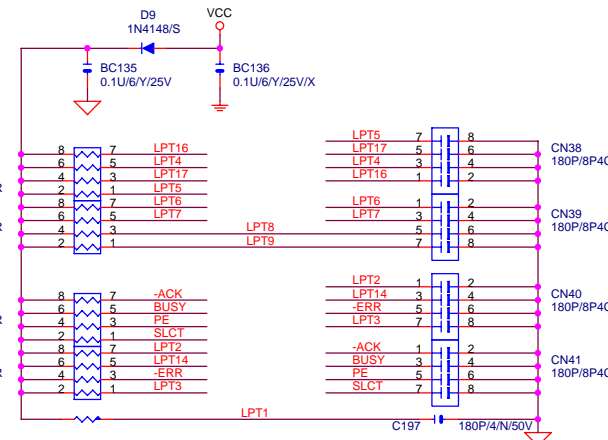
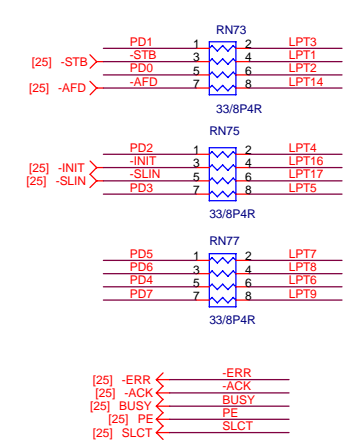
PLACE NEAR VGA\_COM CONNECTOR

凡ATX系列 (含 NXP, PRO, Ultra, 戰鬥版, both 915P or 915G...) 只support 一個COM port.

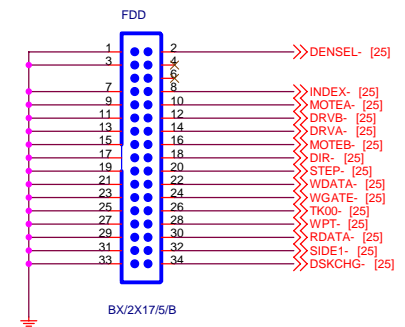
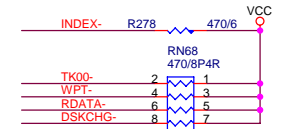
凡uATX系列 (含915P or 915G) 需support 兩個COM port

### LPT

[25] PD[0..7] → PDI0..7



### FLOPPY



**GIGABYTE**

**COM, LPT, FDD**

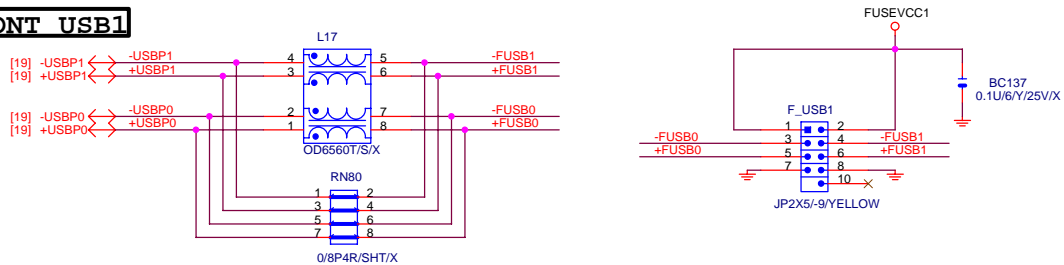
**81915G-MF**

Title: \_\_\_\_\_

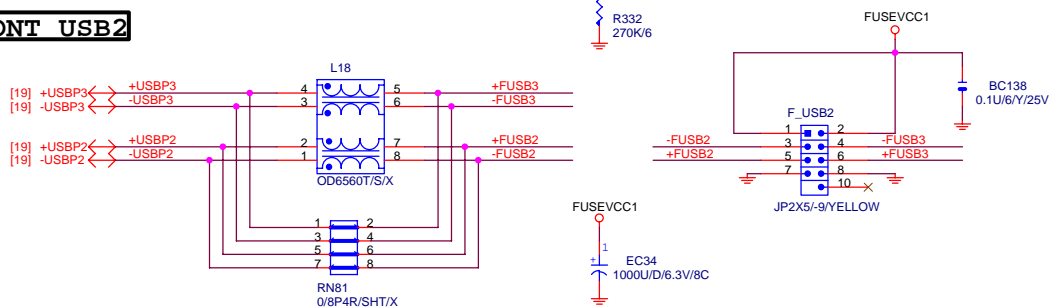
Size B Document Number \_\_\_\_\_ Rev 1.0

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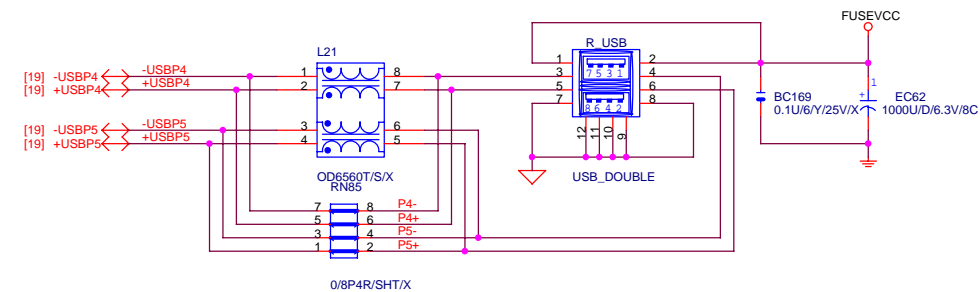
### FRONT USB1



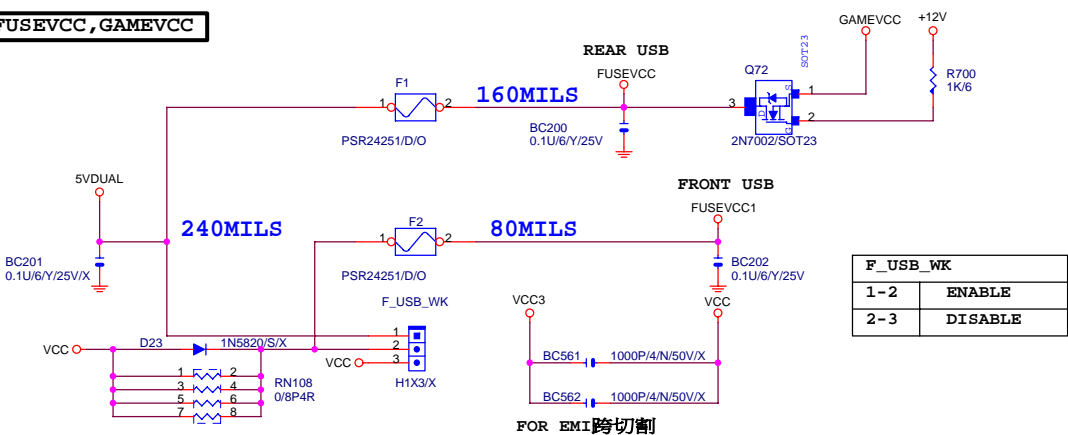
### FRONT USB2



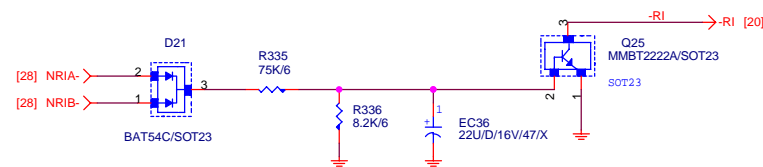
### REAR USB



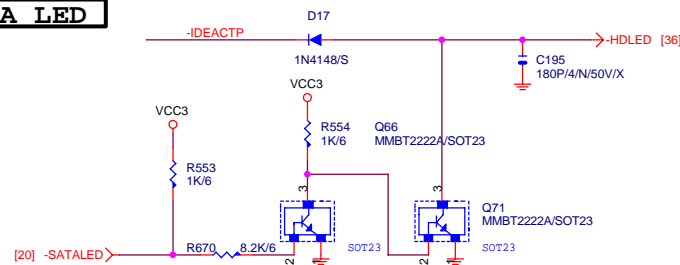
### FUSEVCC, GAMEVCC



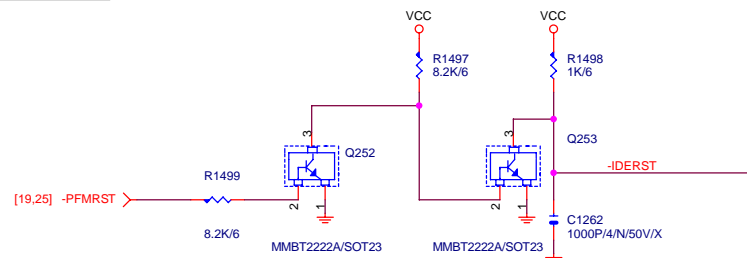
### RING IN



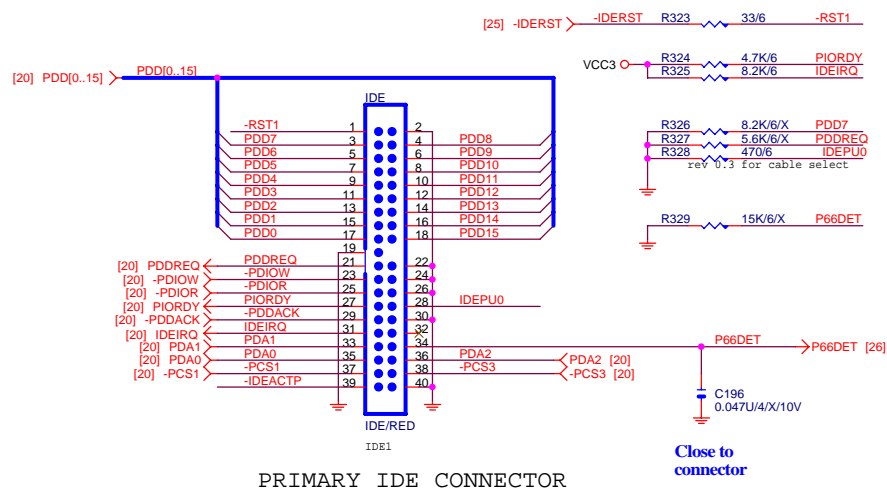
### IDE/SATA LED



### IDE RESET



### IDE



PRIMARY IDE CONNECTOR

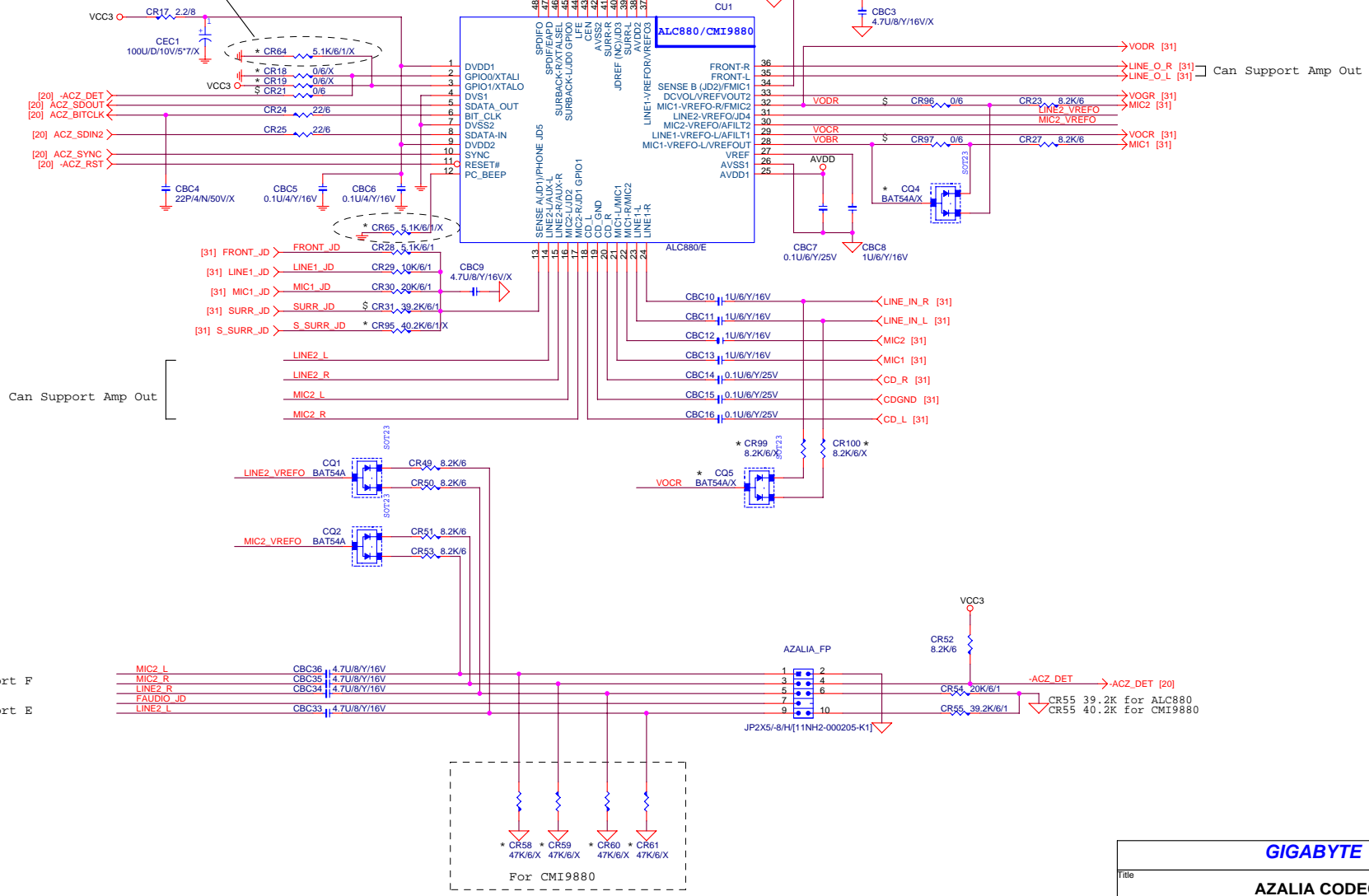
GIGABYTE

FRONT/REAR USB CONNECTOR/IDE			
Title	FRONT/REAR USB CONNECTOR/IDE		
Size	Document Number	8I915G-MF	
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"\$" means for ALC880 only  
"\*" means for CMI9880 only

For CMI9880 A3 version  
CR64=N/A & CR65=5.1K/6/1  
For ACM9880 A4 version  
CR64=5.1K/6/1 & CR65=DNI

CR98=10ohm for ALC880  
CR98=150ohm for CMI9880



Can Support Amp Out

Can Support Amp Out

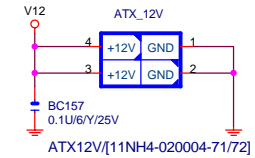
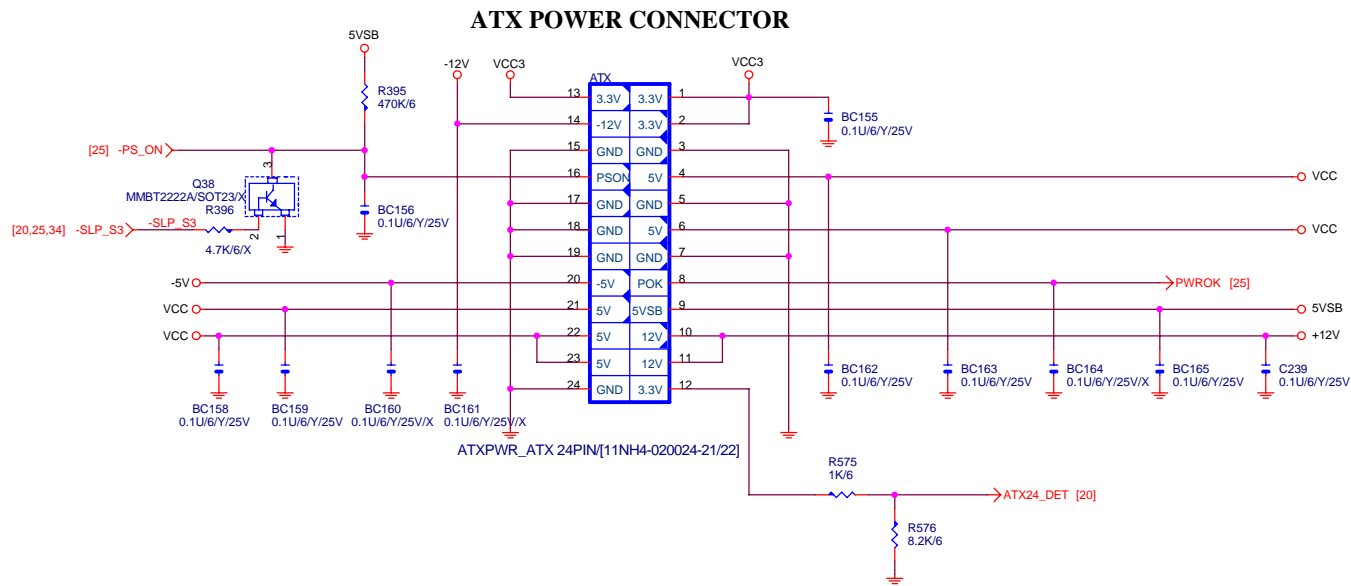
Azalia Port F  
Azalia Port E

For CMI9880

<b>GIGABYTE</b>			
<b>AZALIA CODEC</b>			
Title		Rev <b>1.0</b>	
Size	Document Number	<b>81915G-MF</b>	
Customer	Date: 星期二 六月 09, 2004 Sheet 30 of 37		



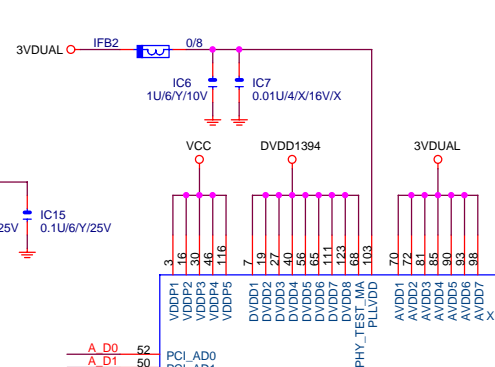
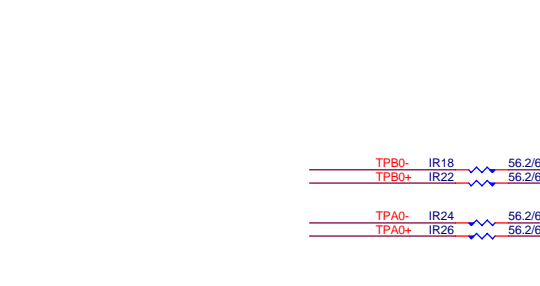
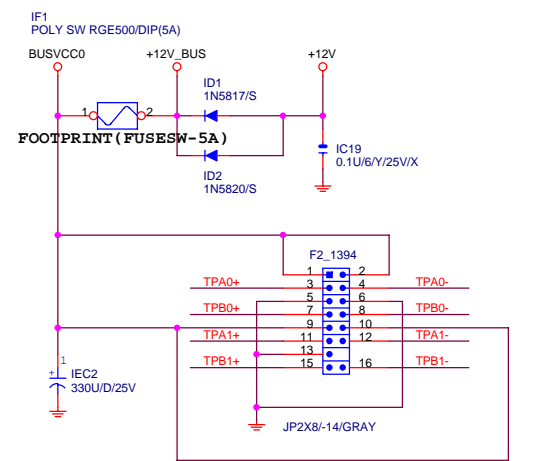
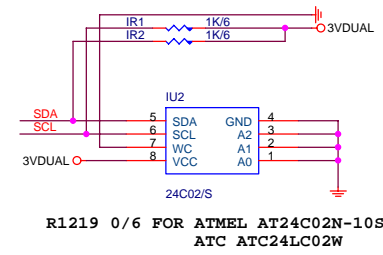
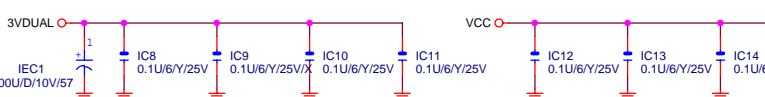
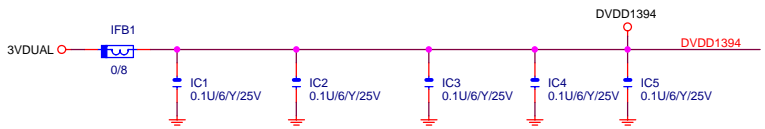




**GIGABYTE**

Title			ATX POWER CONNECTOR		
Size			Document Number		
B			8I915G-MF		
Date:			Rev		1.0
Date: 星期二, 六月 09, 2004			Sheet		33 of 37
			1		

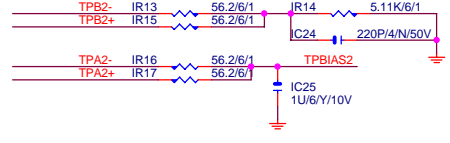
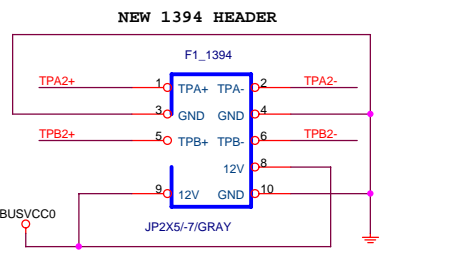
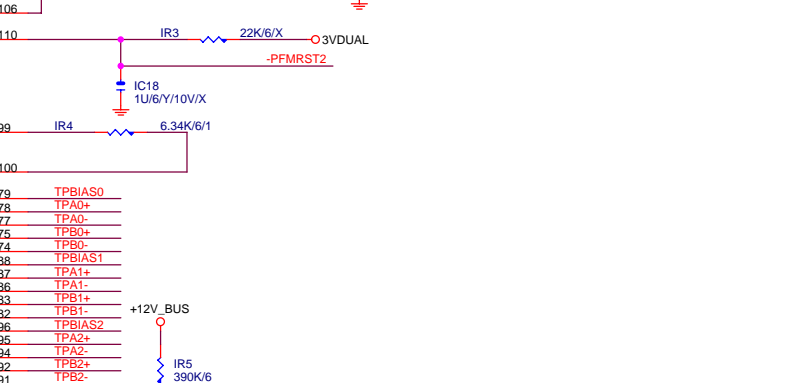
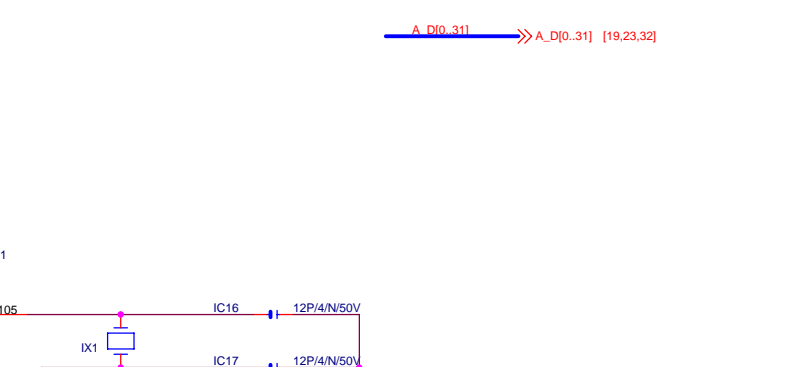
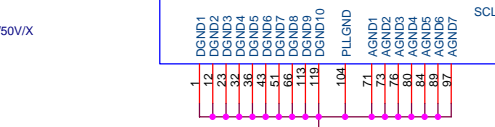




**TSB43AB23**

A D0	52	PCI_AD0
A D1	50	PCI_AD1
A D2	49	PCI_AD2
A D3	48	PCI_AD3
A D4	47	PCI_AD4
A D5	45	PCI_AD5
A D6	44	PCI_AD6
A D7	42	PCI_AD7
A D8	39	PCI_AD8
A D9	38	PCI_AD9
A D10	37	PCI_AD10
A D11	35	PCI_AD11
A D12	34	PCI_AD12
A D13	33	PCI_AD13
A D14	31	PCI_AD14
A D15	29	PCI_AD15
A D16	14	PCI_AD16
A D17	13	PCI_AD17
A D18	11	PCI_AD18
A D19	10	PCI_AD19
A D20	9	PCI_AD20
A D21	8	PCI_AD21
A D22	6	PCI_AD22
A D23	5	PCI_AD23
A D24	128	PCI_AD24
A D25	127	PCI_AD25
A D26	125	PCI_AD26
A D27	124	PCI_AD27
A D28	122	PCI_AD28
A D29	121	PCI_AD29
A D30	120	PCI_AD30
A D31	118	PCI_AD31

[19,23,32] -C_BE0	41	PCI_C/BE0
[19,23,32] -C_BE1	28	PCI_C/BE1
[19,23,32] -C_BE2	15	PCI_C/BE2
[19,23,32] -C_BE3	2	PCI_C/BE3
[19,23,32] PAR	26	PCI_PAR
[19,23,32] -FRAME	17	PCI_FRAME
[19,23,32] -IRDY	18	PCI_IRDY
[19,23,32] -TRDY	20	PCI_TRDY
[19,23,32] -DEVSEL	21	PCI_DEVSEL
[19,23,32] -STOP	22	PCI_STOP
A D23	4	PCI_IDSEL
[19,23] -REQ3	115	PCI_REQ
[19,23] -GNT3	114	PCI_GNT
[19,23,32] -PERR	24	PCI_PERR
[19,23,32] -SERR	25	PCI_SERR
[22] PCICLK1394	112	PCI_PCLK
[25,26,32] -PFMRST2	53	PCI_RST
[19,23] -PIRQH	109	PCI_INTA
[19,23,32] -PCIPME	117	PCI_PME

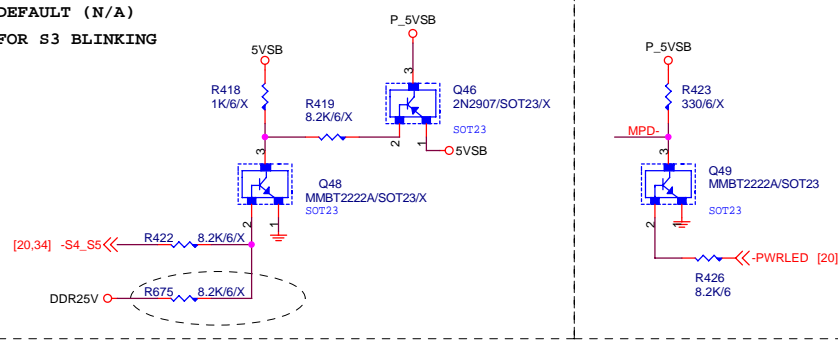
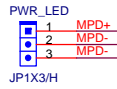


GIGABYTE		
Title		
TSB43AB23A 1394		
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# INTEL FRONT PANEL

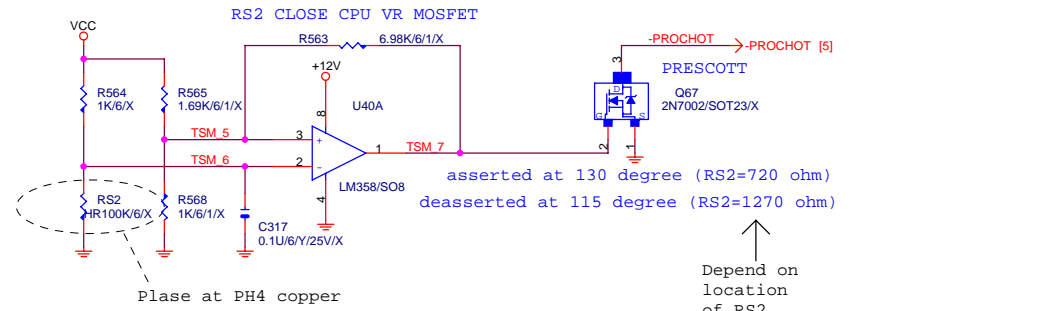
DEFAULT (N/A)  
FOR S3 BLINKING

3 PIN POWER LED  
LAYOUT PLACE CLOSE  
TO F\_PANEL

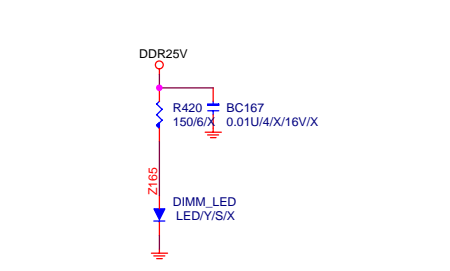
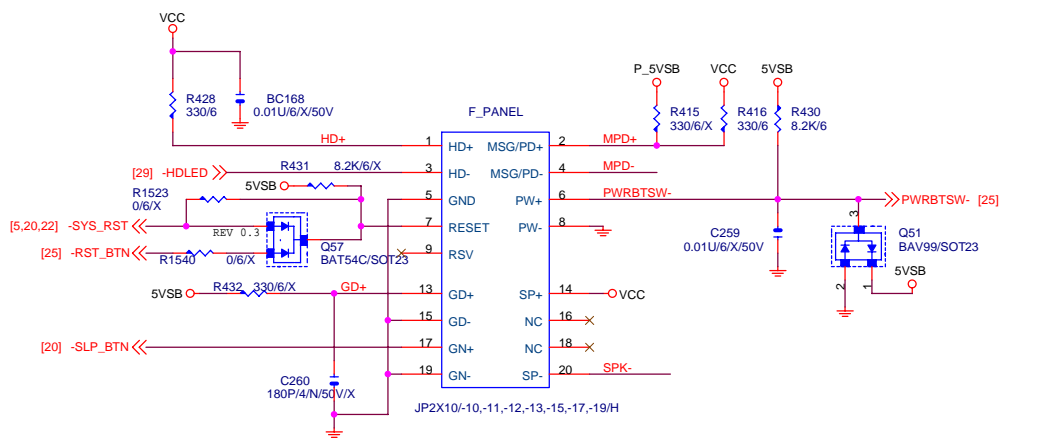
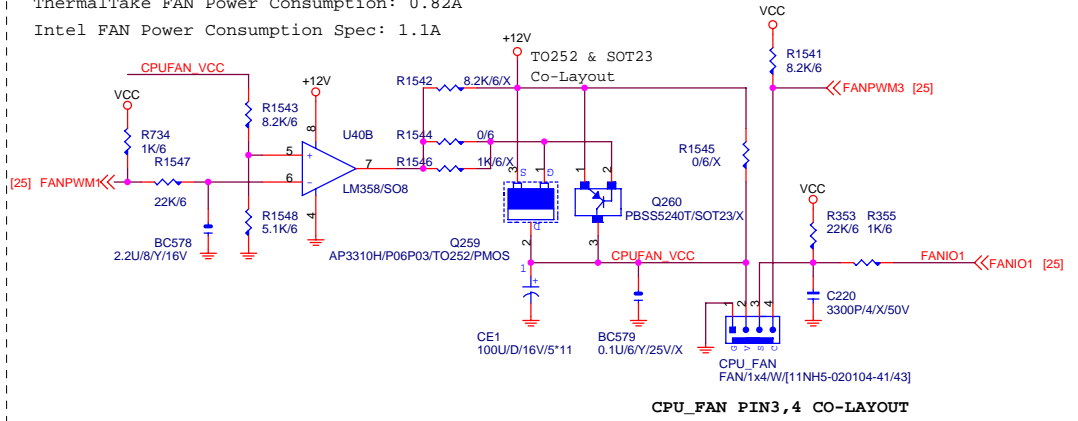


# PROCESSOR HOT

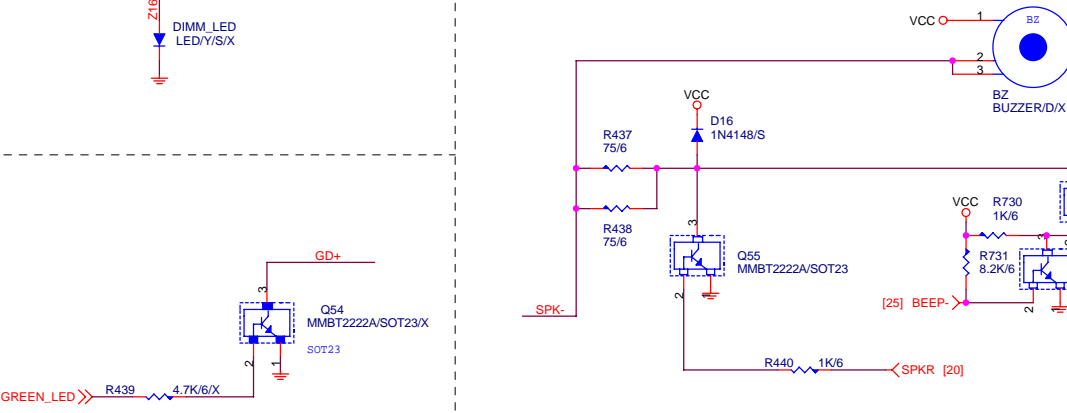
U-ATX PROCESSOR HOT NO POP



ThermalTake FAN Power Consumption: 0.82A  
Intel FAN Power Consumption Spec: 1.1A



# BUZZER



States for green LED NO1 GPO22 only S1 PROGRAMMING LOW

LED States	ACPI States	GPIO28
ON	S1,S3	0
OFF	S0,S5	1

(GPIO22 DEFAULT HIGH, main power)

States for a single-color power LED

LED States	ACPI States	GPIO25	GPIO27	GPIO24
OFF	S1,S3,S5	1	1	NO1
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	B	1
Steady Yellow	S1,S3	1	0	NO1
Blinking Yellow	S1,S3(message waiting)	1	B	NO1

LED States	ACPI States	GPIO25	GPIO27	GPIO22
OFF	S5	1	1	X
Steady Green	S0	1	1	1
Blinking Green	S0(message waiting)	1	B	1
Steady Yellow	S1,S3	1	0	NO1
Blinking Yellow	S1,S3(message waiting)	1	B	NO1

GIGABYTE

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ICH6 GPIO Table:

NAME	PWR LANE	USAGE	NAME	PWR LANE	USAGE
GPI0	V5REF	M/B ID (-REQ6)	GPI41	VCC3	M/B ID1
GPI1	V5REF	-REQ5	GPO48	VCC3	-GNT4(TP)
GPI2	V5REF	-PIRQE	GPO49	V-CPUIO	CPUPWOK
GPI3	V5REF	-PIRQF			
GPI4	V5REF	-PIRQG			
GPI5	V5REF	-PIRQH			
GPI6	VCC3	-SLP BTN			
GPI7	VCC3	DUAL BIOS(TP)			
GPI8	3VDAUL	-LANWAKE			
GPI9	3VDAUL	-USBOC4			
GPI10	3VDAUL	-USBOC5			
GPI11	3VDAUL	-SMBALT			
GPI12	VCC3	ATX DET			
GPI13	3VDAUL	-LPCPME			
GPI14	3VDAUL	-USBOC6			
GPI15	3VDAUL	-USBOC7			
GPO16	VCC3	CPU OV1 (-GNT6)			
GPO17	VCC3	-GNT5(TP)			
GPO18	VCC3	CPU OV2			
GPO19	VCC3	DUAL BIOS(AGPOV1)			
GPO20	VCC3	BIOS T-BLOCK			
GPO21	VCC3	DUAL BIOS(AGPOV2)			
GPO23	VCC3	DDR OV1			
GPIO24	3VDAUL	GREEN LED			
GPIO25	3VDAUL	2.5V REGULATOR(P.D)			
GPI26	VCC3	SATA GP0			
GPIO27	3VDAUL	DDR OV2			
GPIO28	3VDAUL	-PWRLED			
GPI29	VCC3	SATA GP1			
GPI30	VCC3	SATA GP2			
GPI31	VCC3	SATA GP3			
GPIO32	VCC3	BIOS WP			
GPIO33	VCC3	AZALIA DET			
GPIO34	VCC3	PWRLED(MB ID0)			
GPI40	V5REF	-REQ4			

PWROK/RESET Table:

ITE8712BHX PIN	NET NAME	TARGET
PIN62/-PCIRST1	-PCIE_RST	1. PCI-E * 1 Slot1 2. PCI-E * 16 slot
PIN64/-PCIRST2	-PFMRST2	1. Onboard PCI Lan 2. Onboard 1394 Chip 3. OnBoard FWH
PIN65/-PCIRST3	-PFMRST1	1. GMCH
PIN115/-PCIRST4	-IDERST	Reserved For IDE
PIN63/PWROK1	PWROK1	1. GMCH 2. ICH6
PIN109/PWROK2	-THERM	1. ICH6

**GIGABYTE THCHNOLOGIES , INC.**

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