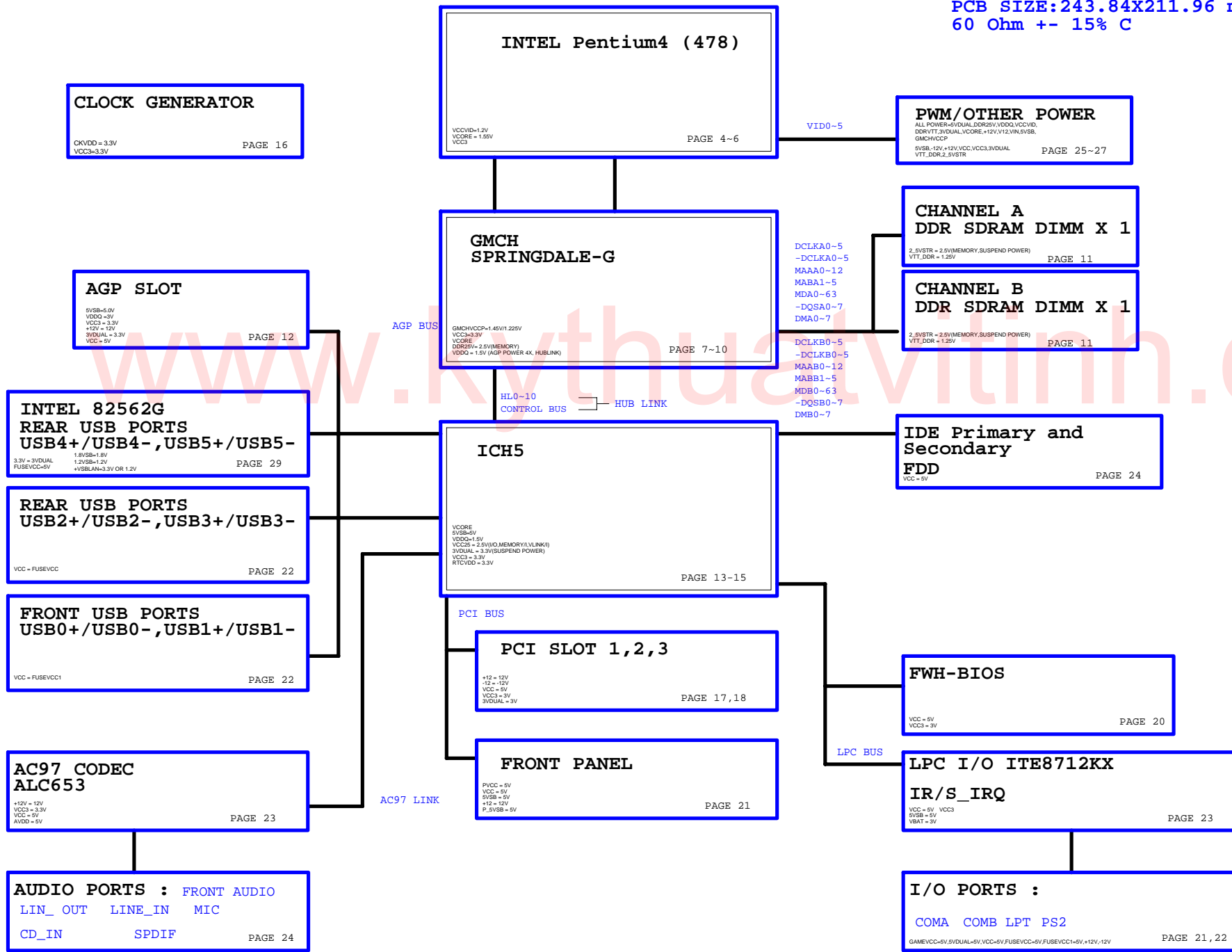


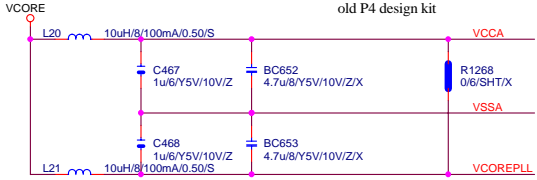
BLOCK DIAGRAM

PCB SIZE: 243.84X211.96 mm.
60 Ohm +/- 15% C



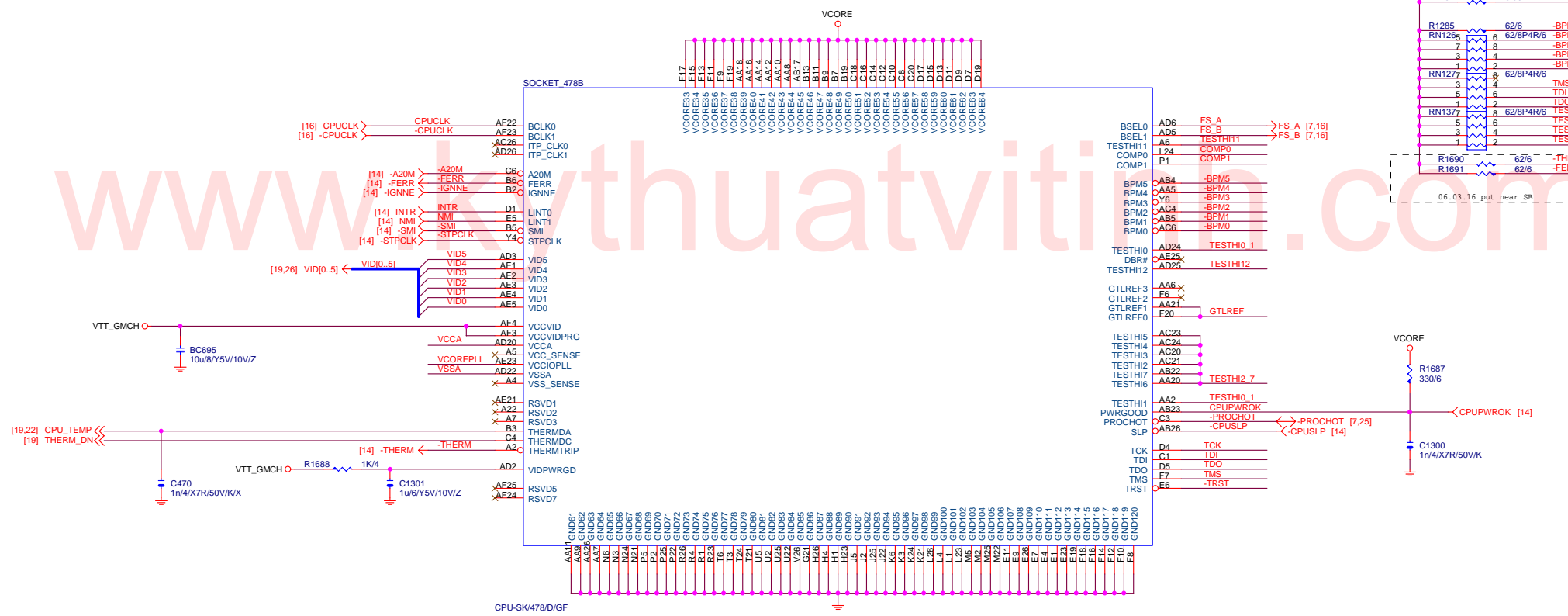
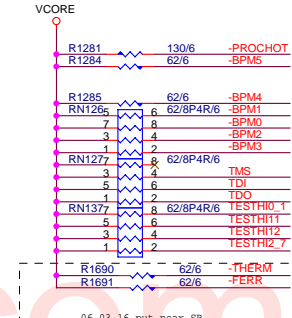
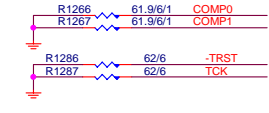
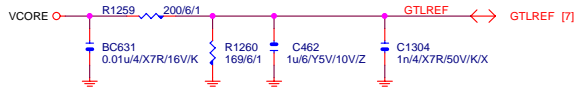
GIGABYTE			
BOM & PCB MODIFY HISTORY			
File	Document Number	Rev	
Size	81865GME	1.01	
Sheet	3 of 29		

Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



Trace width doesn't
less than 12 Mil

As close as possible to
CPU socket

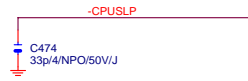


CPU

NA	FSB	FSA	
FBSSEL3	FBSSEL1	FBSSEL0	Clock
1	0	1	100MHz
0	0	1	133MHz
0	1	1	166MHz
0	1	0	200MHz
0	0	0	266MHz

ICS952603

FS_E	FS_3	FS_C	FS_B	FS_A	Clock
1	0	0	0	0	100MHz
1	0	0	0	1	133MHz
1	0	0	1	1	166MHz
1	0	0	1	0	200MHz

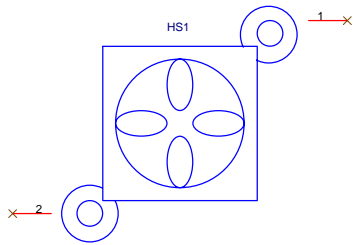


GIGABYTE

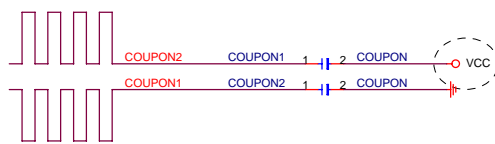
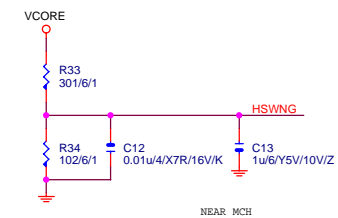
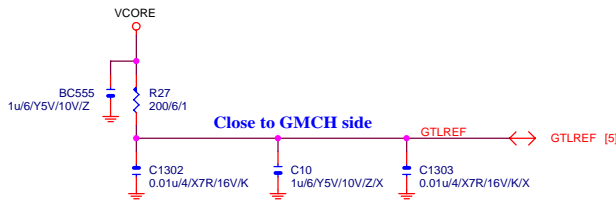
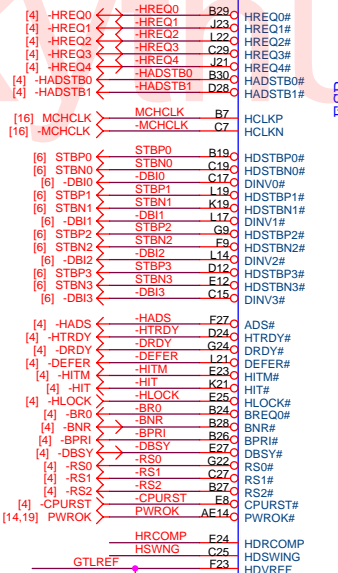
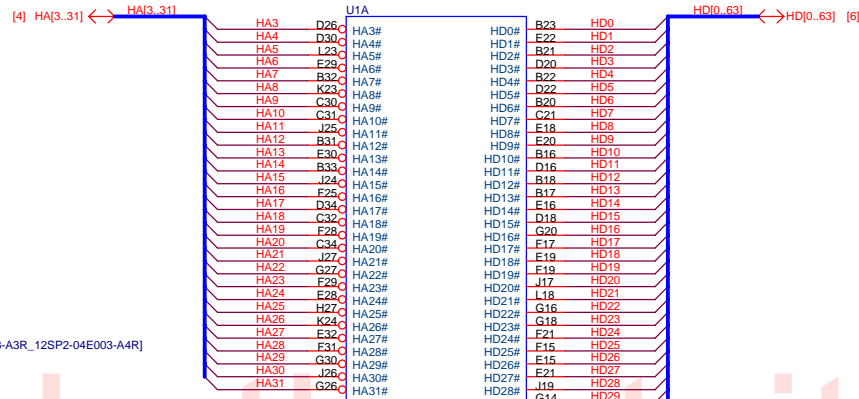
Title
P4_LGA775-B

Size Custom Document Number
81865GME Rev
1.01

Date: 星期四, 五月 04, 2006 Sheet 5 of 29



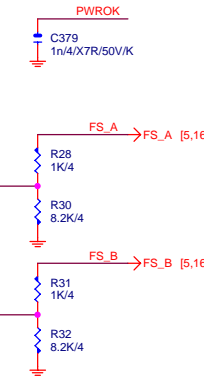
NB_HEATSINK[12SP2-04E003-A1R_12SP2-04E003-A2R_12SP2-04E003-A3R_12SP2-04E003-A4R]



U1A

PROCHOT#

CPU INTERFACE



Title		
SPRINGDALE HOST		
Size	Document Number	Rev
Custom	81865GME	1.01
Date:	Sheet	7 of 29

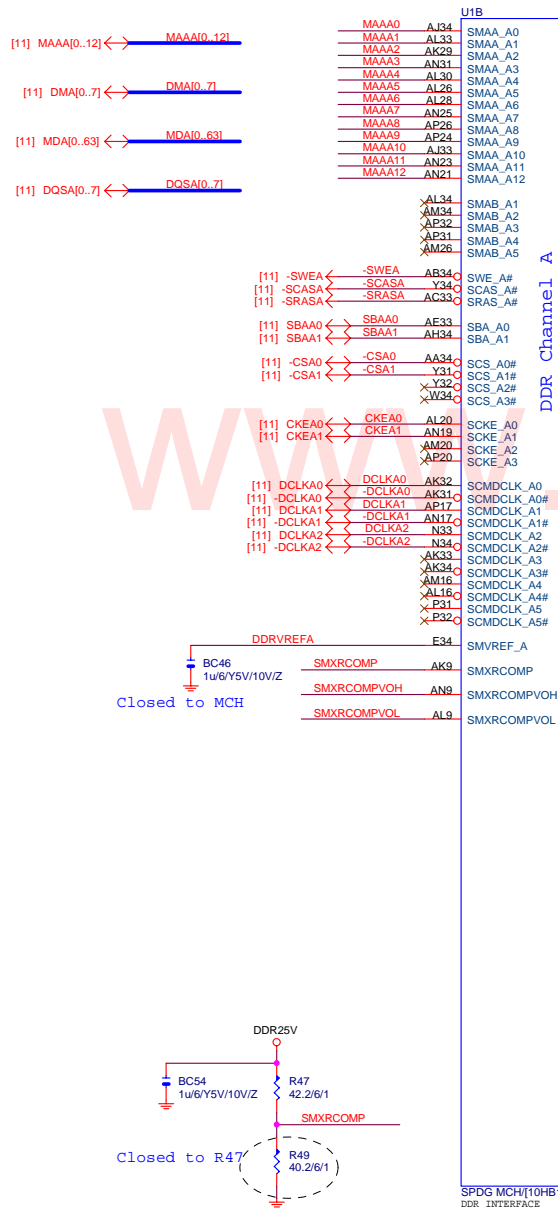


FIG 1.3V(PQI/TWINMOS DDR400)-->REV 1.0

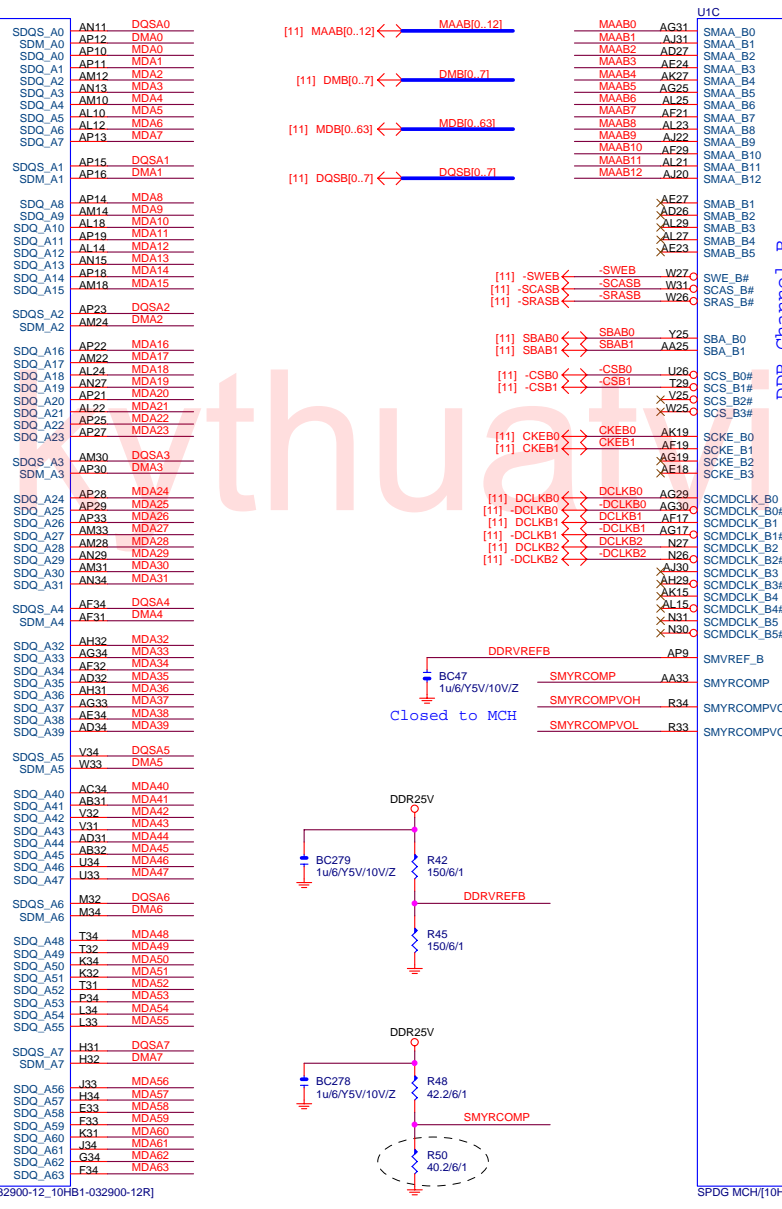
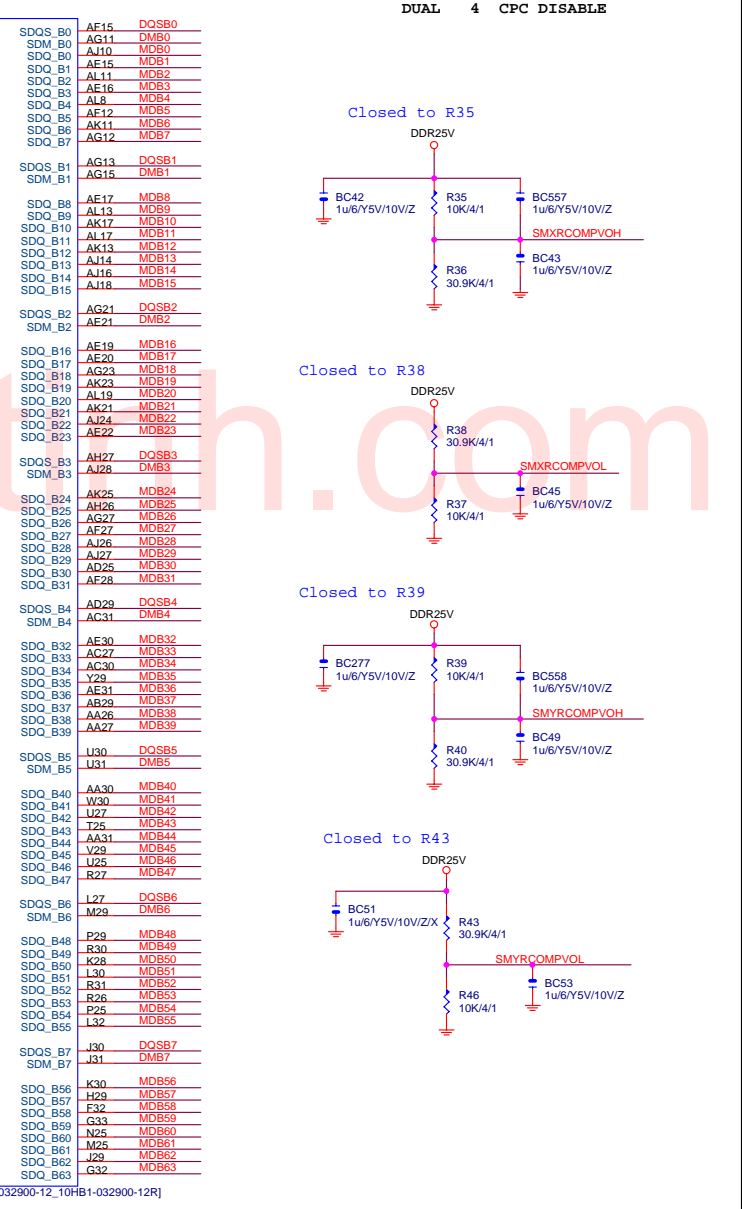
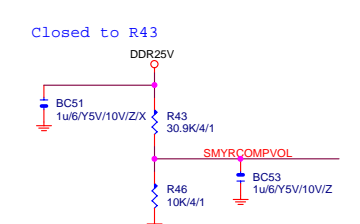
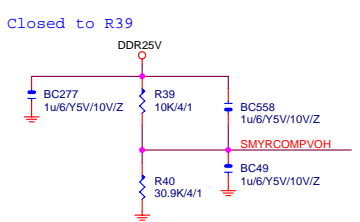
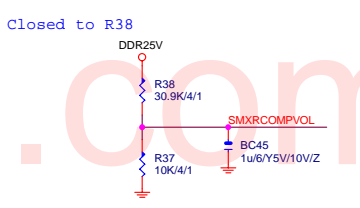
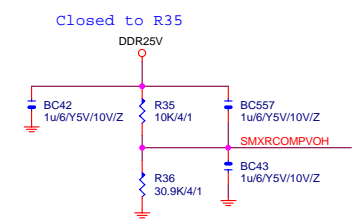


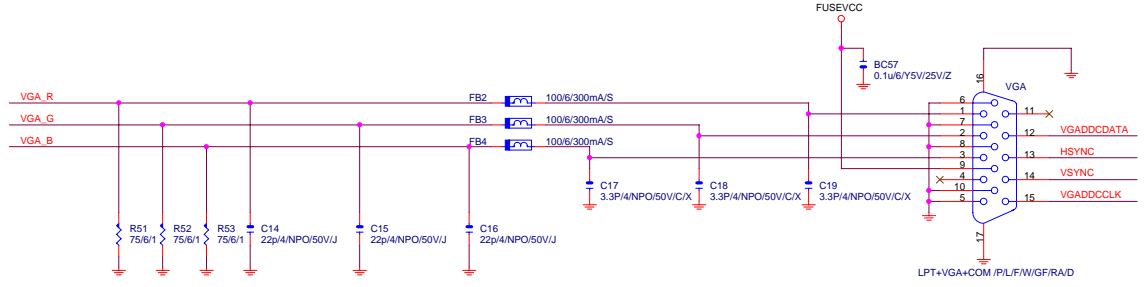
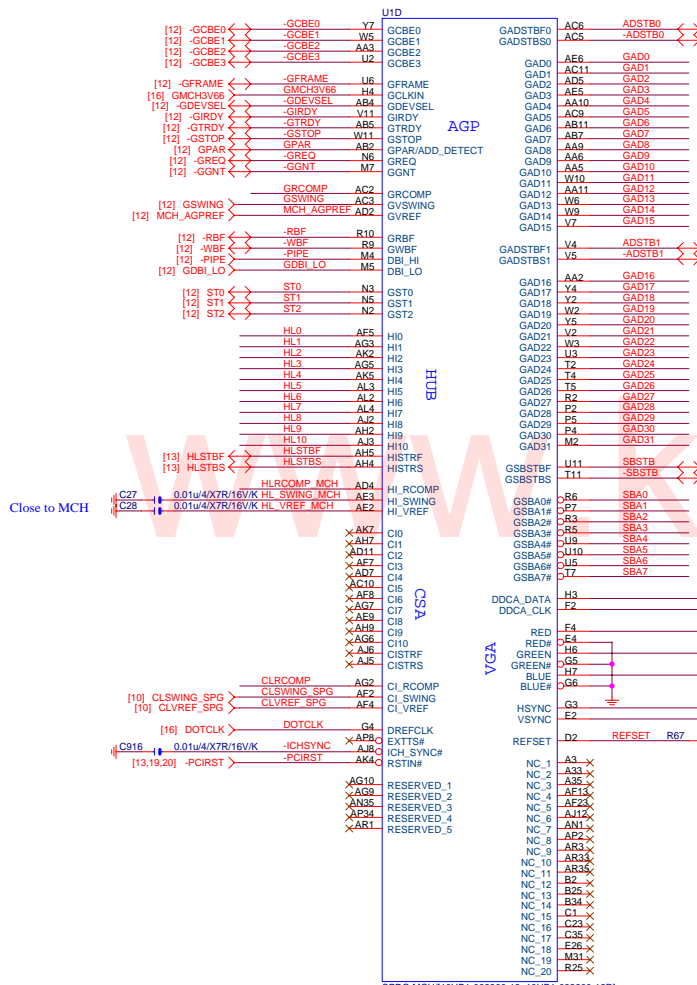
FIG 1.3V(PQI/TWINMOS DDR400)-->REV 1.0

DDR 400 1-2-3-4 CPC DISABLE
 DDR 333 SIGNAL 1 CPC ENABLE
 2 CPC DISABLE
 DUAL 2 CPC ENABLE
 DUAL 4 CPC DISABLE

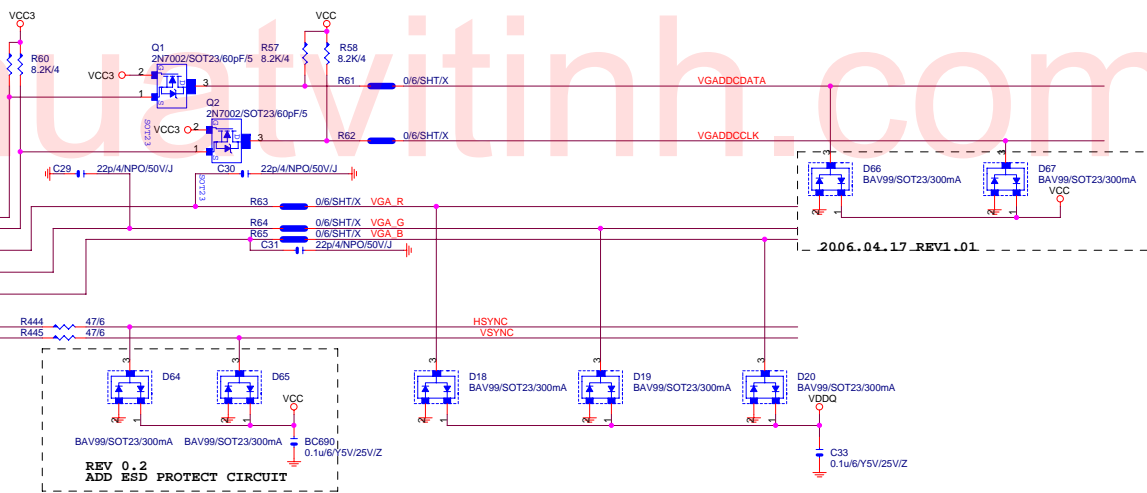


Title			SPRINGDALE DDR		
Size	Document Number		Rev		
Custom	81865GME		1.01		
Date:			Sheet	8	of 29

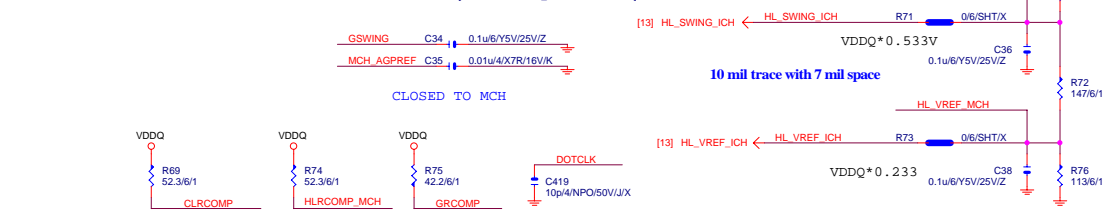
- [12] GAD[0..31] ← GAD[0..31]
- [12] SBA[0..7] ← SBA[0..7]
- [13] HL[0..10] ← HL[0..10]



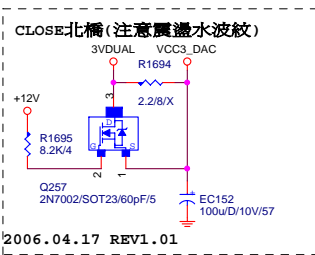
REV 0.2 EMI



2006.04.17_REV1.01



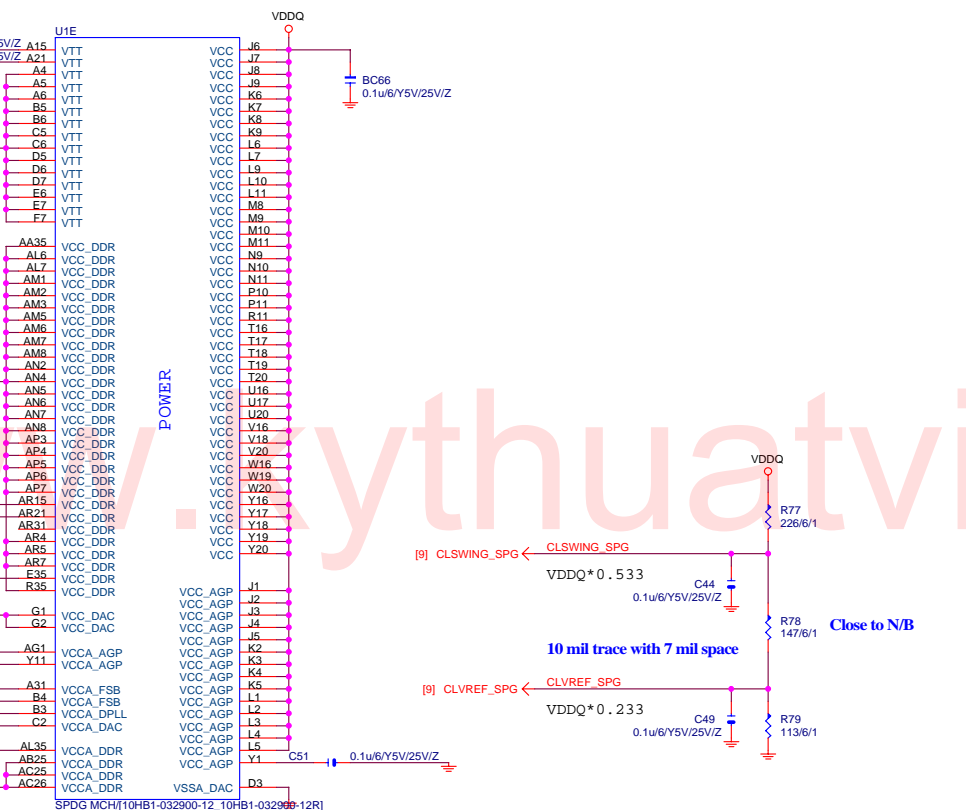
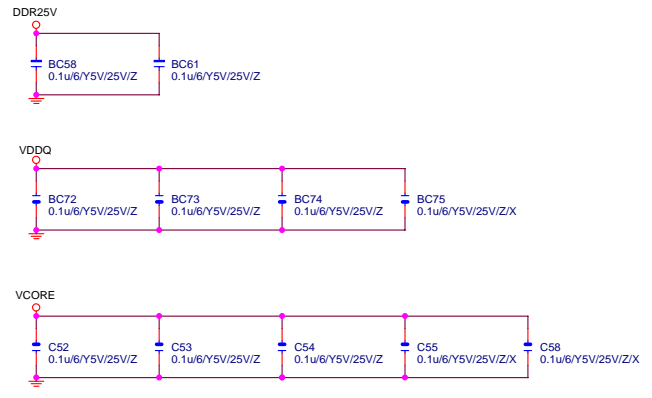
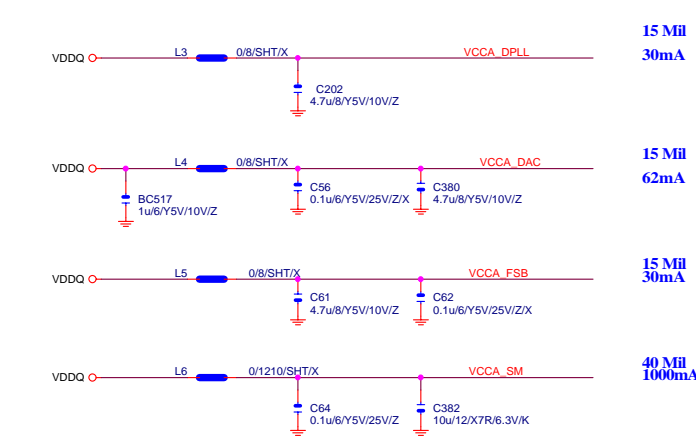
Title		SPRINGDALE AGP,HUB,CSA,VGA	
Size	Document Number	81865GME	Rev 1.01
Date:	Sheet	9	of 29



2006.04.17 REV1.01

2006.04.17 REV1.01

VCC3_DAC



[9] CLSWING_SPG ← CLSWING_SPG
VDDQ * 0.533

10 mil trace with 7 mil space

[9] CLVREF_SPG ← CLVREF_SPG
VDDQ * 0.233

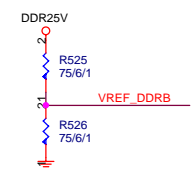
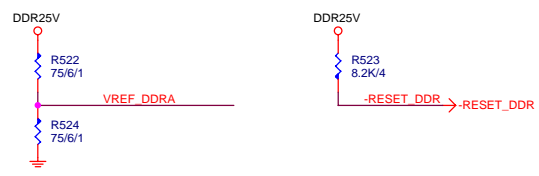
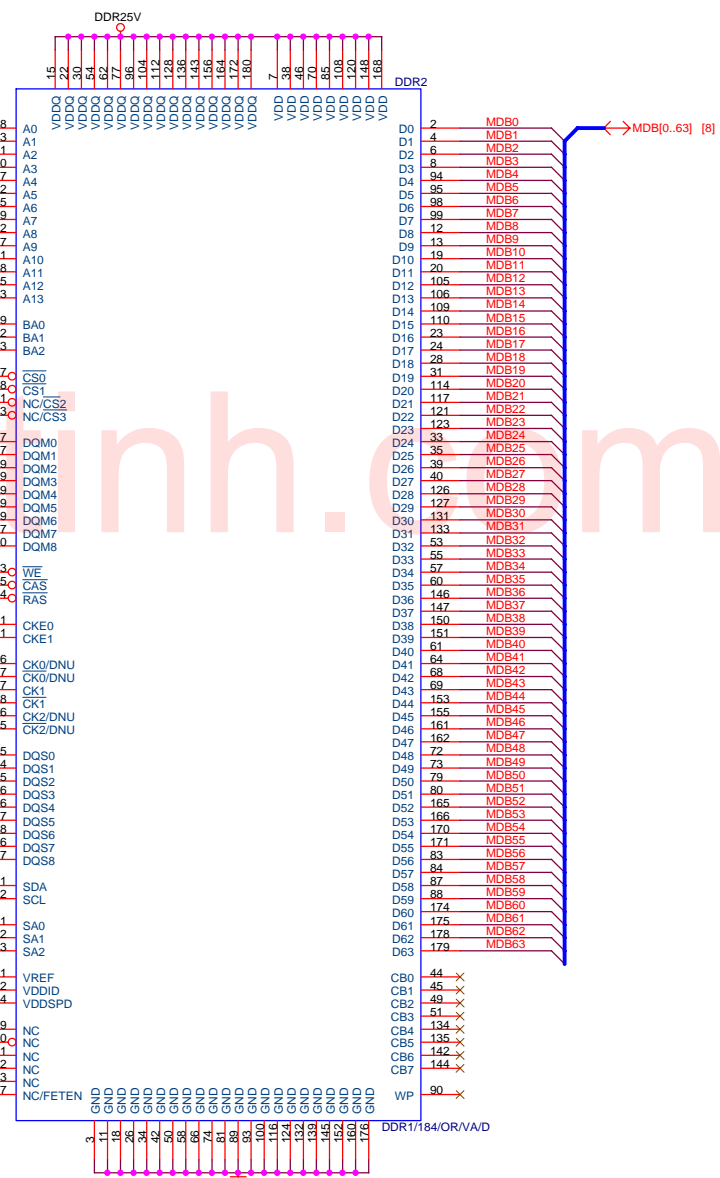
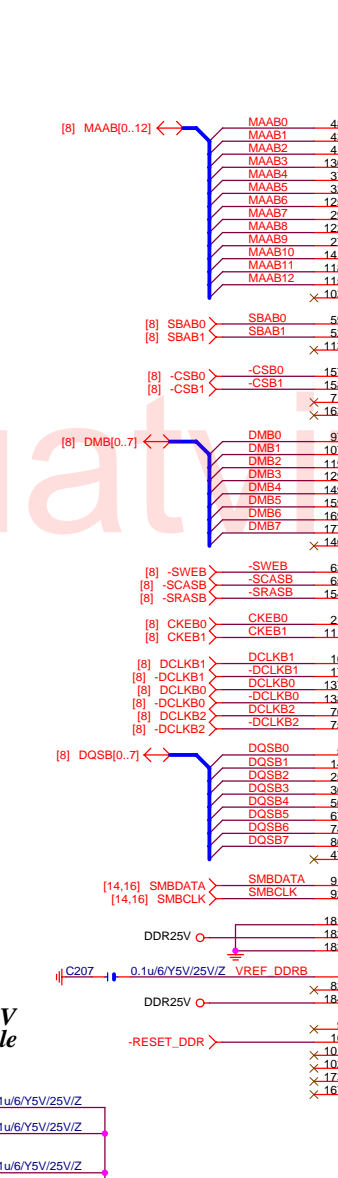
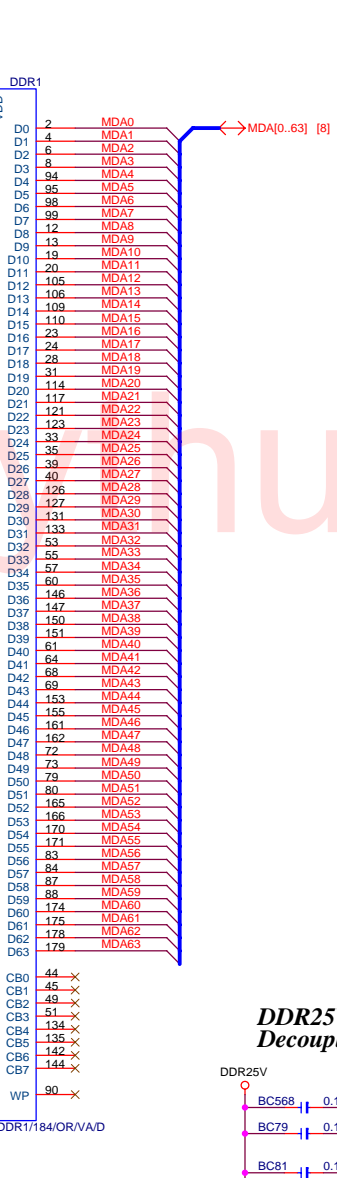
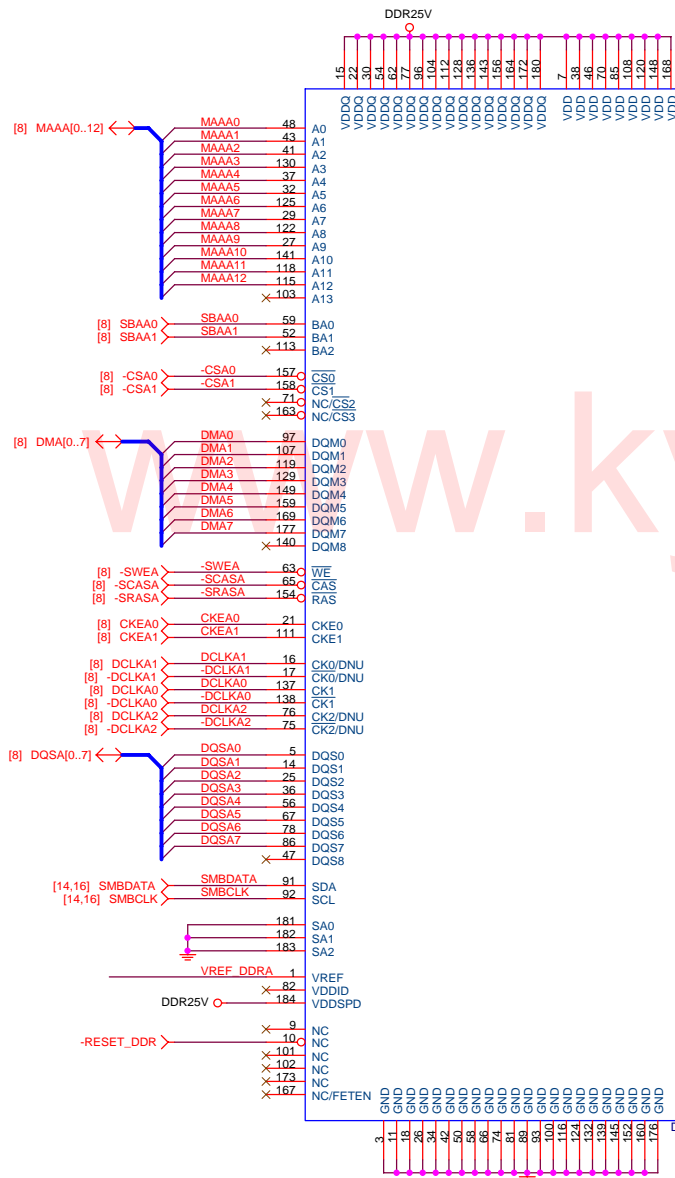
Close to N/B



SPDG MCH[10HB1-032900-12_10HB1-032900-12R]

SPDG MCH[10HB1-032900-12_10HB1-032900-12R]

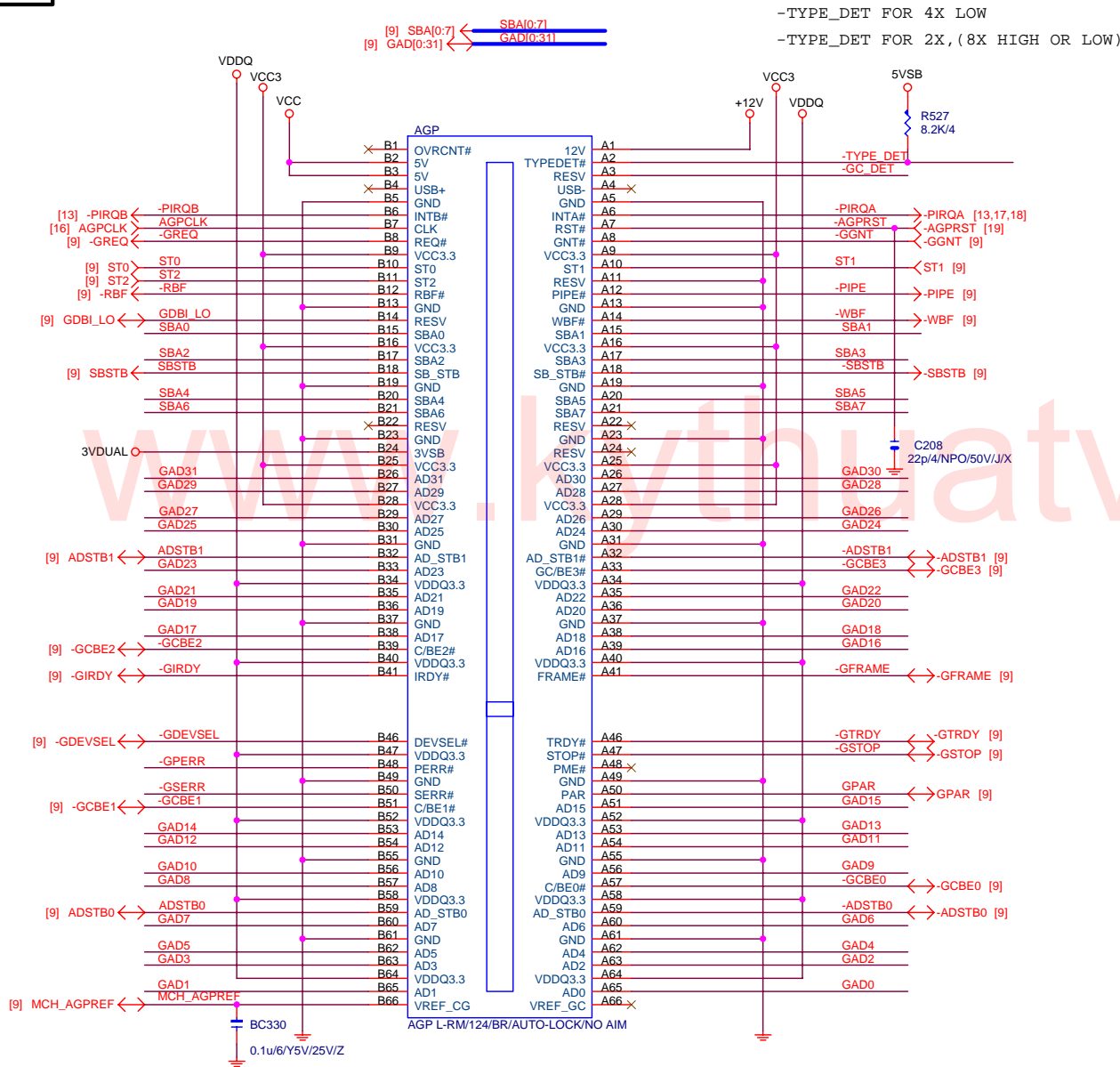
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Size	Document Number	Rev			
Custom	81865GME	1.01			
Date:		Sheet	10	of	29



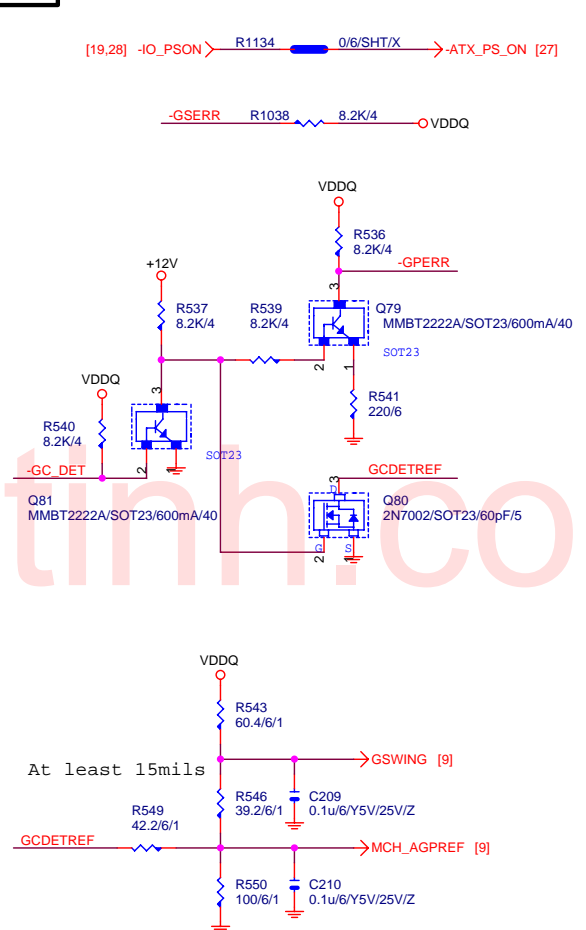
For Register DDR Support

GIGABYTE		
Title: DDR1,2		
Size: Custpm	Document Number: 81865GME	Rev: 1.01
Date:	Sheet: 11	of: 29

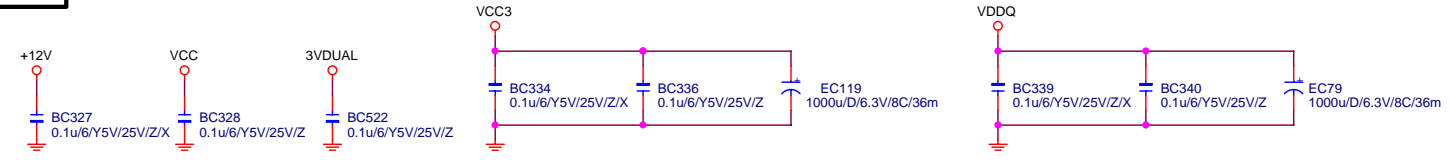
AGP SLOT



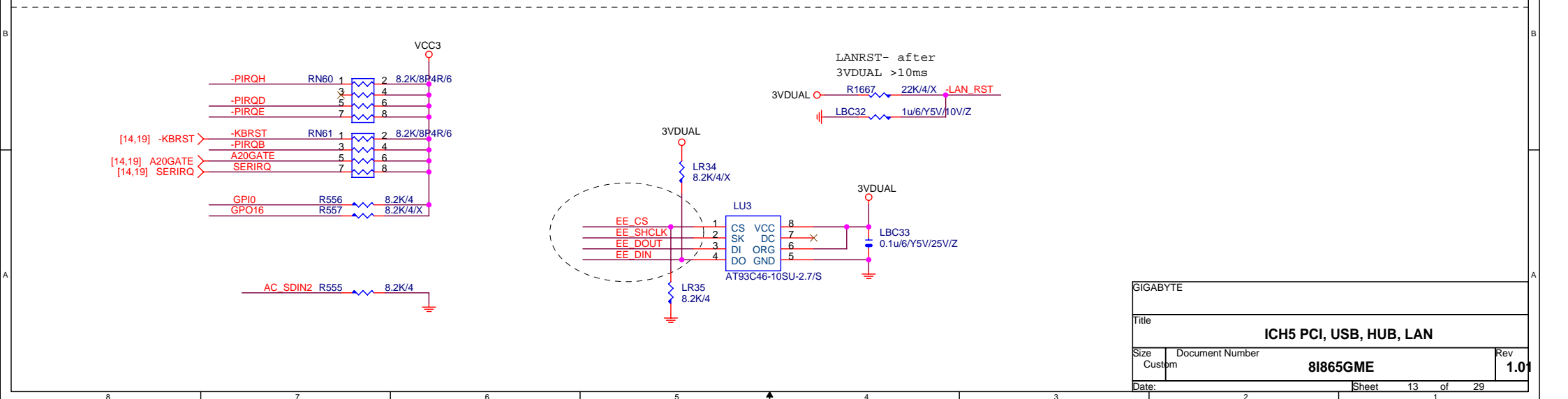
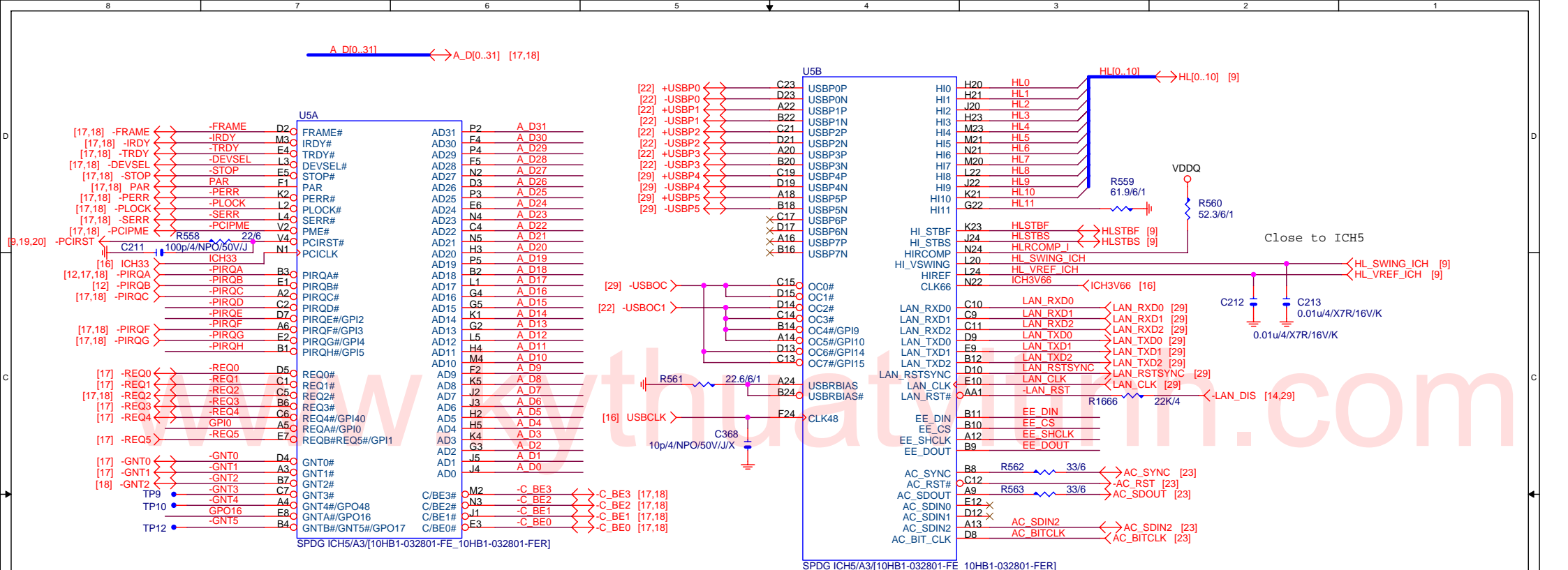
REF SCH



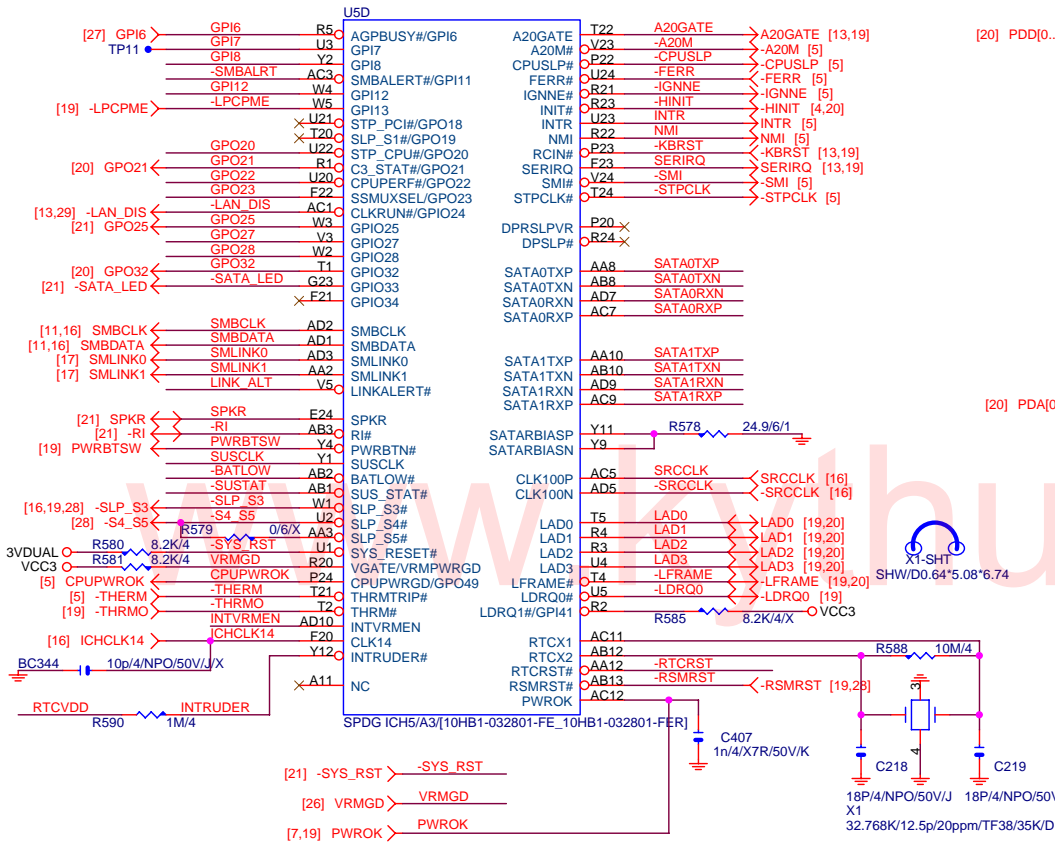
DE-CAP



GIGABYTE		
AGP SLOT		
Title		Rev
Size		1.01
Document Number	81865GME	
Date:	星期四, 五月 04, 2006	Sheet 12 of 29



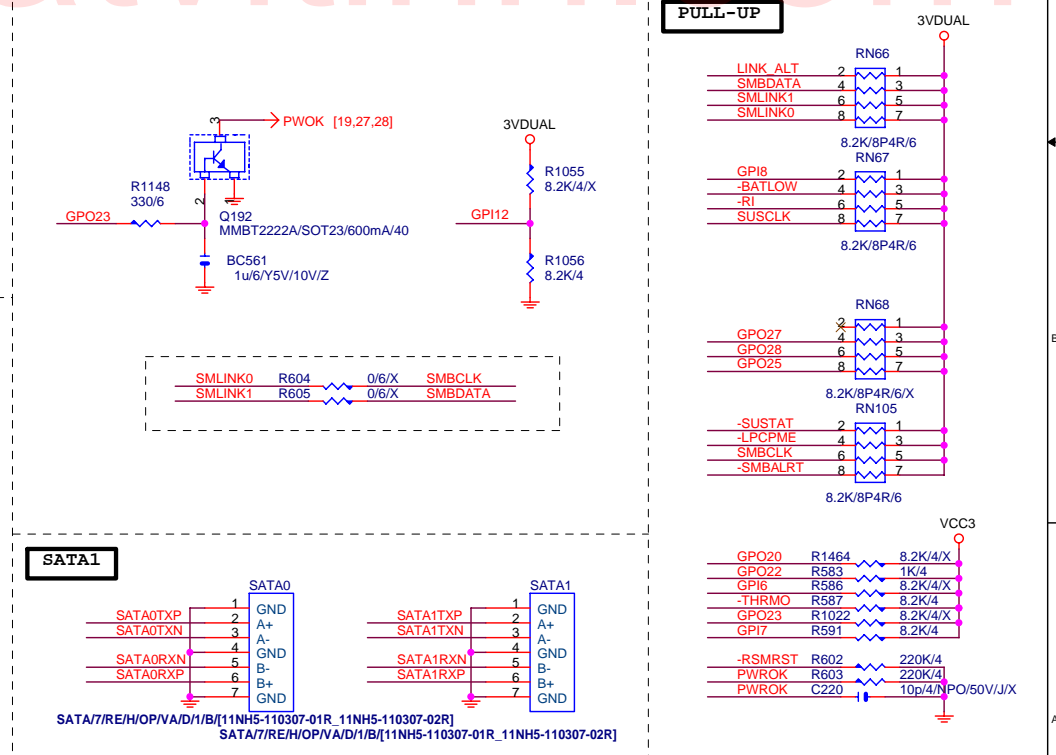
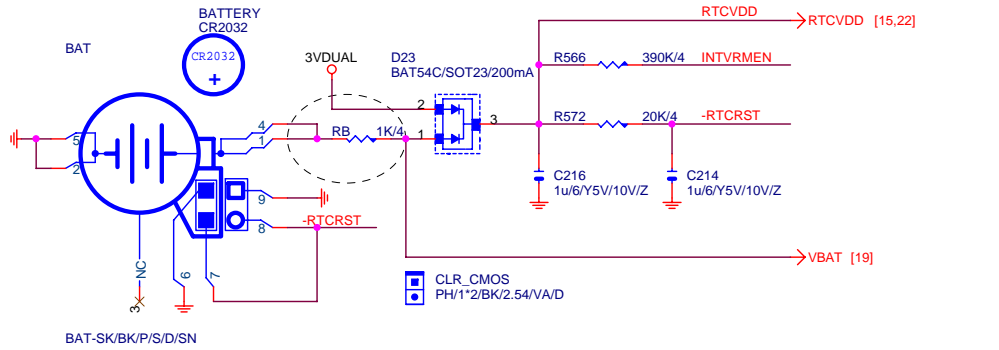
GIGABYTE		
Title		
ICH5 PCI, USB, HUB, LAN		
Size	Document Number	Rev
Custom	81865GME	1.0
Date:	Sheet 13 of 29	



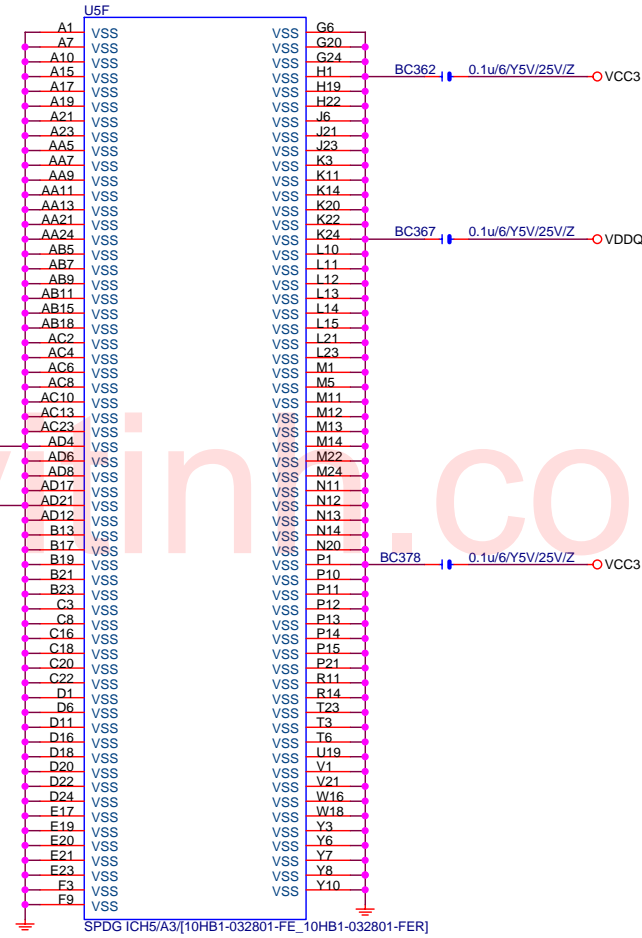
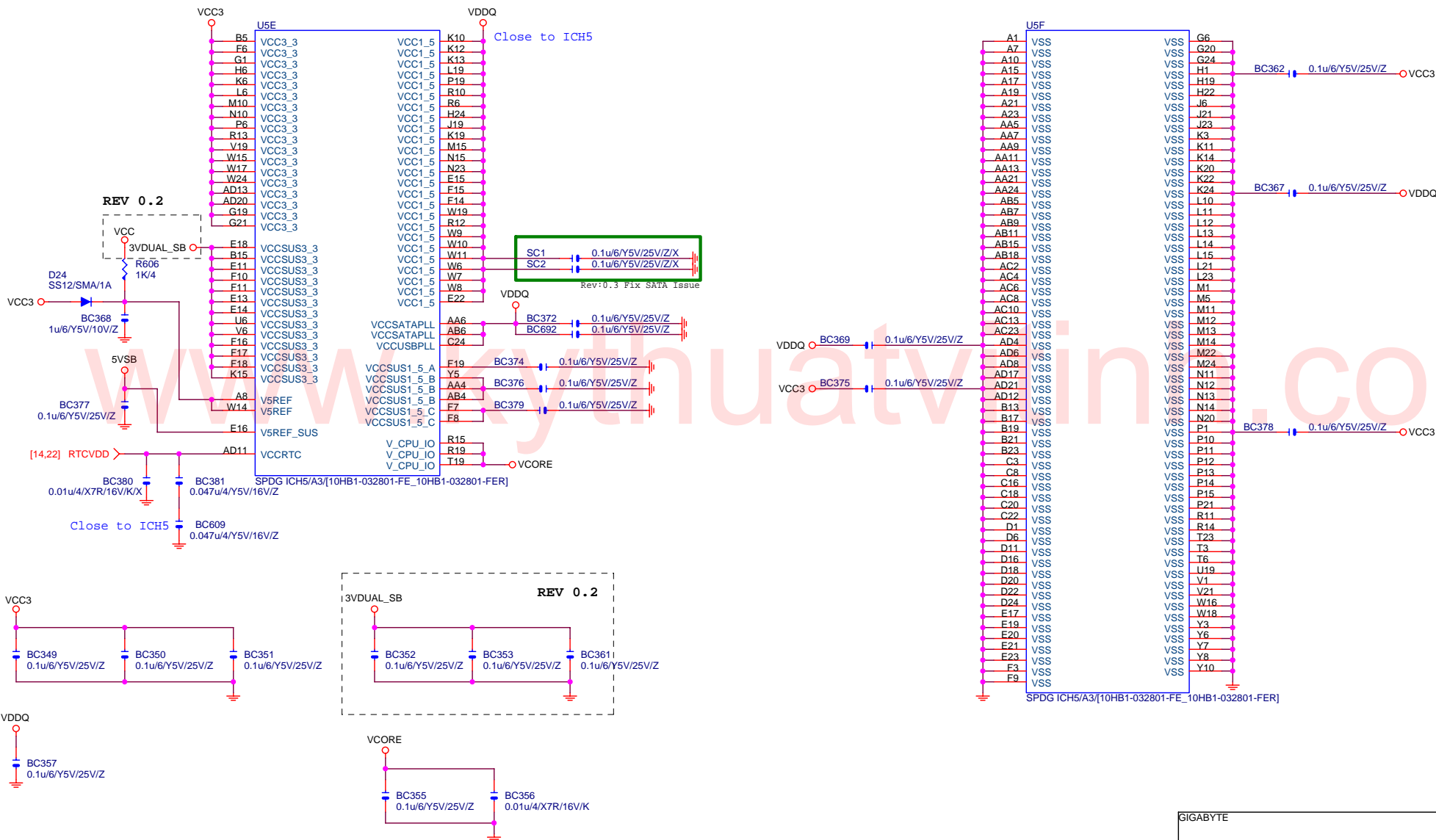
BATTERY

NEW TYPE: BATTERY-DUAL-4
 RB 不可放在 BATTERY SOCKET 下方

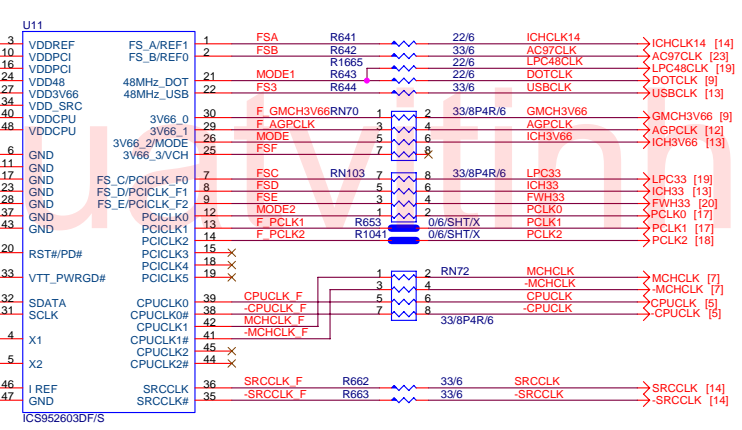
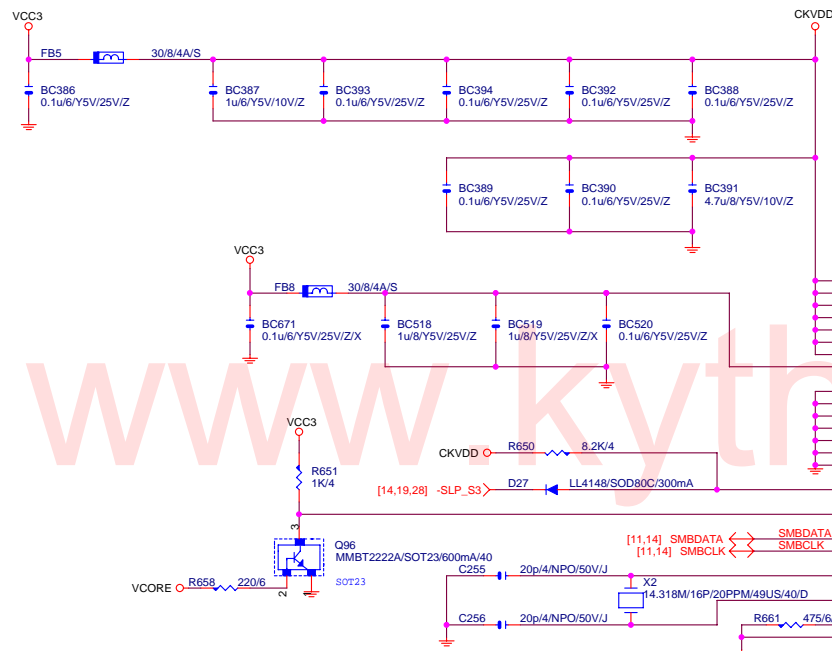
CLR_CMOS :	
SHORT	CLEAR CMOS
OPEN	NORMAL



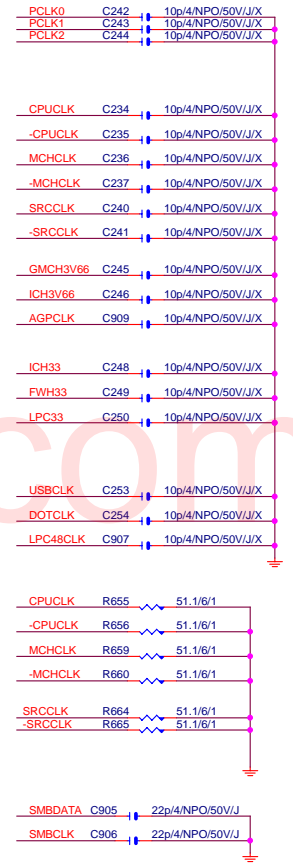
GIGABYTE	
Title	
ICH5 IDE, GPIO, SATA, CTRL	
Size	Document Number
Custom	81865GME
Date:	Rev
	1.01
Sheet 14 of 29	



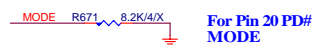
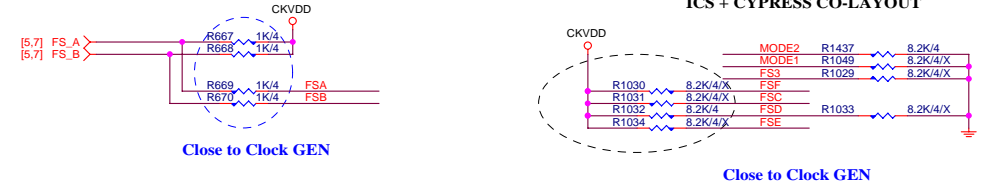
GIGABYTE			
Title			
ICH5 VCC, GND			
Size	Document Number	Rev	
Custom	81865GME	1.01	
Date:	Sheet 15 of 29		



For P/PE only
 Un-POP R643
 POP C254 -> 0 Ohm (0402)



ICS + CYPRESS CO-LAYOUT

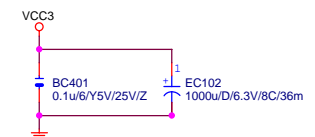
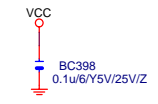
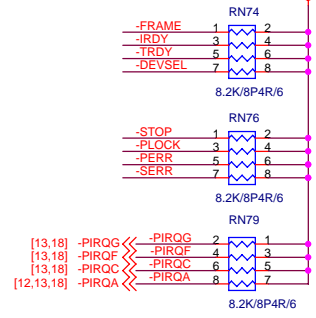
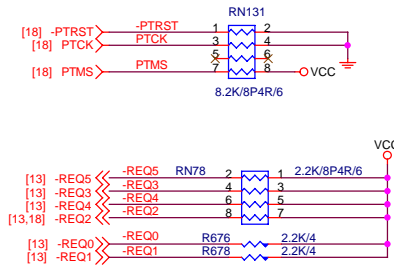
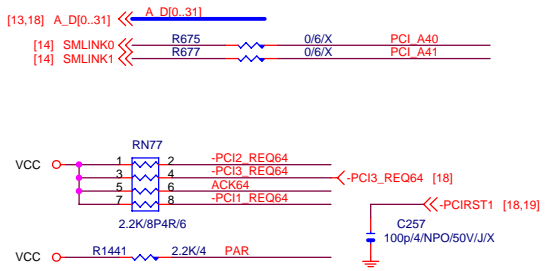
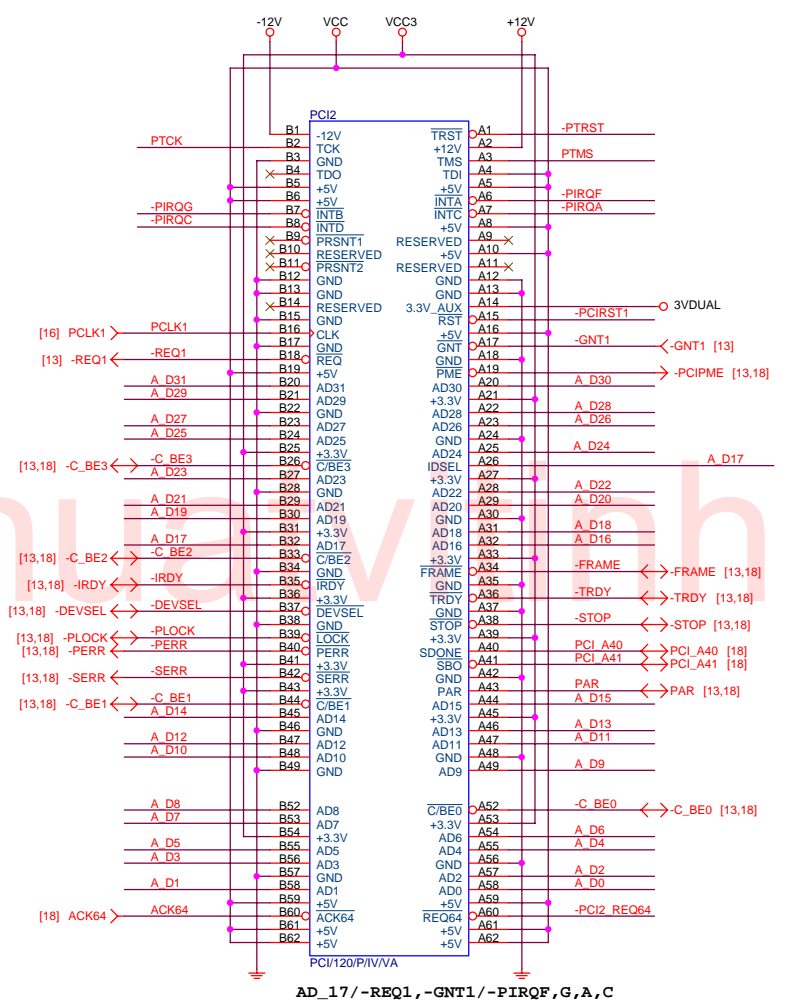
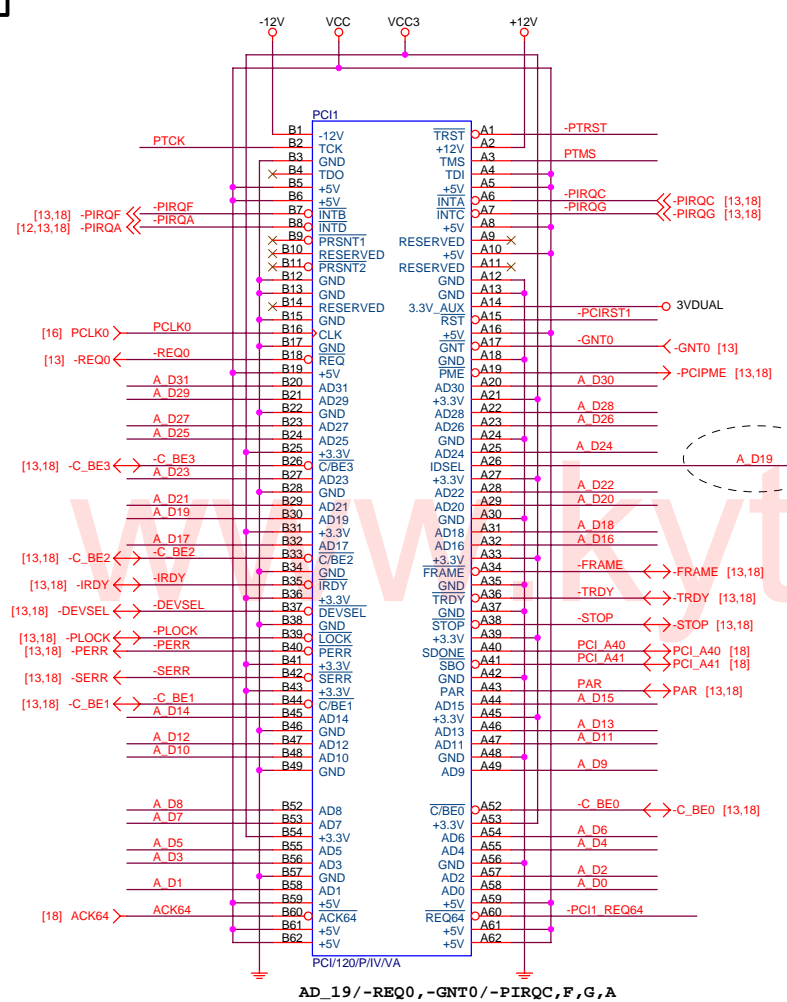


ICS952603/RTM363-205

FS_E	FS_3	FS_C	FS_B	FS_A	Clock
1	0	0	0	0	100MHz
1	0	0	0	1	133MHz
1	0	0	1	1	166MHz
1	0	0	1	0	200MHz

GIGABYTE			
Title CLOCK GENERATOR			
Size	Document Number	Rev	
Custom	81865GME	1.01	
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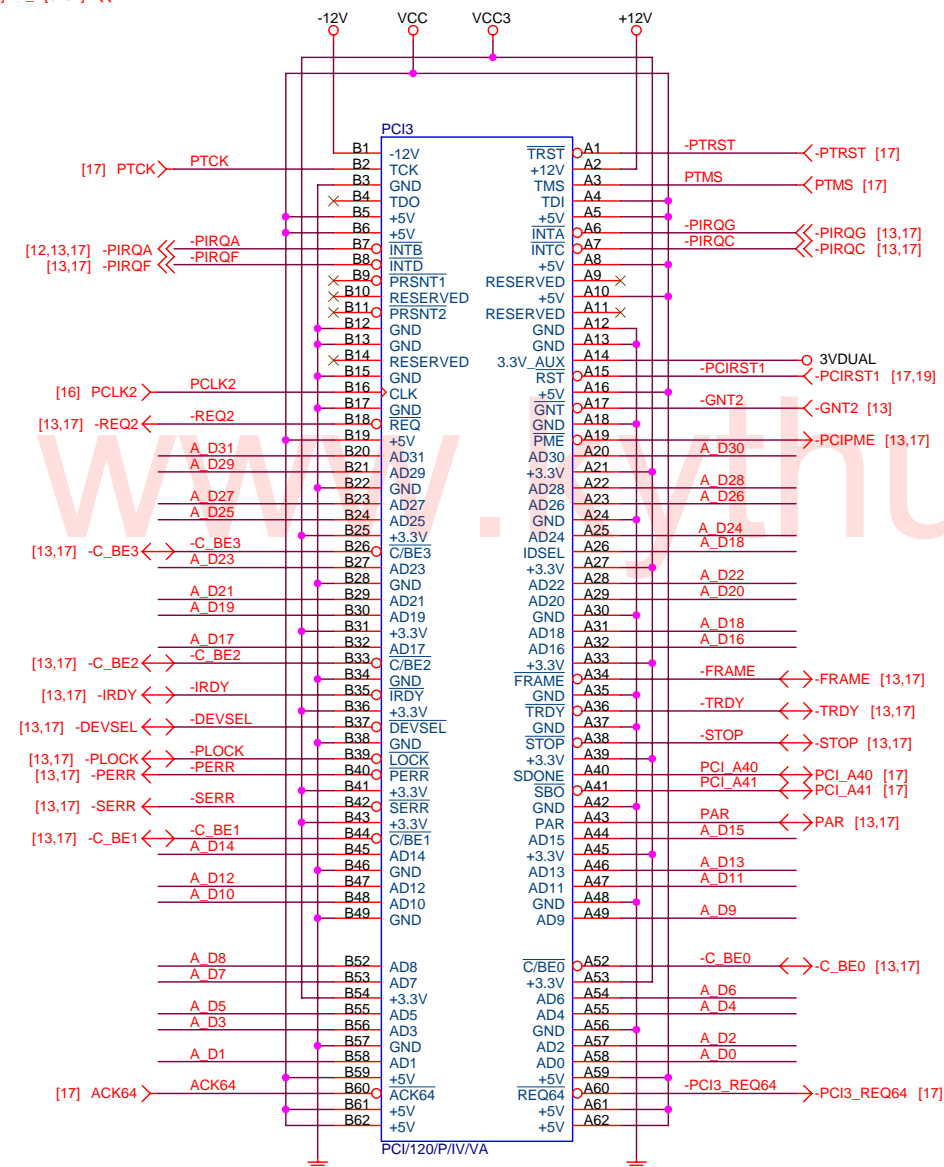
PCI1,2



GIGABYTE		
Title PCI SLOT 1/2		
Size Custom	Document Number 81865GME	Rev 1.01
Date: 星期四, 五月 04, 2006	Sheet 17	of 29

PCI3

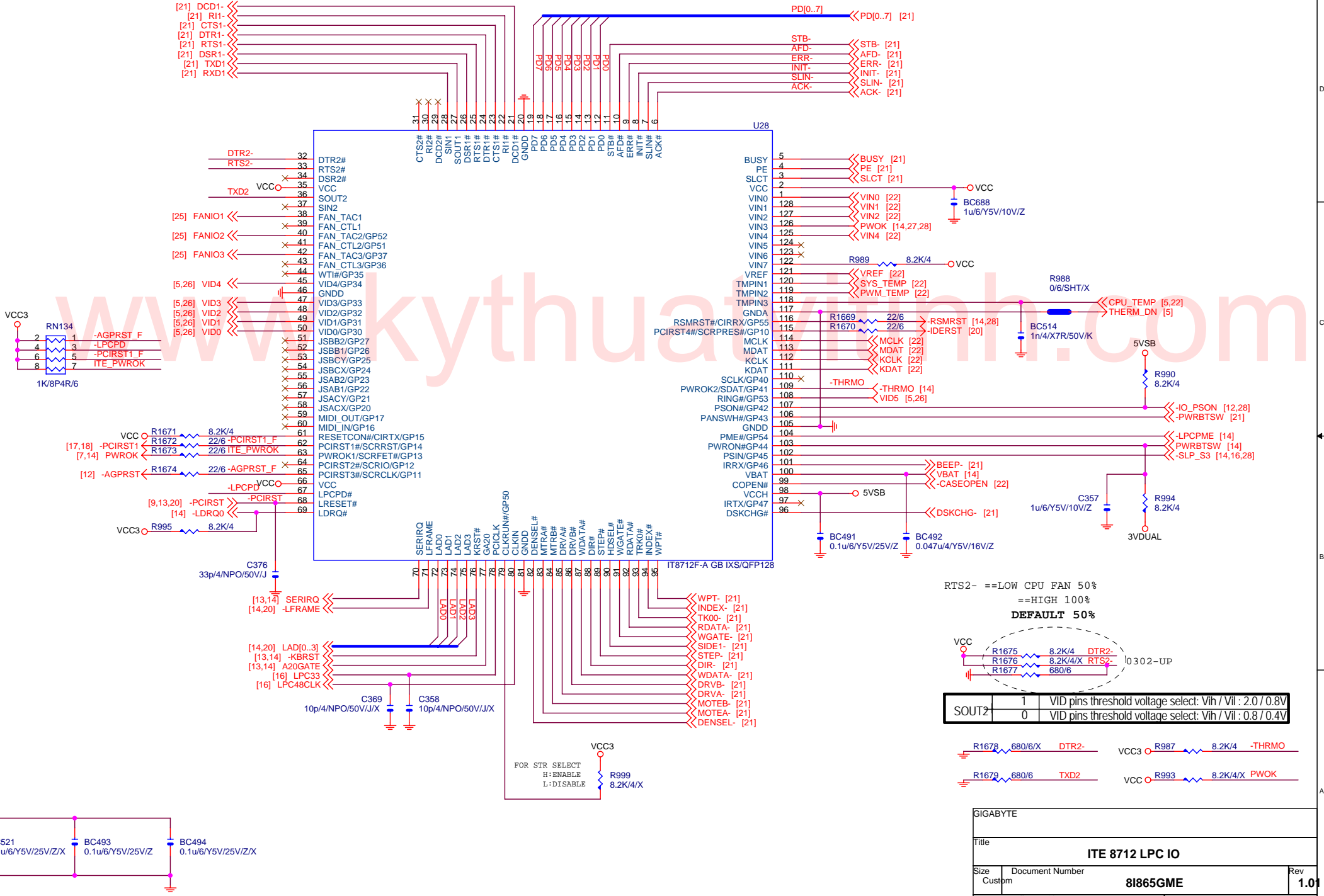
[13,17] A_D[0..31] << A D[0..31]



AD_18 / -REQ2, -GNT2 / -PIRQG, A, C, F

GIGABYTE			
Title			
PCI SLOT 3			
Size	Document Number	Rev	
Custom	81865GME	1.01	
Date:	星期四, 五月 04, 2006	Sheet	18 of 29

SUPER I/O

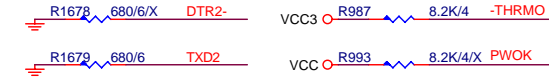


RTS2- ==LOW CPU FAN 50%
 ==HIGH 100%
DEFAULT 50%

VCC

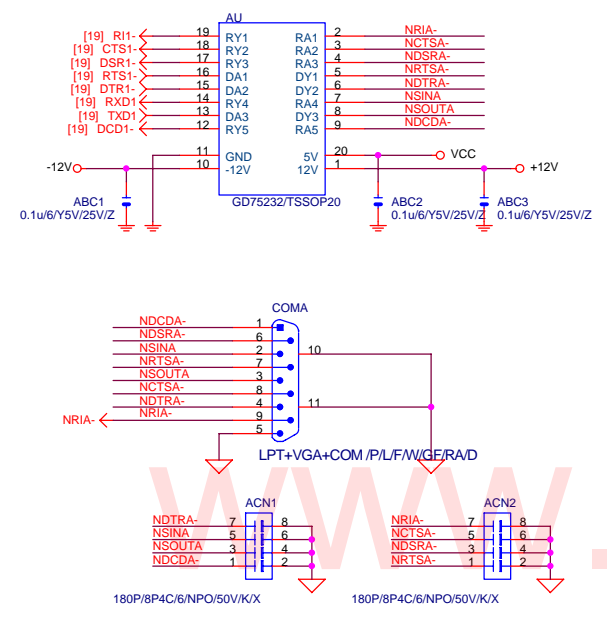
R1675 8.2K/4 DTR2-
 R1676 8.2K/4/X RTS2-
 R1677 680/6

SOUT2	1	VID pins threshold voltage select: Vih / Vil : 2.0 / 0.8V
	0	VID pins threshold voltage select: Vih / Vil : 0.8 / 0.4V

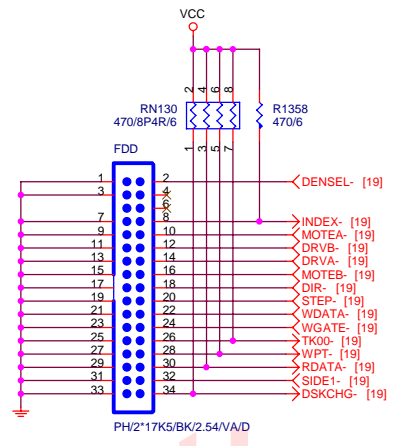


GIGABYTE			
Title			
ITE 8712 LPC IO			
Size	Document Number	Rev	
Custom	81865GME	1.01	
Date:	星期四, 五月 04, 2006	Sheet	19 of 29
	2		1

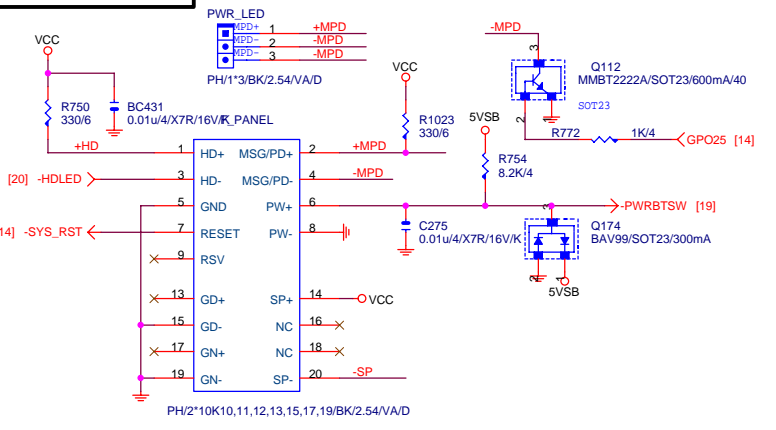
COMA



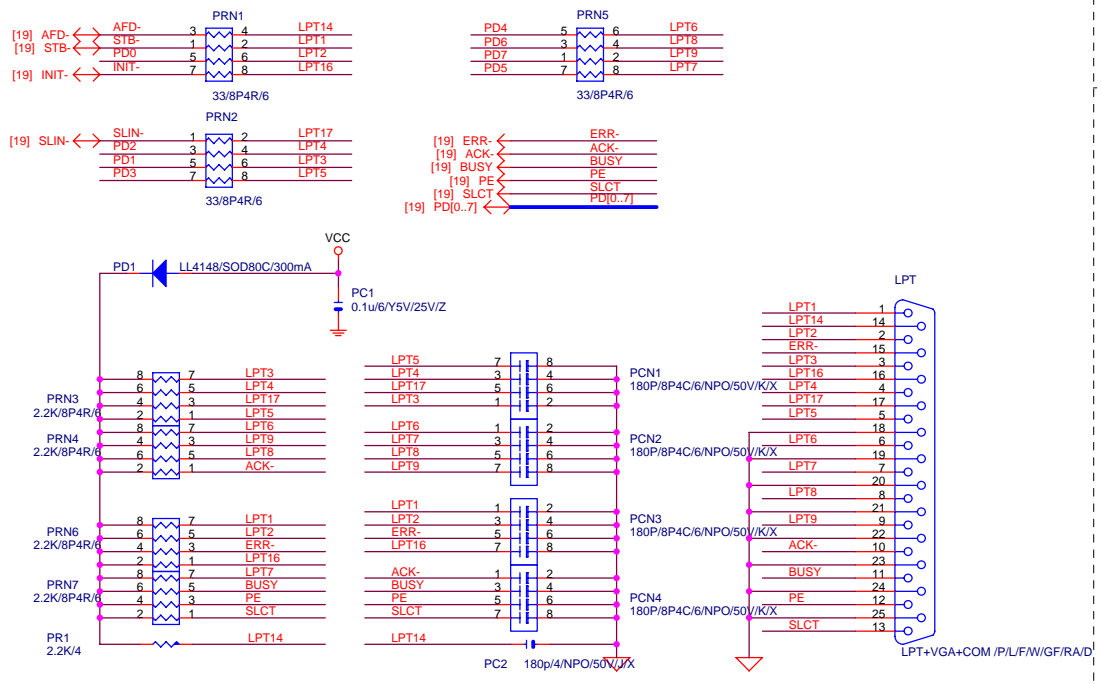
FLOPPY



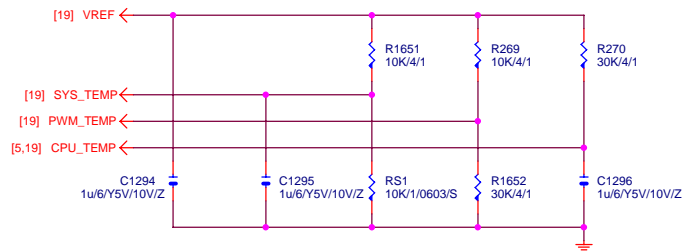
INTEL FRONT PANEL



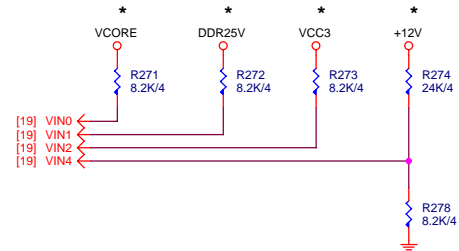
PRT PORT



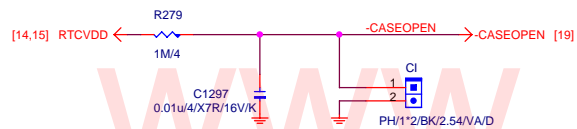
TEMP H/W MONITOR



VOLTAGE-- H/W MONITOR



CASE OPEN

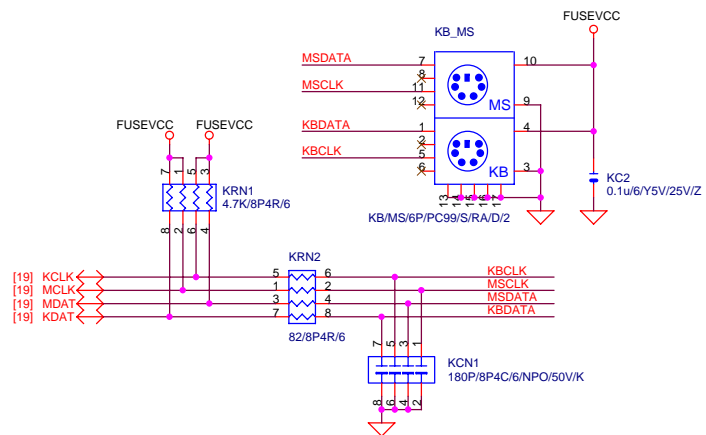


Case Open Circuits

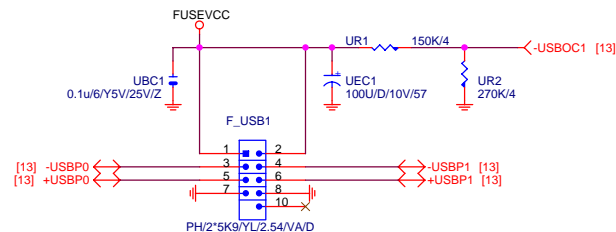
REAR USB



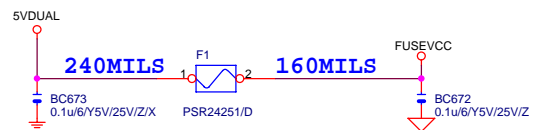
KB/MS



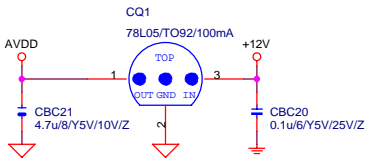
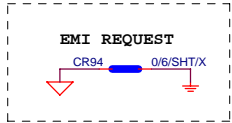
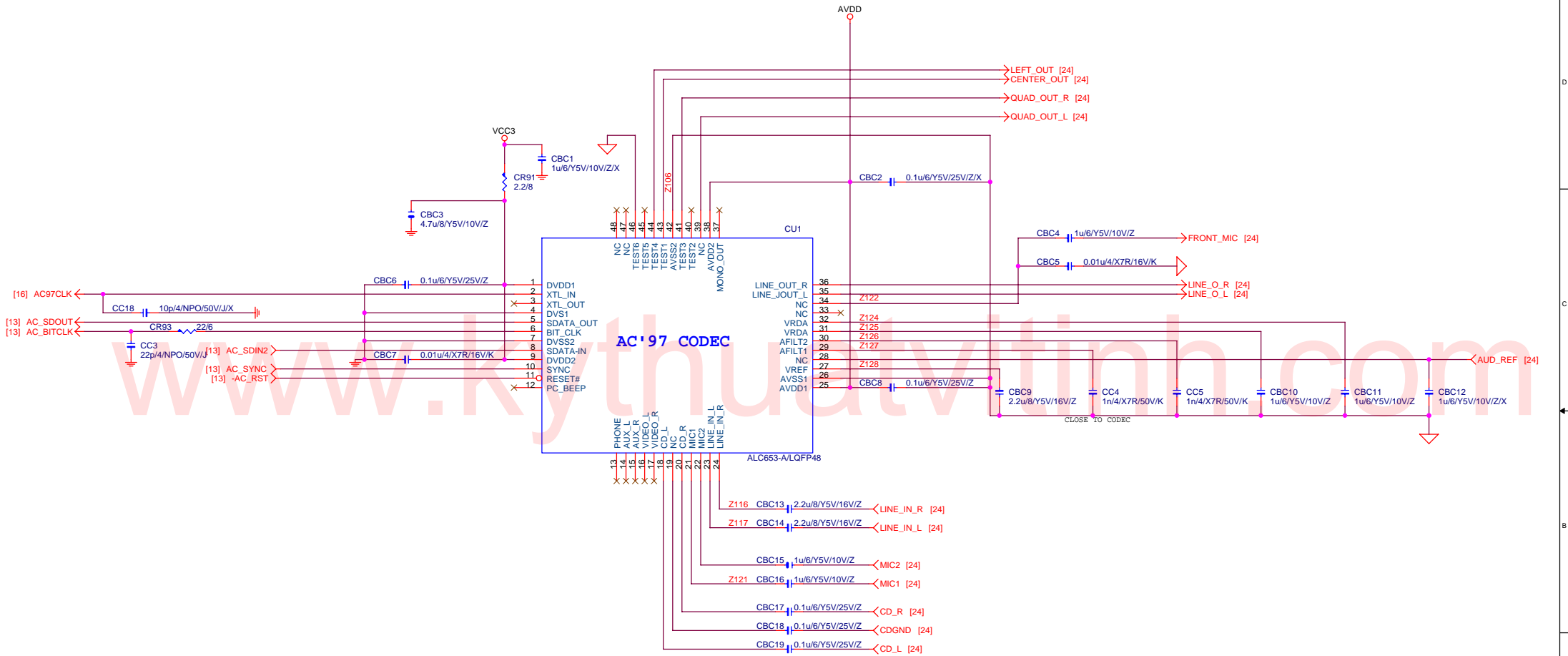
FRONT SIDE USB1



FUSEVCC, GAMEVCC

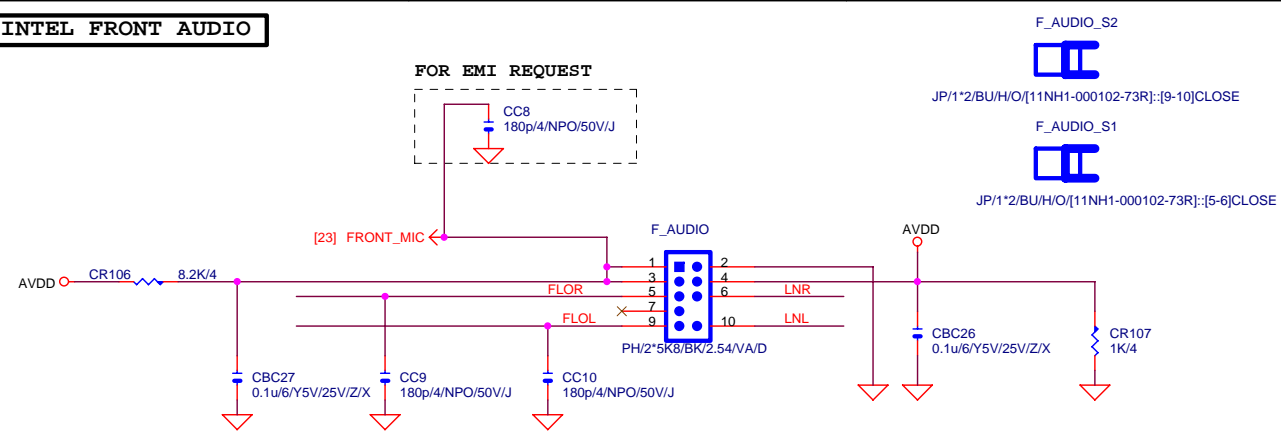


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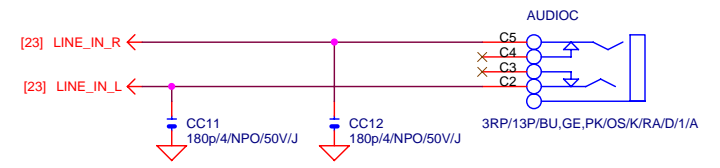


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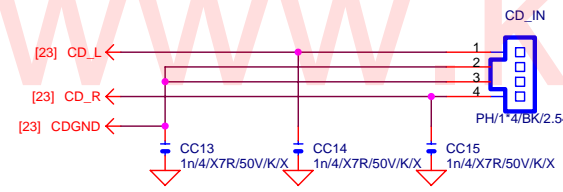
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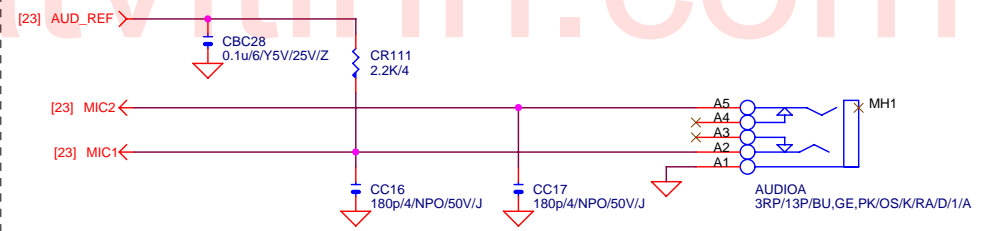
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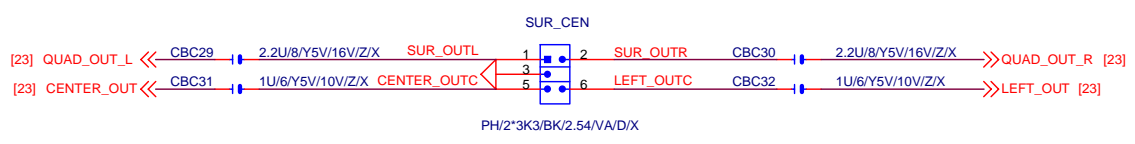
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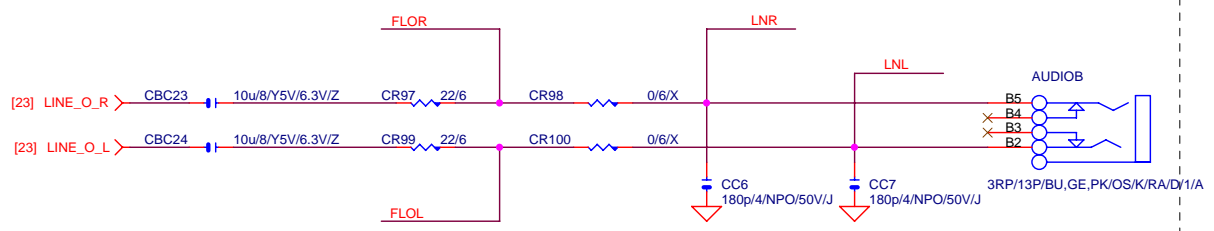
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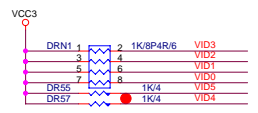
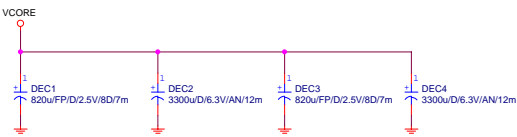
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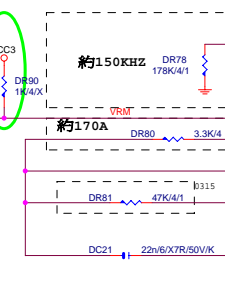
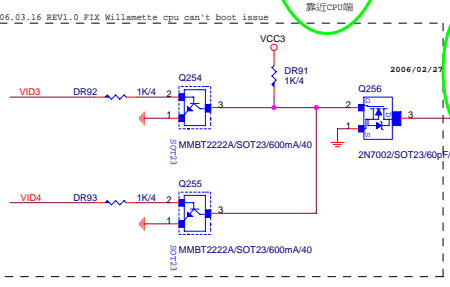
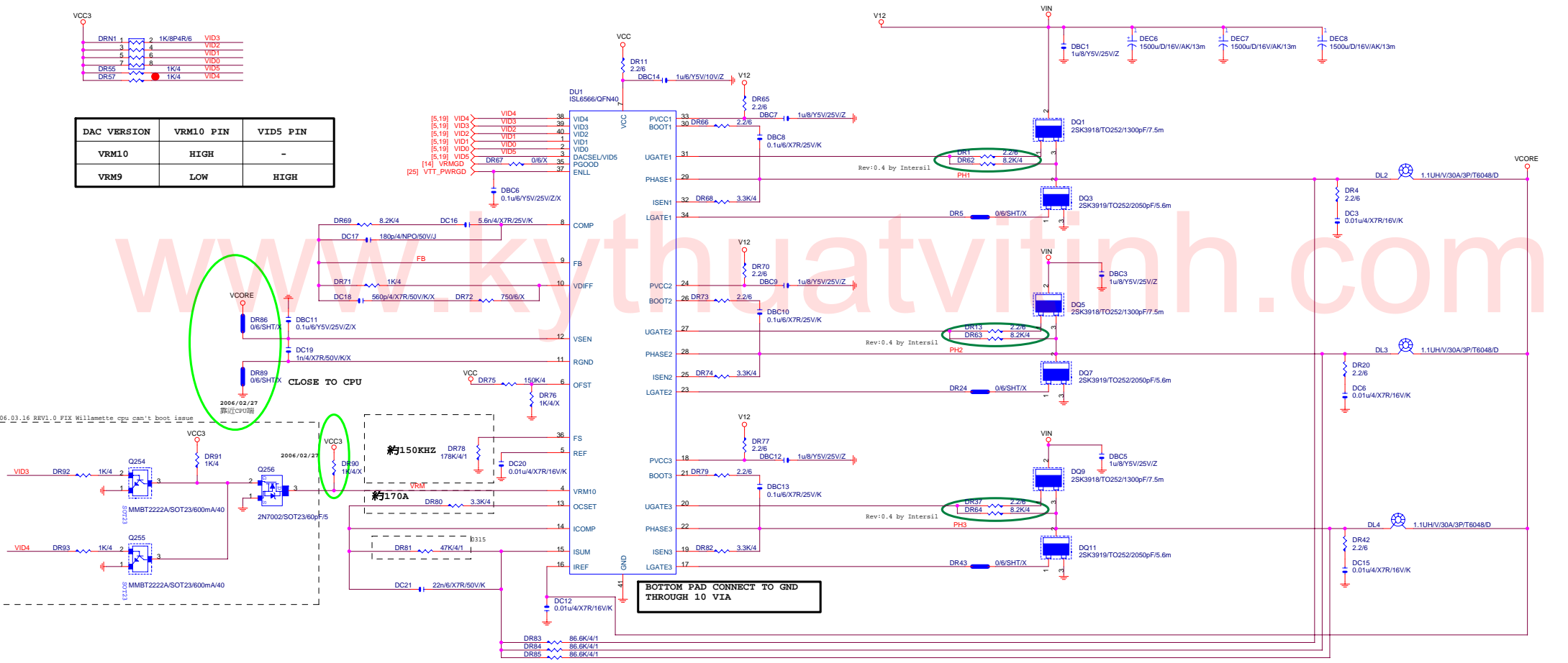
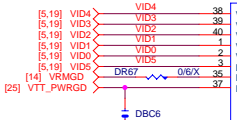
LINE OUT



GIGABYTE		
AUDIO OUTPUT		
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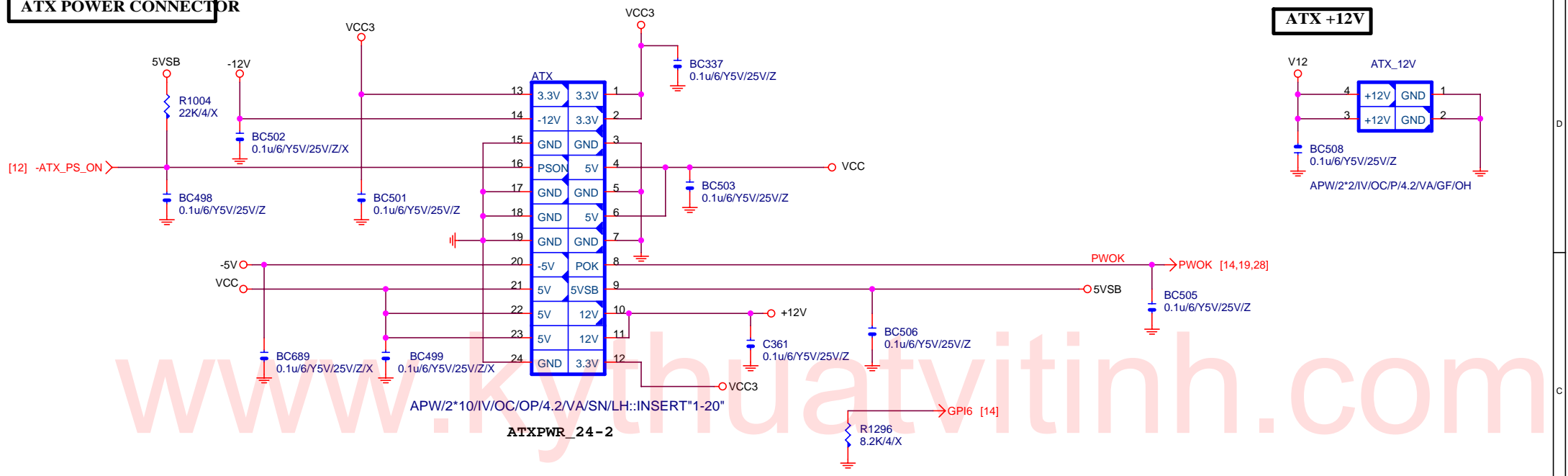
DAC VERSION	VRM10 PIN	VID5 PIN
VRM10	HIGH	-
VRM9	LOW	HIGH



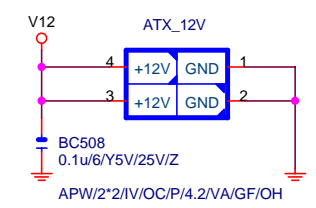
BOTTOM PAD CONNECT TO GND THROUGH 10 VIA

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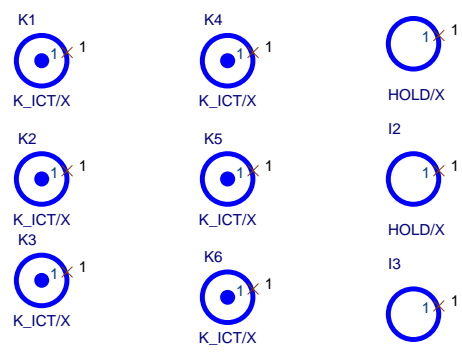
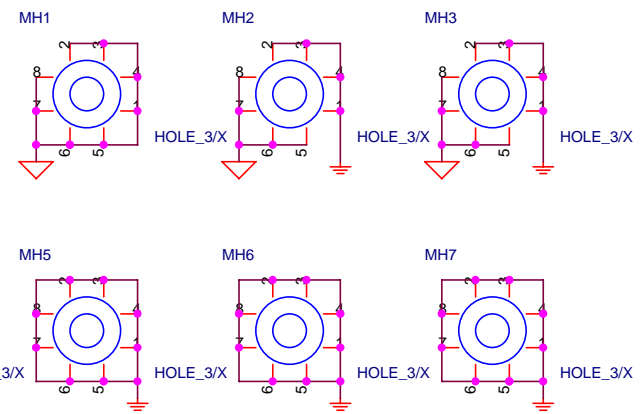
ATX POWER CONNECTOR



ATX +12V

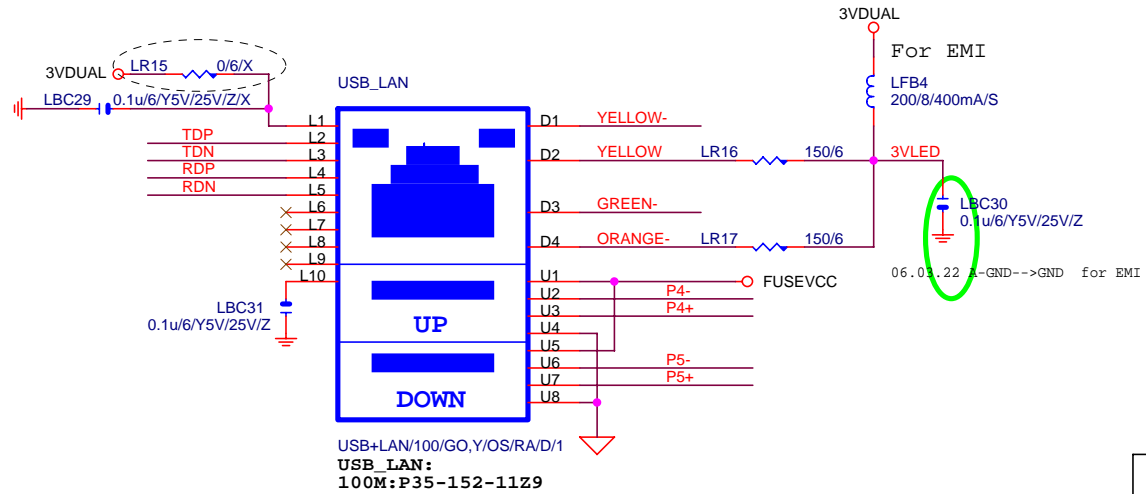
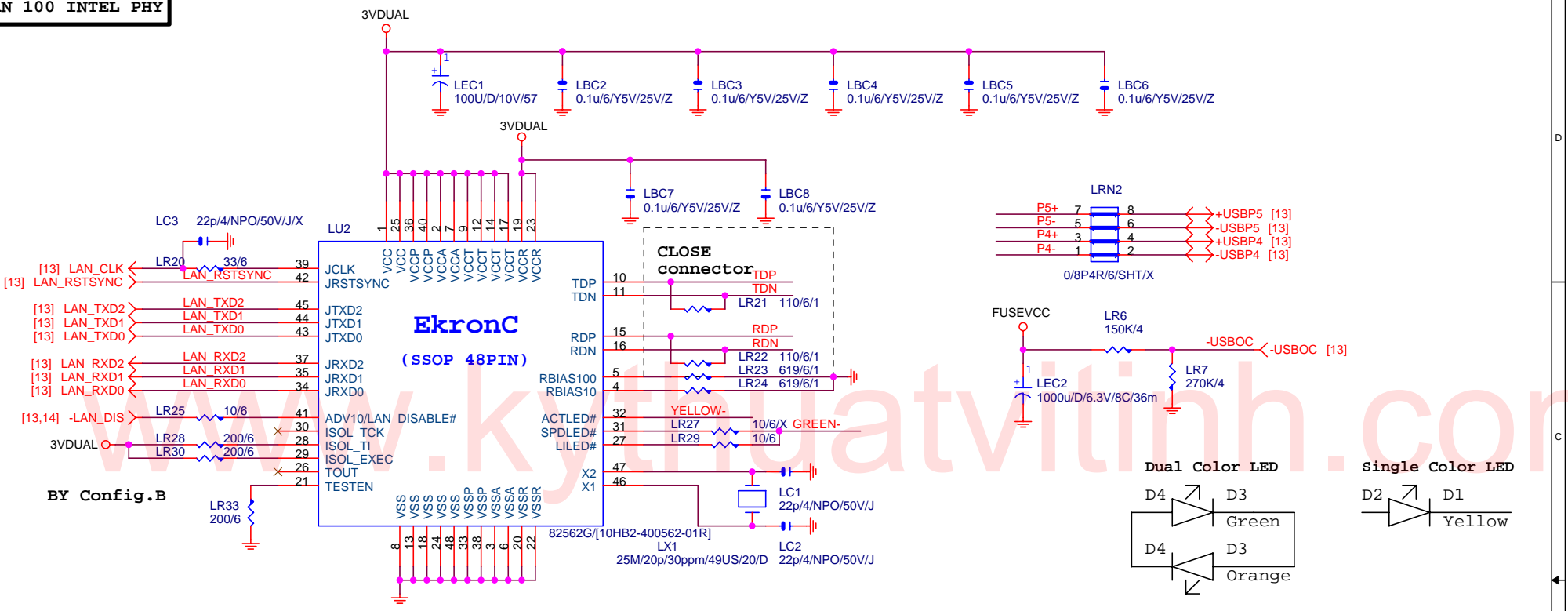


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LAN 100 INTEL PHY



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INTEL 82562G		
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