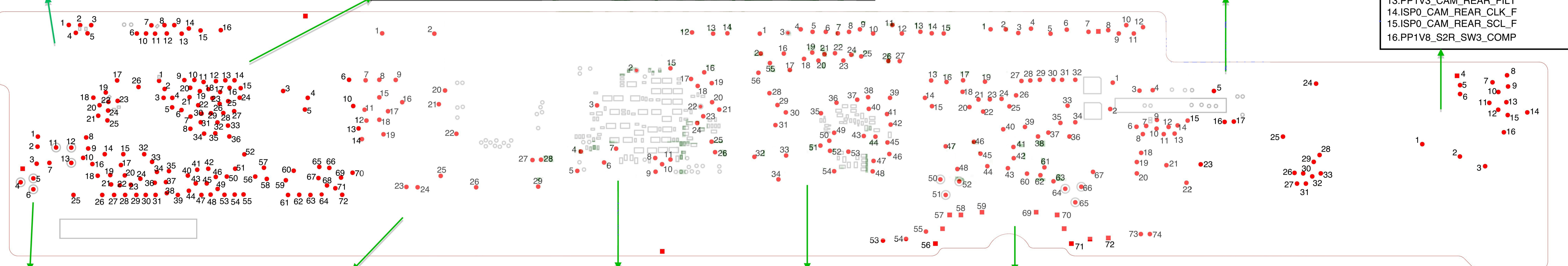


- 1.HSIC1\_WLAN2SOC\_DEVICE\_RDY
- 2.PMU\_GPIO\_BT\_REG\_ON\_R
- 3.PMU\_GPIO\_WLAN\_REG\_ON\_R
- 4.PMU\_GPIO\_BT\_REG\_ON
- 5.PMU\_GPIO\_WLAN\_REG\_ON
- 6.JTAG\_WLAN\_TMS\_TX\_BLANK
- 7.JTAG\_WLAN\_TDI\_OSCAR\_A
- 8.HSIC1\_SOC2WLAN\_HOST\_RDY\_R
- 9.JTAG\_WLAN\_SEL
- 10.TP\_JTAG\_WLAN\_TCK
- 11.TP\_JTAG\_WLAN\_TRST\_L
- 12.JTAG\_WLAN\_TDO\_OSCAR\_B
- 13.OSCAR2RADIO\_CONTEXT\_A
- 14.PMU\_GPIO\_WLAN\_HOST\_WAKE
- 15.VCC\_MAIN\_GRAPE\_RAMP
- 16.WLAN\_TX\_BLANK
- 17.PP3V3\_S2R
- 18.GPIO\_BT\_WAKE
- 19.SIM\_TRAY\_DETECT
- 20.UART2\_SOC2WLAN\_TX\_R
- 21.SIMCRD\_CLK\_CONN\_FILT
- 22.UART2\_WLAN2SOC\_TX\_R
- 23.UART\_BB2WLAN\_LTE\_COEX\_R
- 24.UART\_WLAN2BB\_LTE\_COEX\_R
- 25.BOARD\_TEMP3\_P
- 26.SIMCRD\_CLK\_CONN
- 7.CUMULUS\_M\_VDDCORE
- 8.CUMULUS\_M\_VDDANA
- 9.CUMULUS\_S\_VDDCORE
- 10.TP\_CUMULUS\_S\_H\_SDO
- 11.TP\_CUMULUS\_S\_H\_SDI
- 13.CUMULUS\_MS\_CK
- 14.CUMULUS\_S\_BCFG\_RTCK
- 16.JTAG\_CUMULUS\_S\_TMS
- 18.CUMULUS\_MS\_SD
- 19.TP\_CUMULUS\_S\_H\_SCLK
- 20.CUMULUS\_S\_VDDANA
- 22.PP5V25\_GRAPE
- 23.TP\_CUMULUS\_S\_H\_CS\_L
- 24.GPIO\_GRAPE2SOC\_IRQ\_L
- 25.PP1V8\_GRAPE\_SW
- 27.DISPLAY\_SYNC
- 29.CUMULUS\_M\_BCFG\_RTCK
- 30.SPI2\_GRAPE\_MOSI
- 31.TP\_JTAG\_CUMULUS\_M\_TDI
- 32.TP\_JTAG\_CUMULUS\_M\_TDO
- 33.SPI2\_GRAPE\_CS\_L
- 34.TP\_JTAG\_CUMULUS\_M\_TCK
- 35.SPI2\_GRAPE\_SCLK
- 36.JTAG\_CUMULUS\_M\_TMS
- 1&19&20&22&24&25&26&27&28&29&30&31&32&33.GND
- 2.PP1V8\_SW1\_FOREHEAD
- 3.PP\_RF1\_1V8\_DIG
- 4.GPIO\_SOC2BB\_WAKE\_MODEM
- 5.HSIC2\_SOC2BB\_HOST\_RDY
- 6.PS\_HOLD\_PMIC
- 7.BB\_JTAG\_TMS
- 8.DEBUG\_RST\_L
- 9.USB\_BB\_DEBUG\_N
- 10.USB\_BB\_DEBUG\_P
- 11.BB\_JTAG\_TRST\_L
- 12.BB\_JTAG\_TCK
- 13.BB\_JTAG\_TDI
- 14.BB\_JTAG\_TDO
- 15.BB\_JTAG\_RTCLK
- 16.PP\_SMPS1\_MSMC\_1V05
- 17.PP\_SMPS4\_RF2\_2V05
- 18.TP\_BB\_TEST\_MODE\_1
- 21.TP\_BB\_TEST\_MODE\_0
- 23.PP\_SMPS2\_RF1\_1V3
- 1&2&4&5&6.GND
- 3.PP3V0\_SENSOR\_PROX\_AD7149\_FILT
- 7.PP2V9\_AVDD\_CAM\_REAR\_FILT
- 8.PP2V6\_CAM\_REAR\_AF\_FILT
- 9.CAM\_REAR\_VSYNC
- 10.PP1V8\_CAM\_REAR\_FILT
- 11.ISP0\_CAM\_REAR\_SHUTDOWN\_L\_F
- 12.ISP0\_CAM\_REAR\_SDA\_F
- 13.PP1V3\_CAM\_REAR\_FILT
- 14.ISP0\_CAM\_REAR\_CLK\_F
- 15.ISP0\_CAM\_REAR\_SCL\_F
- 16.PP1V8\_S2R\_SW3\_COMP



- 1.SIM\_TRAY\_DETECT\_FILT
- 2.SIMCRD\_RST\_CONN\_FILT
- 3.PP3V0\_S2R\_NAVAJA\_FILT
- 4&5&6.PPVBUS\_E75\_USB\_CONN
- 7.PMU\_GPIO\_MB\_HALL1\_IRQ
- 8.SIMCRD\_IO\_CONN\_FILT
- 9.PP\_LDO6\_RUIM\_1V8\_FILT
- 10.PMU\_GPIO\_MB\_HALL2\_IRQ\_FILT
- 11&12&13&14&60.GND
- 15.MAX983X4\_L1\_GAIN
- 16.PMU\_GPIO\_MB\_HALL2\_IRQ
- 17.SPKRAMP\_L1\_OUT\_P
- 18.LEFT\_CH\_OUT\_N
- 19.LEFT\_CH\_OUT\_P
- 20.SPKRAMP\_L1\_OUT\_N
- 21.MAX983X4\_L2\_GAIN
- 22.AUD\_SPKRAMP\_MUTE\_L
- 23.RIGHT\_CH\_OUT\_N
- 24.RIGHT\_CH\_OUT\_P
- 25.PPOUT\_E75\_ACC\_ID1\_CONN
- 26.SPKRAMP\_L2\_OUT\_N
- 27.SPKRAMP\_L2\_OUT\_P
- 28.PPOUT\_E75\_ACC\_ID2\_CONN
- 29.E75\_DPAIR2\_CONN\_N
- 30.E75\_DPAIR2\_CONN\_P
- 31.SPKRAMP\_R2\_OUT\_P
- 32.MAX983X4\_R1\_GAIN
- 33.SPKRAMP\_R1\_OUT\_P
- 34.SPKRAMP\_R1\_OUT\_N
- 35.MAX983X4\_R2\_GAIN
- 36.SPKRAMP\_R2\_OUT\_N
- 37.E75\_ACC\_DET\_CONN\_L
- 38.E75\_DPAIR1\_CONN\_N
- 39.E75\_DPAIR1\_CONN\_P
- 40.LED\_IO\_1\_B
- 41.LED\_IO\_2\_B
- 42.LED\_IO\_4\_B
- 43.LED\_IO\_3\_B
- 44.LED\_IO\_6\_A
- 45.LED\_IO\_5\_B
- 46.LED\_IO\_6\_B
- 47.LED\_IO\_5\_A
- 48.LED\_IO\_4\_A
- 49.LED\_IO\_3\_A
- 50.PPVCC\_MAIN\_LCD\_SW\_CONN
- 51.PPVCC\_MAIN\_LCD\_SW
- 52.CLK\_32K\_SOC2CUMULUS
- 53.LED\_IO\_2\_A
- 54.LED\_IO\_1\_A
- 55.EDP\_HPD\_EMI\_CONN
- 56.PPLED\_BACK\_REG\_A
- 57.PPLED\_BACK\_REG\_B
- 58.PPLED\_OUT\_A

- 1.FMIO\_CLE
- 2.BOARD\_TEMP5\_P
- 3.GPIO\_BTN\_HOME\_L
- 4.PPVREF\_FMI\_NAND
- 5.PP1V8\_EXT\_SW
- 6.FMIO\_RE\_L
- 7.FMIO\_CE0\_L
- 8.FMIO\_WE\_L
- 9.FMIO\_ALE
- 10.FMIO\_AD<7>
- 11.FMIO\_DQS
- 12.FMIO\_AD<6>
- 13.FMIO\_AD<5>
- 14.FMIO\_AD<4>
- 15.FMIO\_AD<0>
- 16.FMI1\_AD<0>
- 17.FMIO\_AD<1>
- 18.FMIO\_AD<2>
- 19.FMIO\_AD<3>
- 20.PP1V2\_S2R
- 21.PP1V2\_SW1
- 22.PP3V3\_SW
- 23.I2C2\_SDA\_1V8
- 24.VCC\_MAIN\_PP3V3SW\_RAMP
- 25.PP1V8\_SW1
- 26.I2C2\_SCL\_1V8
- 27.JTAG\_SOC\_TDI
- 28.JTAG\_SOC\_SEL
- 29.UART6\_TS\_ACC\_TXD

- 1.WDOG\_SOC
- 2.SOC\_TST\_CLKOUT
- 3.PP1V2\_SW1
- 4.JTAG\_SOC\_TRST\_L
- 5.TP\_JTAG\_SOC\_TDO
- 6.PPVREF\_FMI\_SOC
- 7&10&11&13&14.GND
- 8.HSIC1\_WLAN2SOC\_REMOTE\_WAKE
- 9.UART6\_TS\_ACC\_RXD
- 12.SOC\_TESTMODE
- 15.UART1\_SOC2BT\_TX
- 16.TP\_GPIO\_DFU\_STATUS
- 17.SPI2\_GRAPE\_MISO
- 18.SPI3\_CODECS\_CS\_L
- 19.GPIO\_SOC2PMU\_KEEPACT
- 20.UART0\_SOC\_TXD
- 21.WDOG\_SOC2PMU\_RESET\_IN
- 22.SOC\_TST\_CPUSWITCH\_OUT
- 23.PMU\_GPIO\_BT\_HOST\_WAKE
- 24.SOCHOT0\_L
- 25.OSCAR\_TIME\_SYNC\_HOST\_INT
- 26.BOARD\_TEMP7\_P

- 1.GPIO\_TS2SOC2PMU\_INT
- 2.SPI\_OSCAR2COMPASS\_CS\_L
- 3.PP1V2\_SW1
- 4.GPIO\_SOC2BB\_RST\_L
- 5.PMU\_GPIO\_CLK\_32K\_OSCAR
- 6.GPIO\_SOC2OSCAR\_DBGEN
- 7.TP\_OSCAR\_P0\_22
- 8.UART4\_SOC2OSCAR\_TXD
- 9.SPI\_OSCAR\_MISO
- 10.GPIO\_SOC2OSCAR\_DBGEN\_R
- 11.PP3V0\_S2R\_SENSOR
- 12.PP1V2\_S2R\_SW2
- 13.GPIO\_BB2SOC\_GPS\_SYNC
- 14.OSCAR2RADIO\_CONTEXT\_B
- 15.PP1V8\_S2R\_SW3
- 16.SOCHOT0\_R\_L
- 17.PPVDD\_SRAM
- 18.PP3V0\_UVLO
- 19.PMU\_GPIO\_OSCAR2PMU\_HOST\_WAKE
- 20.PMU\_SHDWN
- 21.PP3V0\_S2R\_NAVAJA
- 22.UART4\_OSCAR2SOC\_RXD
- 23.PPVDD\_SOC
- 24.ACCEL2OSCAR\_INT1
- 25.GPIO\_OSCAR\_RESET\_L
- 26.PP2V9\_CAM
- 27.PPVBUS\_USB\_DCIN
- 28.PPVDD\_GPU
- 29.DWL\_AP\_CLK
- 30.SOCHOT1\_L
- 31.PPVDD\_CPU
- 32.PP1V0\_SOC
- 33.USB\_VBUS\_DETECT
- 34.PP1V8\_ALWAYS
- 35.BOARD\_TEMP2\_P
- 36.GPIO\_PMU2SOC\_IRQ\_L
- 37.PP3V0\_S2R\_TRISTAR
- 38.PP2V6\_CAM\_AF
- 39.VBUS\_PROT\_G
- 40.PMU\_TCAL
- 41.PP3V0\_ALS
- 42.PMU\_GPIO\_PMU2BBPMU\_RST\_L
- 43.PPVBUS\_PROT
- 44.BOARD\_TEMP8\_P
- 45.PMU\_VCENTER
- 46.BOARD\_TEMP6\_P
- 47.TP\_HV\_CHG\_EN
- 48.PPVCC\_MAIN
- 49.TP\_AMUX\_B3
- 50.TP\_AMUX\_BY
- 51.TP\_AMUX\_AY
- 52.TP\_AMUX\_A3
- 53.DWL\_AP\_DO
- 54.PP1V3\_CAM
- 55.I2C0\_SCL\_1V8
- 56.I2C0\_SDA\_1V8

- 1.GPIO\_SOC2BB\_WAKE\_MODEM
- 2.GPIO\_BTN\_VOL\_UP\_L
- 3.GPIO\_FORCE\_DFU
- 4.GPIO\_BTN\_VOL\_DOWN\_L
- 5.GPIO\_BTN\_SRL\_L\_FILT
- 6.PP3V0\_GYRO
- 7.SPI\_OSCAR2GYRO\_CS\_L
- 8.GPIO\_BTN\_VOL\_UP\_L\_FILT
- 9.SIMCRD\_IO\_CONN
- 10.SIMCRD\_RST\_CONN
- 11.GPIO\_BTN\_ONOFF\_L\_FILT
- 12.GPIO\_BTN\_VOL\_DOWN\_L\_FILT
- 13.GPIO\_HS4\_SHUNT\_EN
- 14.PP3V0\_SPARE1
- 15.PP6V0\_LCM\_VBOOST
- 16.GPIO\_BB2SOC\_RESET\_DET\_L
- 17.UART\_WLAN2BB\_LTE\_COEX
- 18&58.BATT\_NTC
- 19.GPIO\_SOC2BB\_RADIO\_ON\_L
- 20.GPIO\_BTN\_SRL\_L
- 21.GPIO\_BTN\_VOL\_ONOFF\_L
- 22.PA\_NTC\_P
- 23.PP\_LDO6\_RUIM\_1V8
- 24.GPIO\_CODECS2SOC\_IRQ\_L
- 25.SPI3\_CODECS\_MISO
- 26.SPI3\_CODECS\_SCLK
- 27.GPIO\_BB2SOC\_GSM\_TXBURST
- 28.ACCEL2OSCAR\_INT2
- 29.GYRO2OSCAR\_INT1
- 30.GYRO\_DEN
- 31.SPI\_OSCAR2ACCEL\_CS\_L
- 32.PP3V0\_ACCEL
- 33.PPBATT\_POS\_RC
- 34.AIN3P
- 35.DMIC1\_FF\_SCLK
- 36.AIN3N
- 37.SPI3\_CODECS\_MOSI
- 38.PMU\_GPIO\_CODECS\_HS\_INT\_L
- 39.L81\_SPEAKER\_VQ
- 40.GND
- 41.GND\_AUDIO\_CODECS
- 42&64&65&66&69.PPBATT\_VCC
- 43&57.BATT\_SWI\_CONN
- 44.PMU\_GPIO\_BB\_VBUS\_DET
- 45.PMU\_GPIO\_BB2PMU\_HOST\_WAKE
- 46.PP1V8\_S2R
- 47.PPLED\_OUT\_B
- 48.COMPASS2OSCAR\_INT
- 50&51&52&56&59&71&72.GND
- 53.UART\_BB2WLAN\_LTE\_COEX
- 54.MIKEY\_TS\_P
- 55.MIKEY\_TS\_N

- 1.PP3V0\_COMP
- 2.PP3V0\_S2R\_SENSOR
- 3.SPI\_OSCAR\_SCLK
- 4.SPI\_OSCAR\_MOSI
- 5.PP1V8\_COMP
- 6.I2C0\_CAM\_ALS\_SCL\_1V8\_F
- 7.GPIO\_CAM\_ALS2SOC\_IRQ\_L\_F
- 8.ISP1\_CAM\_FRONT\_SDA\_F
- 9.SIMCRD\_IO\_CONN
- 10.PP3V0\_ALS\_FILT
- 11.I2C0\_CAM\_ALS\_SDA\_1V8\_F
- 12.ISP1\_CAM\_FRONT\_CLK\_F
- 13.ISP1\_CAM\_FRONT\_SHUTDOWN\_L\_F
- 14.PP2V9\_AVDD\_CAM\_FRONT\_FILT
- 15.PP1V8\_CAM\_FRONT\_FILT
- 16.GPIO\_HS3\_SHUNT\_EN\_FILT
- 17.CONN\_HP\_HEADSET\_DET\_FILT
- 18.GPIO\_HS4\_SHUNT\_EN\_FILT
- 19.CONN\_HP\_HS4\_REF\_FILT
- 20.CONN\_HP\_HS3\_FILT
- 21.CONN\_HP\_RIGHT\_FILT
- 22.CONN\_HP\_LEFT\_FILT
- 23.CONN\_HP\_HS4\_FILT
- 24.CONN\_HP\_HS3\_REF\_FILT
- 25.BOARD\_TEMP4\_P
- 26.DMIC1\_FF\_SCLK\_FILT
- 27.DMIC1\_FF\_SD
- 28.DMIC1\_FF\_SD\_FILT
- 29.PP1V8\_DMIC\_FILT
- 30.GPIO\_HS3\_SHUNT\_EN

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- 59.PP1V8\_SW2
- 61.PP3V3\_ACC
- 62.PMU\_USB\_BRICKID
- 63.RESET\_SOC\_L
- 64.UART0\_SOC\_RXD
- 65.USB\_SOC\_P
- 66.USB\_SOC\_N
- 67.JTAG\_SOC\_TCK
- 68.USB\_BB\_P
- 69.TS\_E75\_ACC\_DET\_L
- 70.JTAG\_SOC\_TMS
- 71.USB\_BB\_N
- 72.PMU\_E75\_ACC\_DET\_L