

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	000080153	PRODUCTION RELEASED		2010-03-23

N90 SINGLE_BRD PVT

Fri Apr 16 15:07:25 2010

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H3P ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339S0123	339S0108	?	U52	H3P + ELFIDA 512MB
339S0126	339S0108	?	U52	H3P + HYNIX 512MB

L9 (STROBE) ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S1173	152S0979	?	L9	

VIDEO BUFFER ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
353S2684	353S2493	?	U9	INTERSIL BUFFER

G3_RF ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0371	197S0349	?	G3_RF	
197S0364	197S0349	?	G3_RF	

FL1_RF ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
155S0452	155S0435	?	FL1_RF	

G2_RF ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0334	197S0292	?	G2_RF	

Y1_RF ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0329	197S0299	?	Y1_RF	
197S0369	197S0299	?	Y1_RF	

Y2_RF ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0342	197S0297	?	Y2_PMU	

L2_PMU ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S1172	152S0928	?	L2_PMU	

L7_PMU ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S1051	152S0927	?	L7_PMU	

LCD BL DRIVER ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
376S0769	376S0768	?	Q1_PMU	LCD BL FET DRIVER

Y1_PMU ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0329	197S0299	?	Y1_PMU	
197S0369	197S0299	?	Y1_PMU	

PMU POWER IND ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
607-6810	607-6809	?	L1_L3_L16_L18_PMU	CYNTEC INDUCTORS
197S0369	197S0299	?	Y1_PMU	

SCH & PCB BOM CALLOUT

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-7921	1	N90_SCHEMATIC_TOP	SCH	Y	?
820-2548	1	N90_SINGLE_BOARD	PCB	Y	?

H3P BOM CALLOUT

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
339S0108	1	H3P WITH 512MB SAMSUNG DDR	U52	Y	?

BB MEMORY BOM CALLOUT

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
341S2509	1	BB_MEM_PROGRAMMED	U4_RF	Y	BB_MEM

N90 EEE BOM LABELS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
825-2029	1	EEE FOR 630-9941 (16G)	EEE_6LU	Y	GOOD_MURATA
825-2029	1	EEE FOR 639-0609 (32G)	EEE_D7F	Y	BETTER_MURATA
825-2029	1	EEE FOR 639-0607 (64G)	EEE_D7D	Y	BEST_MURATA
825-2029	1	EEE FOR 639-0648 (16G)	EEE_DFM	Y	GOOD_USI
825-2029	1	EEE FOR 639-0437 (32G)	EEE_BA0	Y	BETTER_USI
825-2029	1	EEE FOR 639-0608 (64G)	EEE_D7E	Y	BEST_USI

NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0682	1	16GB SAMSUNG 35NM FLASH WLG52	U1	Y	GOOD_MURATA
335S0665	1	32GB SAMSUNG 35NM FLASH TLGA52	U1	Y	BETTER_MURATA
335S0707	1	64GB SAMSUNG 35NM FLASH LGA52	U1	Y	BEST_MURATA
117S0161	2	64GB NAND CE7 CE8 JUMPERS	R101, R102	Y	BEST_MURATA
335S0682	1	16GB SAMSUNG 35NM FLASH WLG52	U1	Y	GOOD_USI
335S0665	1	32GB SAMSUNG 35NM FLASH TLGA52	U1	Y	BETTER_USI
335S0707	1	64GB SAMSUNG 35NM FLASH LGA52	U1	Y	BEST_USI
117S0161	2	64GB NAND CE7 CE8 JUMPERS	R101, R102	Y	BEST_USI

WIFI/BT BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
339S0092	1	MURATA WIFI_BT MODULE	U2_RF	Y	GOOD_MURATA
117S0161	1	MURATA ID RESISTOR	R47_RF	Y	GOOD_MURATA
339S0092	1	MURATA WIFI_BT MODULE	U2_RF	Y	BETTER_MURATA
117S0161	1	MURATA ID RESISTOR	R47_RF	Y	BETTER_MURATA
339S0092	1	MURATA WIFI_BT MODULE	U2_RF	Y	BEST_MURATA
117S0161	1	MURATA ID RESISTOR	R47_RF	Y	BEST_MURATA
339S0091	1	USI WIFI_BT MODULE	U2_RF	Y	GOOD_USI
117S0161	1	USI ID RESISTOR	R46_RF	Y	GOOD_USI
339S0091	1	USI WIFI_BT MODULE	U2_RF	Y	BETTER_USI
117S0161	1	USI ID RESISTOR	R46_RF	Y	BETTER_USI
339S0091	1	USI WIFI_BT MODULE	U2_RF	Y	BEST_USI
117S0161	1	USI ID RESISTOR	R46_RF	Y	BEST_USI

PMU POWER IND CALLOUT

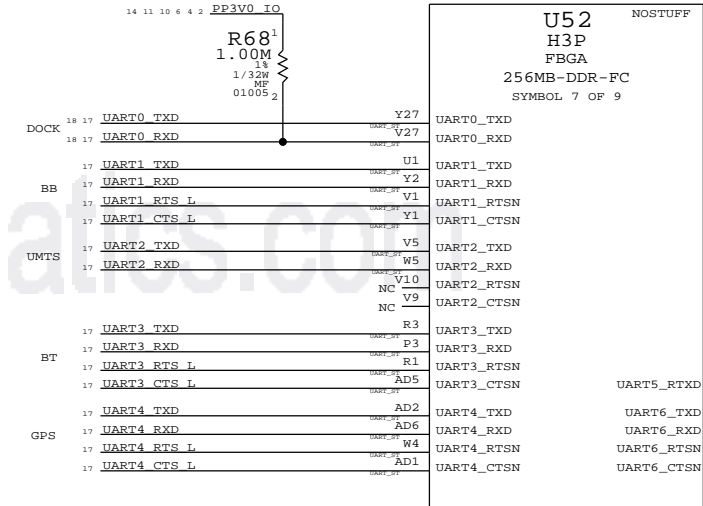
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
607-6809	1	POWER INDUCTORS	L1_L3_L16_L18_PMU	Y	?

MCO 056-3008
 BRD 820-2548 / 820-2803
 SCH 051-7921
 BOM 639-0648 (16GB, USI)
 BOM 630-9941 (16GB, MURATA)
 BOM 639-0437 (32GB, USI)
 BOM 639-0609 (32GB, MURATA)
 BOM 639-0608 (64GB, USI)
 BOM 639-0607 (64GB, MURATA)

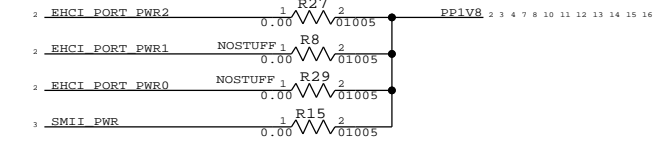
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U52 H3P 256MB-DDR-FC FBGA SYMBOL 6 OF 9		NOSTUFF	
13 11 MENU KEY_BUFF_L	F6	GPIO0	
18 16 13 11 HOLD KEY_L	B6	GPIO1	
18 13 10 VOL UP_L	B7	GPIO2	
18 13 10 VOL_DWN_L	D9	GPIO3	
18 13 11 10 RINGER_A	D5	GPIO4	
12 GYRO_INT2	A6	GPIO5	
17 WLAN_RESET_L	A7	GPIO6	
17 BT_WAKE	B9	GPIO7	
17 BT_RESET_L	E6	GPIO8	
17 RADIO_ON	A9	GPIO9	
17 BB_RESET_L	C5	GPIO10	
17 BB_RESET_DET_L	C9	GPIO11	
17 IPC_SRDY	C6	GPIO12	
17 PMU_IRO_L	D6	GPIO13	
9 CODEC_RESET_L	F5	GPIO14	
NC - DIF_MIC_SEL	E5	GPIO15	
13 AUDIENCE_RESET_L	F9	GPIO16	
9 JTAG_TRSTN_GPIO	F3	GPIO17	
9 CODEC_INT_L	F4	GPIO18	
11 KEEPFACCT	C10	GPIO19	
15 GRAPE_RESET_L	F10	GPIO20	
18 NIMBUS_INT_L	E10	GPIO21	
15 LCD_RESET_L	A4	GPIO22	
15 LCD_HIFPA	B4	GPIO23	
10 COMPASS_BRD_INT	E4	GPIO24	
NC - GPIO25_CONFIG1	E2	GPIO25	
14 FORCE_DFU	D10	GPIO26	
NC - DFU_STATUS	E9	GPIO27	
GPIO28_CONFIG2	L2	GPIO28	
GPIO29_CONFIG3	G1	GPIO29	
GPS_RESET_AP_L	G2	GPIO30	
GPS_STANDBY_AP_L	F3	GPIO31	
GPS_INTR_L	R3	GPIO32	
GYRO_INT1	A3	GPIO33	
CAM_VGA_SHUTDOWN_L	B2	GPIO34	
ACCEL_INT1_L	H2	GPIO35	
ALS_INT_L	H1	GPIO37	
ACCEL_INT2_L	E1	GPIO38	
VIDEO_AMP_EN_3V0	W24	GPIO_3V0	
NC - SEL_SECURE_BOOT_3V0	W25	GPIO_3V1	

TODO: DO WE NEED A SERIES R HERE (THERE IS ONE ON DEV2)



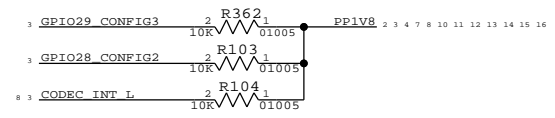
```
BOARD_REV[3:0] = {EHCI_PORT_PWR2, EHCI_PORT_PWR1, EHCI_PORT_PWR0, SMII_PWR}
1111 PROTO0
1110 PROTO1
1101 PROTO2
1100 EVT1
1011 EVT2
1010 EVT2 COMBUTON
1001 DVT <---SELECTED
```



```
BOARD_ID[0:2] = {SPI0_SCLK, SPI0_MOSI, SPI0_MISO}
000 N90 MLB <---SELECTED
100 N90 DEV
010 XXX AP
110 XXX DEV
001 XXX AP
101 XXX DEV
```

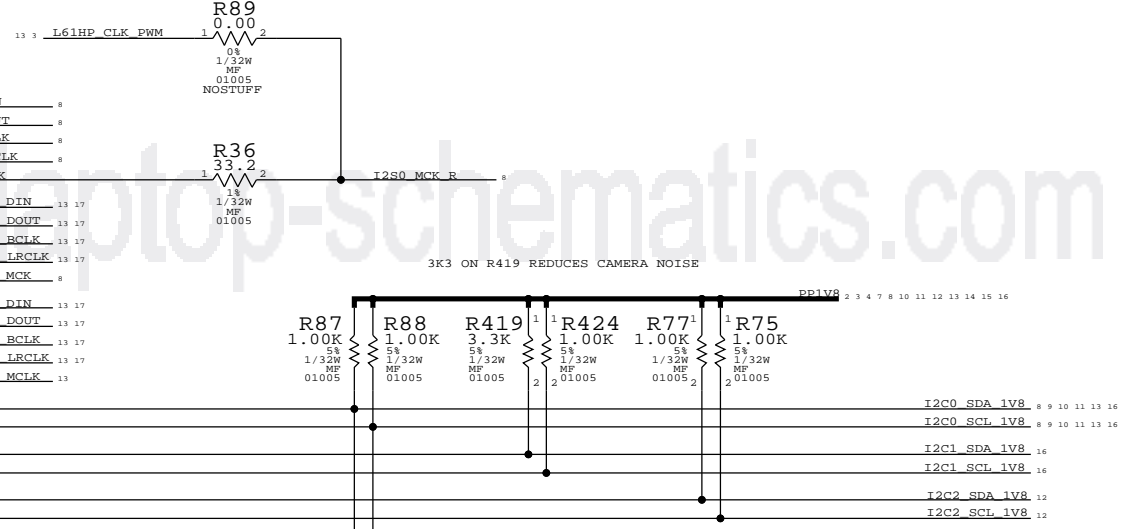
NOTE: NO COMPONENTS REQIOERD!

```
FLOAT=LOW, PULLUP=HIGH
BOOT_CONFIG[3:0] = {GPIO29_CONFIG3, GPIO28_CONFIG2, GPIO25_CONFIG1, CODEC_INT_L}
0000 SPI0
0001 SPI3
0010 SPI0 W/TEST
0011 SPI3 W/TEST
0100 FMIO 2CS
0101 FMIO 4CS
0110 FMIO 4CS W/TEST
0111 RESERVED
1000 FMIO 2 CS
1001 FMIO 4 CS
1010 FMIO 4CS W/TEST
1100 FMIO/1 2/2 CS
1101 FMIO/1 4/4 CS <---SELECTED
1110 FMIO/1 4/4 CS W/TEST
1111 RESERVED
```



U52 H3P FBGA 256MB-DDR-FC SYMBOL 5 OF 9		NOSTUFF	
17 WLAN_SDIO_DATA<3:0>	J24	SDIO0_DATA0	
	J25	SDIO0_DATA1	
	J26	SDIO0_DATA2	
	J27	SDIO0_DATA3	
17 WLAN_SDIO_CMD	K25	SDIO0_CMD	
17 WLAN_SDIO_CLK	J21	SDIO0_CLK	
	V4	SPI0_SCLK	
	W7	SPI0_MOSI	
	V6	SPI0_MISO	
	W3	SPI0_SSIN	
18 SPI1_SCLK	SPI1_ST V3	SPI1_SCLK	
18 SPI1_MOSI	SPI1_ST W8	SPI1_MOSI	
18 SPI1_MISO	SPI1_ST U2	SPI1_MISO	
15 SPI1_CS_L	SPI1_ST V2	SPI1_SSIN	
17 SPI2_SCLK	SPI1_ST AA2	SPI2_SCLK	
17 SPI2_MOSI	SPI1_ST AA1	SPI2_MOSI	
17 SPI2_MISO	SPI1_ST V7	SPI2_MISO	
17 SPI2_MRDY	SPI1_ST W6	SPI2_SSIN	

** BACKUP IN CASE I2S0_MCK DOES NOT WORK



3K3 ON R419 REDUCES CAMERA NOISE

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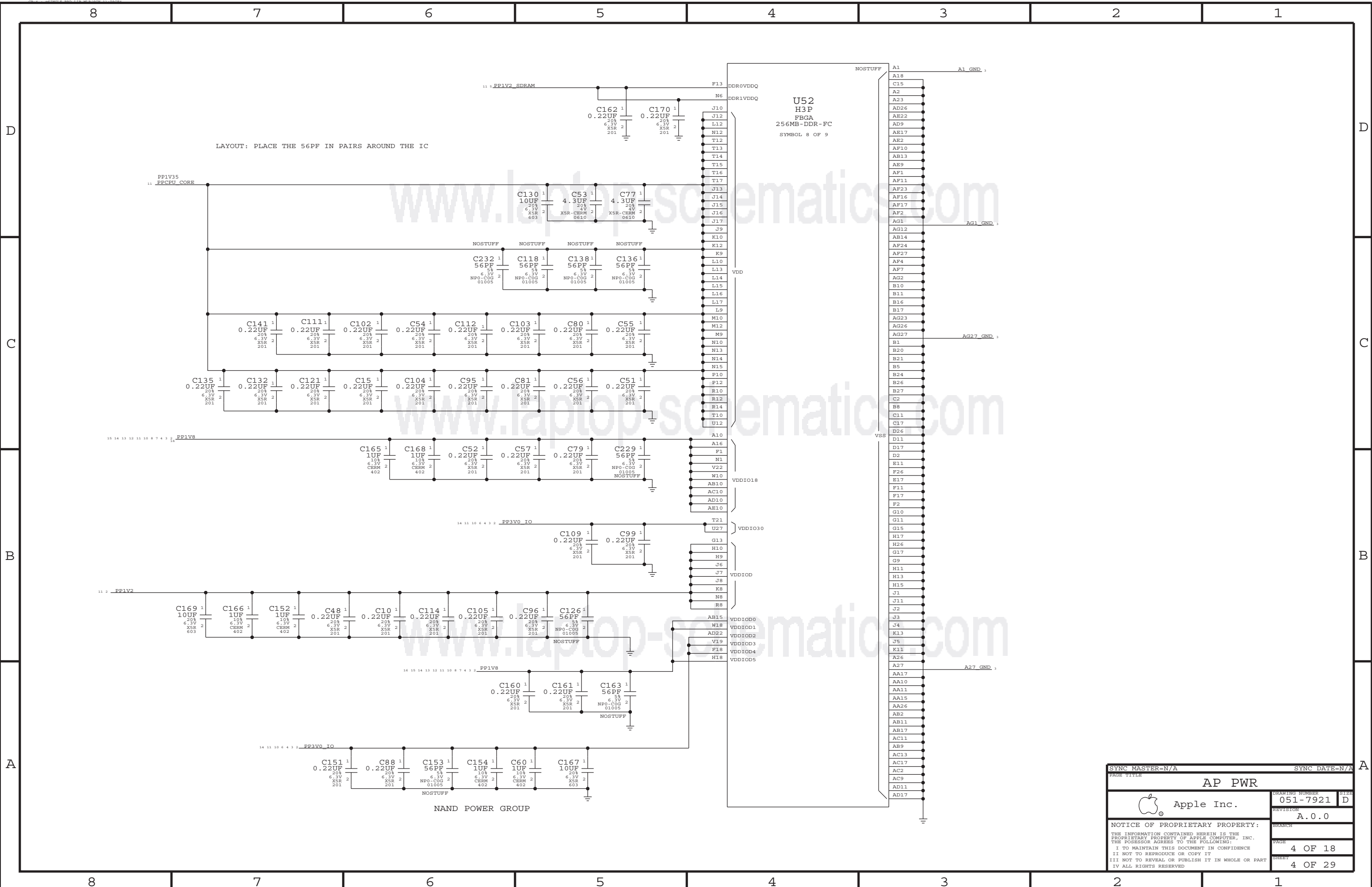
H3 GPIO, UART, SPI, I2S, I2C, SDIO

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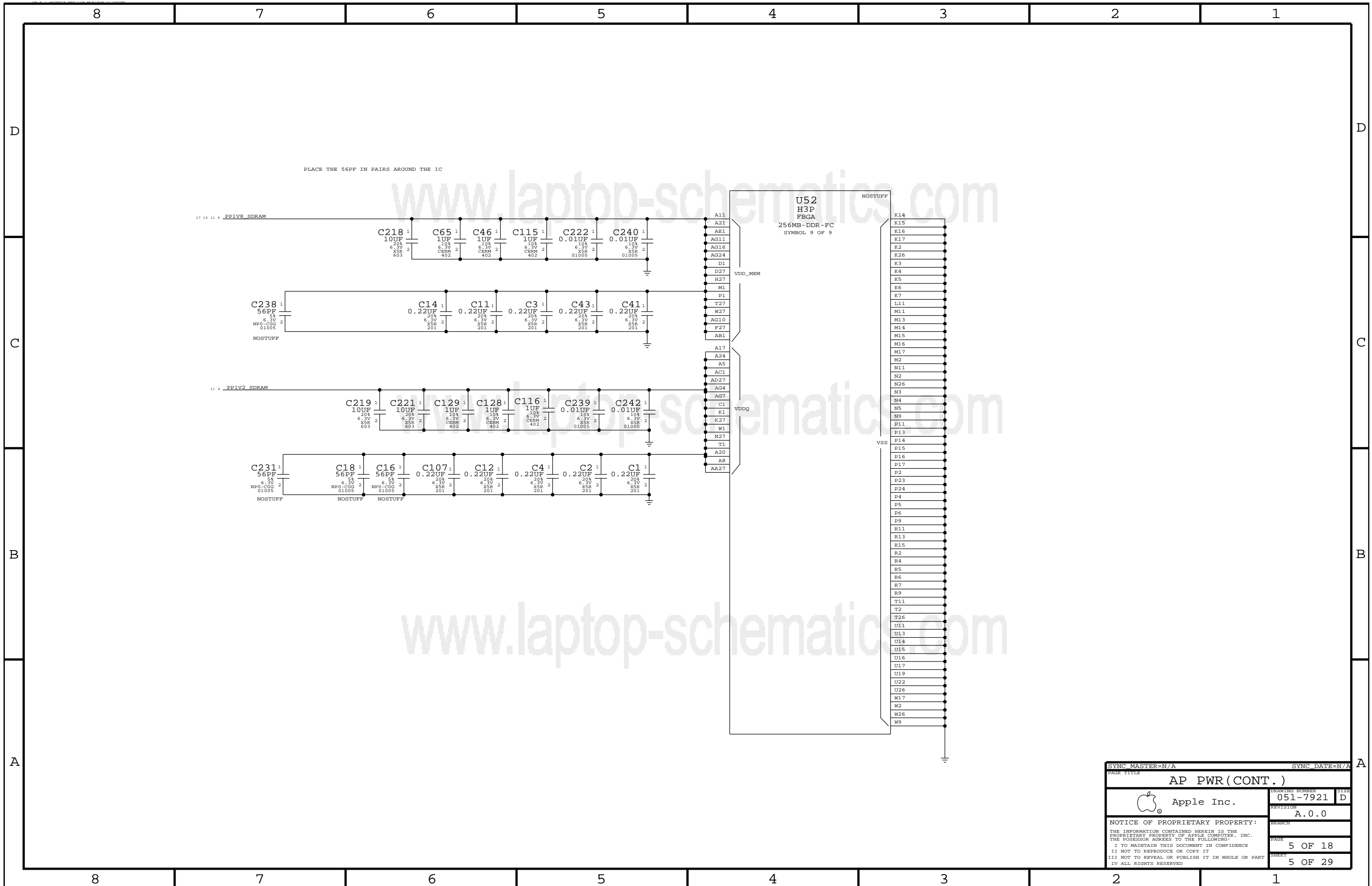
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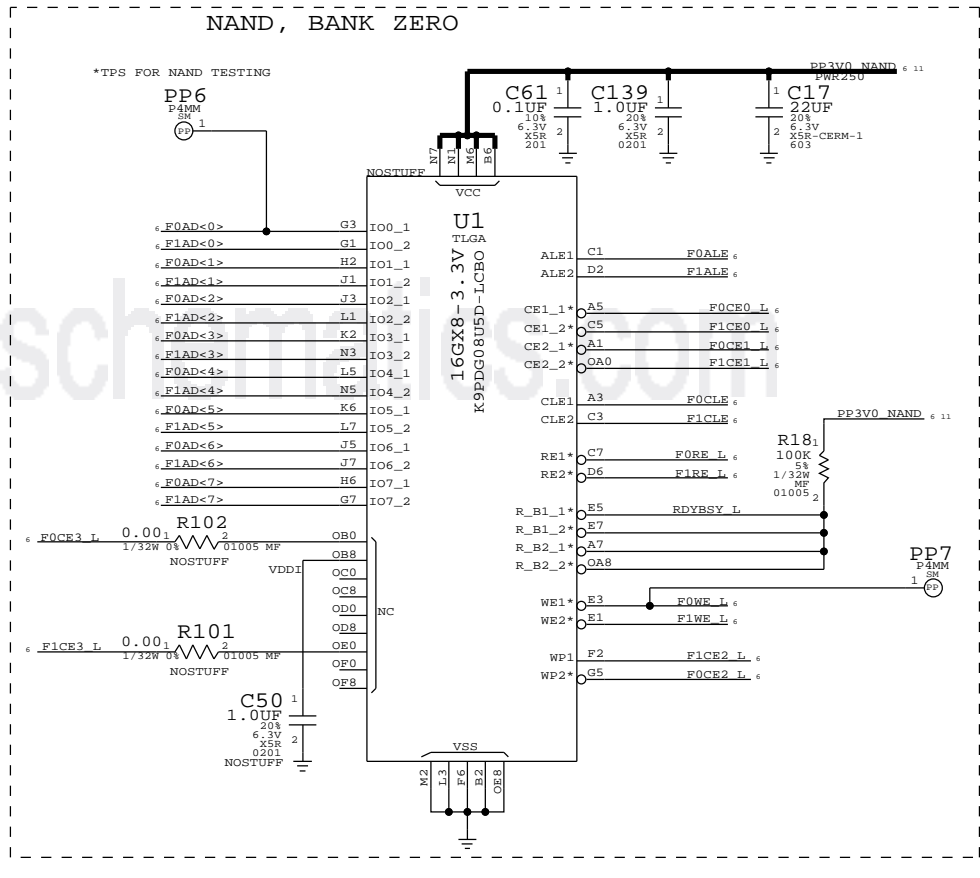
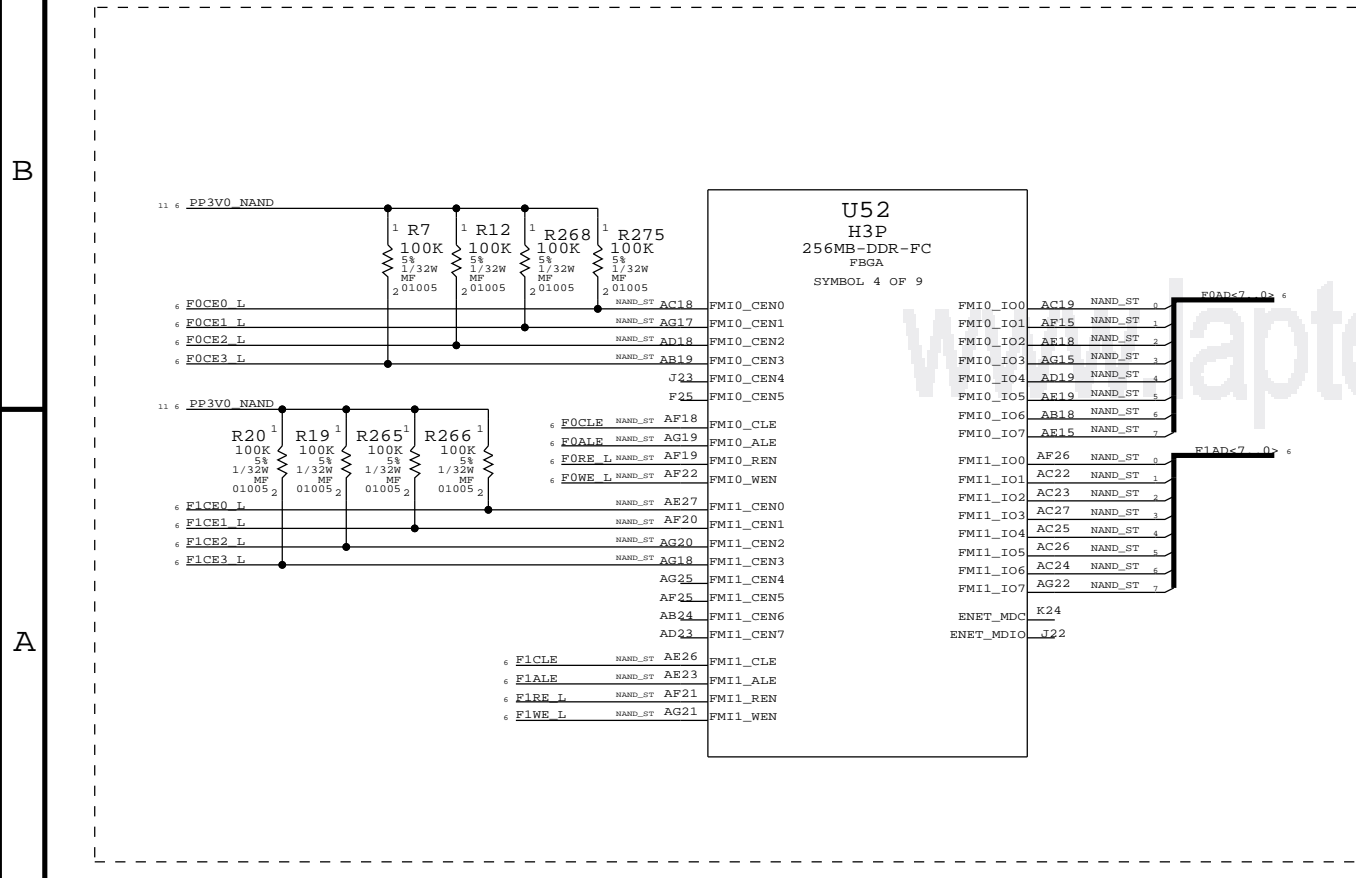
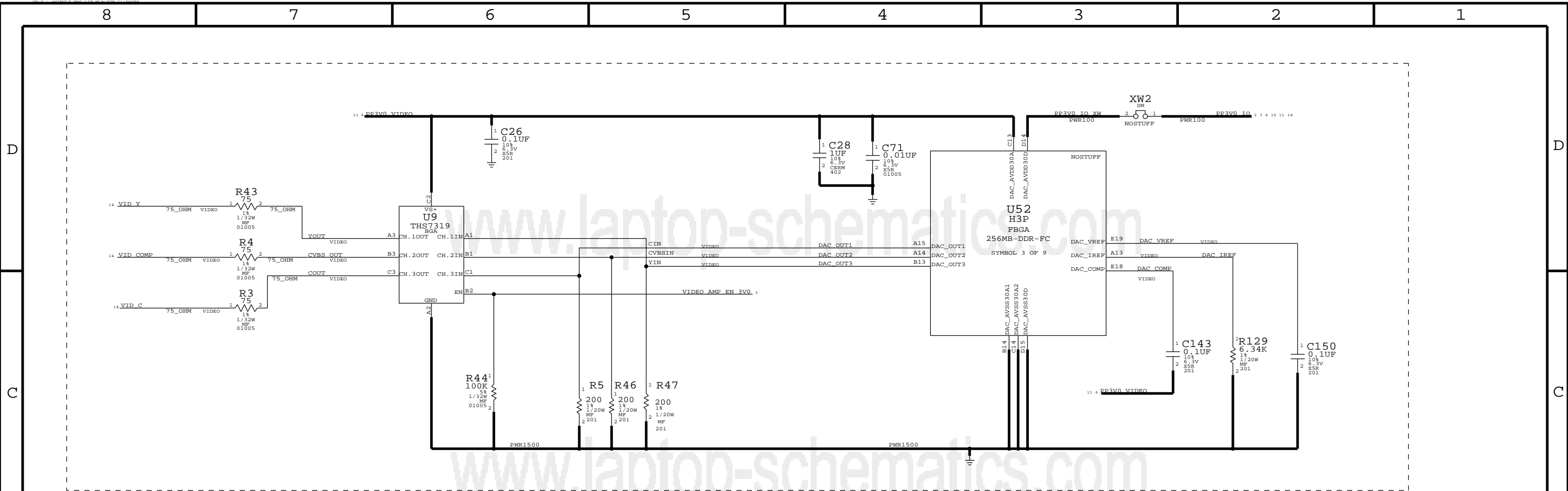
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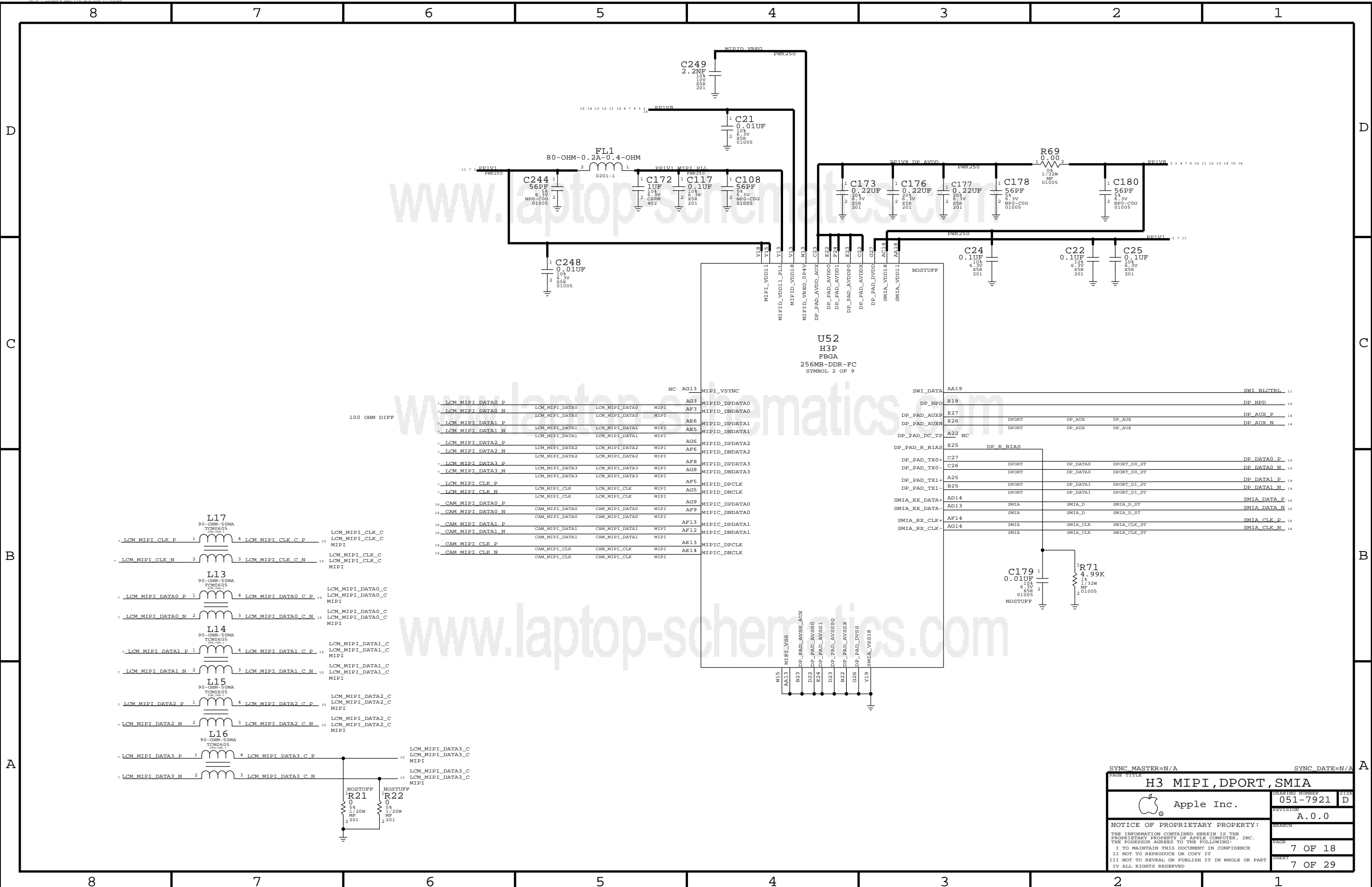
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PAGE TITLE	
ANALOG VIDEO,H3 NAND, NAND	
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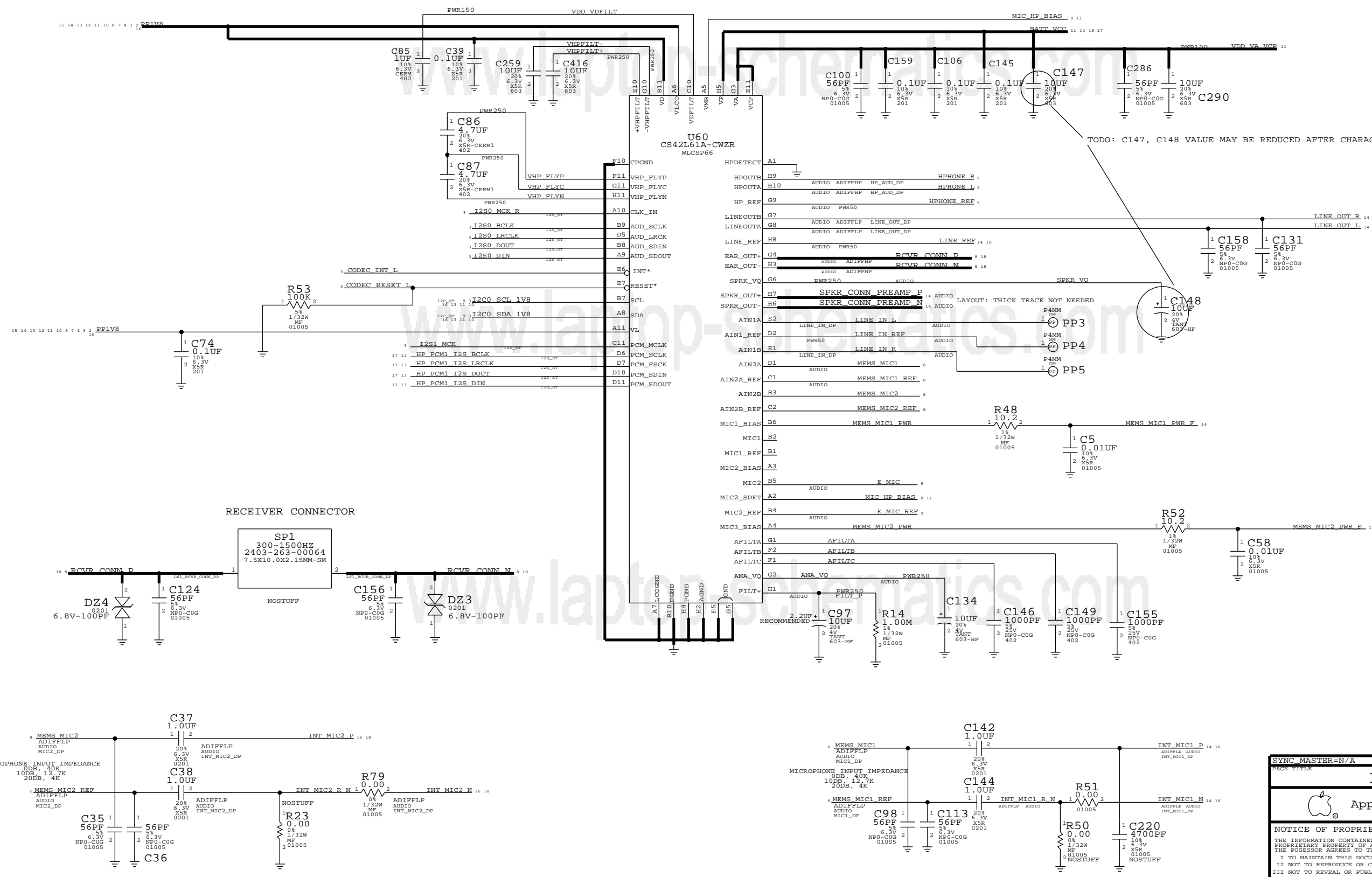
100 OHM DIFF

LCM MIPI DATA0 P	LCM_MIPI_DATA0	LCM_MIPI_DATA0	MIPI	AG3	MIPI_VSYNC	NC	AG13	MIPI_VSYNC
LCM MIPI DATA0 N	LCM_MIPI_DATA0	LCM_MIPI_DATA0	MIPI	AF3	MIPI_DPDATA0			MIPI_DPDATA0
LCM MIPI DATA1 P	LCM_MIPI_DATA1	LCM_MIPI_DATA1	MIPI	AE6	MIPI_DPDATA1			MIPI_DPDATA1
LCM MIPI DATA1 N	LCM_MIPI_DATA1	LCM_MIPI_DATA1	MIPI	AE5	MIPI_DNDATA1			MIPI_DNDATA1
LCM MIPI DATA2 P	LCM_MIPI_DATA2	LCM_MIPI_DATA2	MIPI	AG6	MIPI_DPDATA2			MIPI_DPDATA2
LCM MIPI DATA2 N	LCM_MIPI_DATA2	LCM_MIPI_DATA2	MIPI	AF6	MIPI_DNDATA2			MIPI_DNDATA2
LCM MIPI DATA3 P	LCM_MIPI_DATA3	LCM_MIPI_DATA3	MIPI	AF8	MIPI_DPDATA3			MIPI_DPDATA3
LCM MIPI DATA3 N	LCM_MIPI_DATA3	LCM_MIPI_DATA3	MIPI	AG8	MIPI_DNDATA3			MIPI_DNDATA3
LCM MIPI CLK P	LCM_MIPI_CLK	LCM_MIPI_CLK	MIPI	AF5	MIPI_DPCLK			MIPI_DPCLK
LCM MIPI CLK N	LCM_MIPI_CLK	LCM_MIPI_CLK	MIPI	AG5	MIPI_DNCLK			MIPI_DNCLK
CAM MIPI DATA0 P	CAM_MIPI_DATA0	CAM_MIPI_DATA0	MIPI	AG9	MIPI_DPDATA0			MIPI_DPDATA0
CAM MIPI DATA0 N	CAM_MIPI_DATA0	CAM_MIPI_DATA0	MIPI	AF9	MIPI_DNDATA0			MIPI_DNDATA0
CAM MIPI DATA1 P	CAM_MIPI_DATA1	CAM_MIPI_DATA1	MIPI	AF13	MIPI_DPDATA1			MIPI_DPDATA1
CAM MIPI DATA1 N	CAM_MIPI_DATA1	CAM_MIPI_DATA1	MIPI	AF12	MIPI_DNDATA1			MIPI_DNDATA1
CAM MIPI CLK P	CAM_MIPI_CLK	CAM_MIPI_CLK	MIPI	AE13	MIPI_DPCLK			MIPI_DPCLK
CAM MIPI CLK N	CAM_MIPI_CLK	CAM_MIPI_CLK	MIPI	AE14	MIPI_DNCLK			MIPI_DNCLK

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L61 AUDIO INTERFACE

I2C ADDRESS: 1001010X



TODO: C147, C148 VALUE MAY BE REDUCED AFTER CHARACTERIZATION

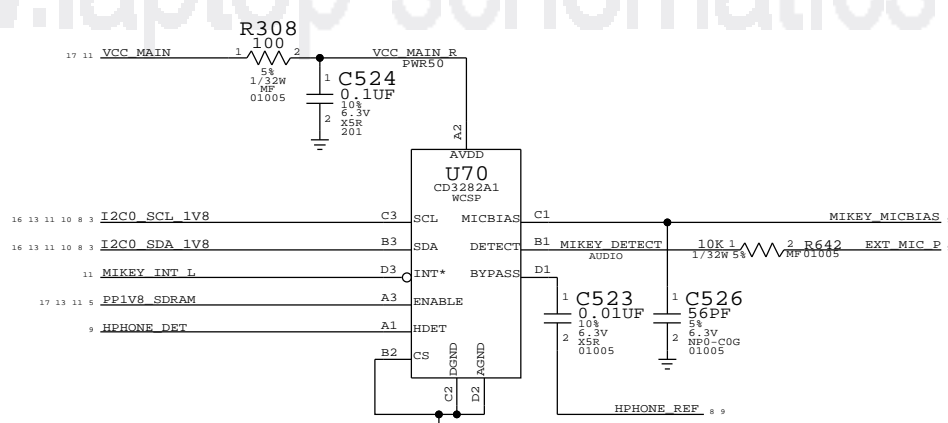
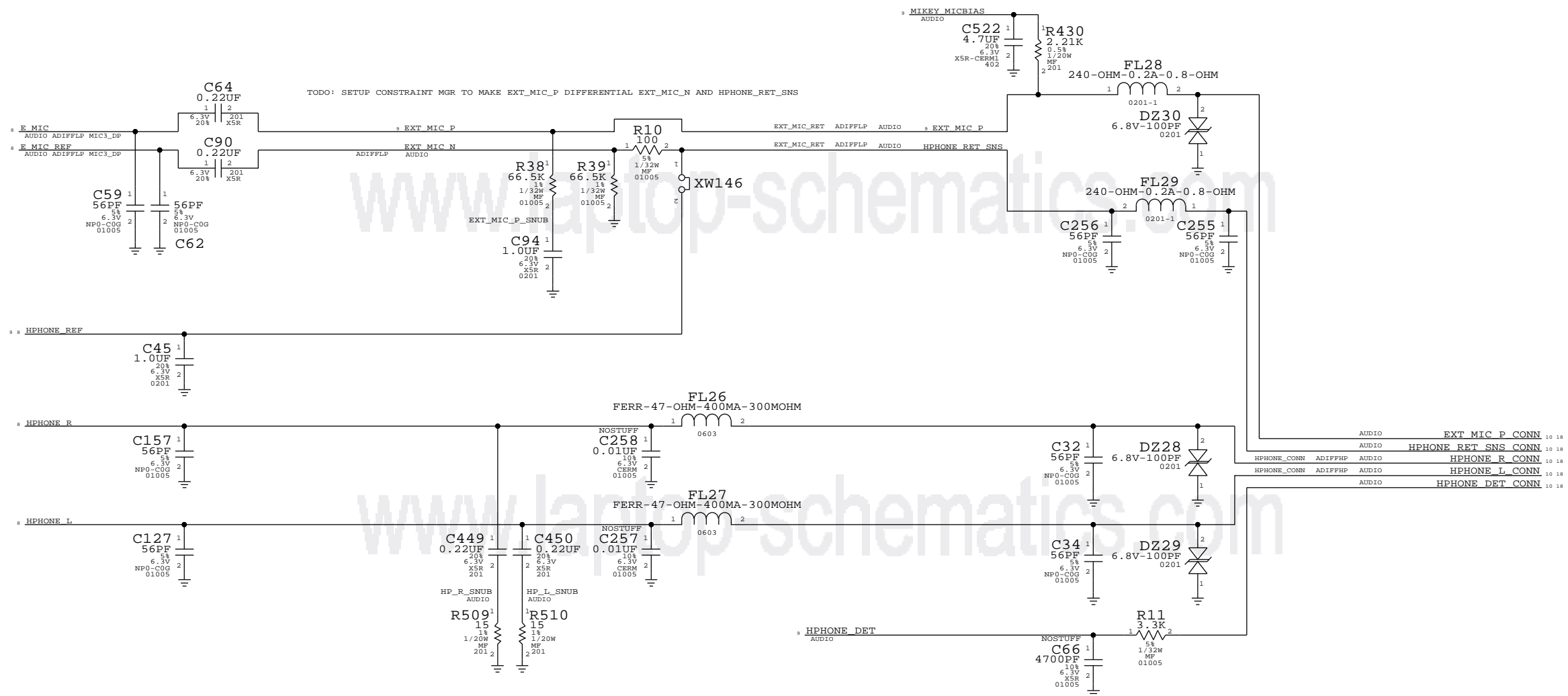
LAYOUT: THICK TRACE NOT NEEDED

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PLACED AT CODEC

PLACED IN CODEC AREA

PLACED BY B2B



I2C ADDRESS: 0111001X (0X39)

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HPHONE FILTERS, MIKEY

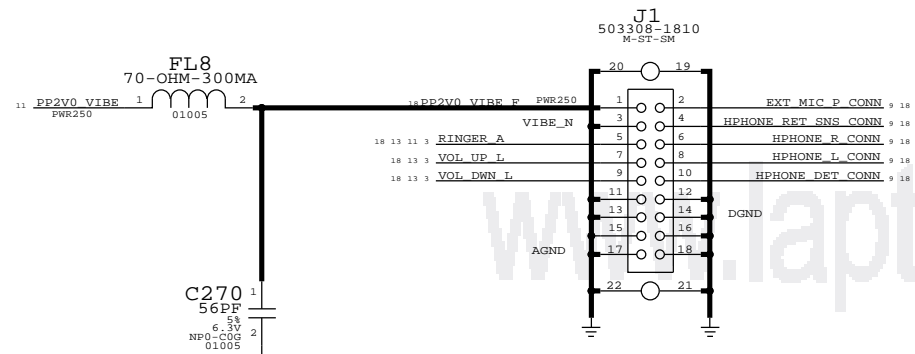
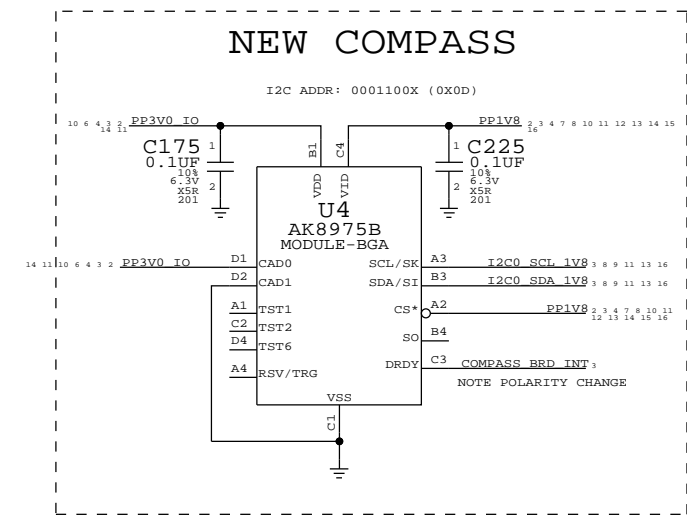
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BUTTON FLEX CONNECTOR

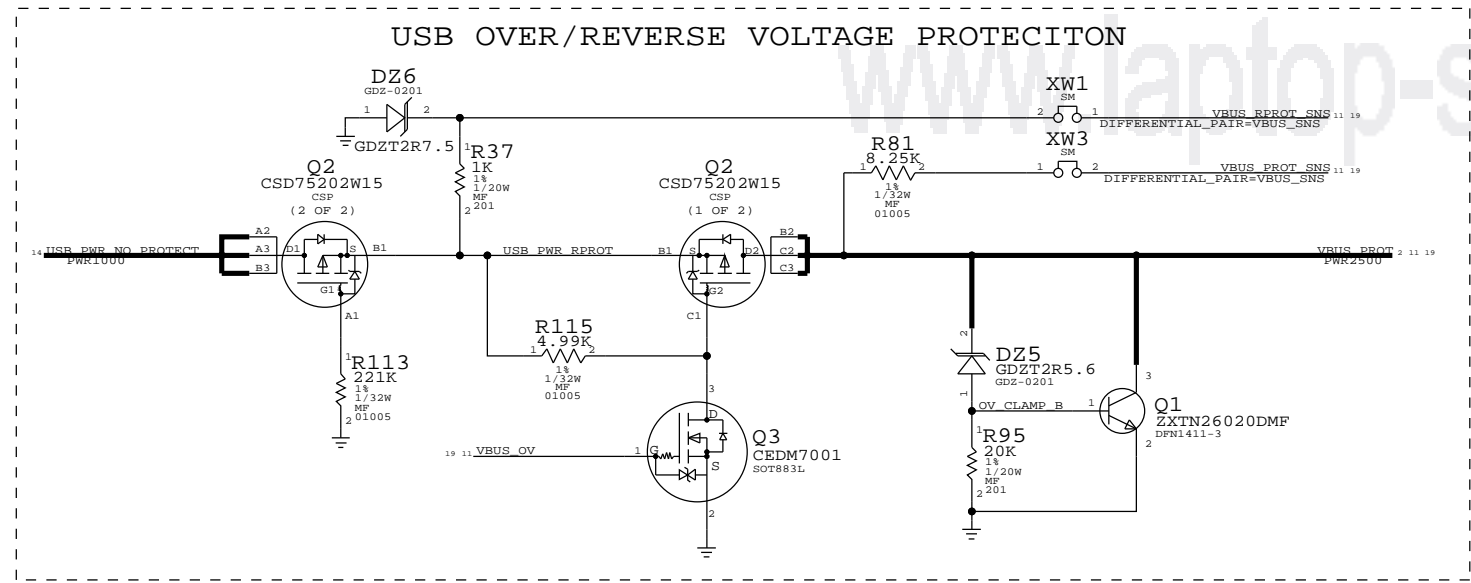
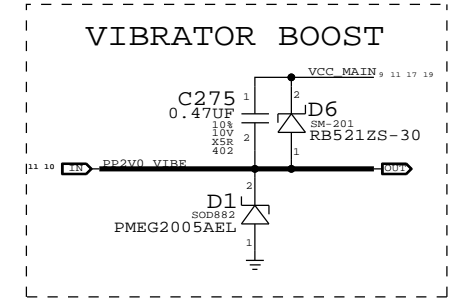
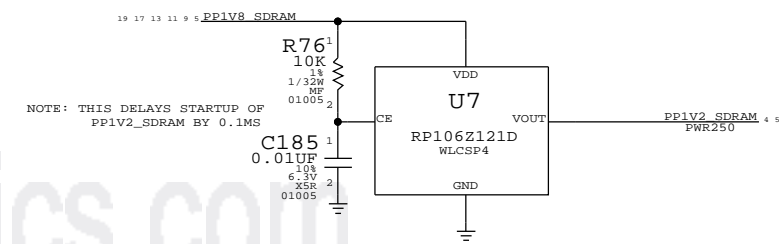
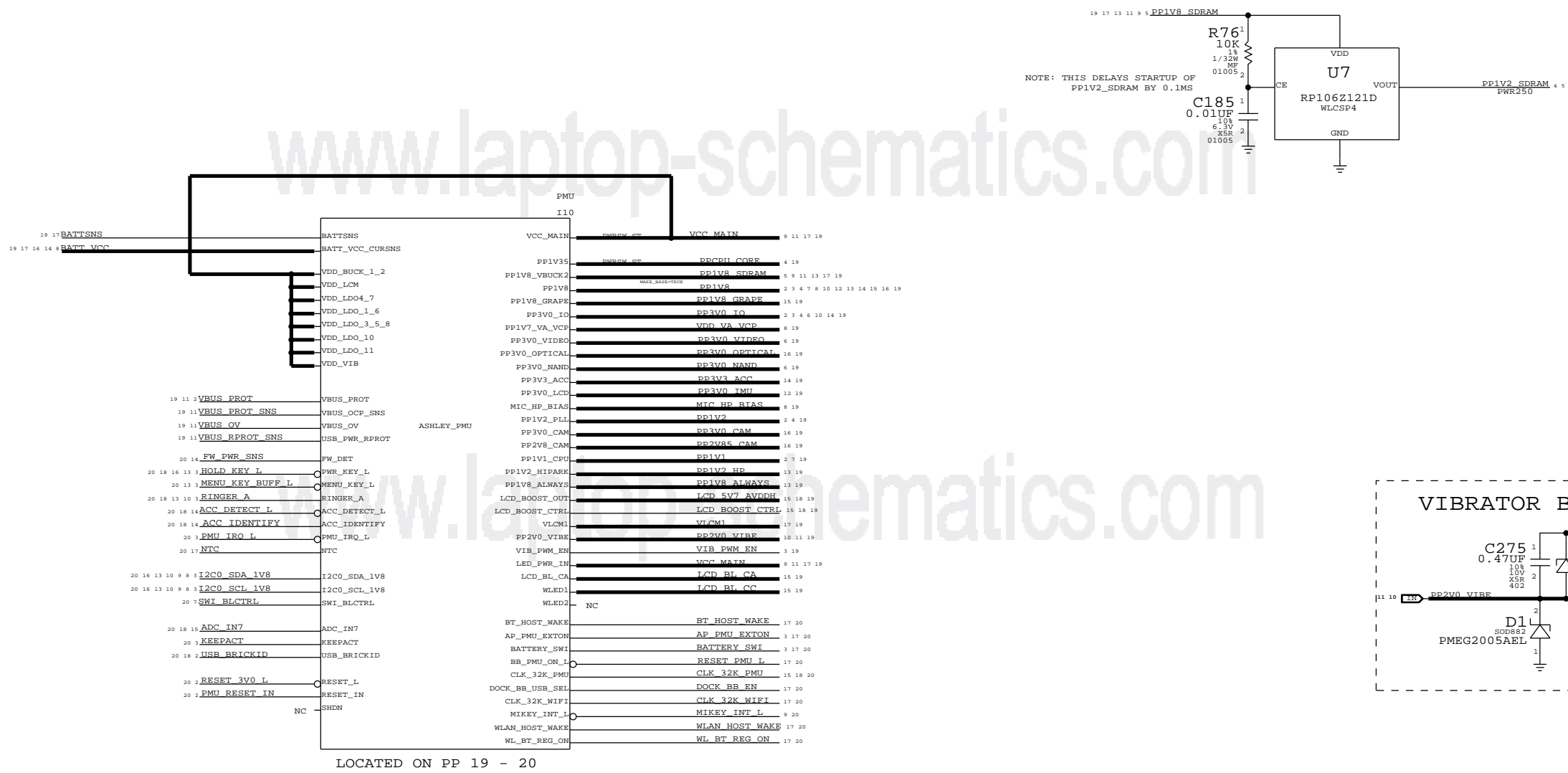
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C

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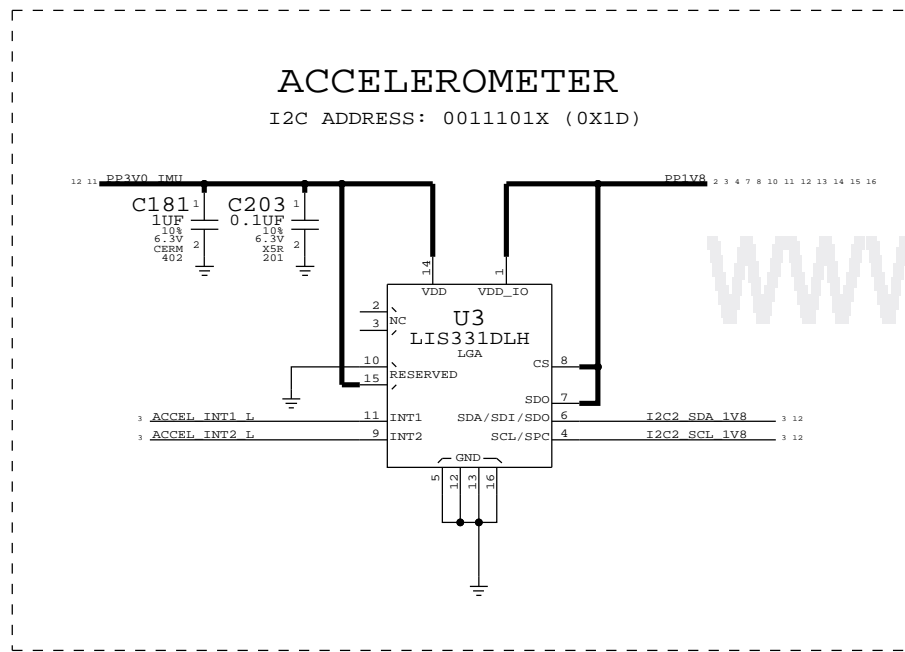
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ACCELEROMETER

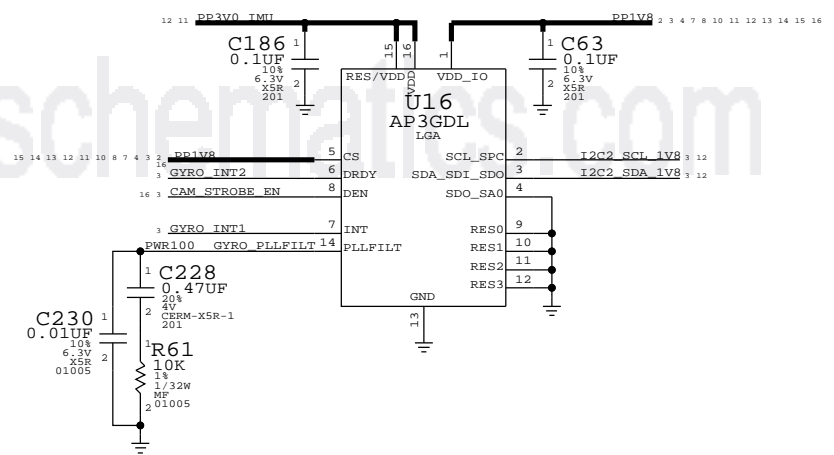
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GYRO

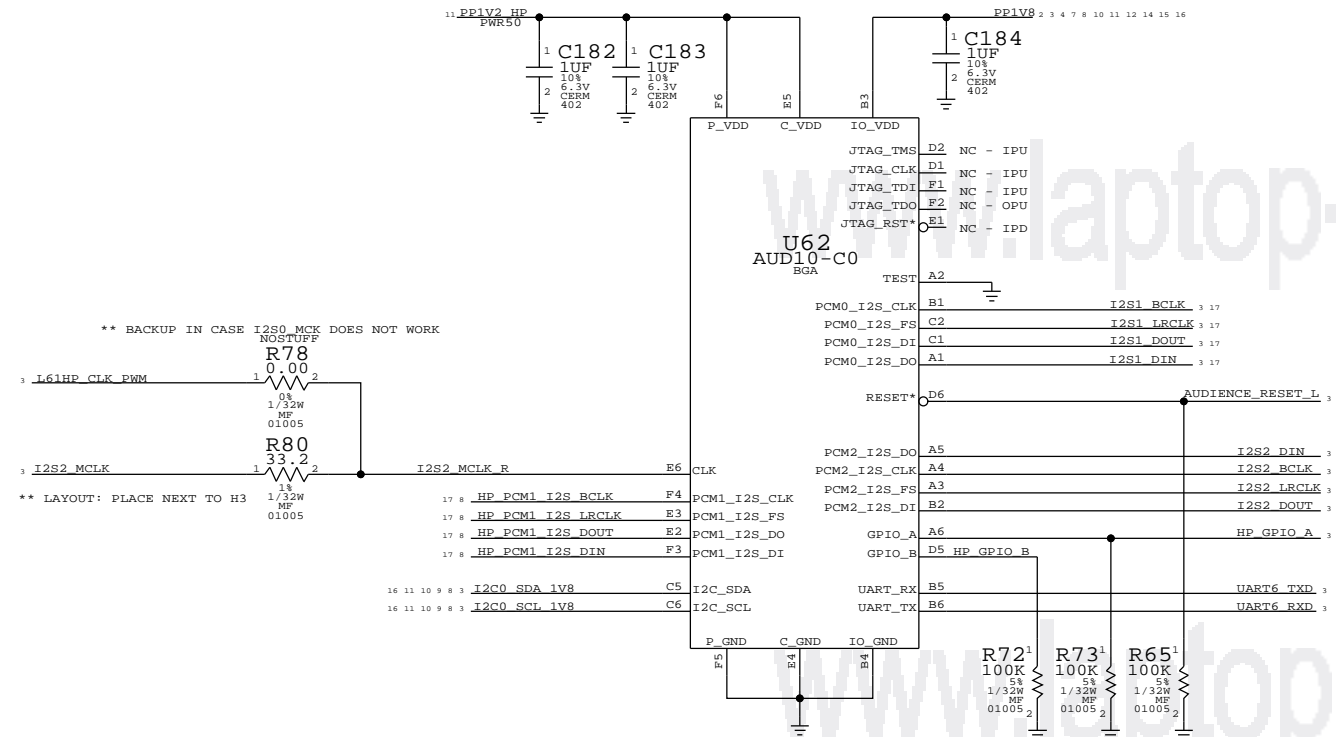


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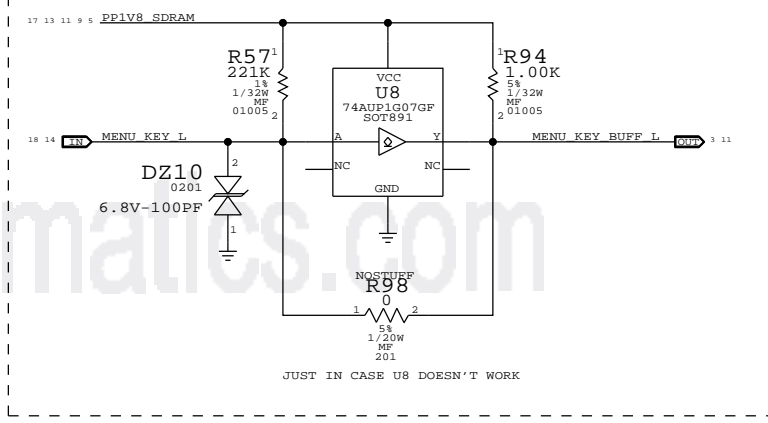
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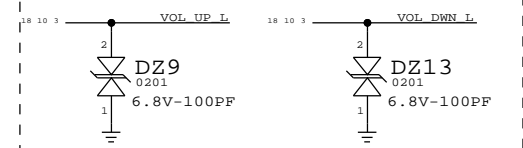


MENU KEY PU, ESD AND BUFFER

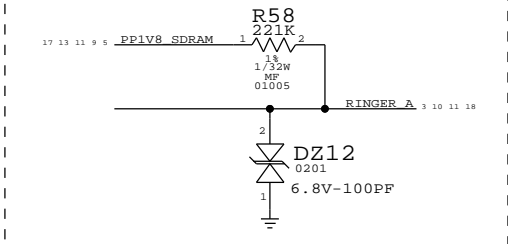
BUFFER IS NEEDED BECAUSE OF IT'S LOW V_{IH} AND LEAKAGE IT MUST BE OPEN DRAIN SO THAT WHEN PPIV8_SDRAM IS DOWN, MENU_KEY_BUFF_L IS PULLED LOW THROUGH R94 AND INTERNAL PULLDOWN IN ASHLEY.



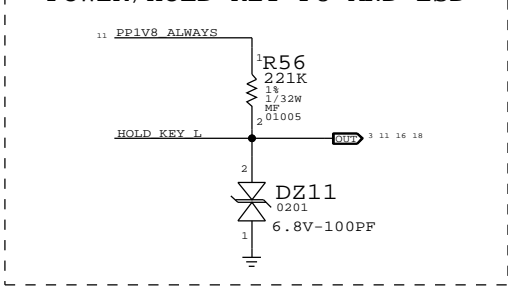
VOLUME SWITCH ESD



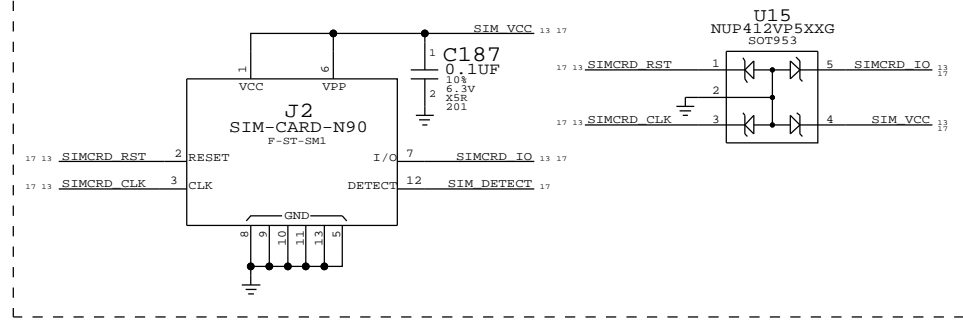
RINGER A/B SWITCH PU AND ESD



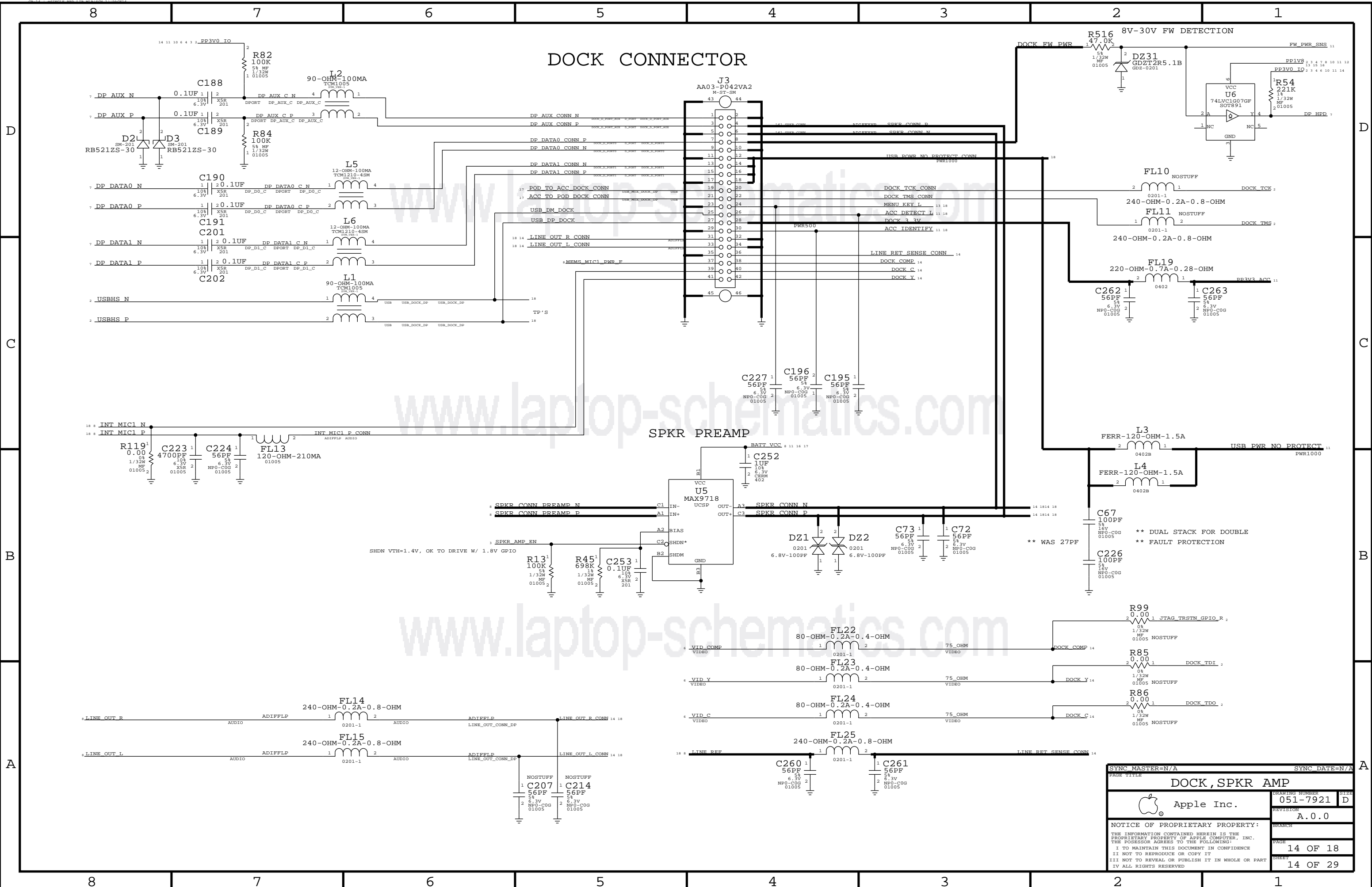
POWER/HOLD KEY PU AND ESD



SIM CARD SLOT AND ESD



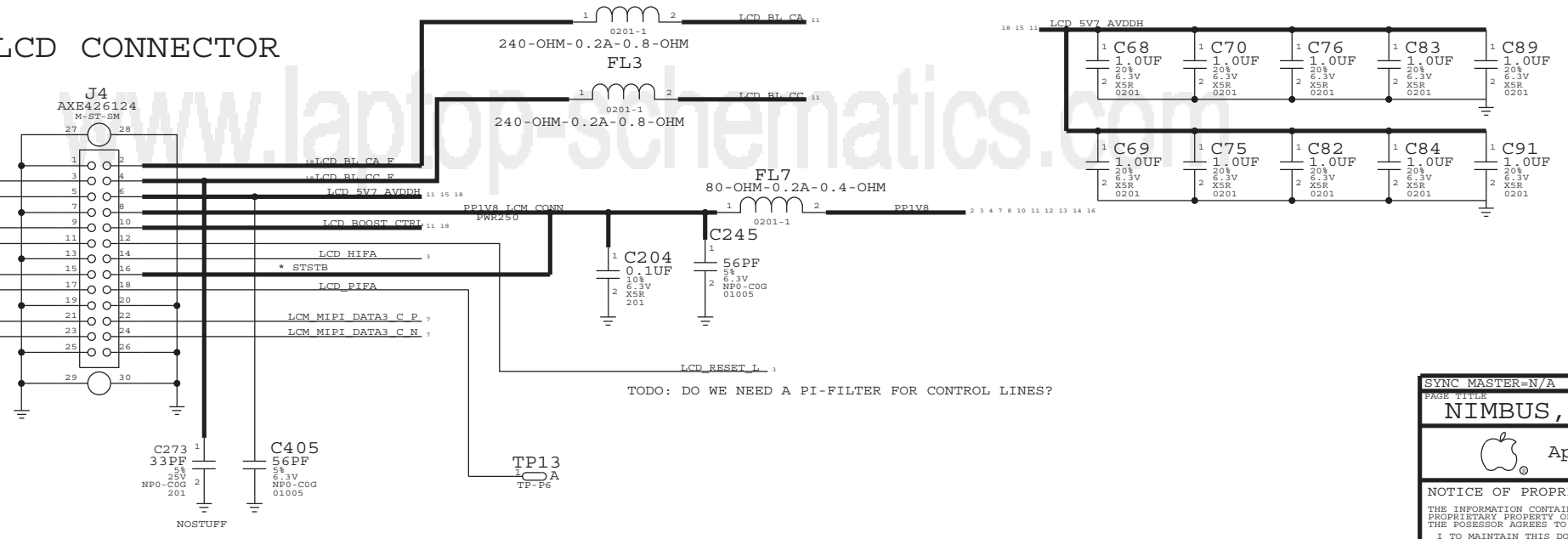
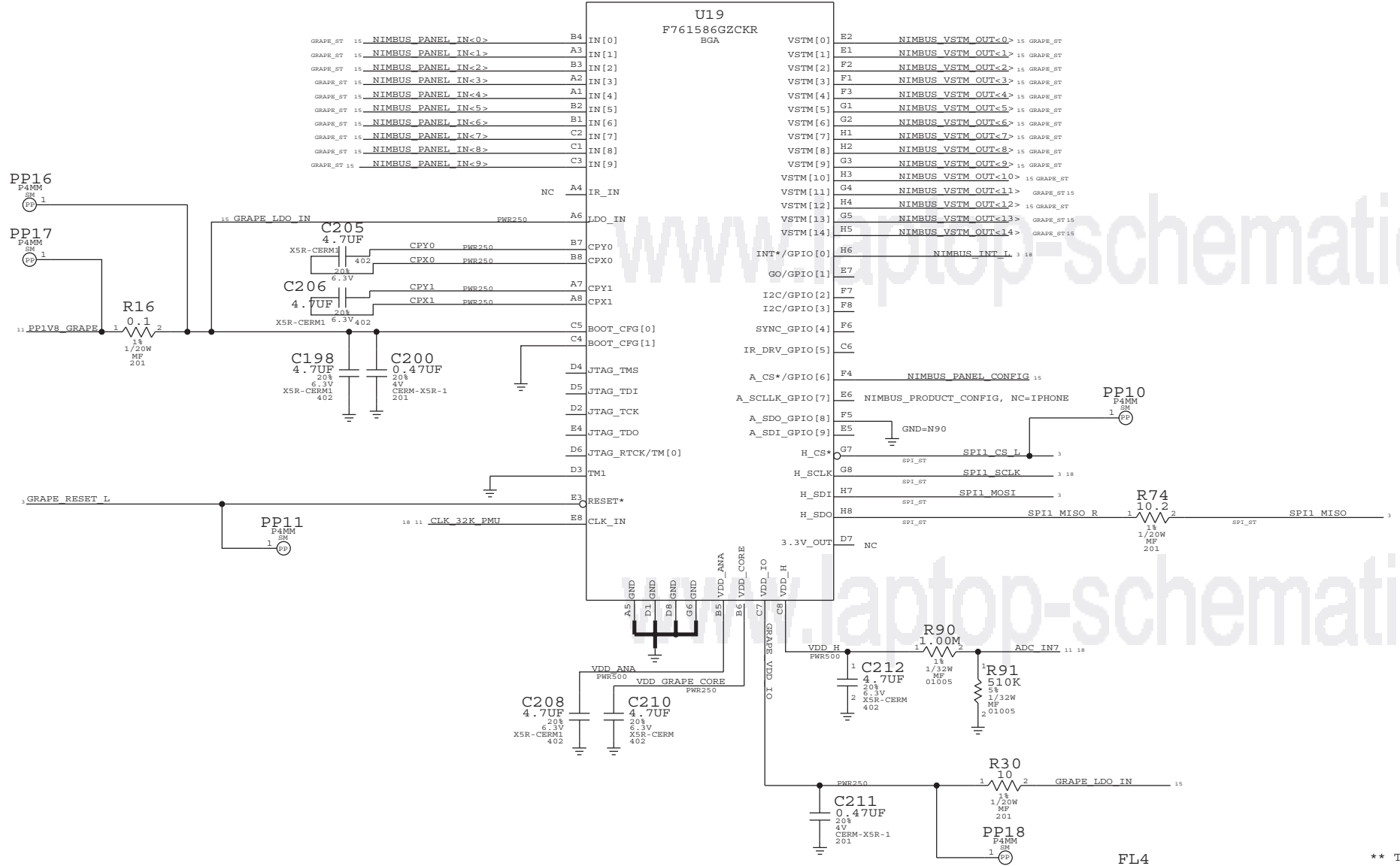
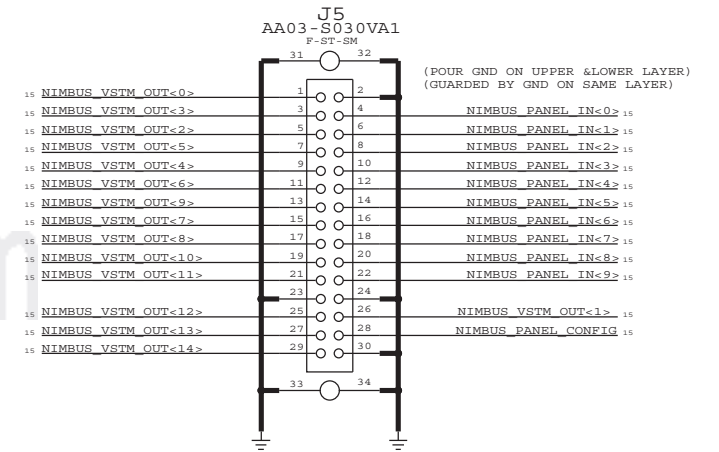
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HPARK, SWITCH ESD, SIM CARD	
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DOCK, SPKR AMP		051-7921		D
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NIMBUS

GRAPE CONNECTOR N90 NIMBUS CONNECTOR

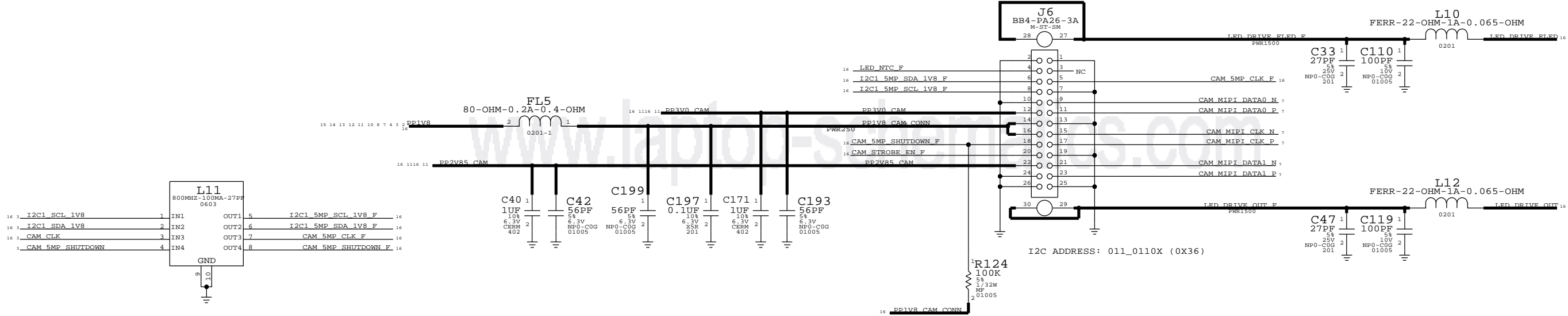


TODO: DO WE NEED A PI-FILTER FOR CONTROL LINES?

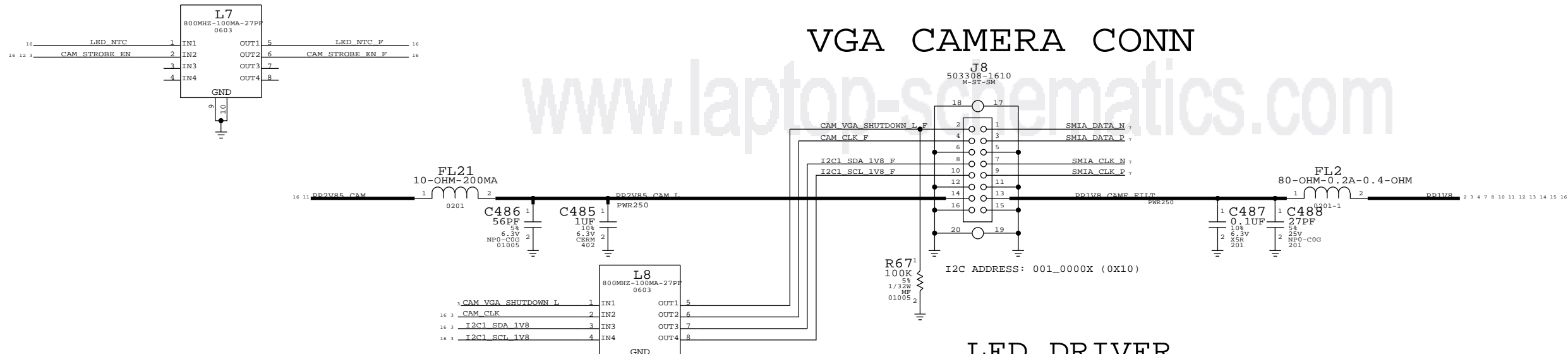
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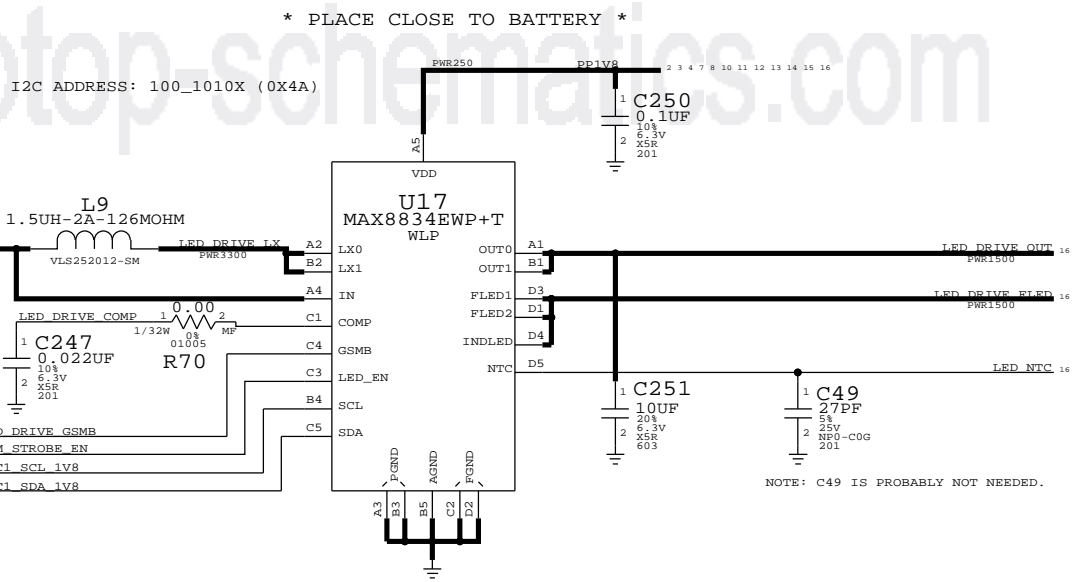
5MP CAMERA CONNECTOR



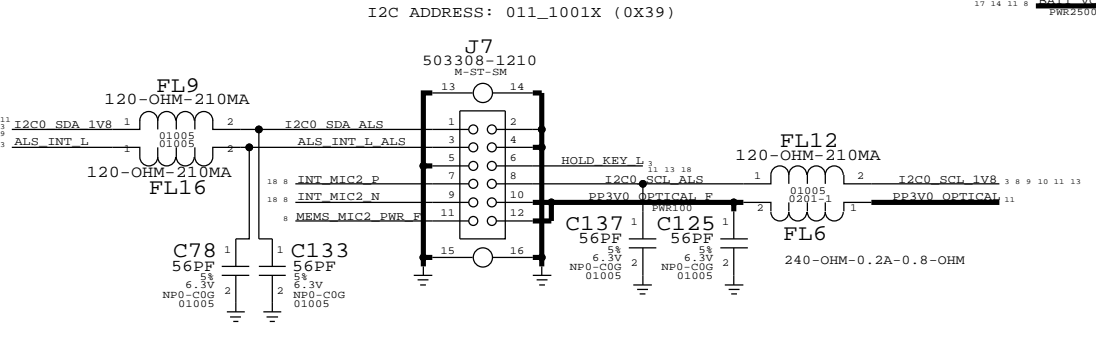
VGA CAMERA CONN



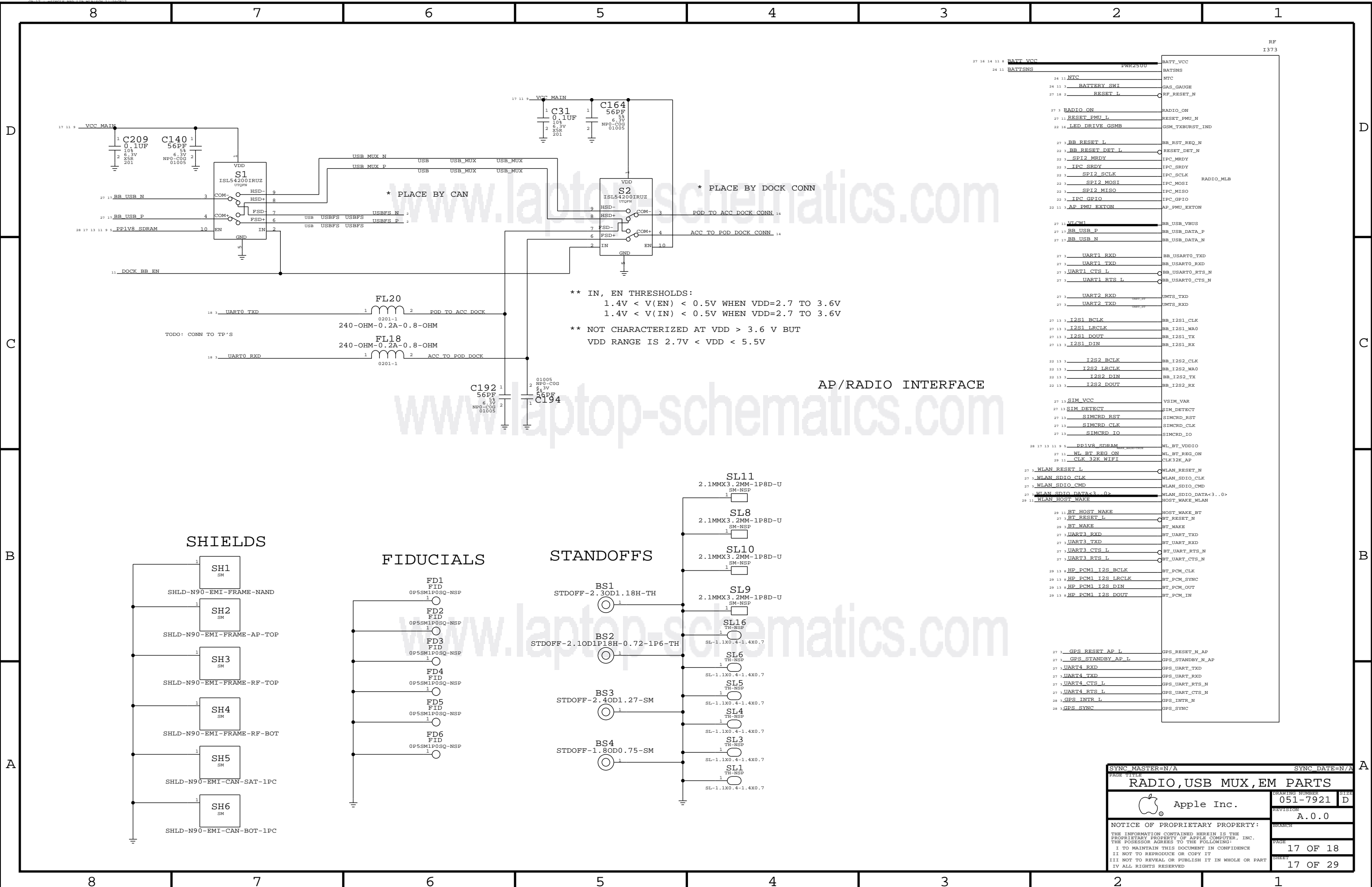
LED DRIVER



ALS/PROX

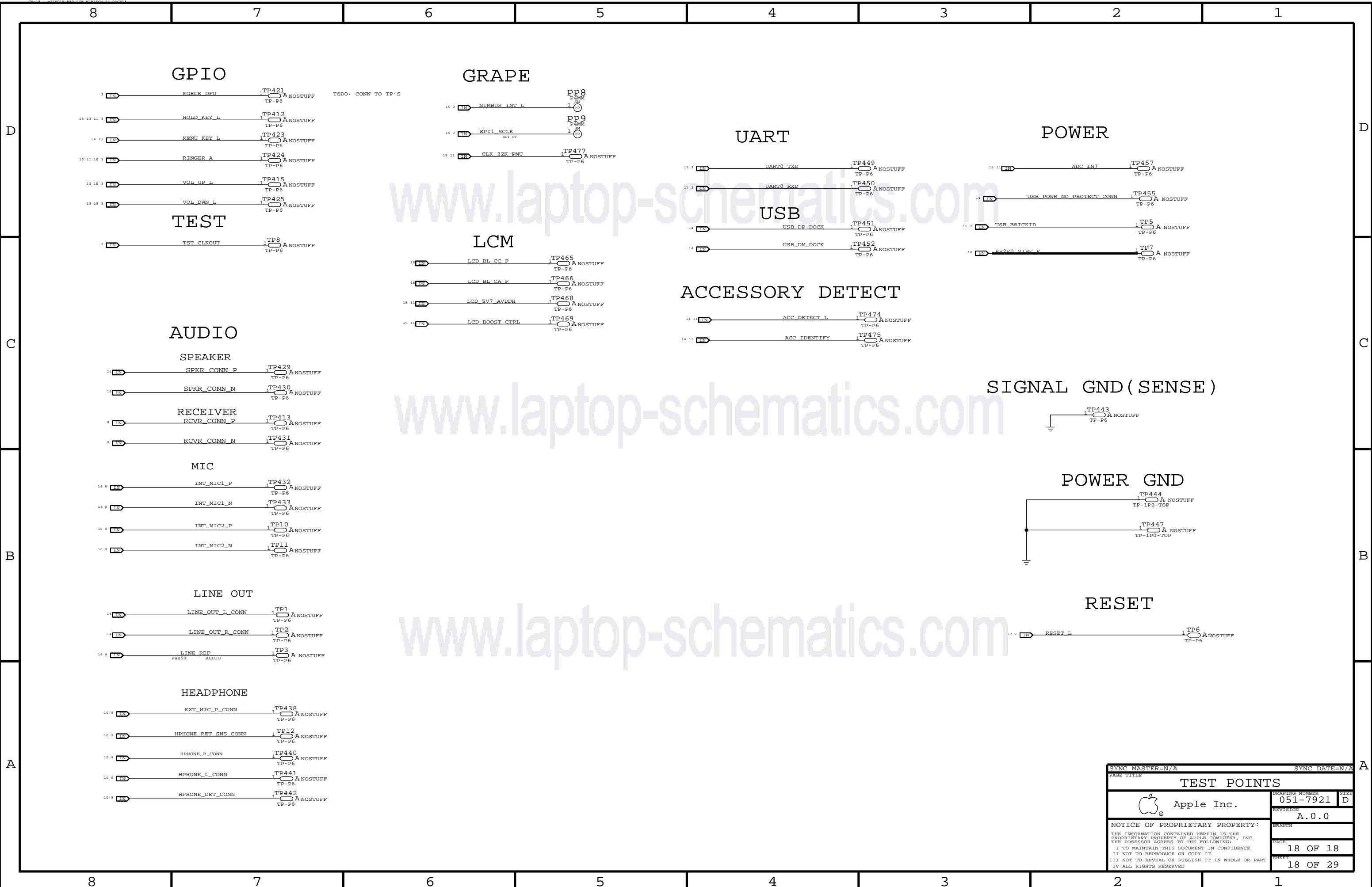


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RF I373	
27 14 11 8	BATT_VCC
24 11	BATTNSNS
24 11	NTC
24 11 3	BATTERY SWI
27 18 2	RESET L
27 3	RADIO_ON
27 11	RESET_PMU_L
22 16	LED_DRIVE_GSM
27 3	BB_RESET_L
22 1	BB_RESET_DET_L
22 3	SPI2_MRDY
22 3	IPC_SRDY
22 3	SPI2_SCLK
22 3	SPI2_MOSI
22 3	SPI2_MISO
22 3	IPC_GPIO
22 11 9	AP_PMU_EXTON
27 11	WFCM1
27 17	BB_USB_P
27 17	BB_USB_N
27 3	UART1_RXD
27 3	UART1_TXD
27 3	UART1_CTS_L
27 3	UART1_RTS_L
27 3	UART2_RXD
27 3	UART2_TXD
27 13 3	I2S1_BCLK
27 13 3	I2S1_LRCLK
27 13 3	I2S1_DOUT
27 13 3	I2S1_DIN
22 13 3	I2S2_BCLK
22 13 3	I2S2_LRCLK
22 13 3	I2S2_DIN
22 13 3	I2S2_DOUT
27 13	SIM_VCC
27 13	SIM_DETECT
27 13	SIMCRD_RST
27 13	SIMCRD_CLK
27 13	SIMCRD_IO
28 17 13 11 9 5	PP1V8_SDRAM
27 11	WL_BT_REG_ON
29 11	CLK_32K_WIFI
27 3	WLAN_RESET_L
27 3	WLAN_SDIO_CLK
27 3	WLAN_SDIO_CMD
27 3	WLAN_SDIO_DATA<3..0>
29 11	WLAN_HOST_WAKE
29 11	BT_HOST_WAKE
27 3	BT_RESET_L
29 11	BT_WAKE
27 3	UART3_RXD
27 3	UART3_TXD
27 3	UART3_CTS_L
27 3	UART3_RTS_L
29 13 8	HP_PCM1_I2S_BCLK
29 13 8	HP_PCM1_I2S_LRCLK
29 13 8	HP_PCM1_I2S_DIN
29 13 8	HP_PCM1_I2S_DOUT
27 3	GPS_RESET_AP_L
27 3	GPS_STANDBY_AP_L
27 3	UART4_RXD
27 3	UART4_TXD
27 3	UART4_CTS_L
27 3	UART4_RTS_L
28 3	GPS_INTR_N
28 3	GPS_SYNC
27 3	GPS_RESET_N_AP
27 3	GPS_STANDBY_N_AP
27 3	GPS_UART_TXD
27 3	GPS_UART_RXD
27 3	GPS_UART_RTS_N
27 3	GPS_UART_CTS_N
27 3	GPS_INTR_N
27 3	GPS_SYNC

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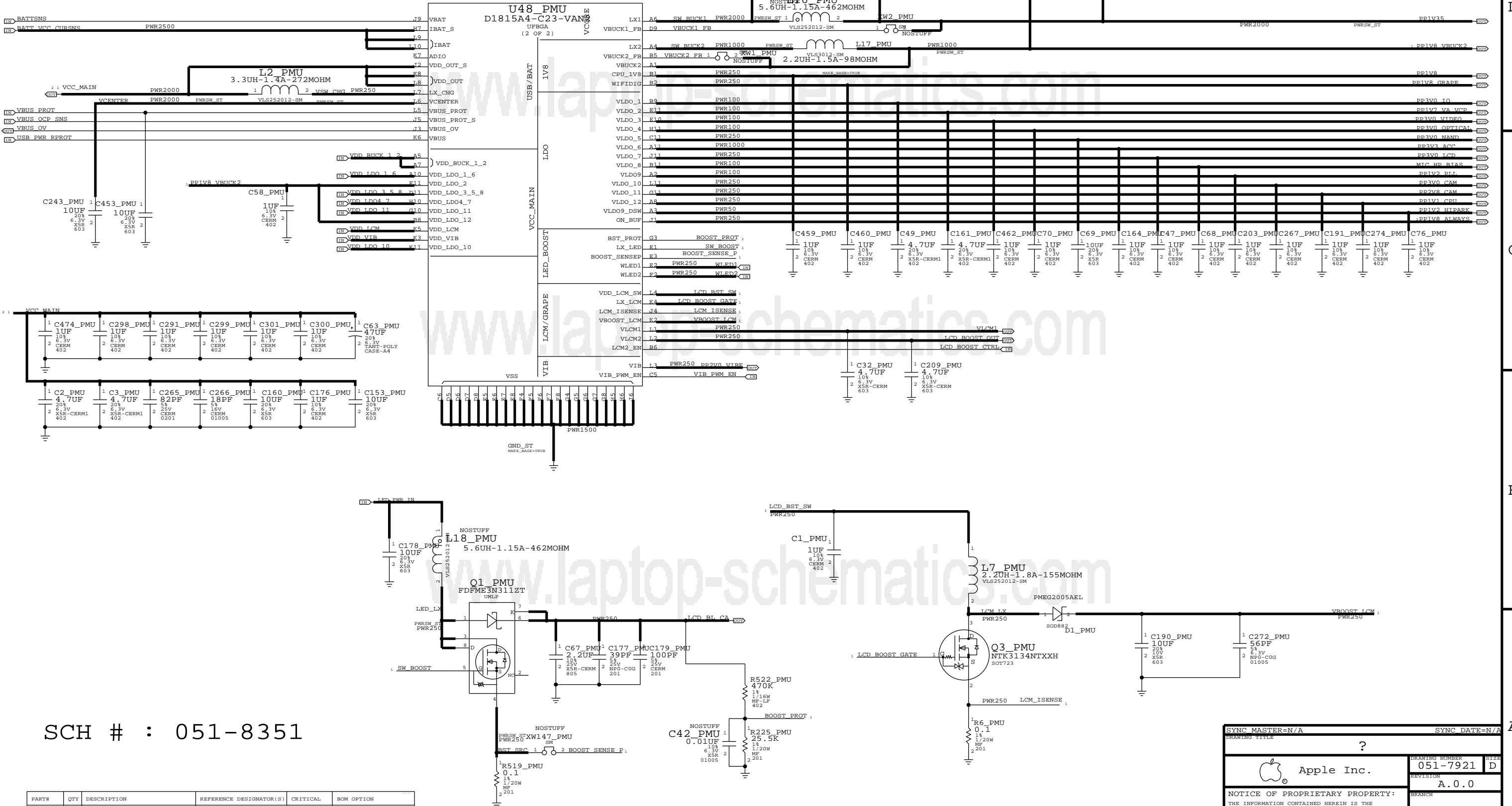
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

N90 ASHLEY_PMU SUB-DESIGN

Thu Mar 18 14:15:23 2010

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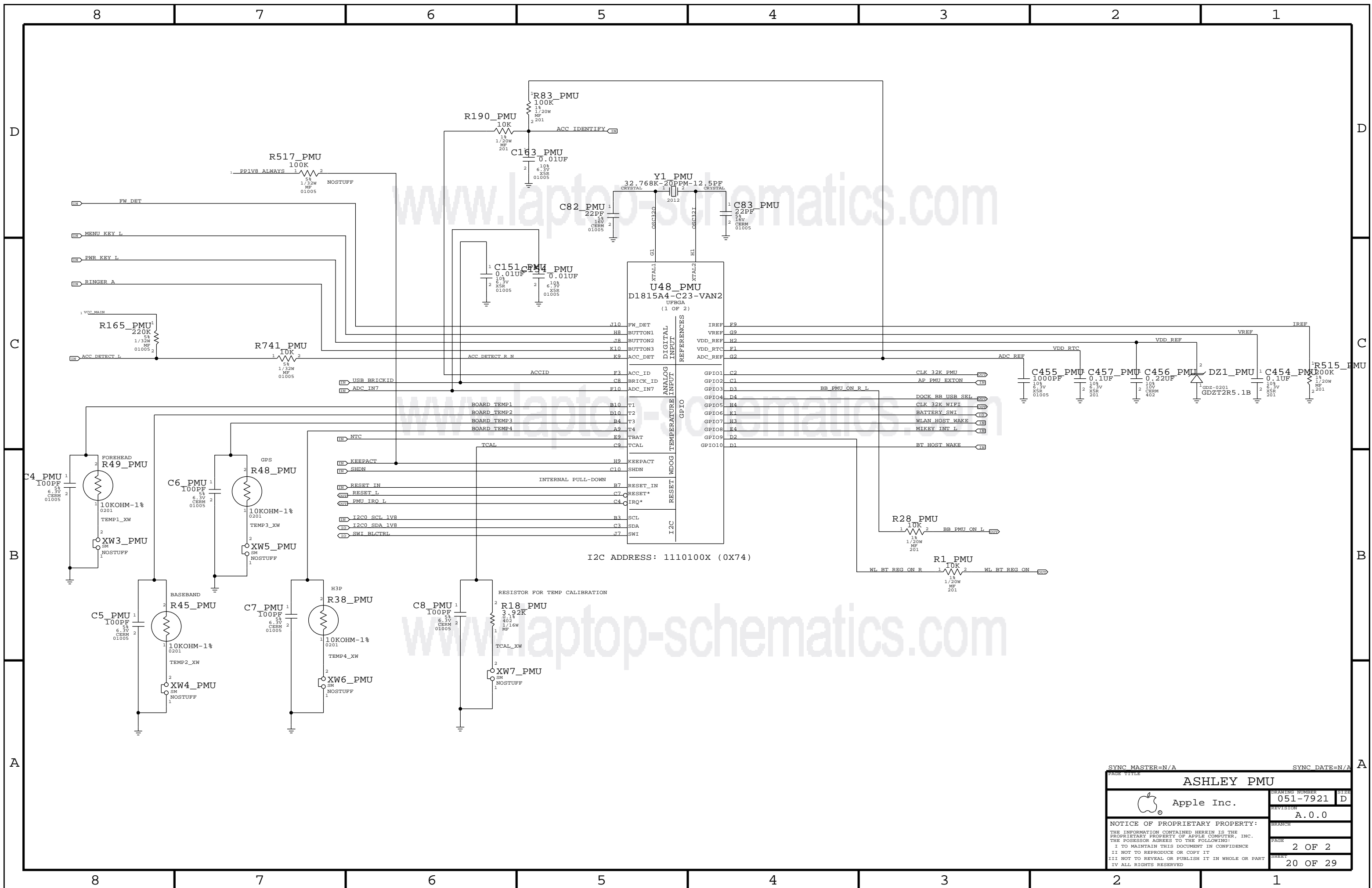
LAYOUT: PLACE XW1 AND XW2 REMOTELY BY AP



SCH # : 051-8351

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
607-6809	1	TDK POWER INDUCTORS	L1_PMU, L3_PMU, L16_PMU, L18_PMU	Y	?

SYNC MASTER=N/A		SYNC DATE=N/A	
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Apple logo		051-7921	D
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			SHEET	20 OF 29	

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N90 (ICE3) HSPA RADIO

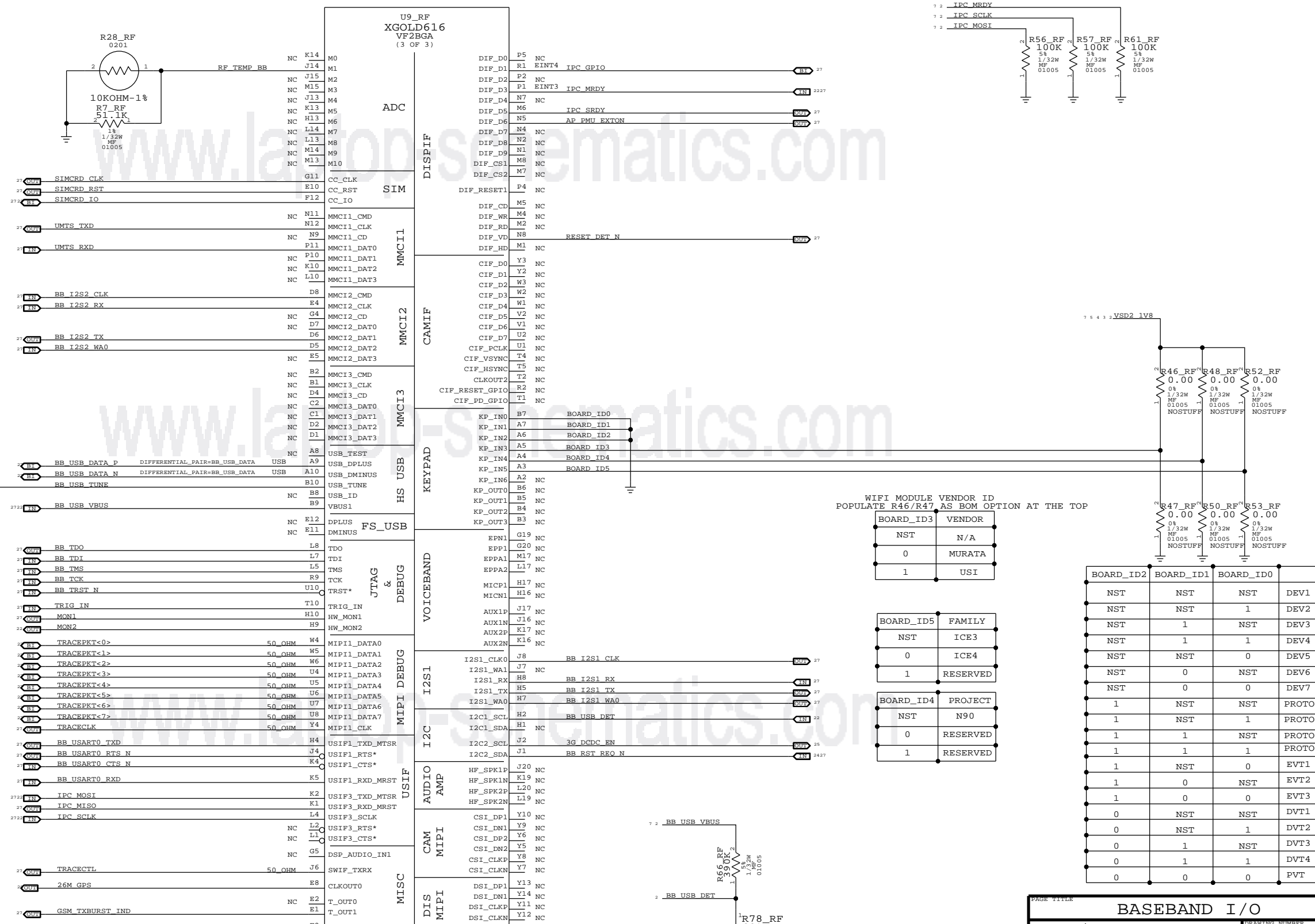
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03	BASEBAND + MEMORY
04	BASEBAND POWER SUPPLY
05	GSM & UMTS TRANSCEIVER
06	POWER AMPS AND RF FRONT END
07	SYSTEM CONNECTORS
08	A-GPS
09	WLAN/BLEETOOTH RADIO

BOARD - 920-XXXX
 SCHEMATIC - 951-XXXX
 BOM - 630-XXXX

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BASEBAND I/O



BOOT CONFIG DESCRIPTION
 DSP_AUDIO_IN1, MON1 AND MON2
 ARE ONLY ON TP AVAILABLE

DSP_AUDIO_IN1 HWCFG[3]	MON1 HWCFG[2]	MON2 HWCFG[1]	TRIG_IN HWCFG[0]	BOOT CONFIG DESCRIPTION
0	0	0	0	EXTERNAL BOOT VIA USB
0	0	0	1	NOR FLASH - EBU CONFIG: 16-BIT AD MUXED
0	0	1	0	NOR FLASH - EBU CONFIG: 16-BIT AAD MUXED

WIFI MODULE VENDOR ID
 POPULATE R46/R47 AS BOM OPTION AT THE TOP

BOARD_ID3	VENDOR
NST	N/A
0	MURATA
1	USI

BOARD_ID5	FAMILY
NST	ICE3
0	ICE4
1	RESERVED

BOARD_ID4	PROJECT
NST	N90
0	RESERVED
1	RESERVED

BOARD_ID2	BOARD_ID1	BOARD_ID0	DEV
NST	NST	NST	DEV1
NST	NST	1	DEV2
NST	1	NST	DEV3
NST	1	1	DEV4
NST	NST	0	DEV5
NST	0	NST	DEV6
NST	0	0	DEV7
1	NST	NST	PROTO0
1	NST	1	PROTO1
1	1	NST	PROTO2
1	1	1	PROTO3
1	NST	0	EVT1
1	0	NST	EVT2
1	0	0	EVT3
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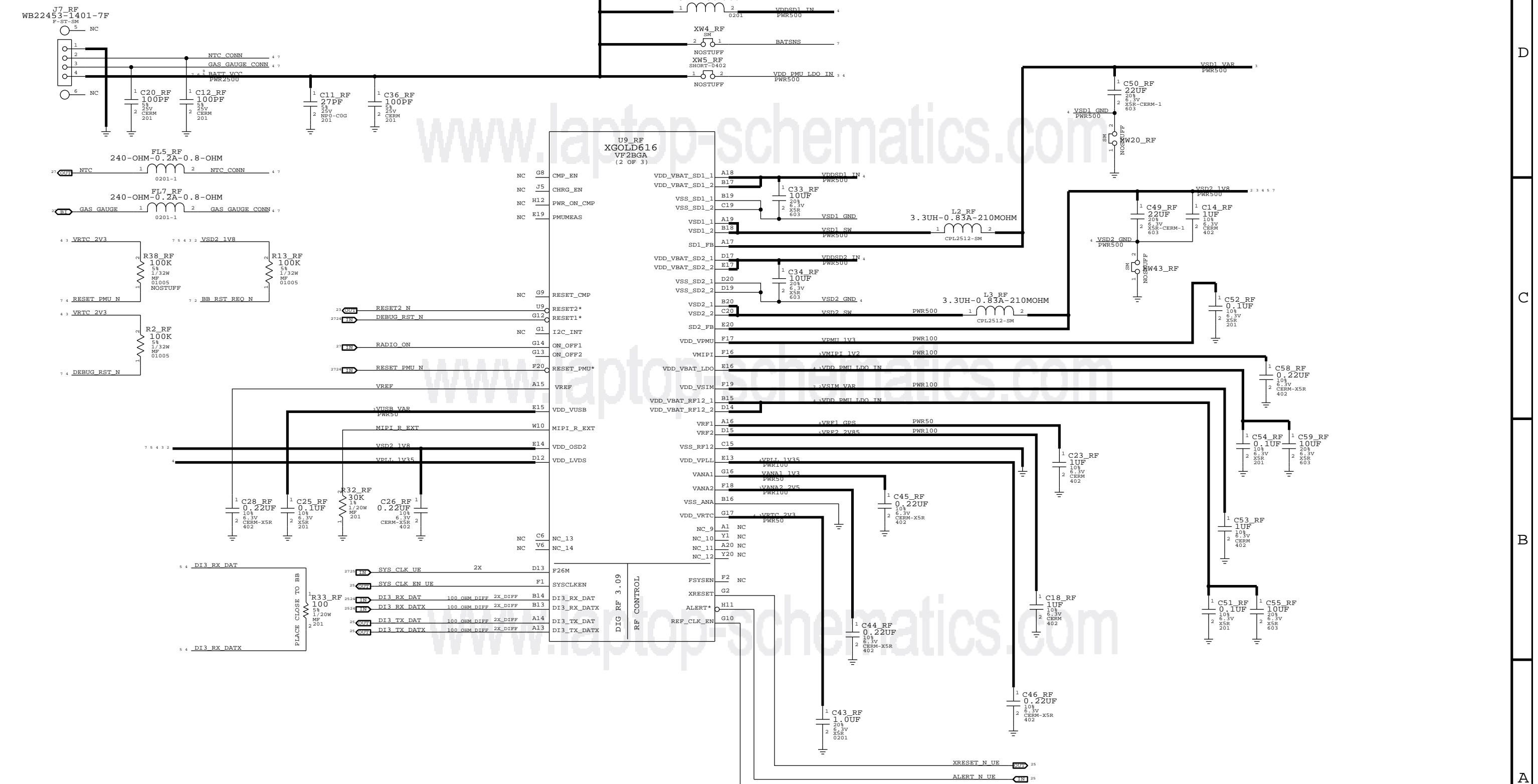
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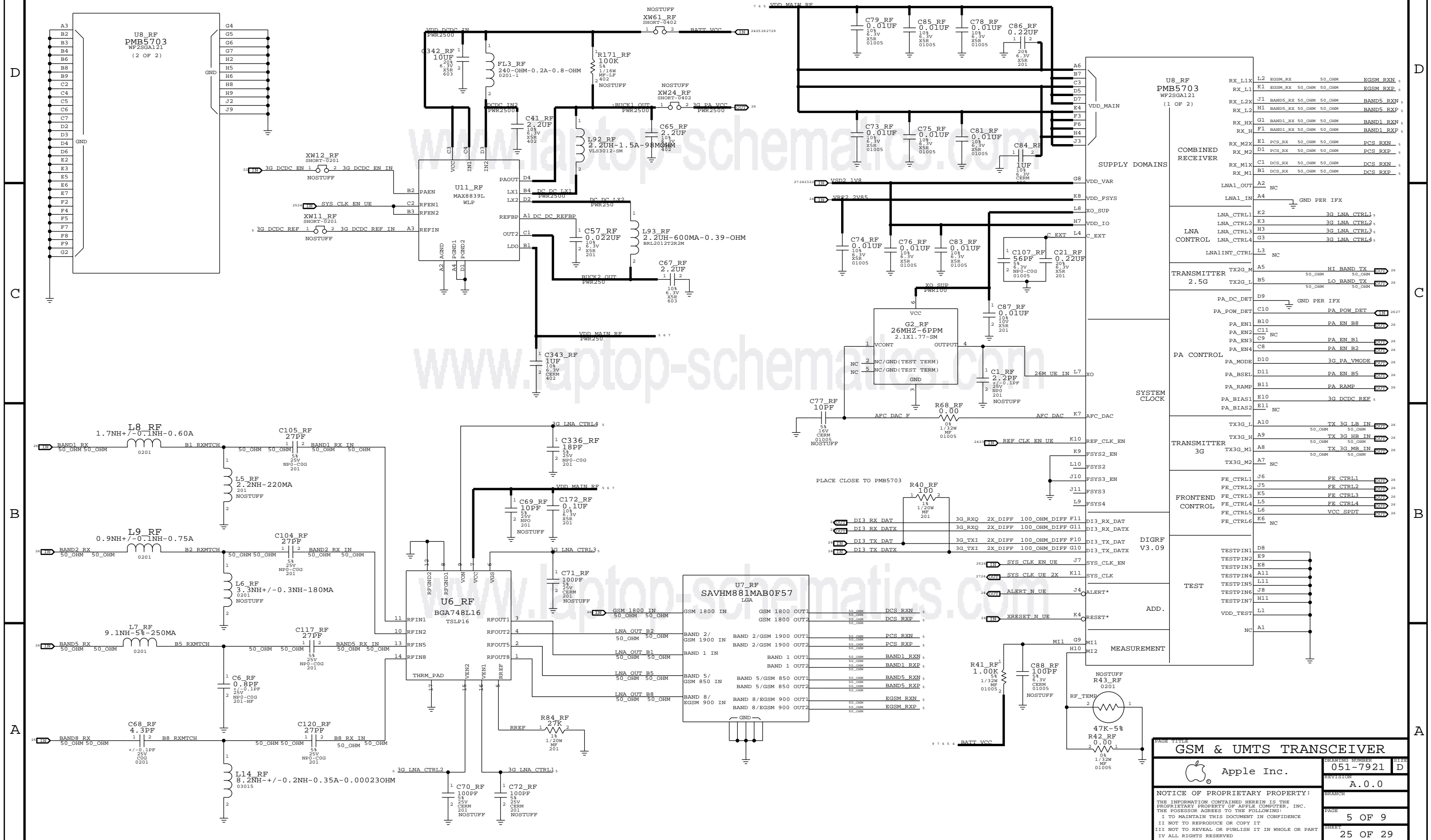
BASEBAND POWER SUPPLY

BATTERY CONNECTOR



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GSM & UMTS TRANSCEIVER - SMARTI UE



GSM & UMTS TRANSCEIVER

Apple Inc.

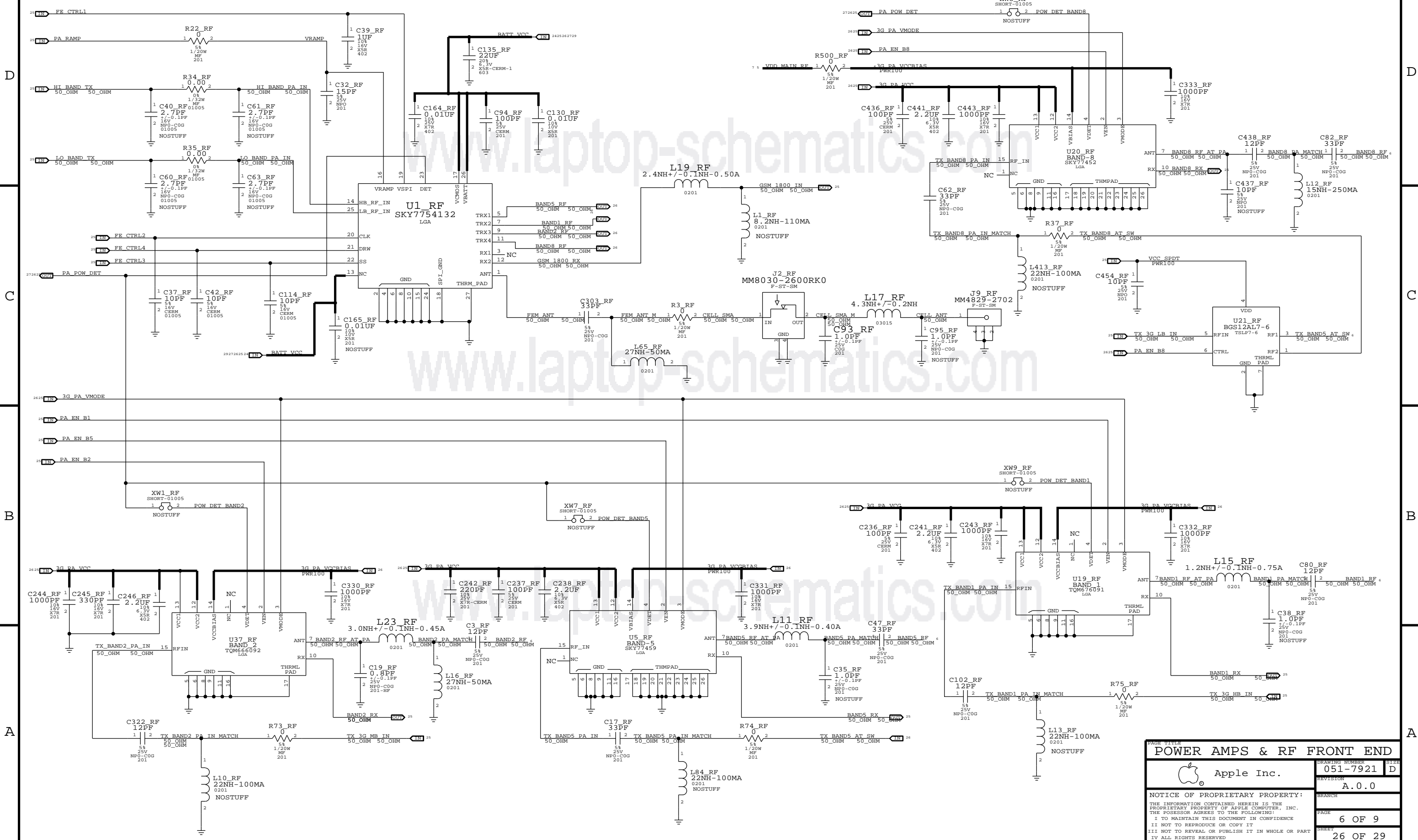
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POWER AMPS AND RF FRONT-END MODULE



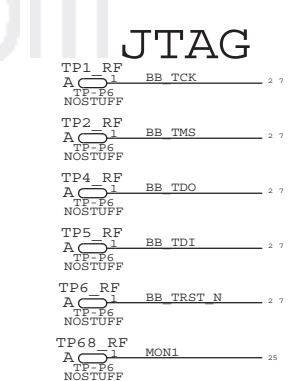
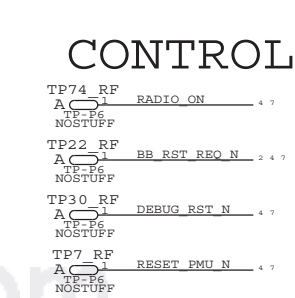
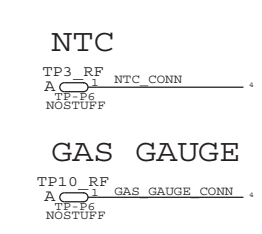
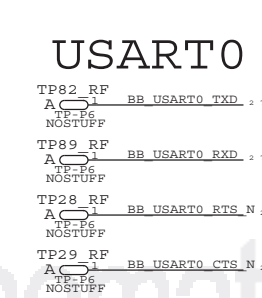
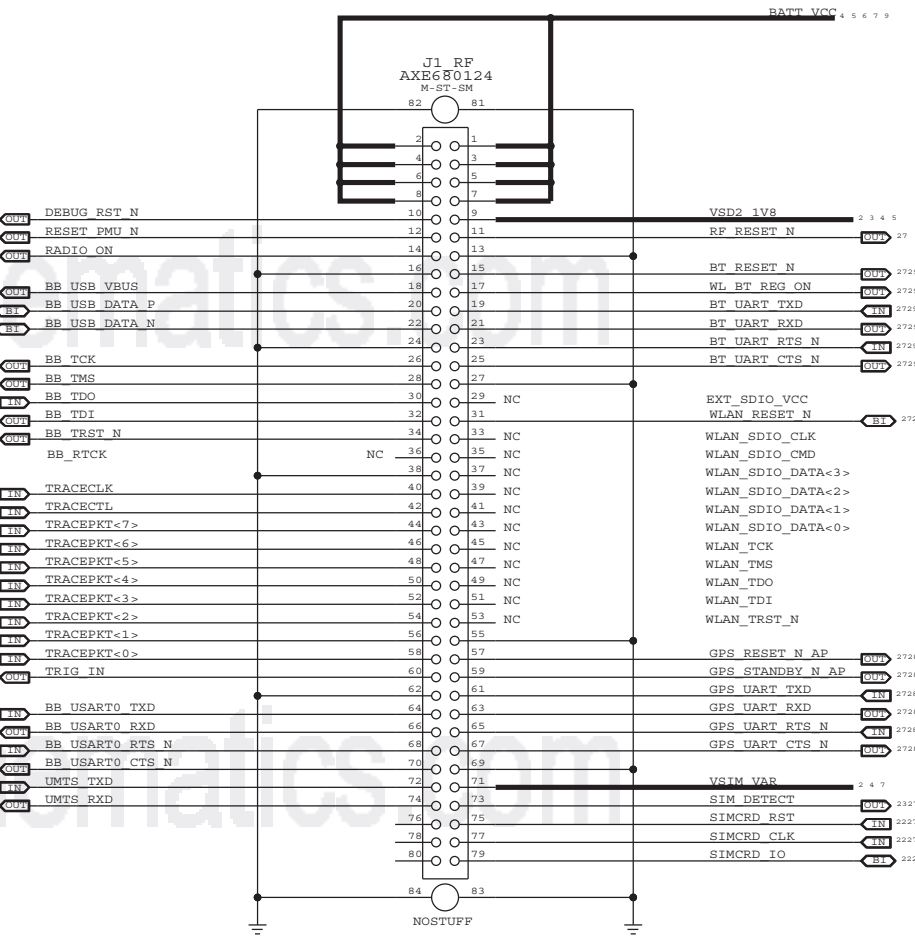
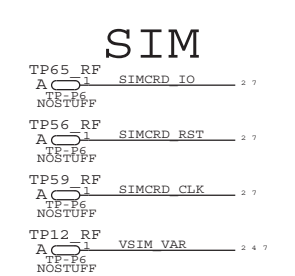
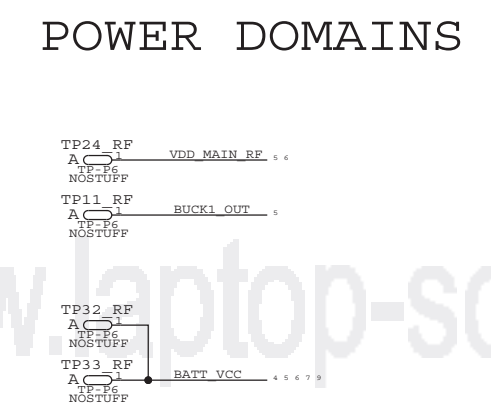
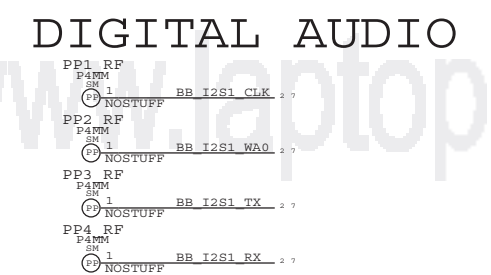
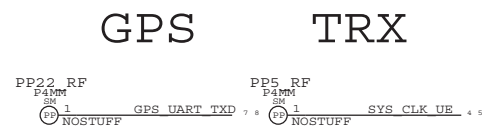
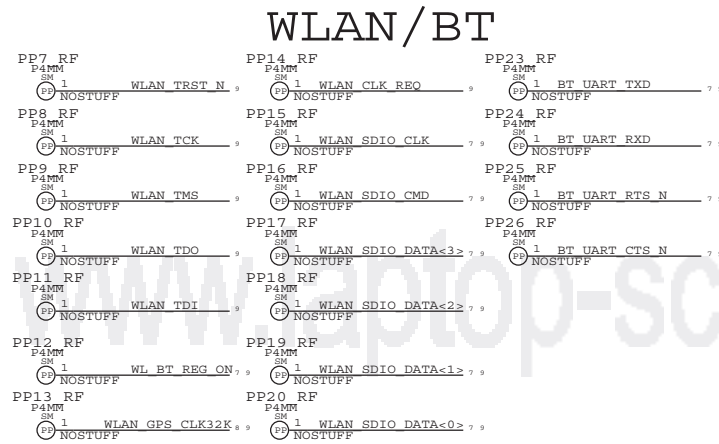
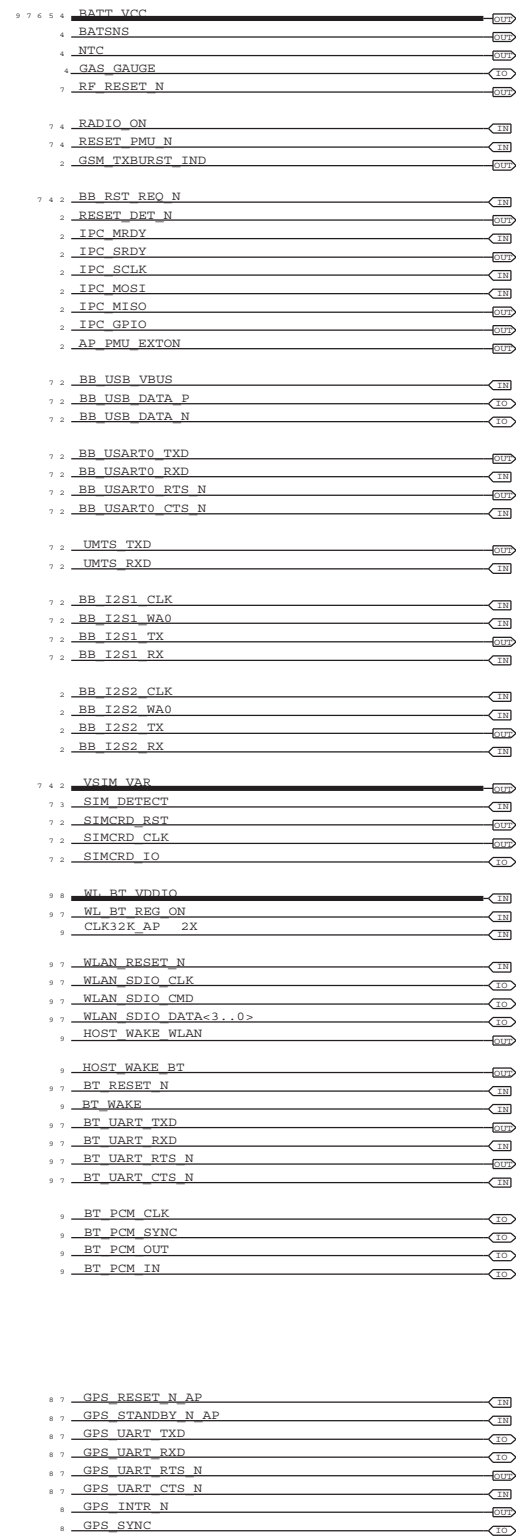
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TITLE: POWER AMPS & RF FRONT END
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SYSTEM CONNECTORS

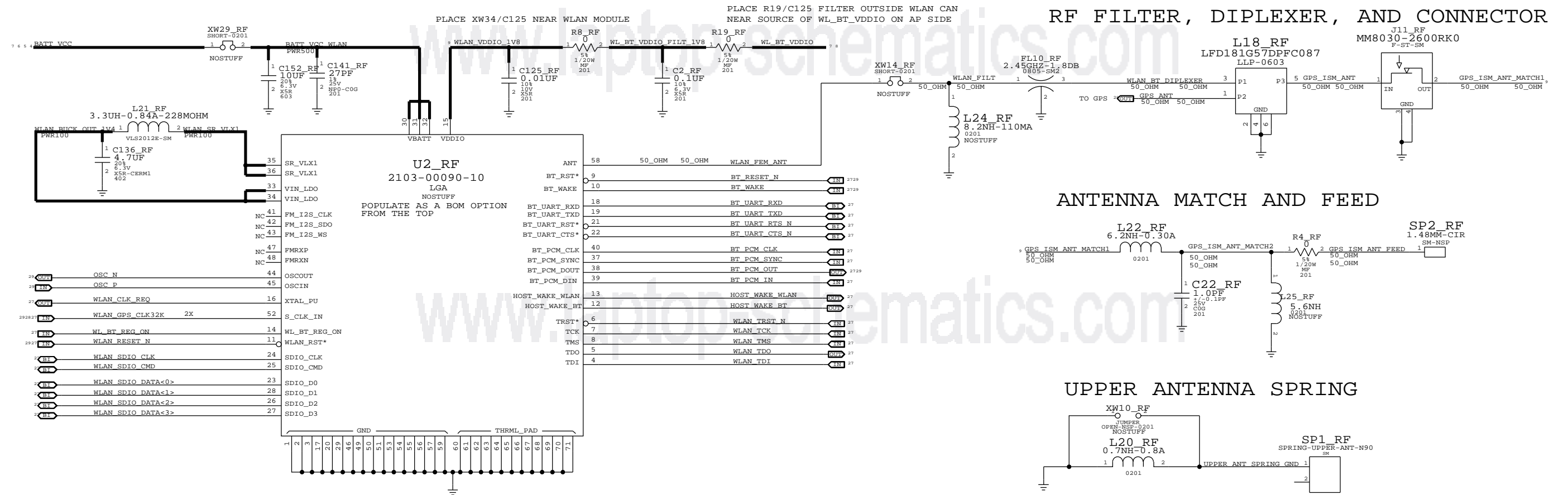
AP CONNECTIONS

DEBUG CONNECTOR



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WLAN/BLUETOOTH/FM RADIO



RF FILTER, DIPLEXER, AND CONNECTOR

ANTENNA MATCH AND FEED

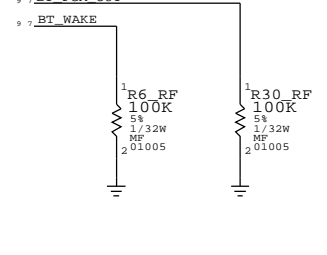
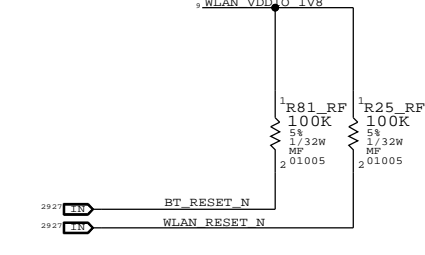
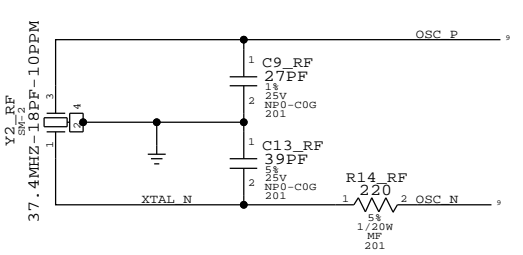
UPPER ANTENNA SPRING

32K INTERFACE TO AP

REFERENCE OSCILLATOR CRYSTAL

PULL UP RESISTORS

PULL DOWN RESISTORS



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