

8

7

6

5

4

3

2

1

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

SCHEM , MLB , M59

09/19/2006

Page

Contents

Sync

1

Table of Contents

N/A

2

System Block Diagram

N/A

3

Power Block Diagram

N/A

4

BOM Configuration

N/A

5

Functional / ICT Test

N/A

6

Signal Aliases/Misc Comps

N/A

7

CPU 1 OF 2-FSB

(MASTER)

8

CPU 2 OF 2-PWR/GND

(MASTER)

9

CPU Decoupling & VID

(MASTER)

10

CPU MISC1-TEMP SENSOR

(MASTER)

11

CPU ITP700FLEX DEBUG

(MASTER)

12

NB CPU Interface

(MASTER)

13

NB PEG / Video Interfaces

(MASTER)

14

NB Misc Interfaces

(MASTER)

15

NB DDR2 Interfaces

(MASTER)

16

NB Power 1

(MASTER)

17

NB Power 2

(MASTER)

18

NB Grounds

(MASTER)

19

NB (GM) Decoupling

M59_MG

20

NB Config Straps

(MASTER)

21

SB: 1 OF 4

(MASTER)

22

SB: 2 of 4

(MASTER)

23

SB: 3 OF 4

M59_MG

24

SB: 4 OF 4

(MASTER)

25

SB Decoupling

(MASTER)

26

SB Misc

(MASTER)

27

M1 SMBus Connections

(MASTER)

28

DDR2 SO-DIMM Connector A

(MASTER)

29

DDR2 SO-DIMM Connector B

(MASTER)

30

Memory Active Termination

(MASTER)

31

Memory Vtt Supply

(MASTER)

32

DDR2 VRef

(MASTER)

33

CLOCKS

(MASTER)

34

Clock Termination

M59_MG

35

Mobile Clocking

(MASTER)

36

PATA Connector

(MASTER)

37

FireWire Link (TSB83AA22)

(MASTER)

38

FireWire PHY (TSB83AA22)

(MASTER)

39

ETHERNET CONTROLLER

(MASTER)

40

Ethernet Connector

(MASTER)

41

Yukon Power Control

(MASTER)

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

55

ALS Support

(MASTER)

56

Fan Connectors

(MASTER)

57

Sudden Motion Sensor (SMS)

(MASTER)

58

TPM

(MASTER)

59

IMVP6 CPU VCore Regulator

(MASTER)

60

5V / 1.5V Power Supply

(MASTER)

61

2.5V & 1.2V Regulators

M59_MG

62

1.8V Supply

M59_MG

63

3.3V / 1.05V Power Supplies

M59_MG

64

3.3V G3Hot Supply & Power Control

M59_MG

65

Power Aliases

M59_MG

66

PBus-In,Batt. & 3G Pwr Connectors

(MASTER)

67

ATI M56 PCI-E

(MASTER)

68

GPU (M56) Core Supplies

(MASTER)

69

ATI M56 Core Power

(MASTER)

70

ATI M56 Frame Buffer I/F

(MASTER)

71

GPU Straps

M59_MG

72

GDDR3 Frame Buffer A

(MASTER)

73

GDDR3 Frame Buffer B

(MASTER)

74

ATI M56 GPIO/DVO/Misc

(MASTER)

75

ATI M56 Video Interfaces

(MASTER)

76

Internal Display Connectors

M59_MG

77

External Display Connector

M59_MG

78

M59 Specific Connectors

(MASTER)

79

LVDS Interface Pull-downs

M59_MG

80

Revision History

N/A

81

Napa Platform Constraints

(MASTER)

82

More System Constraints

(MASTER)

83

M59 Spacing & Physical Constraints

(MASTER)

84

M59 Net Properties

(MASTER)

REV

ZONE

ECN

DESCRIPTION OF CHANGE

CK APPD

ENG APPD

A

463525

PRODUCTION RELEASE

9/19/2006

9/19/2006

Page

Contents

Sync

1

Table of Contents

N/A

2

System Block Diagram

N/A

3

Power Block Diagram

N/A

4

BOM Configuration

N/A

5

Functional / ICT Test

N/A

6

Signal Aliases/Misc Comps

N/A

7

CPU 1 OF 2-FSB

(MASTER)

8

CPU 2 OF 2-PWR/GND

(MASTER)

9

CPU Decoupling & VID

(MASTER)

10

CPU MISC1-TEMP SENSOR

(MASTER)

11

CPU ITP700FLEX DEBUG

(MASTER)

12

NB CPU Interface

(MASTER)

13

NB PEG / Video Interfaces

(MASTER)

14

NB Misc Interfaces

(MASTER)

15

NB DDR2 Interfaces

(MASTER)

16

NB Power 1

(MASTER)

17

NB Power 2

(MASTER)

18

NB Grounds

(MASTER)

19

NB (GM) Decoupling

M59_MG

20

NB Config Straps

(MASTER)

21

SB: 1 OF 4

(MASTER)

22

SB: 2 of 4

(MASTER)

23

SB: 3 OF 4

M59_MG

24

SB: 4 OF 4

(MASTER)

25

SB Decoupling

(MASTER)

26

SB Misc

(MASTER)

27

M1 SMBus Connections

(MASTER)

28

DDR2 SO-DIMM Connector A

(MASTER)

29

DDR2 SO-DIMM Connector B

(MASTER)

30

Memory Active Termination

(MASTER)

31

Memory Vtt Supply

(MASTER)

32

DDR2 VRef

(MASTER)

33

CLOCKS

(MASTER)

34

Clock Termination

M59_MG

35

Mobile Clocking

(MASTER)

36

PATA Connector

(MASTER)

37

FireWire Link (TSB83AA22)

(MASTER)

38

FireWire PHY (TSB83AA22)

(MASTER)

39

ETHERNET CONTROLLER

(MASTER)

40

Ethernet Connector

(MASTER)

41

Yukon Power Control

(MASTER)

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

55

ALS Support

(MASTER)

56

Fan Connectors

(MASTER)

57

Sudden Motion Sensor (SMS)

(MASTER)

58

TPM

(MASTER)

59

IMVP6 CPU VCore Regulator

(MASTER)

60

5V / 1.5V Power Supply

(MASTER)

61

2.5V & 1.2V Regulators

M59_MG

62

1.8V Supply

M59_MG

63

3.3V / 1.05V Power Supplies

M59_MG

64

3.3V G3Hot Supply & Power Control

M59_MG

65

Power Aliases

M59_MG

66

PBus-In,Batt. & 3G Pwr Connectors

(MASTER)

67

ATI M56 PCI-E

(MASTER)

68

GPU (M56) Core Supplies

(MASTER)

69

ATI M56 Core Power

(MASTER)

70

ATI M56 Frame Buffer I/F

(MASTER)

71

GPU Straps

M59_MG

72

GDDR3 Frame Buffer A

(MASTER)

73

GDDR3 Frame Buffer B

(MASTER)

74

ATI M56 GPIO/DVO/Misc

(MASTER)

75

ATI M56 Video Interfaces

(MASTER)

76

Internal Display Connectors

M59_MG

77

External Display Connector

M59_MG

78

M59 Specific Connectors

(MASTER)

79

LVDS Interface Pull-downs

M59_MG

80

Revision History

N/A

81

Napa Platform Constraints

(MASTER)

82

More System Constraints

(MASTER)

83

M59 Spacing & Physical Constraints

(MASTER)

84

M59 Net Properties

(MASTER)

8

7

6

5

4

3

2

1

SCHEMATIC / PCB #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-7150	1	SCHEM,MLB,M59	SCH	CRITICAL	
820-2054	1	PCBF,MLB,M59	PCB	CRITICAL	

DRAWING

TITLE=M59_MLB

ABBREV=DRAWING

LAST MODIFIED=Mon Sep 25 10:45:58 2006

8

7

6

5

4

3

2

1

SCHEM , MLB , M59

09/19/2006

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

55

ALS Support

(MASTER)

56

Fan Connectors

(MASTER)

57

Sudden Motion Sensor (SMS)

(MASTER)

58

TPM

(MASTER)

59

IMVP6 CPU VCore Regulator

(MASTER)

60

5V / 1.5V Power Supply

(MASTER)

61

2.5V & 1.2V Regulators

M59_MG

62

1.8V Supply

M59_MG

63

3.3V / 1.05V Power Supplies

M59_MG

64

3.3V G3Hot Supply & Power Control

M59_MG

65

Power Aliases

M59_MG

66

PBus-In,Batt. & 3G Pwr Connectors

(MASTER)

67

ATI M56 PCI-E

(MASTER)

68

GPU (M56) Core Supplies

(MASTER)

69

ATI M56 Core Power

(MASTER)

70

ATI M56 Frame Buffer I/F

(MASTER)

71

GPU Straps

M59_MG

72

GDDR3 Frame Buffer A

(MASTER)

73

GDDR3 Frame Buffer B

(MASTER)

74

ATI M56 GPIO/DVO/Misc

(MASTER)

75

ATI M56 Video Interfaces

(MASTER)

76

Internal Display Connectors

M59_MG

77

External Display Connector

M59_MG

78

M59 Specific Connectors

(MASTER)

79

LVDS Interface Pull-downs

M59_MG

80

Revision History

N/A

81

Napa Platform Constraints

(MASTER)

82

More System Constraints

(MASTER)

83

M59 Spacing & Physical Constraints

(MASTER)

84

M59 Net Properties

(MASTER)

8

7

6

5

4

3

2

1

SCHEMATIC / PCB #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-7150	1	SCHEM,MLB,M59	SCH	CRITICAL	
820-2054	1	PCBF,MLB,M59	PCB	CRITICAL	

DRAWING

TITLE=M59_MLB

ABBREV=DRAWING

LAST MODIFIED=Mon Sep 25 10:45:58 2006

8

7

6

5

4

3

2

1

SCHEM , MLB , M59

09/19/2006

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

55

ALS Support

(MASTER)

56

Fan Connectors

(MASTER)

57

Sudden Motion Sensor (SMS)

(MASTER)

58

TPM

(MASTER)

59

IMVP6 CPU VCore Regulator

(MASTER)

60

5V / 1.5V Power Supply

(MASTER)

61

2.5V & 1.2V Regulators

M59_MG

62

1.8V Supply

M59_MG

63

3.3V / 1.05V Power Supplies

M59_MG

64

3.3V G3Hot Supply & Power Control

M59_MG

65

Power Aliases

M59_MG

66

PBus-In,Batt. & 3G Pwr Connectors

(MASTER)

67

ATI M56 PCI-E

(MASTER)

68

GPU (M56) Core Supplies

(MASTER)

69

ATI M56 Core Power

(MASTER)

70

ATI M56 Frame Buffer I/F

(MASTER)

71

GPU Straps

M59_MG

72

GDDR3 Frame Buffer A

(MASTER)

73

GDDR3 Frame Buffer B

(MASTER)

74

ATI M56 GPIO/DVO/Misc

(MASTER)

75

ATI M56 Video Interfaces

(MASTER)

76

Internal Display Connectors

M59_MG

77

External Display Connector

M59_MG

78

M59 Specific Connectors

(MASTER)

79

LVDS Interface Pull-downs

M59_MG

80

Revision History

N/A

81

Napa Platform Constraints

(MASTER)

82

More System Constraints

(MASTER)

83

M59 Spacing & Physical Constraints

(MASTER)

84

M59 Net Properties

(MASTER)

8

7

6

5

4

3

2

1

SCHEMATIC / PCB #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-7150	1	SCHEM,MLB,M59	SCH	CRITICAL	
820-2054	1	PCBF,MLB,M59	PCB	CRITICAL	

DRAWING

TITLE=M59_MLB

ABBREV=DRAWING

LAST MODIFIED=Mon Sep 25 10:45:58 2006

8

7

6

5

4

3

2

1

SCHEM , MLB , M59

09/19/2006

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

55

ALS Support

(MASTER)

56

Fan Connectors

(MASTER)

57

Sudden Motion Sensor (SMS)

(MASTER)

58

TPM

(MASTER)

59

IMVP6 CPU VCore Regulator

(MASTER)

60

5V / 1.5V Power Supply

(MASTER)

61

2.5V & 1.2V Regulators

M59_MG

62

1.8V Supply

M59_MG

63

3.3V / 1.05V Power Supplies

M59_MG

64

3.3V G3Hot Supply & Power Control

M59_MG

65

Power Aliases

M59_MG

66

PBus-In,Batt. & 3G Pwr Connectors

(MASTER)

67

ATI M56 PCI-E

(MASTER)

68

GPU (M56) Core Supplies

(MASTER)

69

ATI M56 Core Power

(MASTER)

70

ATI M56 Frame Buffer I/F

(MASTER)

71

GPU Straps

M59_MG

72

GDDR3 Frame Buffer A

(MASTER)

73

GDDR3 Frame Buffer B

(MASTER)

74

ATI M56 GPIO/DVO/Misc

(MASTER)

75

ATI M56 Video Interfaces

(MASTER)

76

Internal Display Connectors

M59_MG

77

External Display Connector

M59_MG

78

M59 Specific Connectors

(MASTER)

79

LVDS Interface Pull-downs

M59_MG

80

Revision History

N/A

81

Napa Platform Constraints

(MASTER)

82

More System Constraints

(MASTER)

83

M59 Spacing & Physical Constraints

(MASTER)

84

M59 Net Properties

(MASTER)

8

7

6

5

4

3

2

1

SCHEMATIC / PCB #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-7150	1	SCHEM,MLB,M59	SCH	CRITICAL	
820-2054	1	PCBF,MLB,M59	PCB	CRITICAL	

DRAWING

TITLE=M59_MLB

ABBREV=DRAWING

LAST MODIFIED=Mon Sep 25 10:45:58 2006

8

7

6

5

4

3

2

1

SCHEM , MLB , M59

09/19/2006

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

55

ALS Support

(MASTER)

56

Fan Connectors

(MASTER)

57

Sudden Motion Sensor (SMS)

(MASTER)

58

TPM

(MASTER)

59

IMVP6 CPU VCore Regulator

(MASTER)

60

5V / 1.5V Power Supply

(MASTER)

61

2.5V & 1.2V Regulators

M59_MG

62

1.8V Supply

M59_MG

63

3.3V / 1.05V Power Supplies

M59_MG

64

3.3V G3Hot Supply & Power Control

M59_MG

65

Power Aliases

M59_MG

66

PBus-In,Batt. & 3G Pwr Connectors

(MASTER)

67

ATI M56 PCI-E

(MASTER)

68

GPU (M56) Core Supplies

(MASTER)

69

ATI M56 Core Power

(MASTER)

70

ATI M56 Frame Buffer I/F

(MASTER)

71

GPU Straps

M59_MG

72

GDDR3 Frame Buffer A

(MASTER)

73

GDDR3 Frame Buffer B

(MASTER)

74

ATI M56 GPIO/DVO/Misc

(MASTER)

75

ATI M56 Video Interfaces

(MASTER)

76

Internal Display Connectors

M59_MG

77

External Display Connector

M59_MG

78

M59 Specific Connectors

(MASTER)

79

LVDS Interface Pull-downs

M59_MG

80

Revision History

N/A

81

Napa Platform Constraints

(MASTER)

82

More System Constraints

(MASTER)

83

M59 Spacing & Physical Constraints

(MASTER)

84

M59 Net Properties

(MASTER)

8

7

6

5

4

3

2

1

SCHEMATIC / PCB #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-7150	1	SCHEM,MLB,M59	SCH	CRITICAL	
820-2054	1	PCBF,MLB,M59	PCB	CRITICAL	

DRAWING

TITLE=M59_MLB

ABBREV=DRAWING

LAST MODIFIED=Mon Sep 25 10:45:58 2006

8

7

6

5

4

3

2

1

SCHEM , MLB , M59

09/19/2006

Page

Contents

Sync

42

FW PHY Power Supply

(MASTER)

43

FireWire Port Power

(MASTER)

44

FireWire Ports

(MASTER)

45

Camera Connector

(MASTER)

46

External USB Connector

(MASTER)

47

Left I/O Board Connector

(MASTER)

48

PCI-E Connections

(MASTER)

49

SMC

(MASTER)

50

SMC Support

(MASTER)

51

LPC+ Debug Connector

(MASTER)

52

Thermal Sensors

(MASTER)

53

Current & Voltage Sensing

(MASTER)

54

SPI BOOTROM

(MASTER)

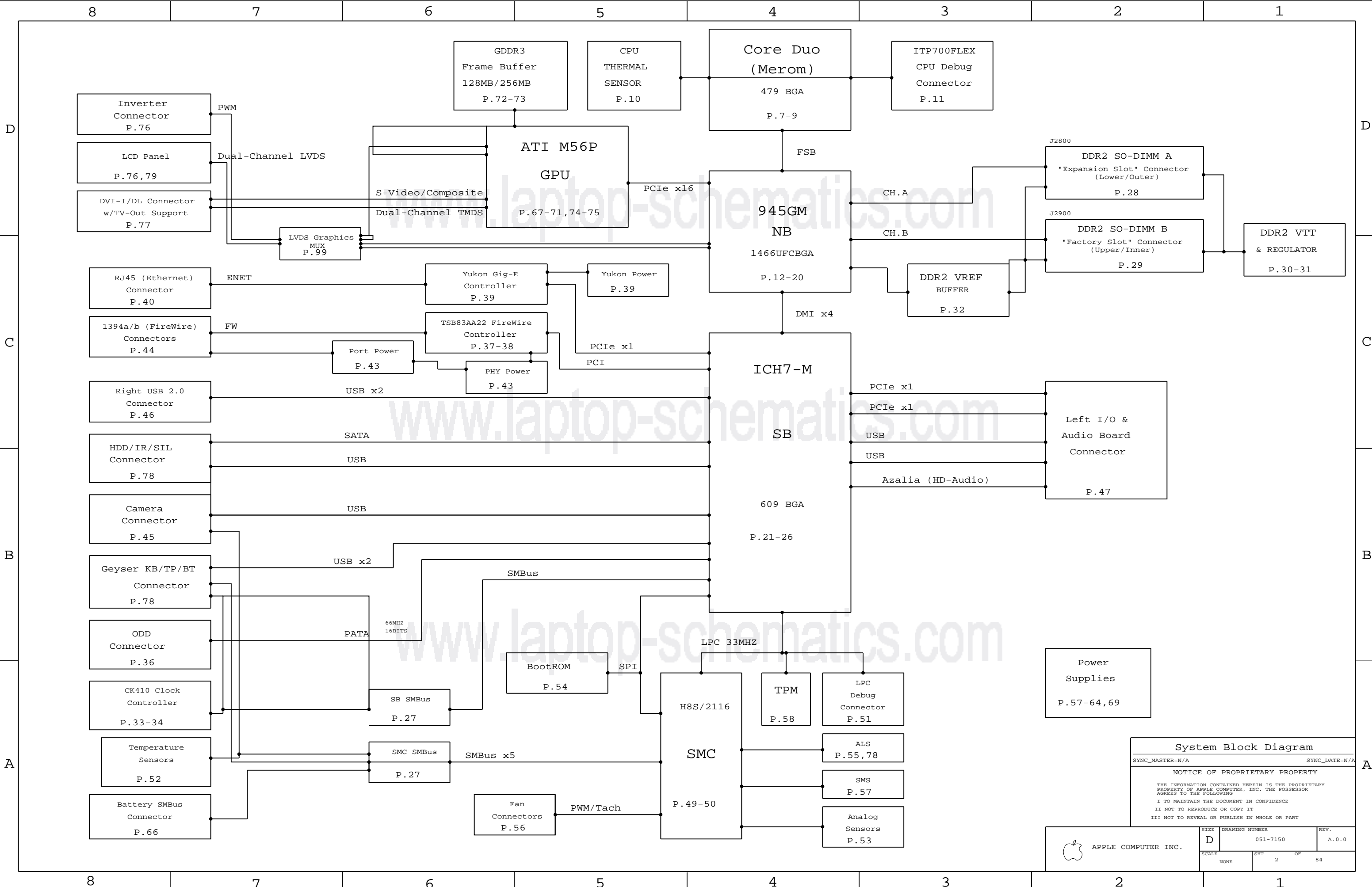
55

ALS Support

(MASTER)

56

<



System Block Diagram

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

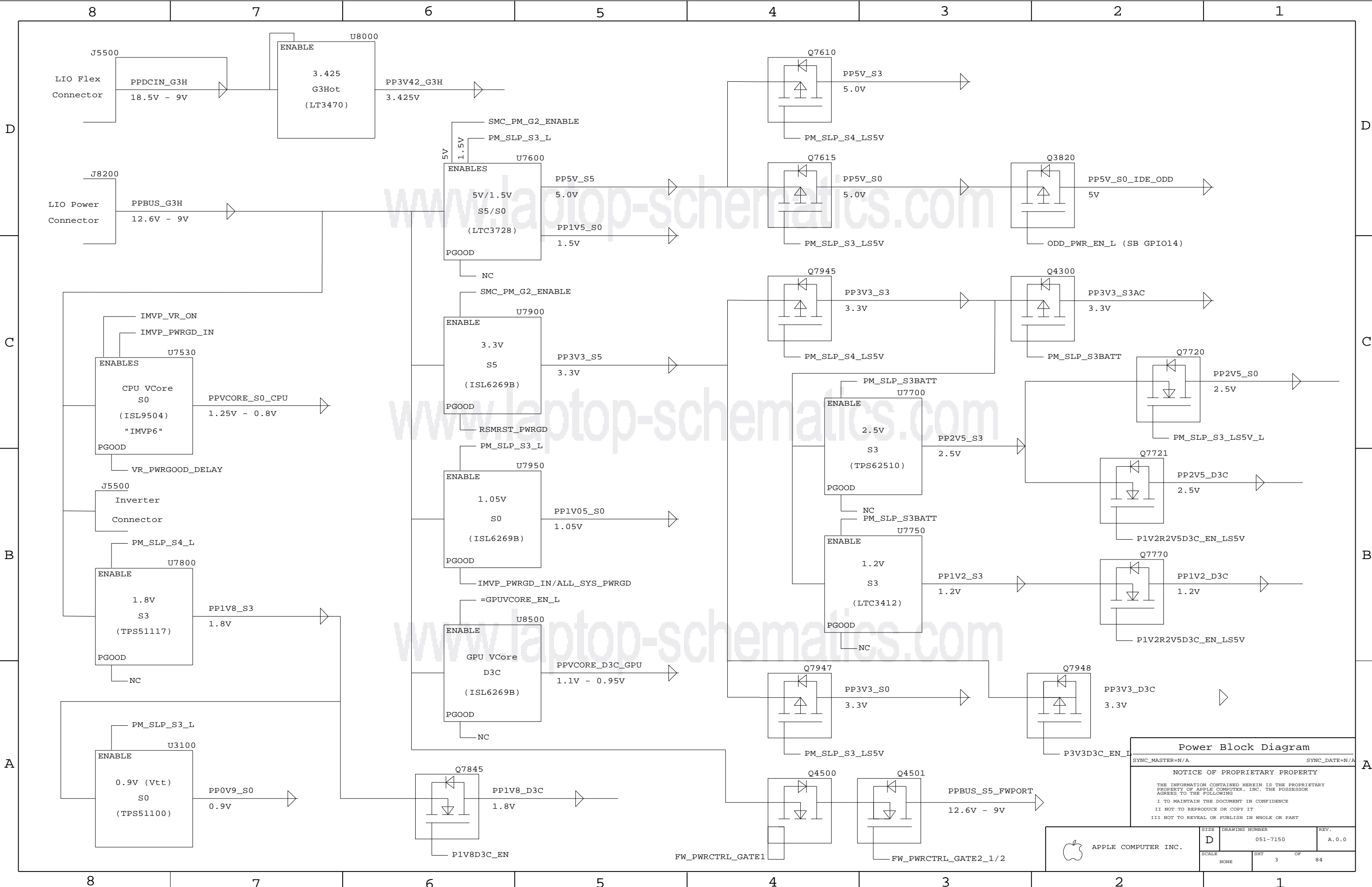
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 2	OF 84



Power Block Diagram

SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

SCALE: NONE

SIZE: D

SHT: 3

OF: 84

DRAWING NUMBER: 051-7150

REV.: A.0.0

8

7

6

5

4

3

2

1

2.16Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7849	PCBA, 2.16GHZ, 128VRAM, M59, MBP15	EEE_WTE, M59_COMMON, CPU_2_16GHZ, VRAM_SAM128

2.33Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7851	PCBA, 2.33GHZ, 256VRAM, M59, MBP15	EEE_WTG, M59_COMMON, CPU_2_33GHZ, VRAM_SAM256

Bar Code Label / EEE #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTE]	CRITICAL	EEE_WTE
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTG]	CRITICAL	EEE_WTG

BOMOPTION Groups

BOM GROUP	BOM OPTIONS
M59_COMMON	ALTERNATE, COMMON, M59_COMMON1, M59_COMMON2, M59_COMMON3
M59_COMMON1	BOOTROM_FINAL, ENET_LOWPWR_EN, ENETPWR_S3AC, GPU_BB_CTL, D3CPGOOD_3V3
M59_COMMON2	ITP, KBDLED_HAS, LPCPLUS, LVDS_PD, MEMVREF_S3
M59_COMMON3	MEMVTT_EN_PU, RTUSB_ESD, SMC_PRGRM, USB_C_OC_PU, USB_D_OC_PU, USB_E_OC_PU
VRAM_INF128	GPU_MEM_NOT_SAM, VRAM_128_INFINEON
VRAM_SAM128	VRAM_128_SAMSUNG
VRAM_INF256	GPU_MEM_256M, GPU_MEM_NOT_SAM, VRAM_256_INFINEON
VRAM_SAM256	GPU_MEM_256M, VRAM_256_SAMSUNG
M59_TPM	TPM

Module Parts

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0354	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_SAMSUNG
333S0350	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_SAMSUNG
333S0358	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_HYNIX
333S0351	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_HYNIX
333S0376	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_INFINEON
333S0377	4	IC, SGRAM, GDDR3, 16MX32, 600MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_INFINEON
337S3391	1	IC, MDC, B2, PRQ, 2.16G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_16GHZ
337S3393	1	IC, MDC, B2, PRQ, 2.33G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_33GHZ
341S1922	1	IC, EFI, BOOTROM DEVELOPMENT (UNLOCKED), M59	U6301	CRITICAL	BOOTROM_DEVEL
341S1923	1	IC, EFI, BOOTROM FINAL (LOCKED), M59	U6301	CRITICAL	BOOTROM_FINAL
338S0274	1	IC, SMC, HSB/2116	U5800	CRITICAL	SMC_BLANK
341S1929	1	IC, PRGRM, SMC (NEW), M59	U5800	CRITICAL	SMC_PRGRM
338S0269	1	IC, 945GM, NORTHERIDGE	U1200	CRITICAL	
338S0270	1	IC, 888053, GIGABIT ENET XCVR, 64P QFN, HO	U4101	CRITICAL	
338S0368	1	IC, ATI, M561-LLP, GRAPHICCTL, LP 880BGA	U8400	CRITICAL	
341S1789	1	IC, TPM, 28-PIN TSSOP	U6700	CRITICAL	TPM
341S1797	1	IC, EEPROM, SERIAL IIC, 8KBIT, SO8	U4102	CRITICAL	
343S0385	1	IC, ICH7M, BGA	U2100	CRITICAL	
353S1461	1	IC, IXL8804, SYNC REG CTRL, QFN48	U7530	CRITICAL	
359S0109	1	LOW POWER CLOCK SYNTHESIZER, 68PIN	U3301	CRITICAL	

Alternate Parts

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
128S0094	128S0060		ALL	330uF, 2V, 5MOHM, D2
128S0095	128S0060		ALL	330uF, 2V, 6MOHM, D2
128S0081	128S0061		ALL	150uF, 6.3V, 25MOHM, C2
376S0448	376S0445		ALL	S17806ADN for P1M6296
353S1465	353S1461		ALL	Screwed IOL6282 for IOL9504
152S0287	152S0435		ALL	Alternate for collocation models
128S0093	128S0092		ALL	33uF, 16V, D2

8

7

6

5

4

3

2

1

8

7

6

5

4

3

2

1

2.16Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7849	PCBA, 2.16GHZ, 128VRAM, M59, MBP15	EEE_WTE, M59_COMMON, CPU_2_16GHZ, VRAM_SAM128

2.33Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7851	PCBA, 2.33GHZ, 256VRAM, M59, MBP15	EEE_WTG, M59_COMMON, CPU_2_33GHZ, VRAM_SAM256

Bar Code Label / EEE #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTE]	CRITICAL	EEE_WTE
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTG]	CRITICAL	EEE_WTG

BOMOPTION Groups

BOM GROUP	BOM OPTIONS
M59_COMMON	ALTERNATE, COMMON, M59_COMMON1, M59_COMMON2, M59_COMMON3
M59_COMMON1	BOOTROM_FINAL, ENET_LOWPWR_EN, ENETPWR_S3AC, GPU_BB_CTL, D3CPGOOD_3V3
M59_COMMON2	ITP, KBDLED_HAS, LPCPLUS, LVDS_PD, MEMVREF_S3
M59_COMMON3	MEMVTT_EN_PU, RTUSB_ESD, SMC_PRGRM, USB_C_OC_PU, USB_D_OC_PU, USB_E_OC_PU
VRAM_INF128	GPU_MEM_NOT_SAM, VRAM_128_INFINEON
VRAM_SAM128	VRAM_128_SAMSUNG
VRAM_INF256	GPU_MEM_256M, GPU_MEM_NOT_SAM, VRAM_256_INFINEON
VRAM_SAM256	GPU_MEM_256M, VRAM_256_SAMSUNG
M59_TPM	TPM

Module Parts

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0354	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_SAMSUNG
333S0350	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_SAMSUNG
333S0358	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_HYNIX
333S0351	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_HYNIX
333S0376	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_INFINEON
333S0377	4	IC, SGRAM, GDDR3, 16MX32, 600MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_INFINEON
337S3391	1	IC, MDC, B2, PRQ, 2.16G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_16GHZ
337S3393	1	IC, MDC, B2, PRQ, 2.33G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_33GHZ
341S1922	1	IC, EFI, BOOTROM DEVELOPMENT (UNLOCKED), M59	U6301	CRITICAL	BOOTROM_DEVEL
341S1923	1	IC, EFI, BOOTROM FINAL (LOCKED), M59	U6301	CRITICAL	BOOTROM_FINAL
338S0274	1	IC, SMC, HSB/2116	U5800	CRITICAL	SMC_BLANK
341S1929	1	IC, PRGRM, SMC (NEW), M59	U5800	CRITICAL	SMC_PRGRM
338S0269	1	IC, 945GM, NORTHERIDGE	U1200	CRITICAL	
338S0270	1	IC, 888053, GIGABIT ENET XCVR, 64P QFN, HO	U4101	CRITICAL	
338S0368	1	IC, ATI, M561-LLP, GRAPHICCTL, LP 880BGA	U8400	CRITICAL	
341S1789	1	IC, TPM, 28-PIN TSSOP	U6700	CRITICAL	TPM
341S1797	1	IC, EEPROM, SERIAL IIC, 8KBIT, SO8	U4102	CRITICAL	
343S0385	1	IC, ICH7M, BGA	U2100	CRITICAL	
353S1461	1	IC, IXL8804, SYNC REG CTRL, QFN48	U7530	CRITICAL	
359S0109	1	LOW POWER CLOCK SYNTHESIZER, 68PIN	U3301	CRITICAL	

Alternate Parts

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
128S0094	128S0060		ALL	330uF, 2V, 5MOHM, D2
128S0095	128S0060		ALL	330uF, 2V, 6MOHM, D2
128S0081	128S0061		ALL	150uF, 6.3V, 25MOHM, C2
376S0448	376S0445		ALL	S17806ADN for P1M6296
353S1465	353S1461		ALL	Screwed IOL6282 for IOL9504
152S0287	152S0435		ALL	Alternate for collocation models
128S0093	128S0092		ALL	33uF, 16V, D2

8

7

6

5

4

3

2

1

8

7

6

5

4

3

2

1

2.16Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7849	PCBA, 2.16GHZ, 128VRAM, M59, MBP15	EEE_WTE, M59_COMMON, CPU_2_16GHZ, VRAM_SAM128

2.33Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7851	PCBA, 2.33GHZ, 256VRAM, M59, MBP15	EEE_WTG, M59_COMMON, CPU_2_33GHZ, VRAM_SAM256

Bar Code Label / EEE #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTE]	CRITICAL	EEE_WTE
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTG]	CRITICAL	EEE_WTG

BOMOPTION Groups

BOM GROUP	BOM OPTIONS
M59_COMMON	ALTERNATE, COMMON, M59_COMMON1, M59_COMMON2, M59_COMMON3
M59_COMMON1	BOOTROM_FINAL, ENET_LOWPWR_EN, ENETPWR_S3AC, GPU_BB_CTL, D3CPGOOD_3V3
M59_COMMON2	ITP, KBDLED_HAS, LPCPLUS, LVDS_PD, MEMVREF_S3
M59_COMMON3	MEMVTT_EN_PU, RTUSB_ESD, SMC_PRGRM, USB_C_OC_PU, USB_D_OC_PU, USB_E_OC_PU
VRAM_INF128	GPU_MEM_NOT_SAM, VRAM_128_INFINEON
VRAM_SAM128	VRAM_128_SAMSUNG
VRAM_INF256	GPU_MEM_256M, GPU_MEM_NOT_SAM, VRAM_256_INFINEON
VRAM_SAM256	GPU_MEM_256M, VRAM_256_SAMSUNG
M59_TPM	TPM

Module Parts

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0354	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_SAMSUNG
333S0350	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_SAMSUNG
333S0358	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_HYNIX
333S0351	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_HYNIX
333S0376	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_INFINEON
333S0377	4	IC, SGRAM, GDDR3, 16MX32, 600MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_INFINEON
337S3391	1	IC, MDC, B2, PRQ, 2.16G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_16GHZ
337S3393	1	IC, MDC, B2, PRQ, 2.33G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_33GHZ
341S1922	1	IC, EFI, BOOTROM DEVELOPMENT (UNLOCKED), M59	U6301	CRITICAL	BOOTROM_DEVEL
341S1923	1	IC, EFI, BOOTROM FINAL (LOCKED), M59	U6301	CRITICAL	BOOTROM_FINAL
338S0274	1	IC, SMC, HSB/2116	U5800	CRITICAL	SMC_BLANK
341S1929	1	IC, PRGRM, SMC (NEW), M59	U5800	CRITICAL	SMC_PRGRM
338S0269	1	IC, 945GM, NORTHERIDGE	U1200	CRITICAL	
338S0270	1	IC, 888053, GIGABIT ENET XCVR, 64P QFN, HO	U4101	CRITICAL	
338S0368	1	IC, ATI, M561-LLP, GRAPHICCTL, LP 880BGA	U8400	CRITICAL	
341S1789	1	IC, TPM, 28-PIN TSSOP	U6700	CRITICAL	TPM
341S1797	1	IC, EEPROM, SERIAL IIC, 8KBIT, SO8	U4102	CRITICAL	
343S0385	1	IC, ICH7M, BGA	U2100	CRITICAL	
353S1461	1	IC, IXL8804, SYNC REG CTRL, QFN48	U7530	CRITICAL	
359S0109	1	LOW POWER CLOCK SYNTHESIZER, 68PIN	U3301	CRITICAL	

Alternate Parts

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
128S0094	128S0060		ALL	330uF, 2V, 5MOHM, D2
128S0095	128S0060		ALL	330uF, 2V, 6MOHM, D2
128S0081	128S0061		ALL	150uF, 6.3V, 25MOHM, C2
376S0448	376S0445		ALL	S17806ADN for P1M6296
353S1465	353S1461		ALL	Screwed IOL6282 for IOL9504
152S0287	152S0435		ALL	Alternate for collocation models
128S0093	128S0092		ALL	33uF, 16V, D2

8

7

6

5

4

3

2

1

8

7

6

5

4

3

2

1

2.16Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7849	PCBA, 2.16GHZ, 128VRAM, M59, MBP15	EEE_WTE, M59_COMMON, CPU_2_16GHZ, VRAM_SAM128

2.33Ghz BOMs

BOM NUMBER	BOM NAME	BOM OPTIONS
630-7851	PCBA, 2.33GHZ, 256VRAM, M59, MBP15	EEE_WTG, M59_COMMON, CPU_2_33GHZ, VRAM_SAM256

Bar Code Label / EEE #'s

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTE]	CRITICAL	EEE_WTE
826-4393	1	LBL, P/N LABEL, PCB, 28MM X 6 MM	[EEE:WTG]	CRITICAL	EEE_WTG

BOMOPTION Groups

BOM GROUP	BOM OPTIONS
M59_COMMON	ALTERNATE, COMMON, M59_COMMON1, M59_COMMON2, M59_COMMON3
M59_COMMON1	BOOTROM_FINAL, ENET_LOWPWR_EN, ENETPWR_S3AC, GPU_BB_CTL, D3CPGOOD_3V3
M59_COMMON2	ITP, KBDLED_HAS, LPCPLUS, LVDS_PD, MEMVREF_S3
M59_COMMON3	MEMVTT_EN_PU, RTUSB_ESD, SMC_PRGRM, USB_C_OC_PU, USB_D_OC_PU, USB_E_OC_PU
VRAM_INF128	GPU_MEM_NOT_SAM, VRAM_128_INFINEON
VRAM_SAM128	VRAM_128_SAMSUNG
VRAM_INF256	GPU_MEM_256M, GPU_MEM_NOT_SAM, VRAM_256_INFINEON
VRAM_SAM256	GPU_MEM_256M, VRAM_256_SAMSUNG
M59_TPM	TPM

Module Parts

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0354	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_SAMSUNG
333S0350	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_SAMSUNG
333S0358	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_HYNIX
333S0351	4	IC, SGRAM, GDDR3, 16MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_HYNIX
333S0376	4	IC, SGRAM, GDDR3, 8MX32, 700MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_128_INFINEON
333S0377	4	IC, SGRAM, GDDR3, 16MX32, 600MHZ, 136 FBGA	U8900, U8950, U9000, U9050	CRITICAL	VRAM_256_INFINEON
337S3391	1	IC, MDC, B2, PRQ, 2.16G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_16GHZ
337S3393	1	IC, MDC, B2, PRQ, 2.33G, 34W, 667M, 4M, 479BGA	U0700	CRITICAL	CPU_2_33GHZ
341S1922	1	IC, EFI, BOOTROM DEVELOPMENT (UNLOCKED), M59	U6301	CRITICAL	BOOTROM_DEVEL
341S1923	1	IC, EFI, BOOTROM FINAL (LOCKED), M59	U6301	CRITICAL	BOOTROM_FINAL
338S0274	1	IC, SMC, HSB/2116	U5800	CRITICAL	SMC_BLANK
341S1929	1	IC, PRGRM, SMC (NEW), M59	U5800	CRITICAL	SMC_PRGRM
338S0269	1	IC, 945GM, NORTHERIDGE	U1200	CRITICAL	
338S0270	1	IC, 888053, GIGABIT ENET XCVR, 64P QFN, HO	U4101	CRITICAL	
338S0368	1	IC, ATI, M561-LLP, GRAPHICCTL, LP 880BGA	U8400	CRITICAL	
341S1789	1	IC, TPM, 28-PIN TSSOP	U6700	CRITICAL	TPM
341S1797	1	IC, EEPROM, SERIAL IIC, 8KBIT, SO8	U4102	CRITICAL	
343S0385	1	IC, ICH7M, BGA	U2100	CRITICAL	
353S1461	1	IC, IXL8804, SYNC REG CTRL, QFN48	U7530	CRITICAL	
359S0109	1	LOW POWER CLOCK SYNTHESIZER, 68PIN	U3301	CRITICAL	

Alternate Parts

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
128S0094	128S0060		ALL	330uF, 2V, 5MOHM, D2
128S0095	128S0060		ALL	330uF, 2V, 6MOHM, D2
128S0081	128S0061		ALL	150uF, 6.3V, 25MOHM, C2
376S0448	376S0445		ALL	S17806ADN for P1M6296
353S1465	353S1461		ALL	Screwed IOL6282 for IOL9504
152S0287	152S0435		ALL	Alternate for collocation models
128S0093	128S0092		ALL	33uF, 16V, D2

8

7

6

5

4

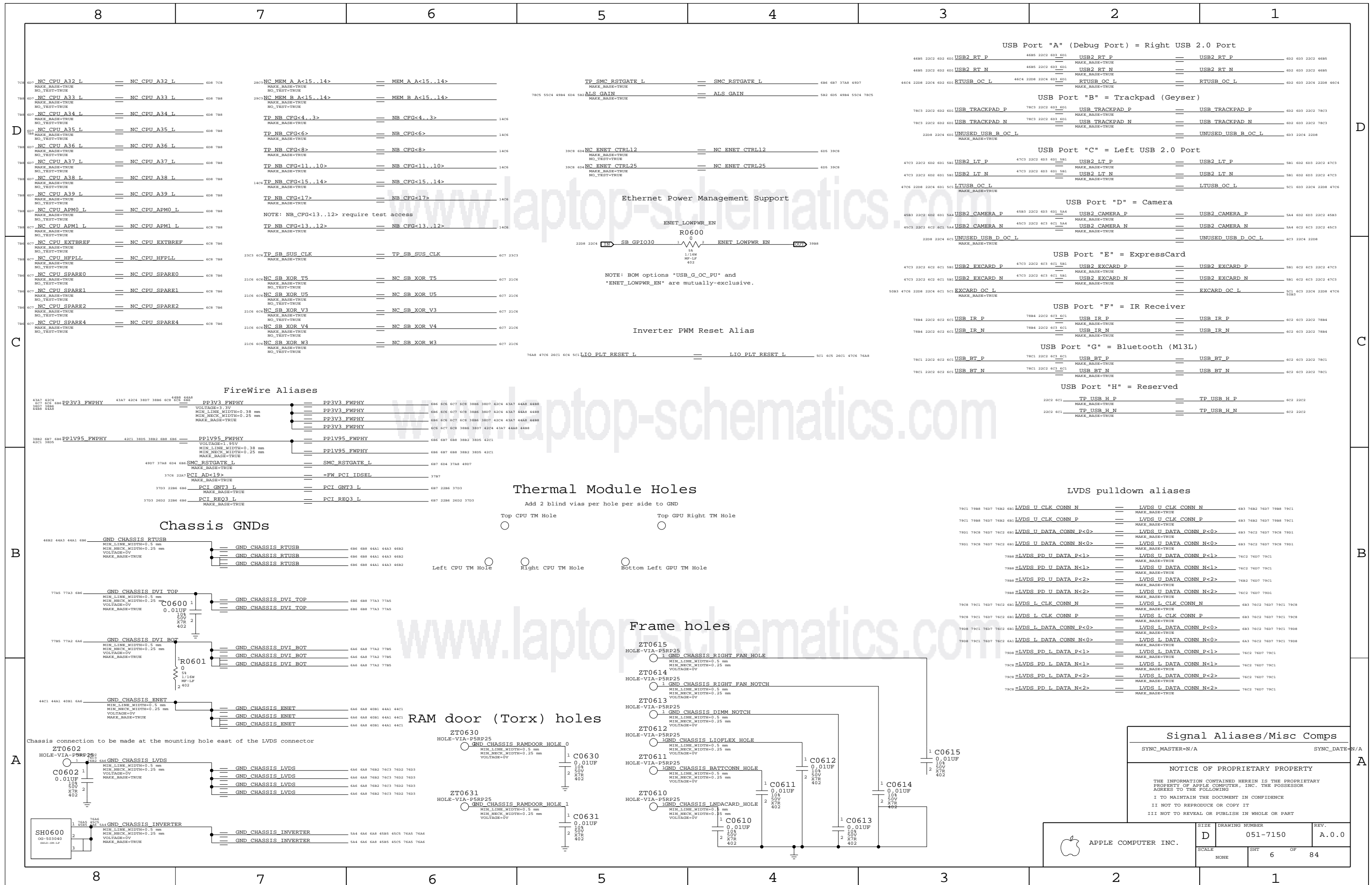
3

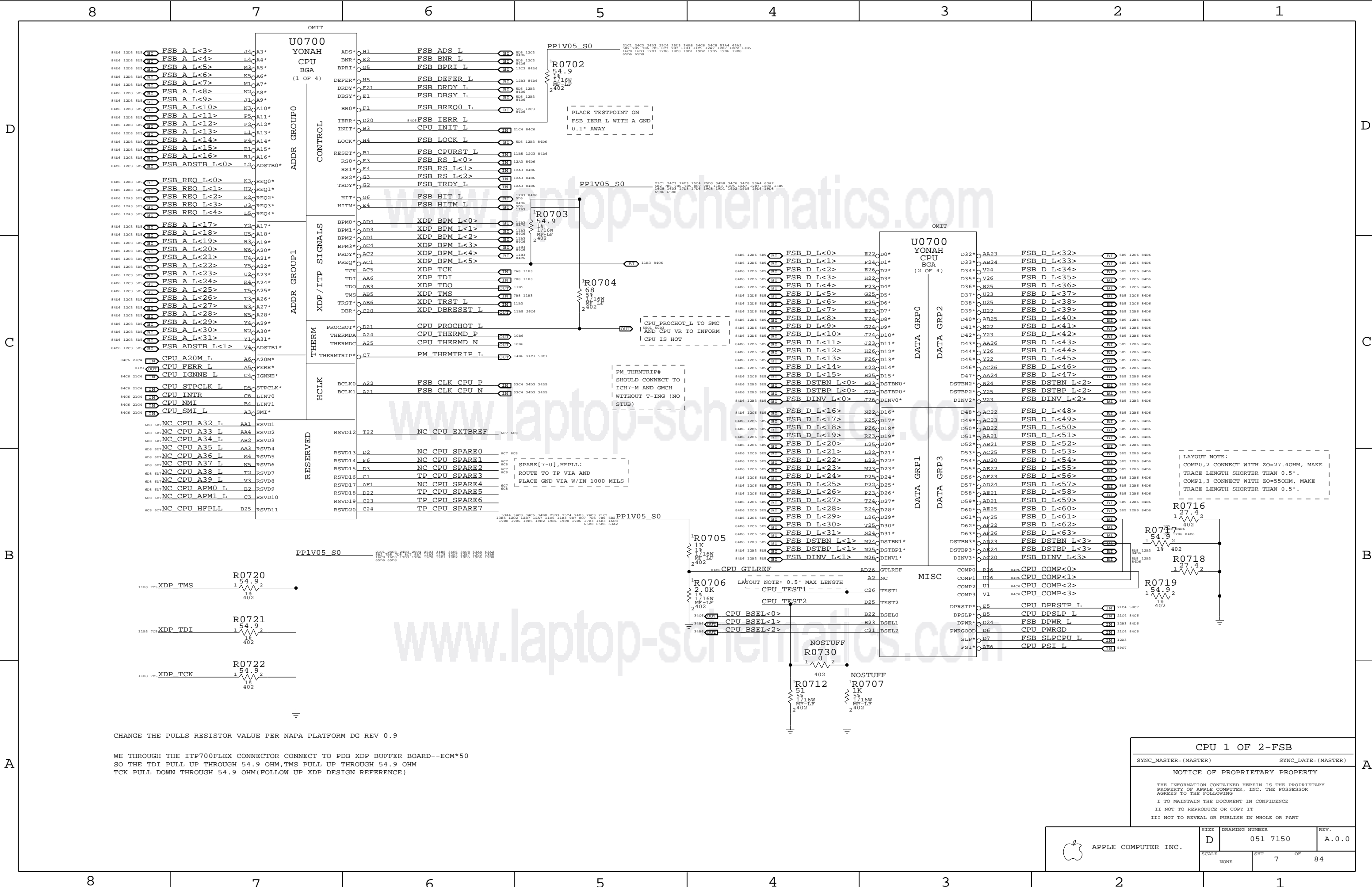
2

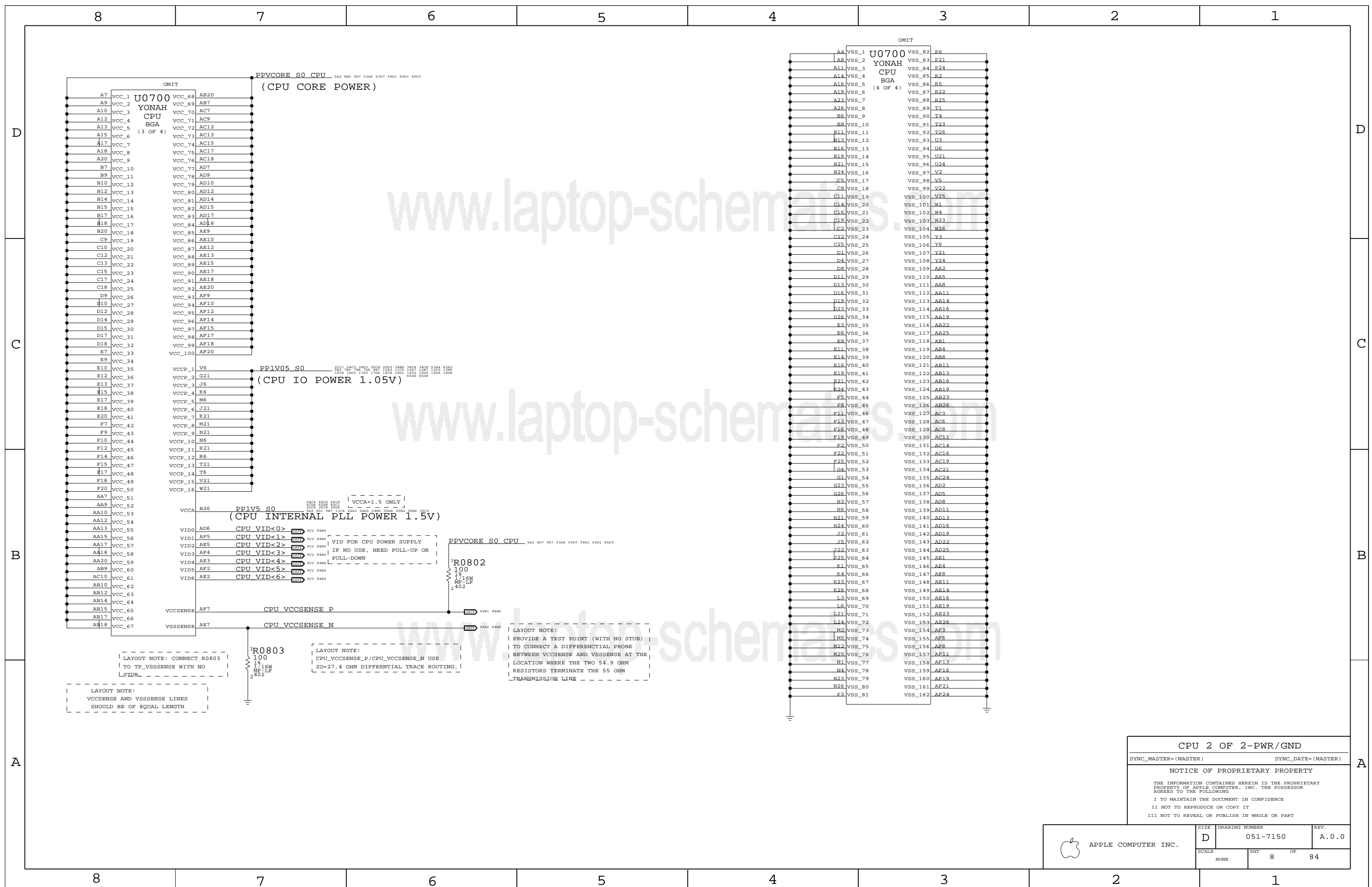
1

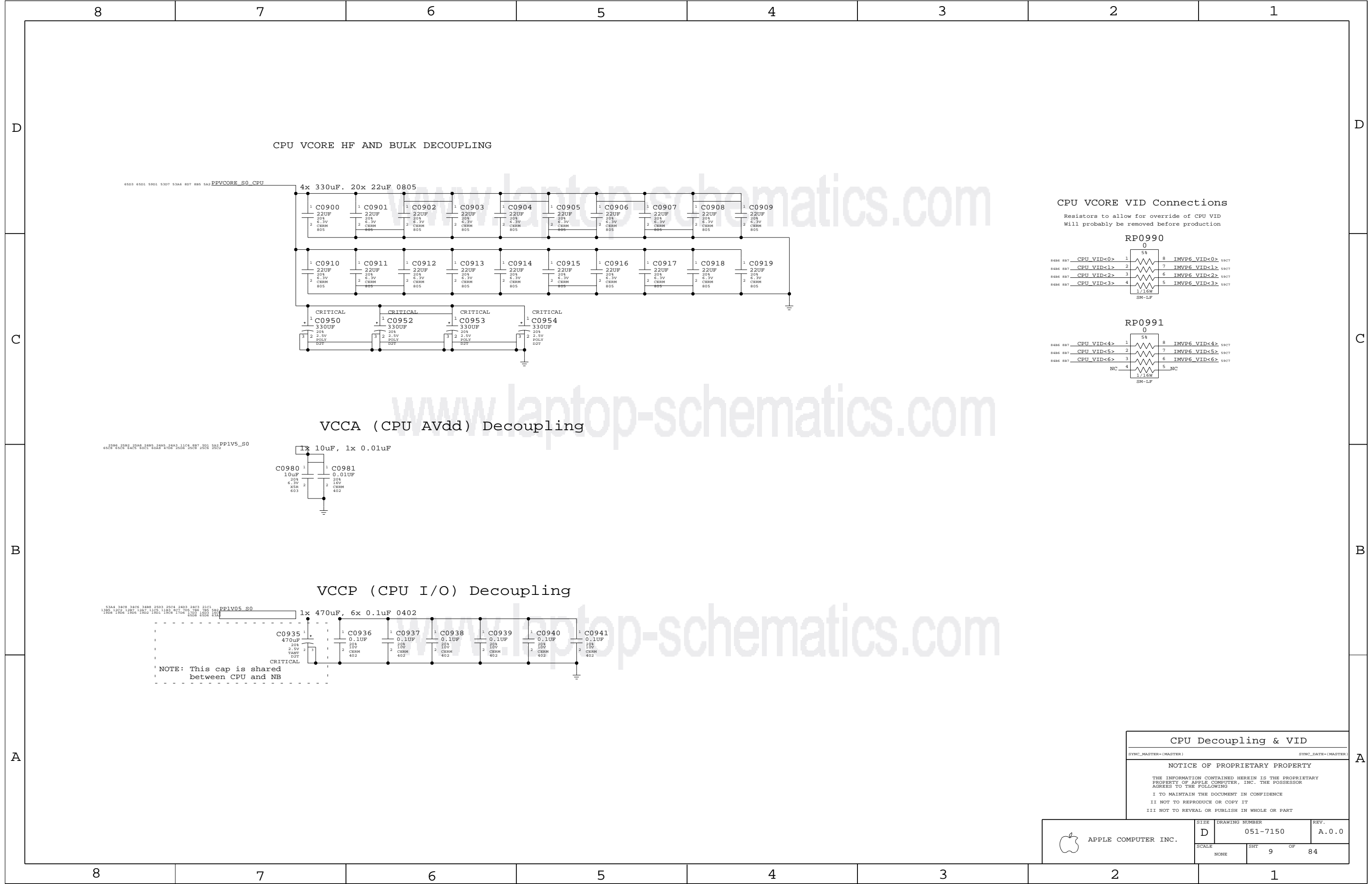
8</

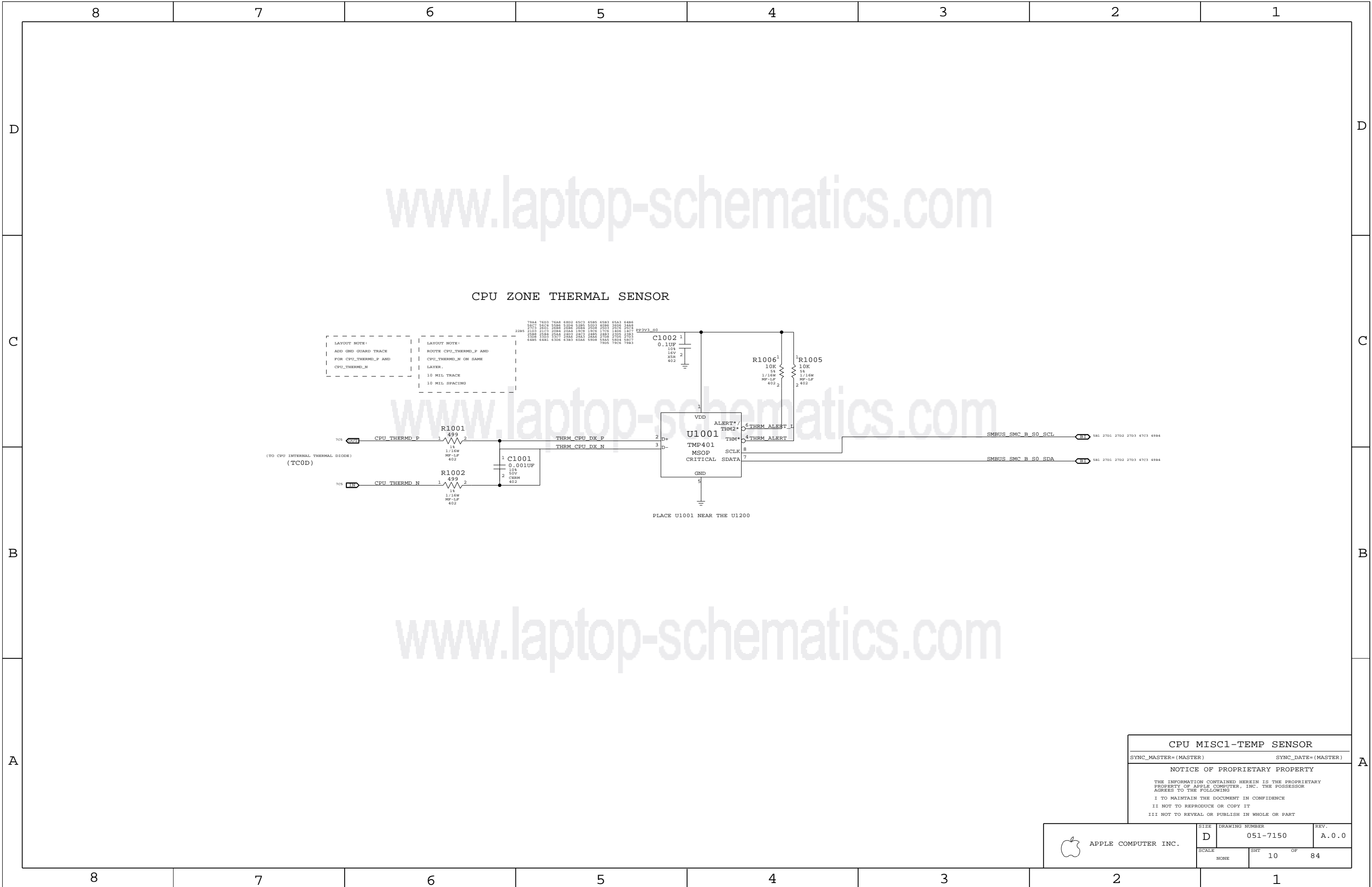
8	7	6	5	4	3	2	1
Functional Test Points							
Power Supply NO_TESTS		CPU FSB NO_TESTS		Fan Connectors		Battery Digital Connector	
NO_TEST	EXPOSED_VIA	NO_TEST	EXPOSED_VIA	FUNC_TEST		FUNC_TEST	
TRUE	IMVP6_RBIA5	TRUE	FSB A L<31..3>	TRUE	PP5V S0	TRUE	SMC BS ALERT L
TRUE	IMVP6_COMP	TRUE	FSB ADS L	TRUE	FAN LT PWM	TRUE	SMBUS SMC BSA SCL
TRUE	P5VS5 RUNSS	TRUE	FSB ADSTB L<1..0>	TRUE	FAN LT TACH	TRUE	SMBUS SMC BSA SDA
TRUE	P1V5S0 RUNSS	TRUE	FSB BNR L	TRUE	FAN RT PWM	TRUE	GND BATT
TRUE	P1V2S3 RT	TRUE	FSB BREQ0 L	TRUE	FAN RT TACH		
TRUE	P1V2S3 RUNSS	TRUE	FSB D L<63..0>				
TRUE	P3V3S5 COMP	TRUE	FSB DBSY L				
TRUE	P3V3S5 FSET	TRUE	FSB DINV L<3..0>				
TRUE	P1V05S0 COMP	TRUE	FSB DRDY L				
TRUE	P1V05S0 FSET	TRUE	FSB DSTBN L<3..0>				
TRUE	P3V42G3H FB	TRUE	FSB DSTBP L<3..0>				
TRUE	GPUVCORE COMP	TRUE	FSB HIT L				
TRUE	GPUVCORE FSET	TRUE	FSB HITM L				
TRUE	GPUBBP ADJ	TRUE	FSB LOCK L				
TRUE	GPUBBN FB	TRUE	FSB REQ L<4..0>				
TRUE	GPUVCORE FB						
TRUE	GPUVCORE FB RC						
TRUE	GPUVCORE ISEN						
TRUE	GPUVCORE LG						
TRUE	GPUVCORE PHASE						
TRUE	GPUVCORE UG						
TRUE	IMVP6_COMP RC						
TRUE	IMVP6_DFB						
TRUE	IMVP6_FB						
TRUE	IMVP6_OCSET						
TRUE	IMVP6_VDIFF						
TRUE	IMVP6_VDIFF_RC						
TRUE	P1V05S0_BOOT						
TRUE	P1V05S0_BOOT_R						
TRUE	P1V05S0_COMP						
TRUE	P1V05S0_COMP_R						
TRUE	P1V05S0_FB						
TRUE	P1V05S0_FB_RC						
TRUE	P1V05S0_FSET						
TRUE	P1V05S0_ISEN						
TRUE	P1V05S0_LG						
TRUE	P1V05S0_PHASE						
TRUE	P1V05S0_UG						
TRUE	P1V5S0_RUNSS						
TRUE	P3V3S5_BOOT						
TRUE	P3V3S5_BOOT_R						
TRUE	P3V3S5_COMP						
TRUE	P3V3S5_COMP_R						
TRUE	P3V3S5_FB						
TRUE	P3V3S5_FB_RC						
TRUE	P3V3S5_FSET						
TRUE	P3V3S5_ISEN						
TRUE	P3V3S5_LG						
TRUE	P3V3S5_UG						
TRUE	CK410_XTAL_IN						
Misc EXPOSED_VIA Nets		Camera Connector		RTC Battery Connector		Functional / ICT Test	
EXPOSED_VIA		FUNC_TEST		FUNC_TEST		SYNC_MASTER=N/A SYNC_DATE=N/A	
TRUE	DMI N2S P<1..0>	TRUE	PP5V S3	TRUE	PPVBATT G3C RTC	NOTICE OF PROPRIETARY PROPERTY	
TRUE	DMI N2S N<1..0>	TRUE	USB2 CAMERA N	TRUE	GND	THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING	
TRUE	SB CLK100M SATA_P	TRUE	USB2 CAMERA P			I TO MAINTAIN THE DOCUMENT IN CONFIDENCE	
TRUE	SB CLK100M SATA_N					II NOT TO REPRODUCE OR COPY IT	
						III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	
		Inverter Connector				SIZE D 051-7150 REV. A.0.0	
		FUNC_TEST				SCALE NONE SHT 5 OF 84	
		TRUE GND CHASSIS INVERTER					
		TRUE PPBUS S0 INVERTER					
		TRUE GND CHASSIS INVERTER					
		TRUE INVERTER PWM					
		TRUE PPVS INVERTER SW					





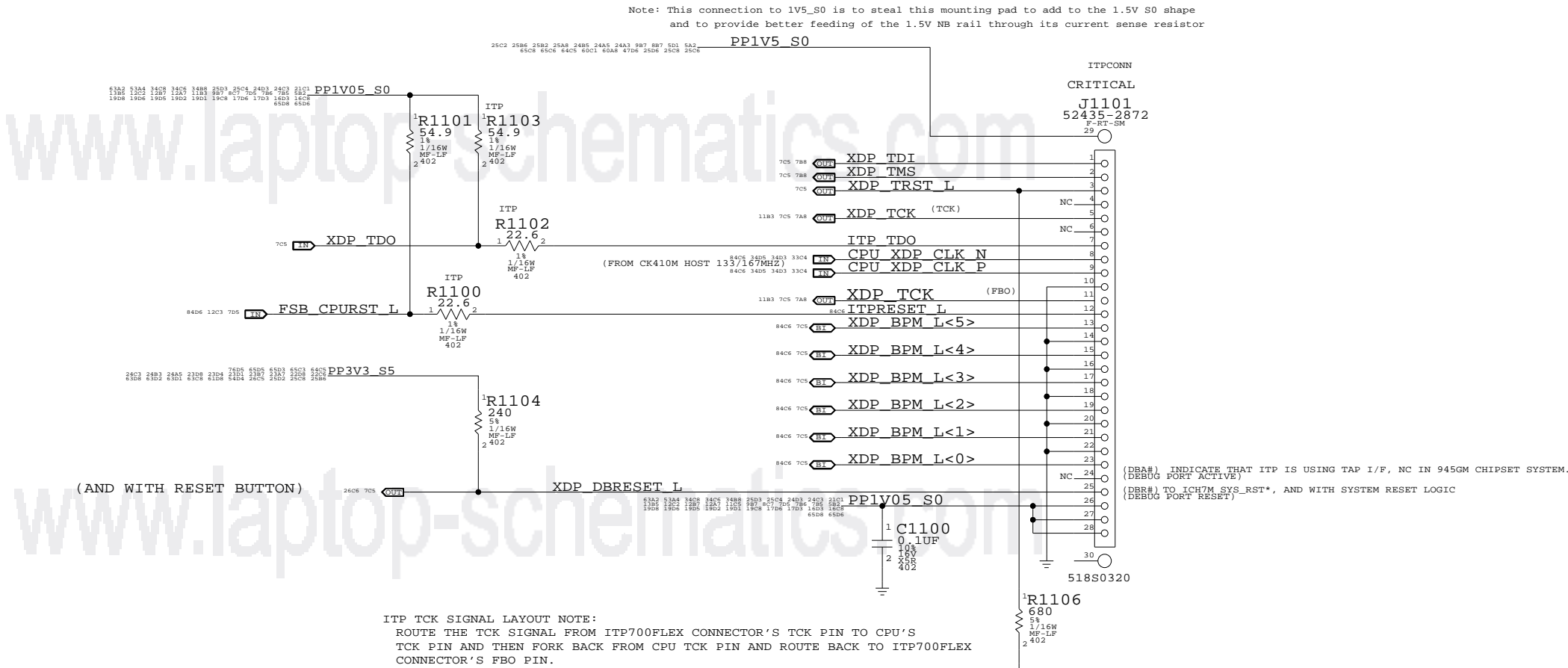






www.laptop-schematics.com

CPU ITP700FLEX DEBUG SUPPORT



CPU ITP700FLEX DEBUG

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

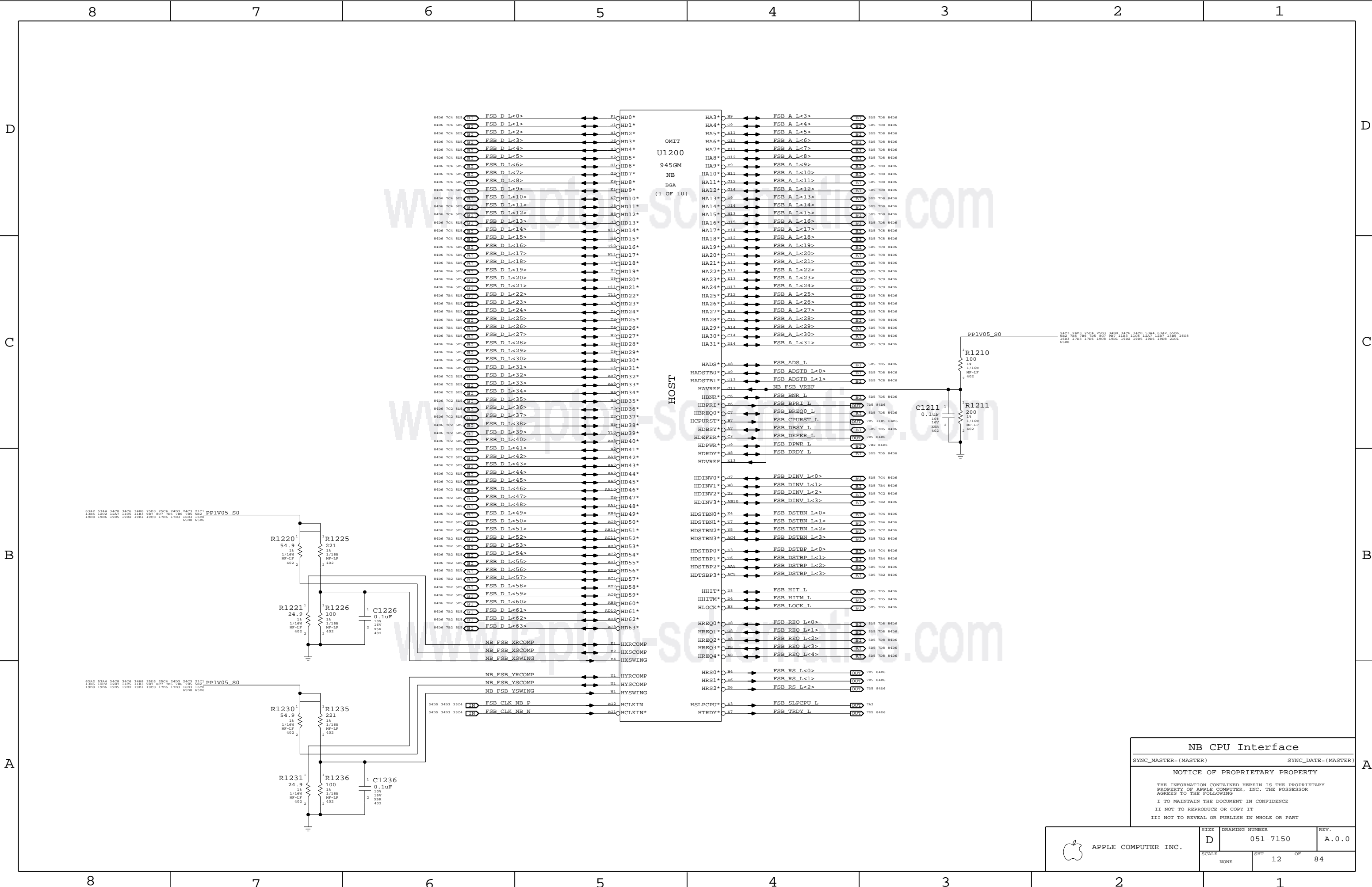
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		11	84



NB CPU Interface

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

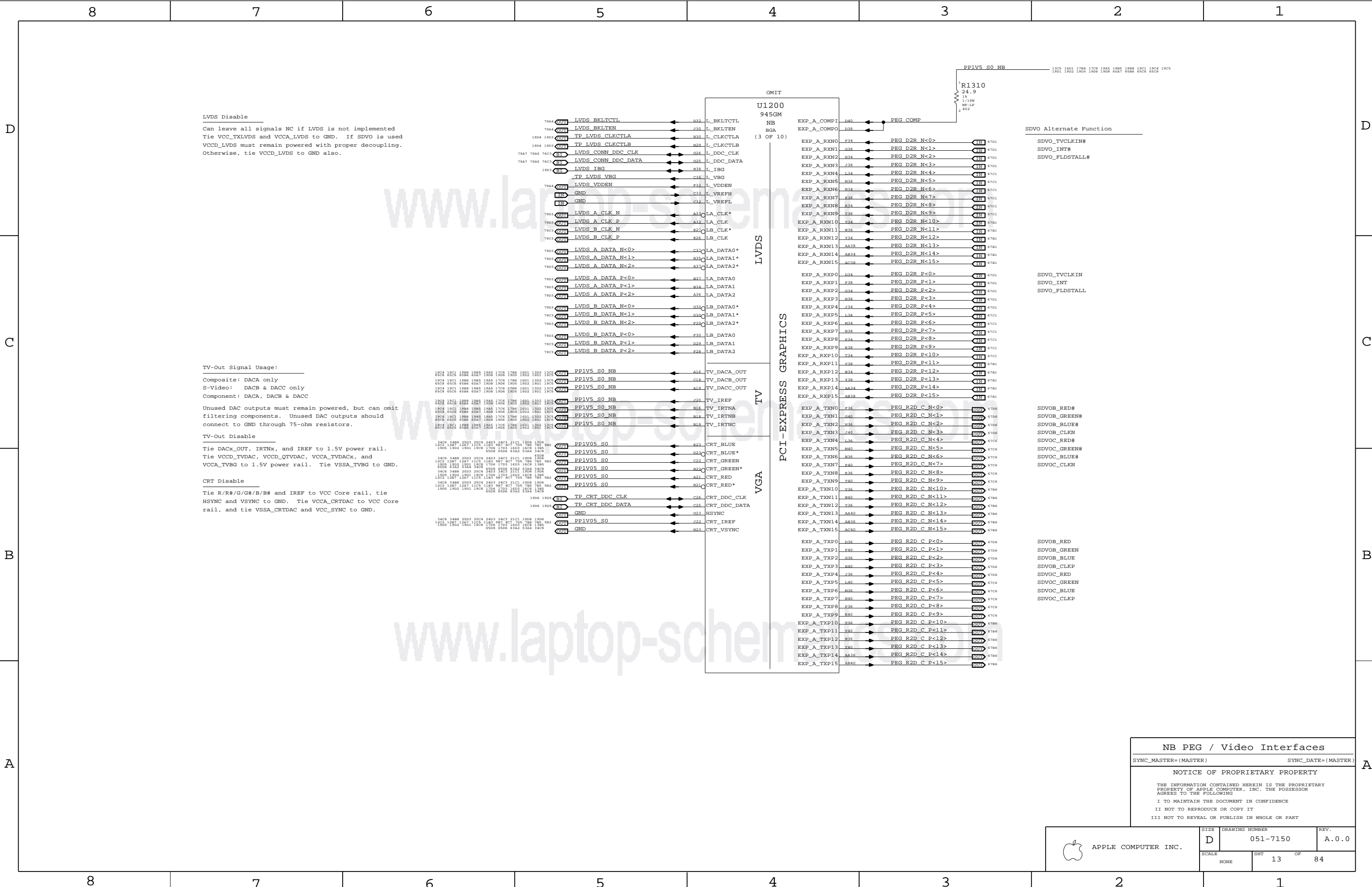
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



LVDS Disable

Can leave all signals NC if LVDS is not implemented
Tie VCC_TXLVDS and VCCA_LVDS to GND. If SDVO is used
VCCD_LVDS must remain powered with proper decoupling.
Otherwise, tie VCCD_LVDS to GND also.

TV-Out Signal Usage:

Composite: DACA only
S-Video: DACB & DACC only
Component: DACA, DACB & DACC

Unused DAC outputs must remain powered, but can omit
filtering components. Unused DAC outputs should
connect to GND through 75-ohm resistors.

TV-Out Disable

Tie DACx_OUT, IRTN_x, and IREF to 1.5V power rail.
Tie VCCD_TV_DAC, VCCD_QTV_DAC, VCCA_TV_DAC_x, and
VCCA_TV_BG to 1.5V power rail. Tie VSSA_TV_BG to GND.

CRT Disable

Tie R/R#/G/G#/B/B# and IREF to VCC Core rail, tie
HSYNC and VSYNC to GND. Tie VCCA_CRT_DAC to VCC Core
rail, and tie VSSA_CRT_DAC and VCC_SYNC to GND.

SDVO Alternate Function

SDVO_TVCLKIN#
SDVO_INT#
SDVO_FLDSTALL#

SDVO_TVCLKIN
SDVO_INT
SDVO_FLDSTALL

SDVOB_RED#
SDVOB_GREEN#
SDVOB_BLUE#
SDVOB_CLKN
SDVOC_RED#
SDVOC_GREEN#
SDVOC_BLUE#
SDVOC_CLKN

SDVOB_RED
SDVOB_GREEN
SDVOB_BLUE
SDVOB_CLKP
SDVOC_RED
SDVOC_GREEN
SDVOC_BLUE
SDVOC_CLKP

NB PEG / Video Interfaces

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7150

REV.

A.0.0

SCALE

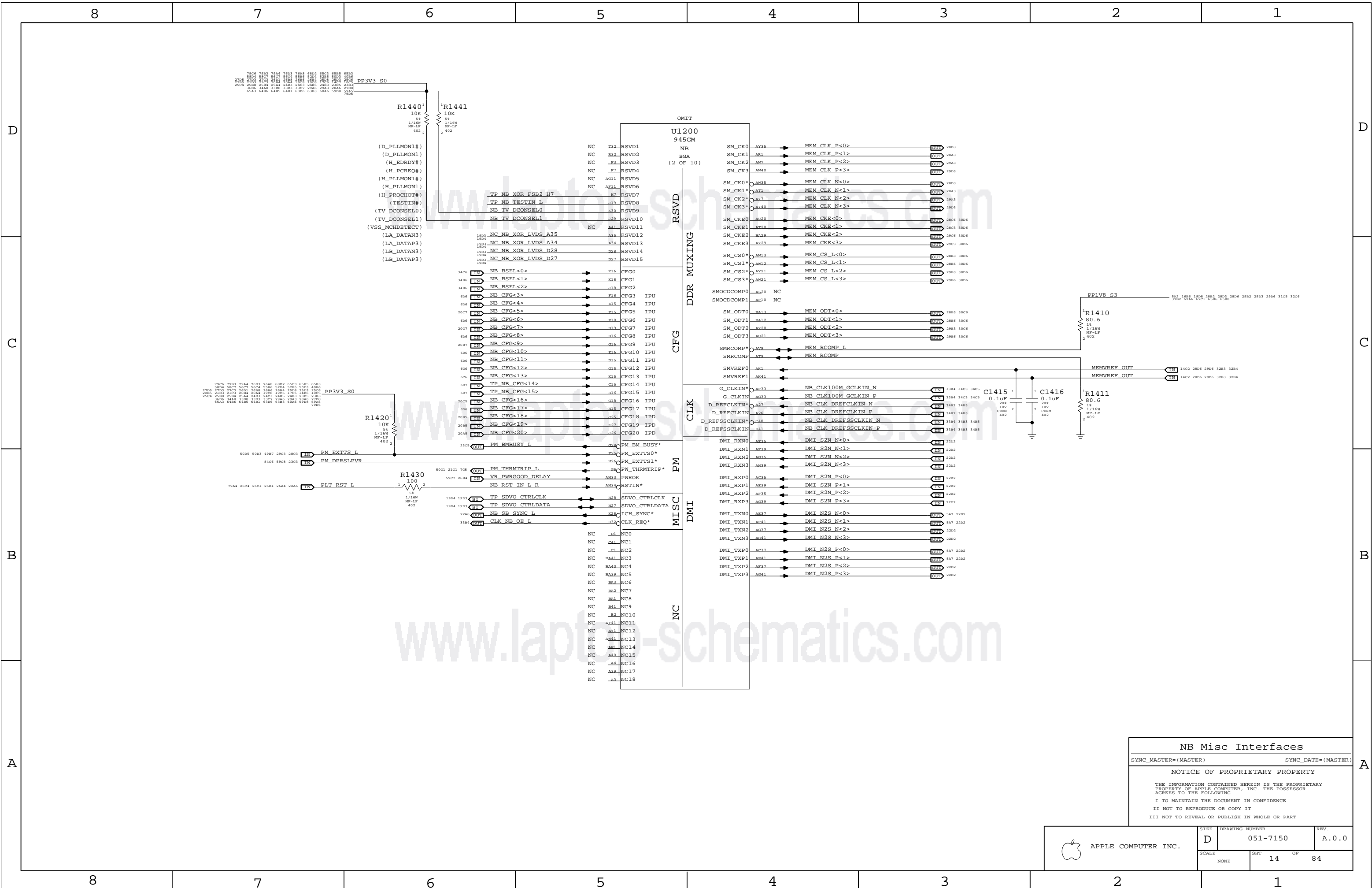
NONE

SHT

13

OF

84



NB Misc Interf

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

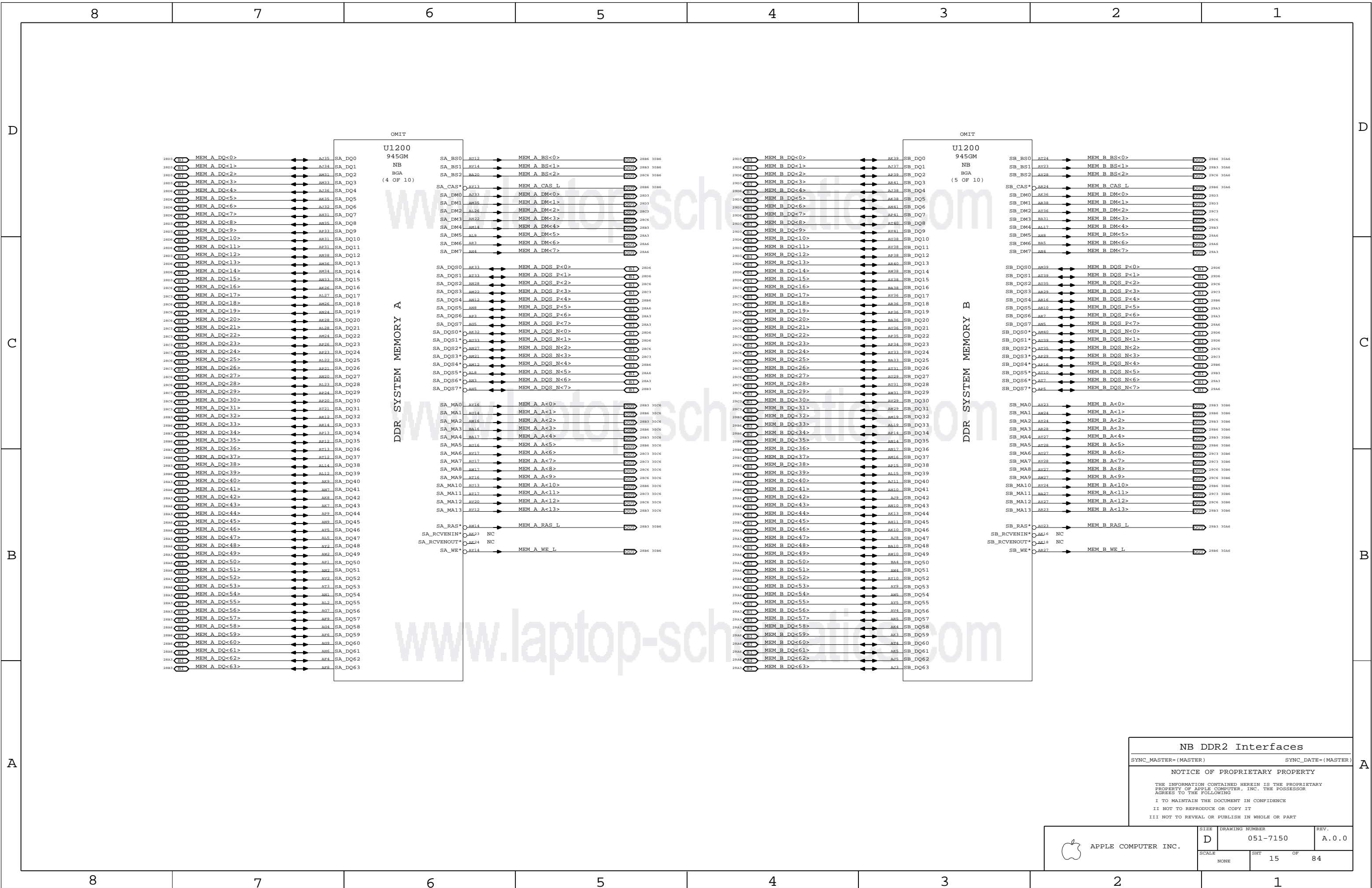
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



NB DDR2 Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

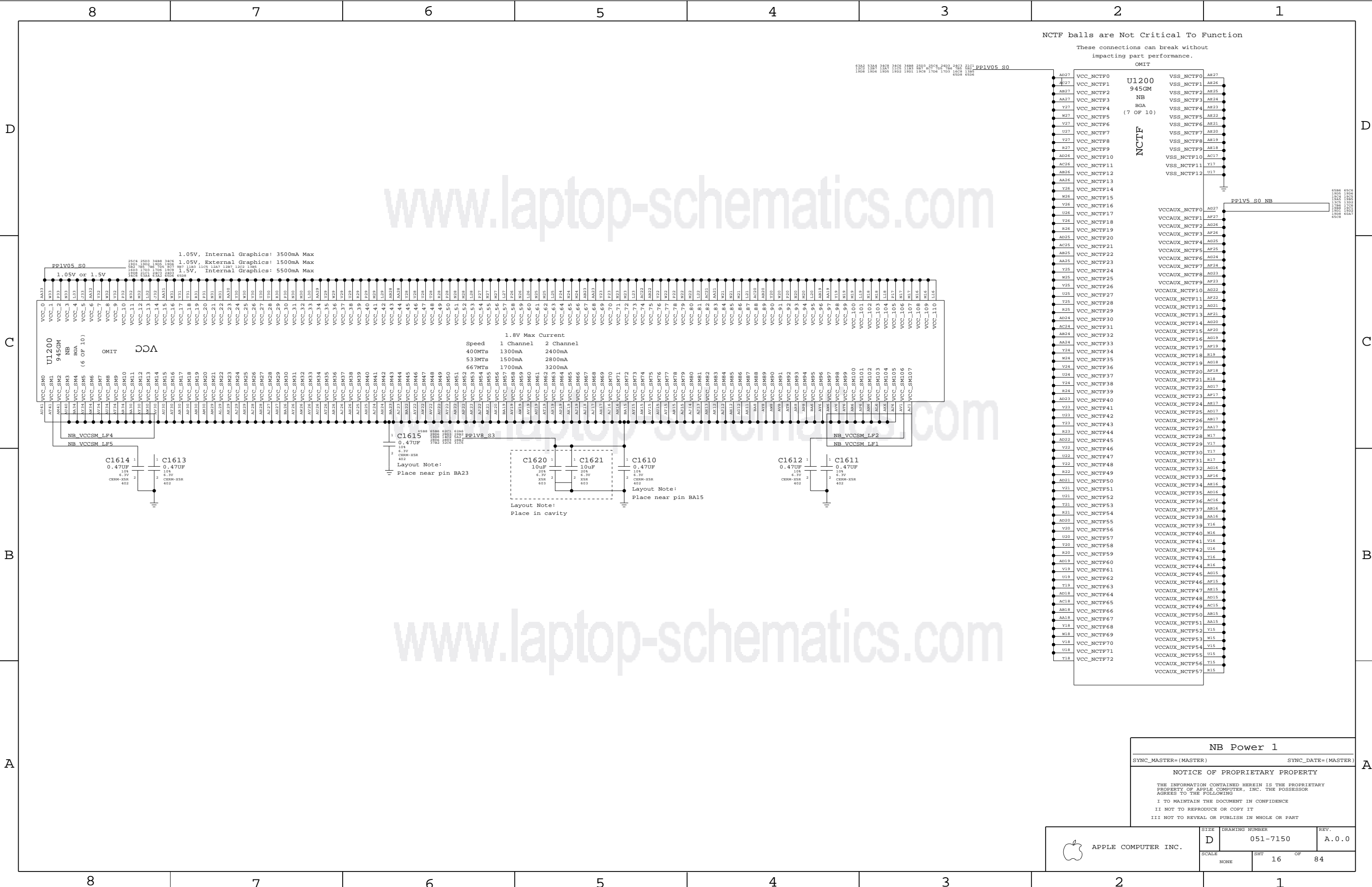
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



NB Power 1

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

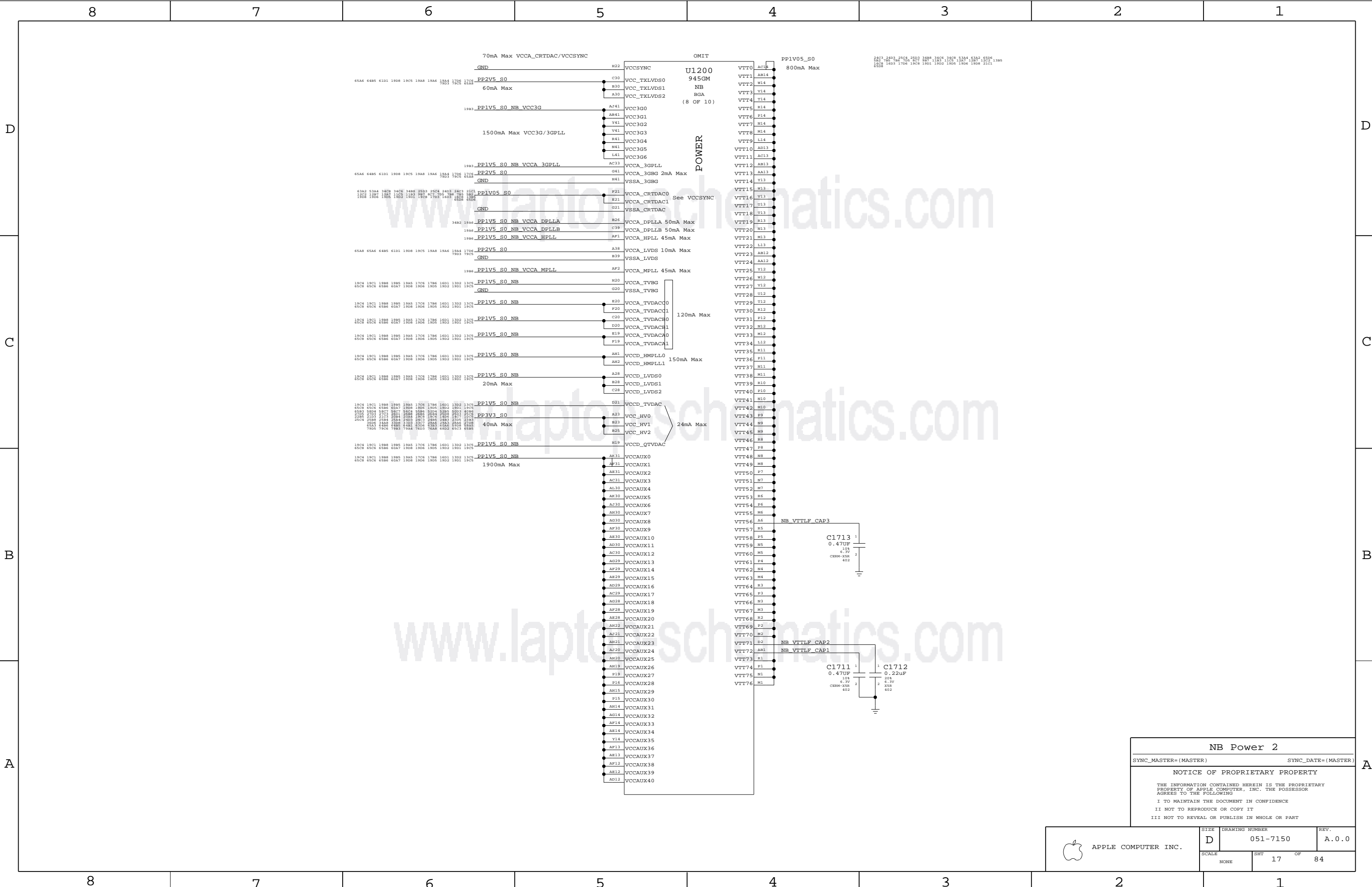
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		16	84



NB Power 2

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

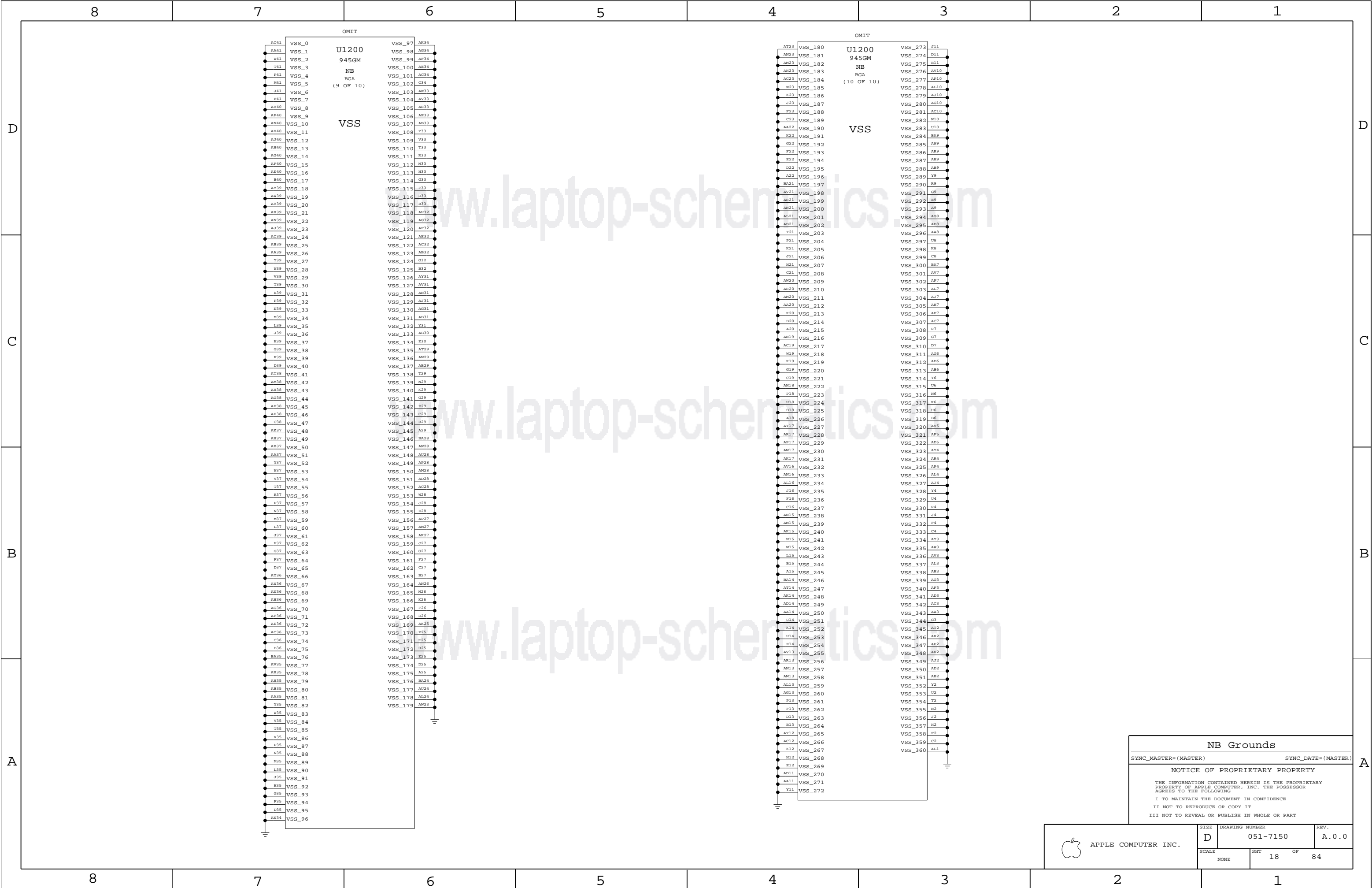
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		17	84



NB Grounds

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7150

REV.

A.0.0

SCALE

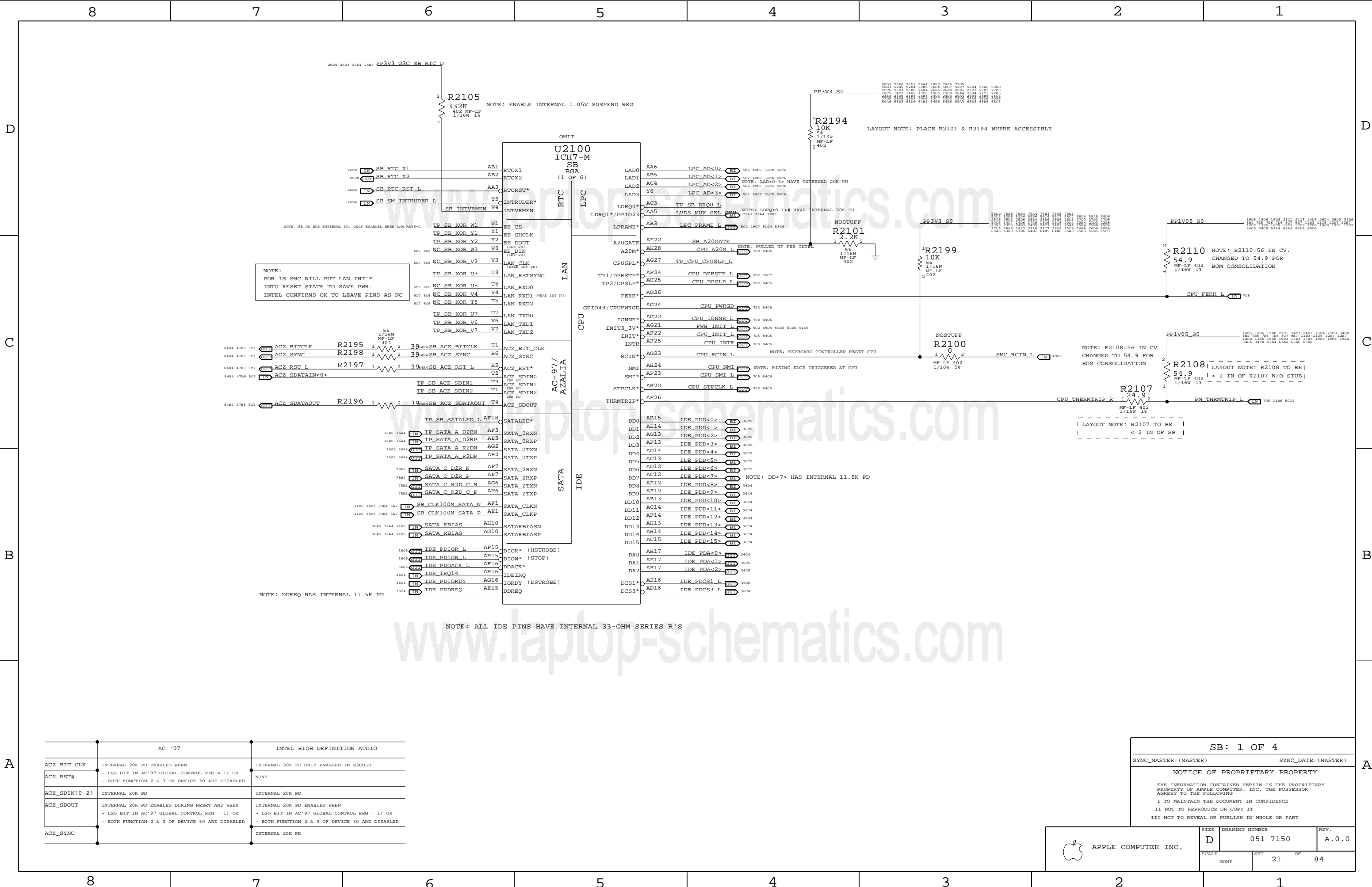
NONE

SHT

18

OF

84



AC '07		INTEL HIGH DEFINITION AUDIO	
ACZ_BIT_CLK	INTERNAL 20K PD ENABLED WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR - BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED	INTERNAL 20K PD ONLY ENABLED IN S3COLD	
ACZ_RST#	NONE		
ACZ_SDIN[0-2]	INTERNAL 20K PD	INTERNAL 20K PD	
ACZ_SDOUT	INTERNAL 20K PD ENABLED DURING RESET AND WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR - BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED	INTERNAL 20K PD ENABLED WHEN - LSO BIT IN AC'97 GLOBAL CONTROL REG = 1; OR - BOTH FUNCTION 2 & 3 OF DEVICE 30 ARE DISABLED	
ACZ_SYNC		INTERNAL 20K PD	

SB: 1 OF 4

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

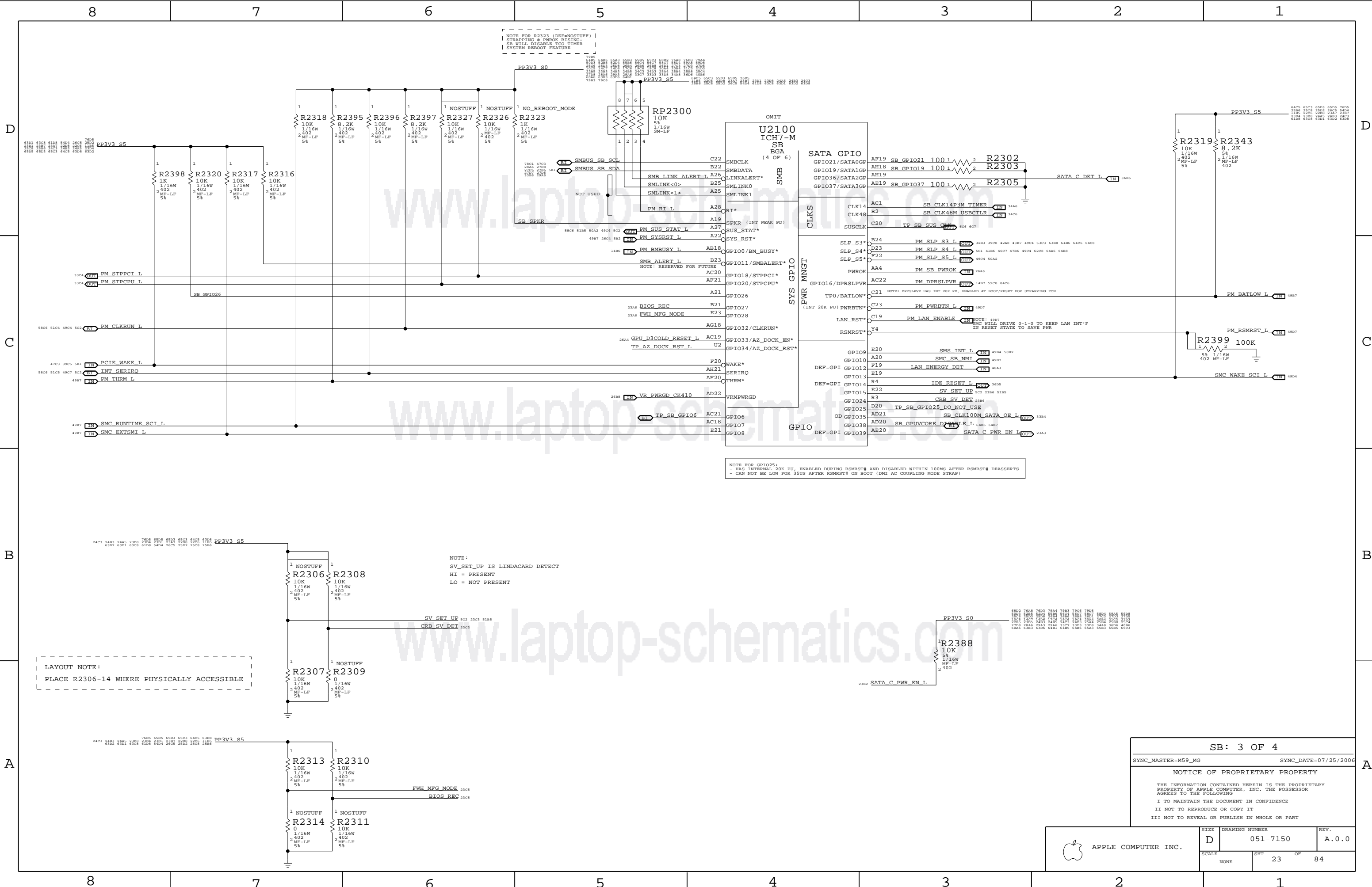
II NOT TO REPRODUCE OR COPY IT

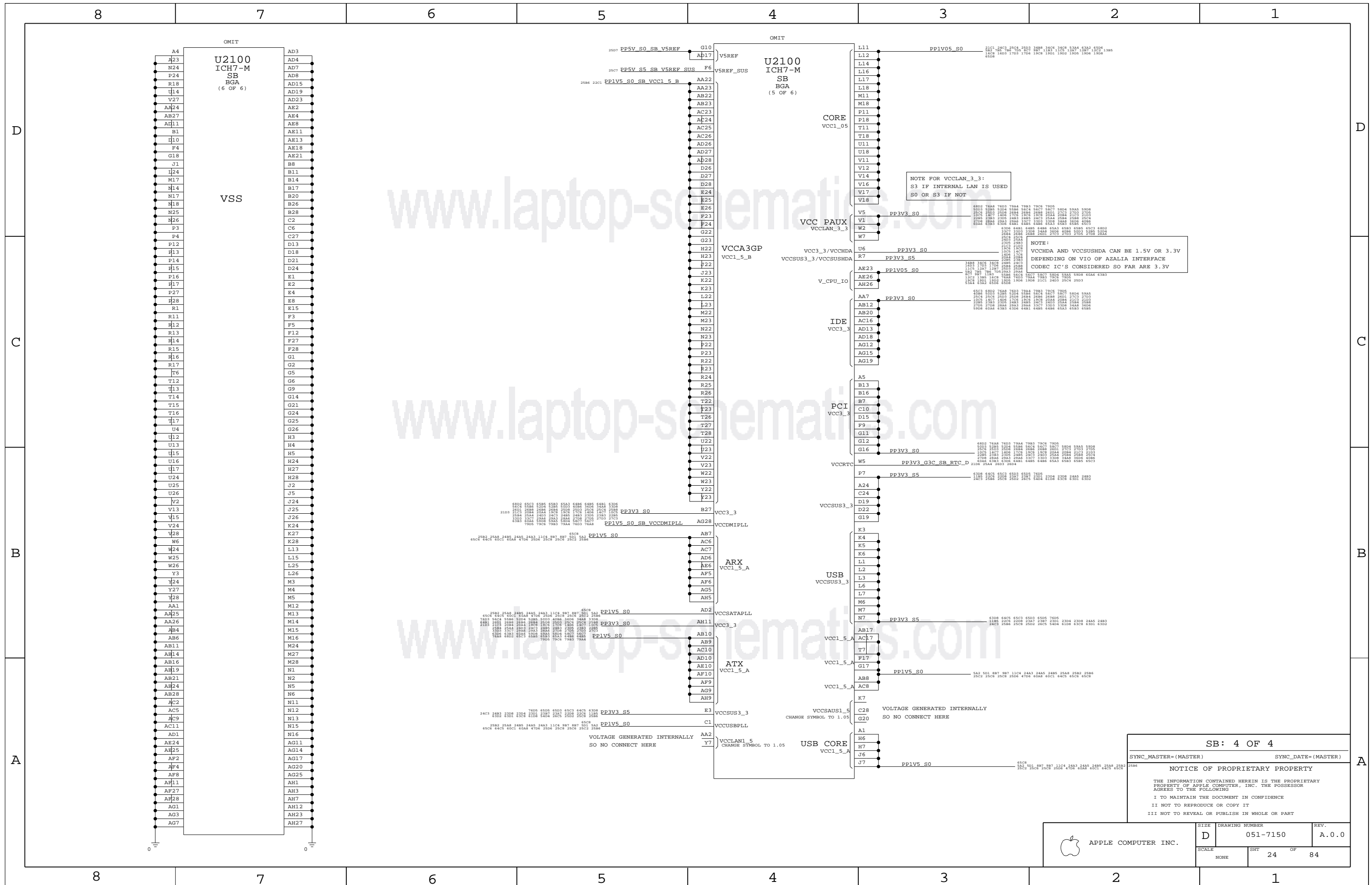
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

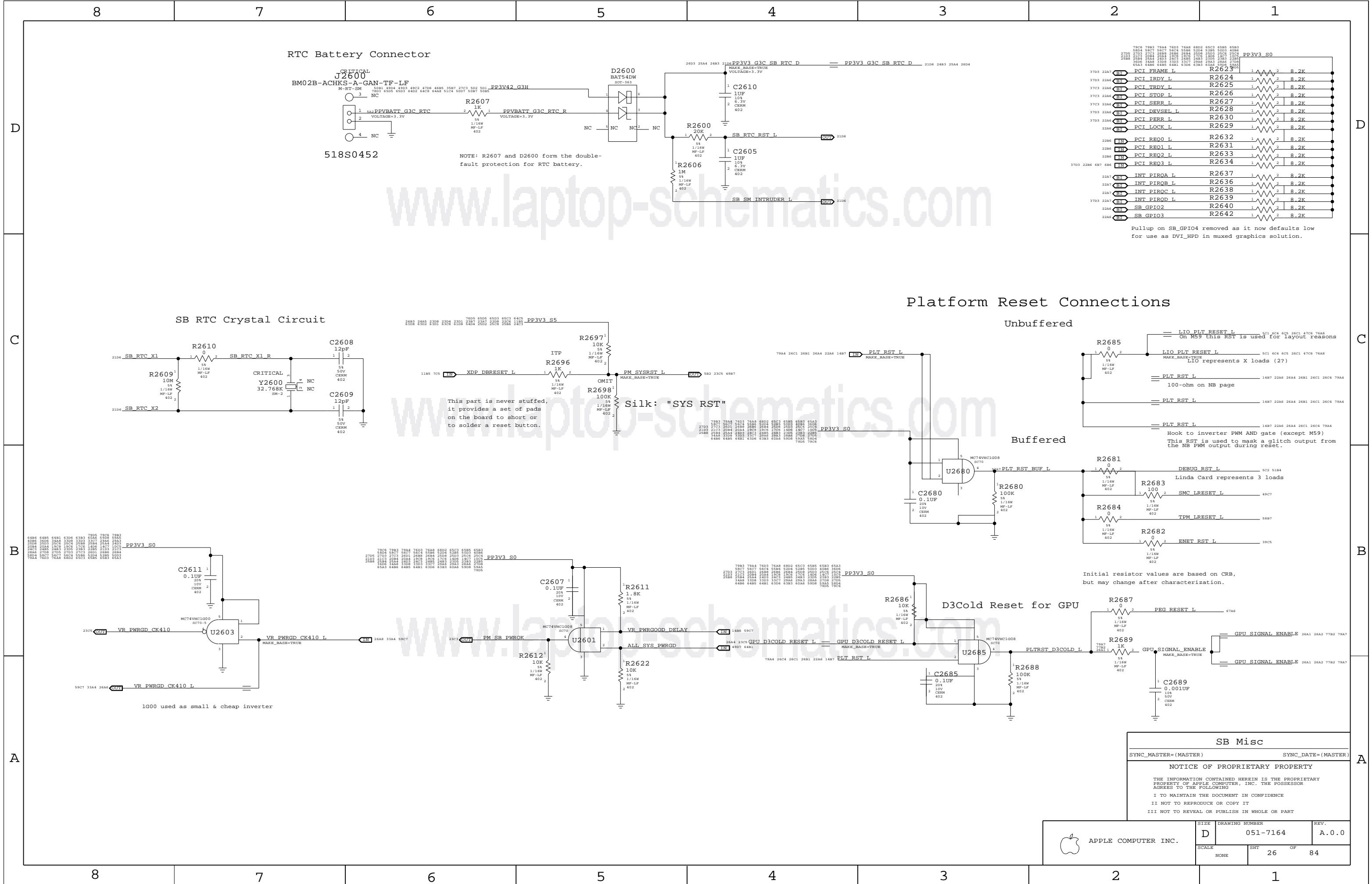
APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-7150 REV. A.0.0

SCALE NONE SHT 21 OF 84







D

C

B

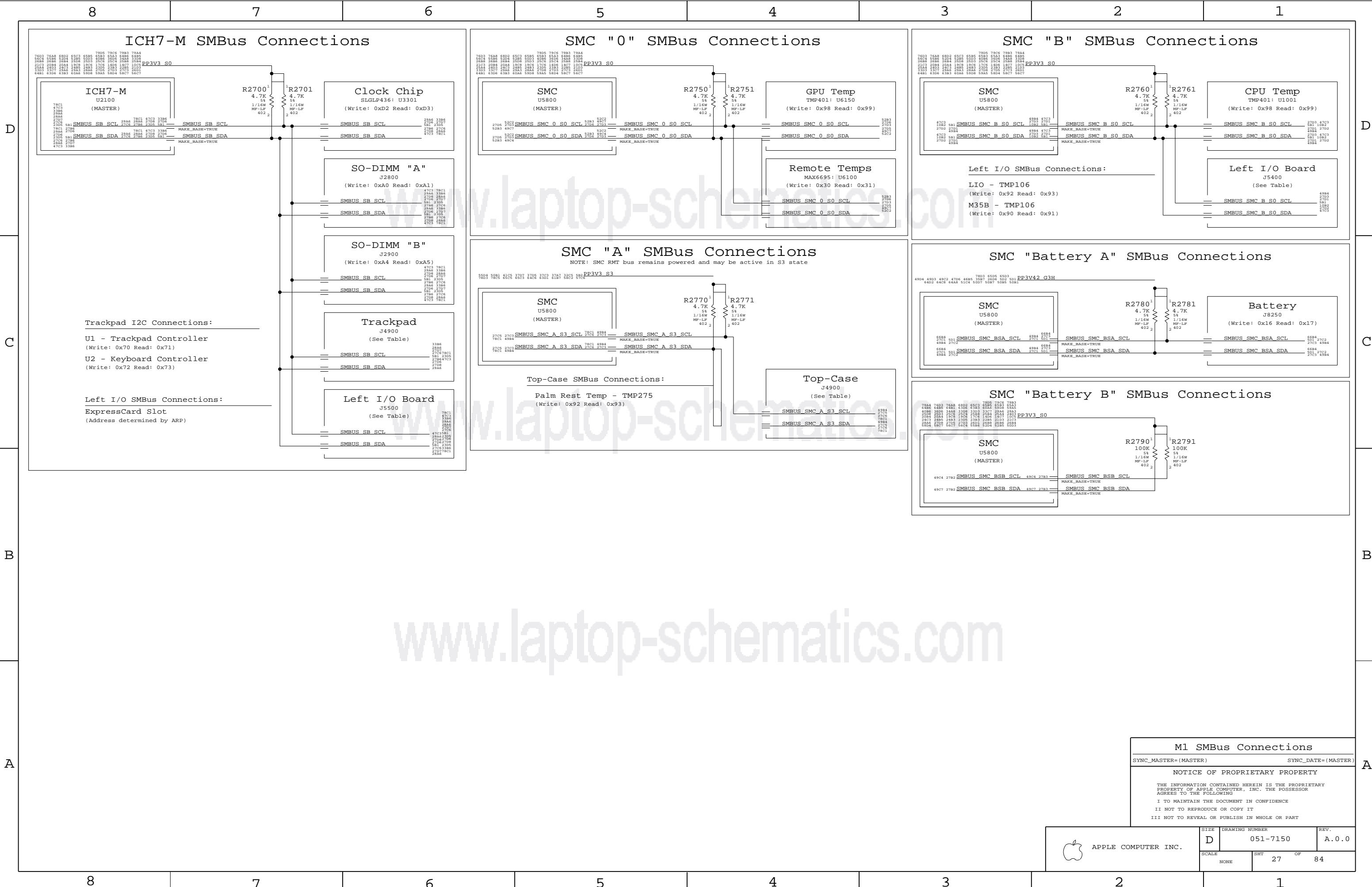
A

D

C

B

A



Page Notes

Power aliases required by this page:

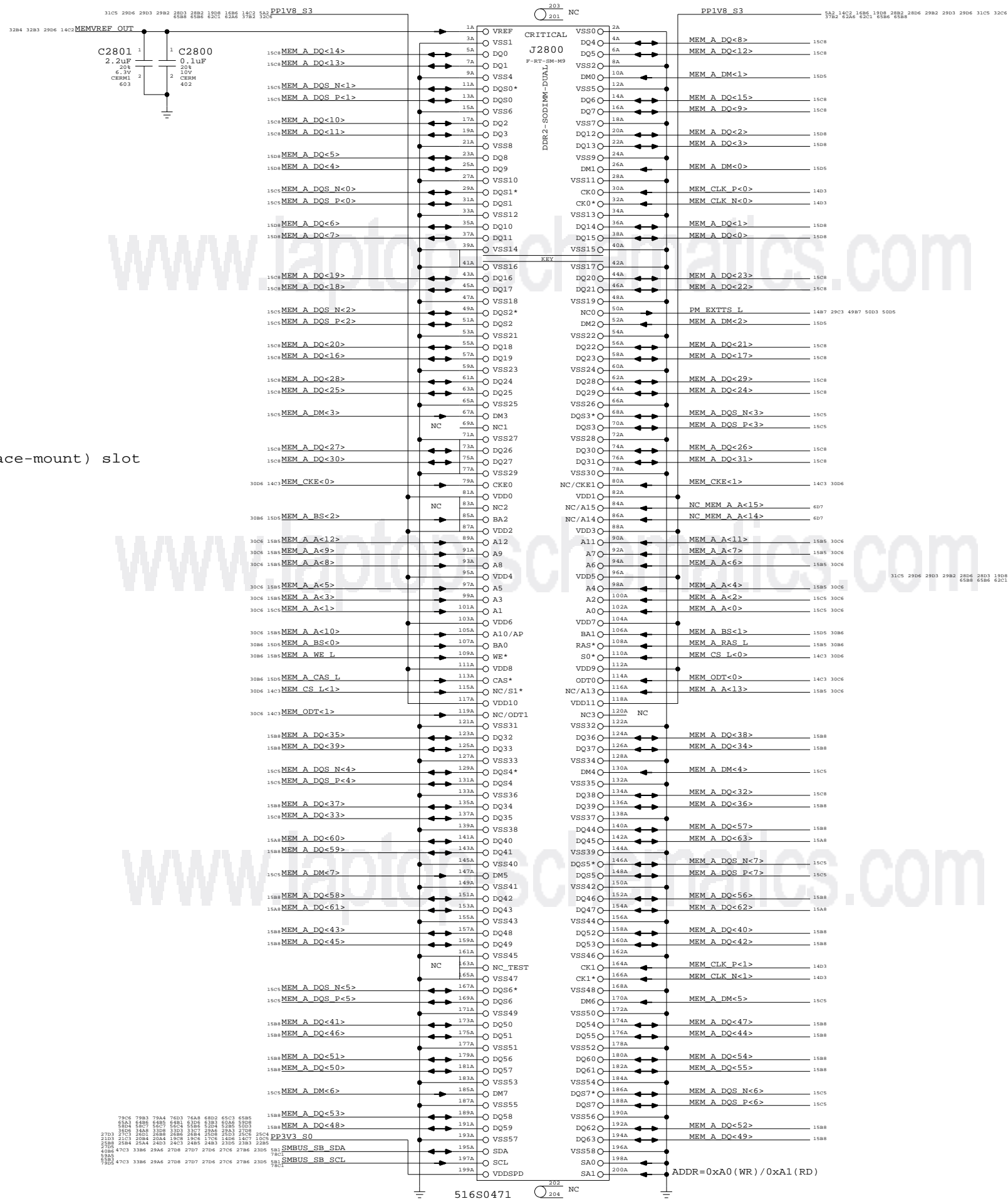
- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:

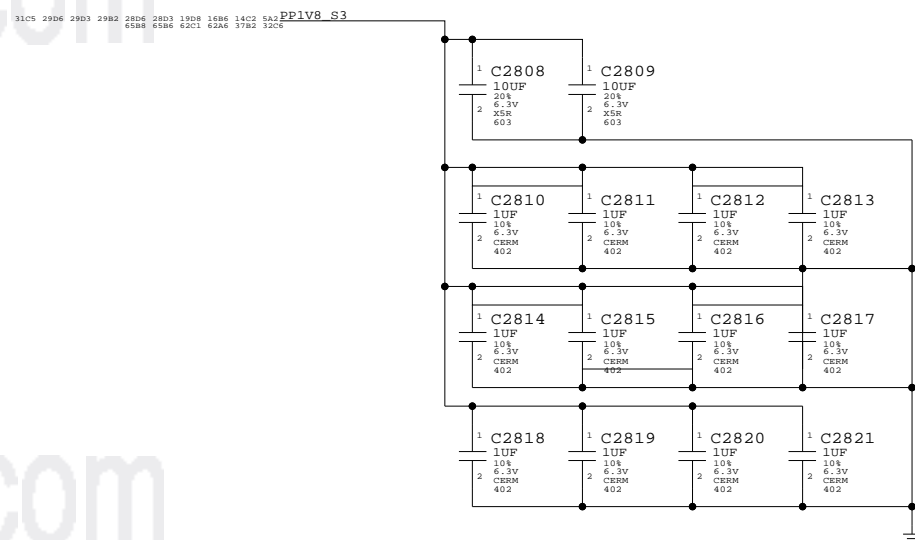
- =I2C_SODIMMA_SCL
- =I2C_SODIMMA_SDA

BOM options provided by this page:
(NONE)

NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.



DDR2 Bypass Caps



DDR2 SO-DIMM Connector A

```
SYNC_MASTER=(MASTER)
```

SYNC_DATE= (MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY
PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR
AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE COMPUTER INC.

SIZE

DRAWING NUMBER

REV.

SCALE

SHT

22

0.4

Power aliases required by this page:

- =PPIV8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:

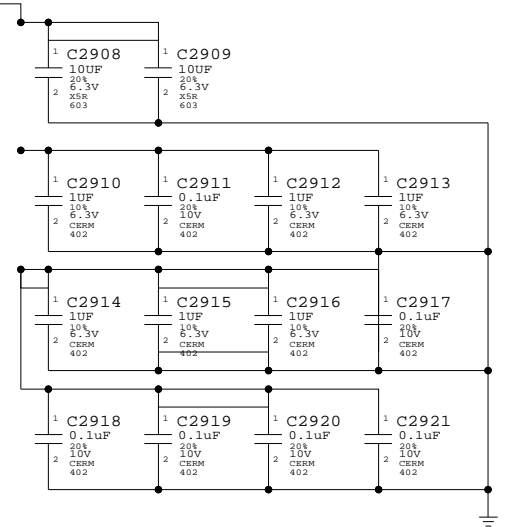
- =I2C_SODIMMB_SCL
- =I2C_SODIMMB_SDA

BOM options provided by this page:

(NONE)

NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.

DDR2 Bypass Caps



SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
----------------------	--------------------

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF LORIS CORNELL AND HIS RECORDS

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

	SIZE	DRAWING NUMBER
--	------	----------------

D	051-7150
---	----------

SIZE	DRAWING NUMBER	REV.
D	051-7150	A.0.0

SCALE	SHT	OF
NONE	29	84



APPLE COMPUTER INC.

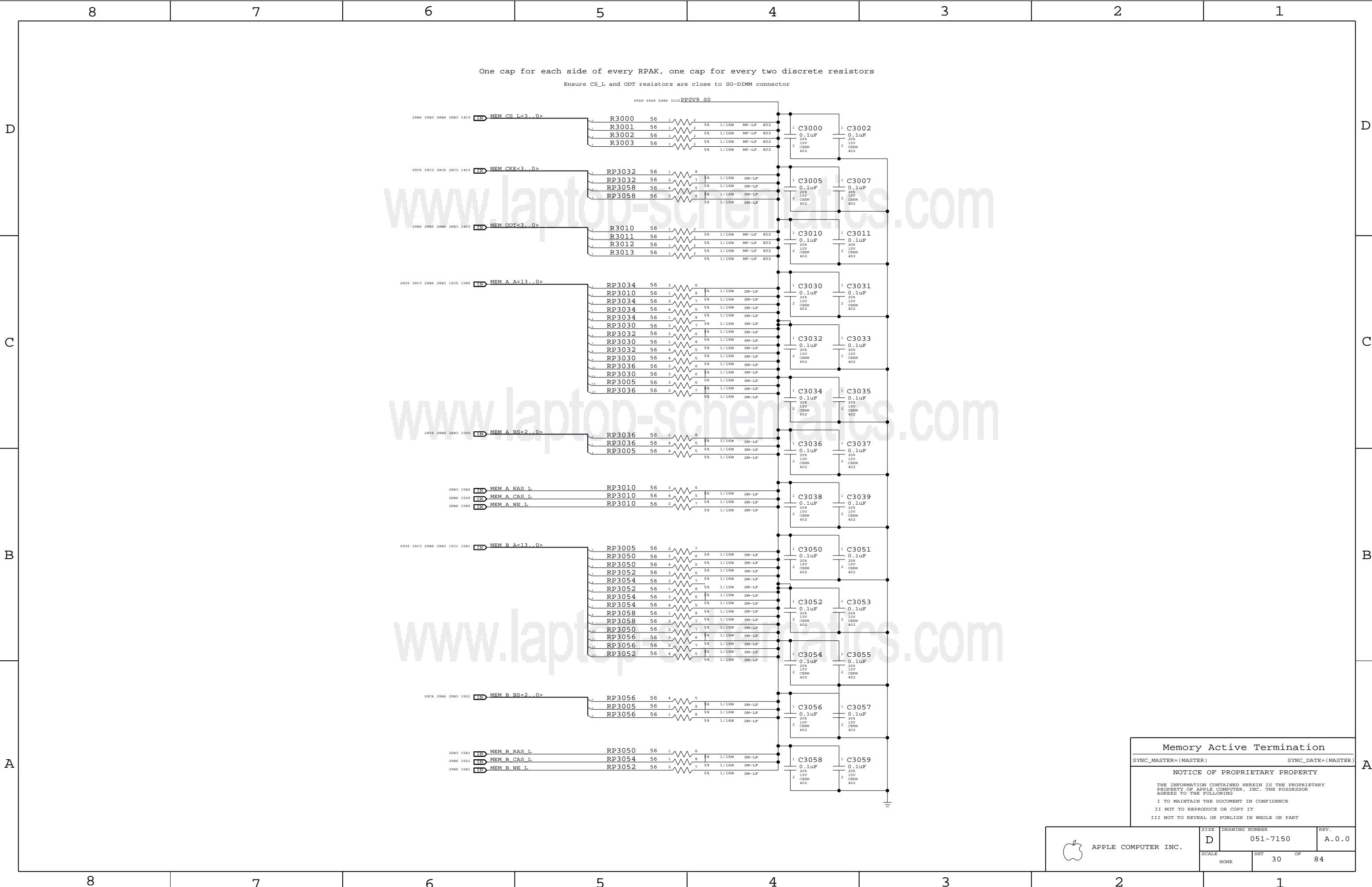
SIZE
D

SCALE

SHT	
-----	--

REV.	A.0.0
------	-------

84	
----	--



Memory Active Termination

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

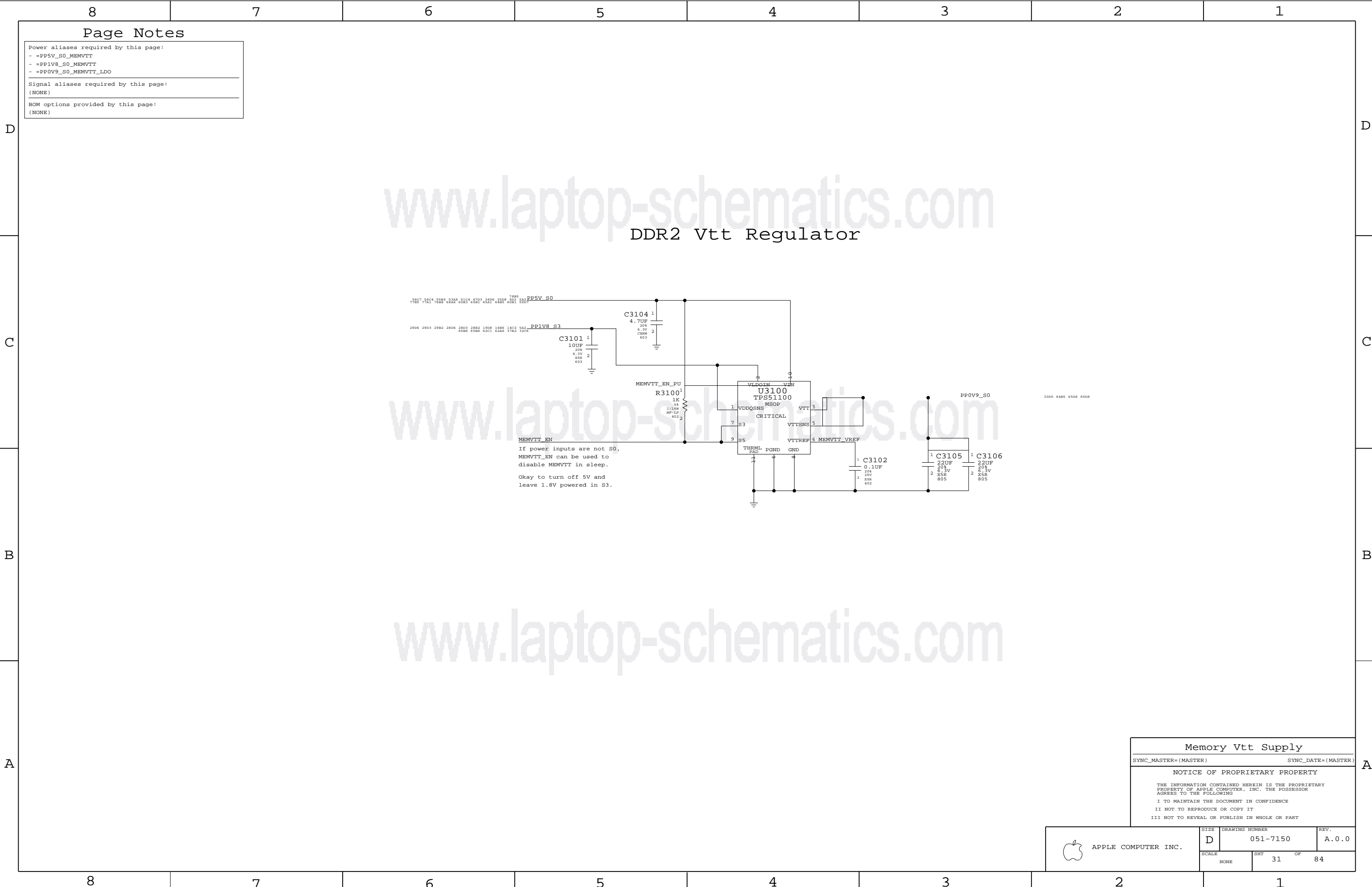
NOTICE OF PROPRIETARY PROPERTY

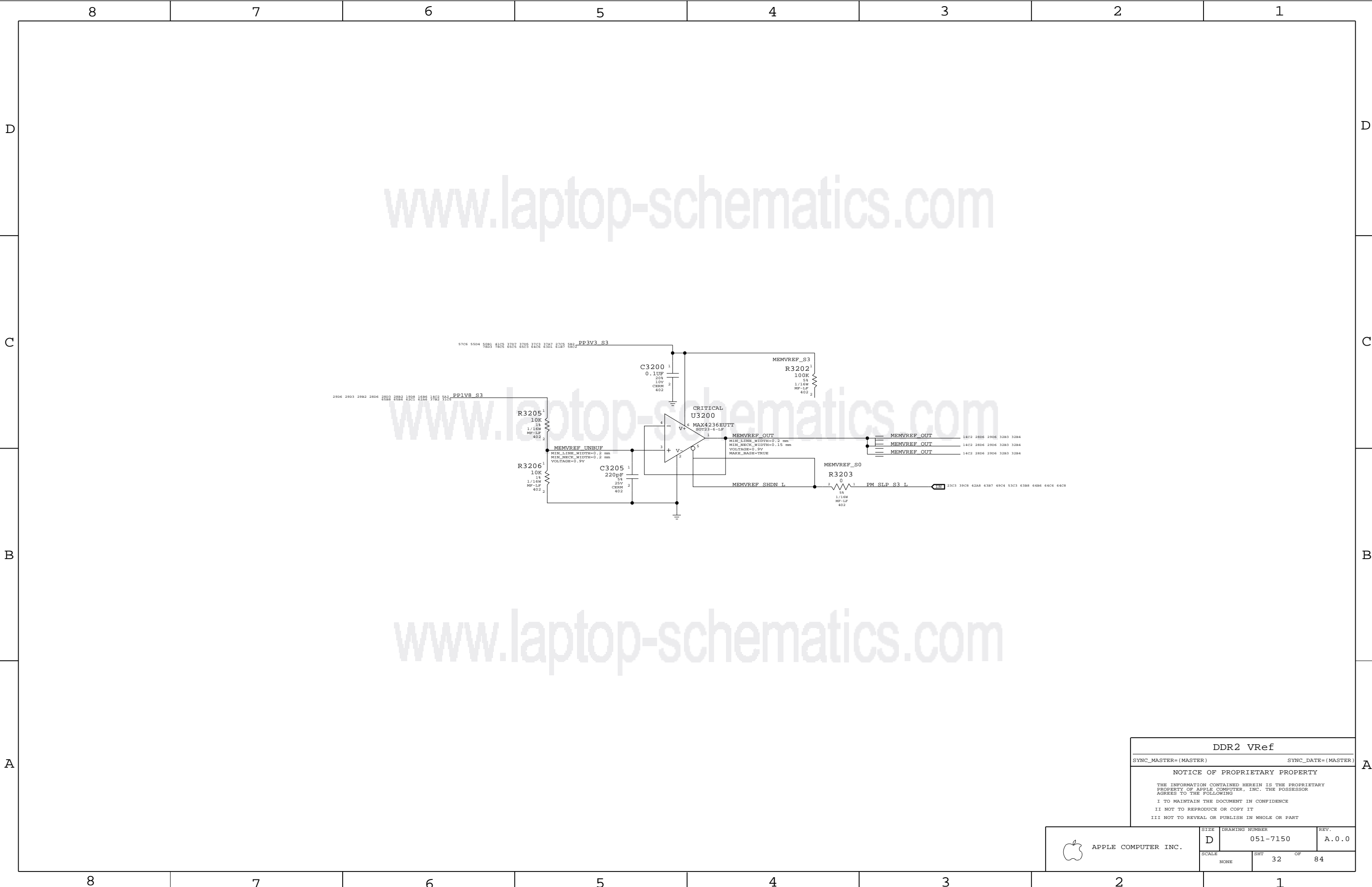
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART





DDR2 Vref

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

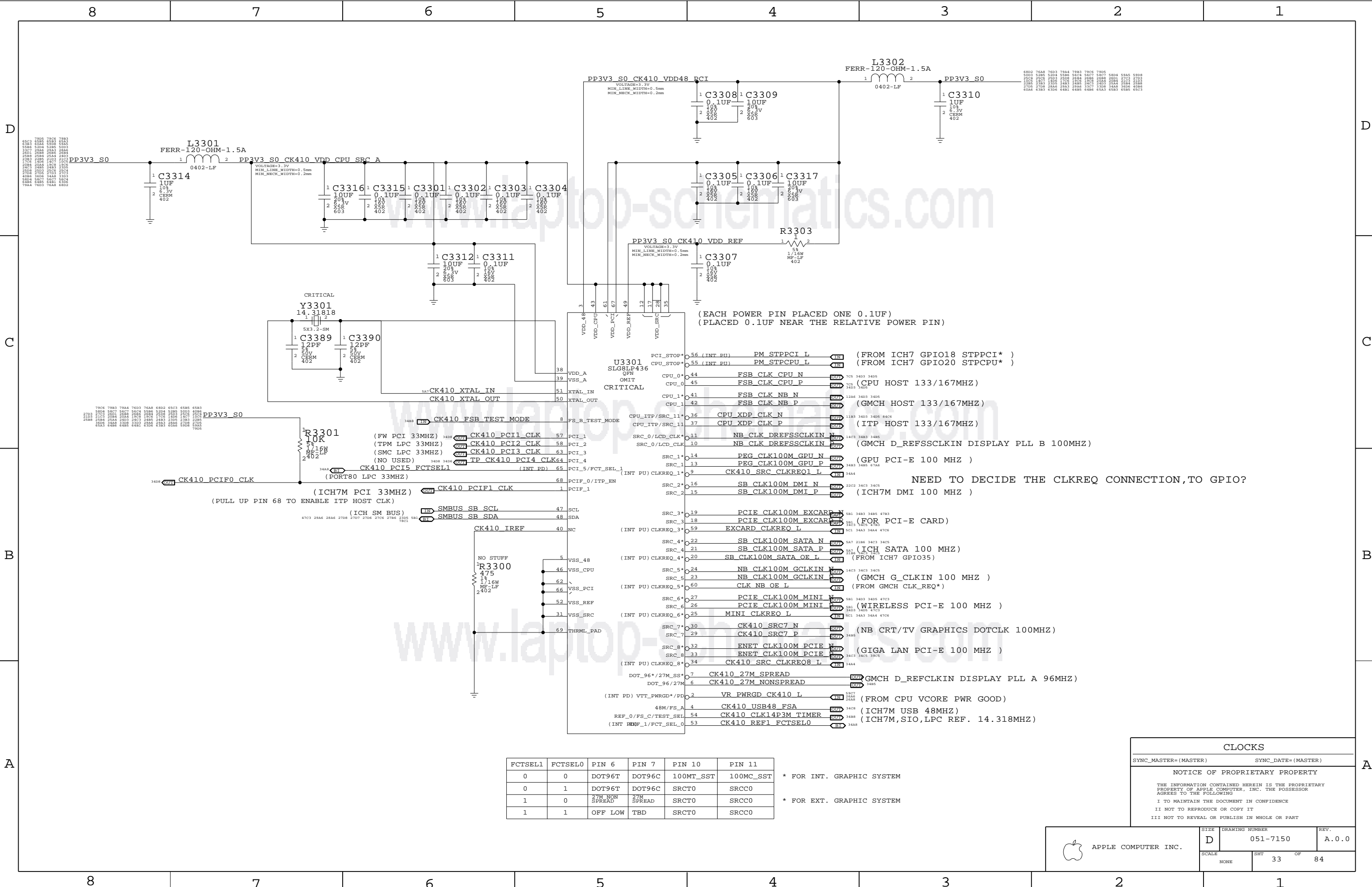
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 32	OF 84



FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M NON SPREAD	27M SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

* FOR INT. GRAPHIC SYSTEM

* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

SIZE D

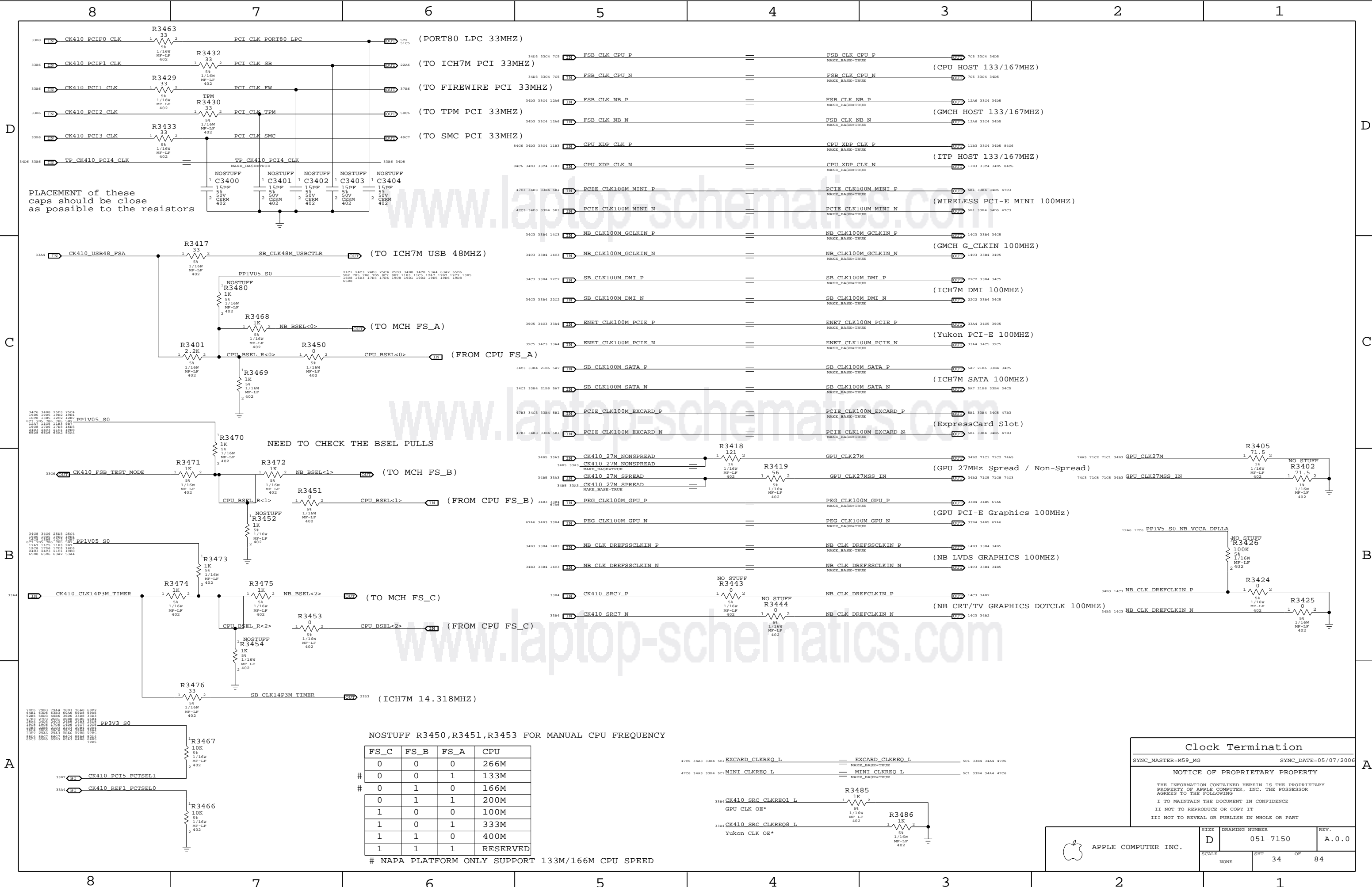
DRAWING NUMBER 051-7150

REV. A.0.0

SCALE NONE

SHT 33

OF 84



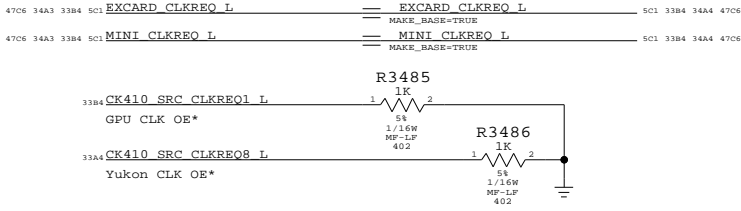
PLACEMENT of these caps should be close as possible to the resistors

NEED TO CHECK THE BSEL PULLS

NOSTUFF R3450,R3451,R3453 FOR MANUAL CPU FREQUENCY

	FS_C	FS_B	FS_A	CPU
#	0	0	0	266M
#	0	0	1	133M
#	0	1	0	166M
#	0	1	1	200M
#	1	0	0	100M
#	1	0	1	333M
#	1	1	0	400M
#	1	1	1	RESERVED

NAPA PLATFORM ONLY SUPPORT 133M/166M CPU SPEED



Clock Termination

SYNC_MASTER=M59_MG SYNC_DATE=05/07/2006

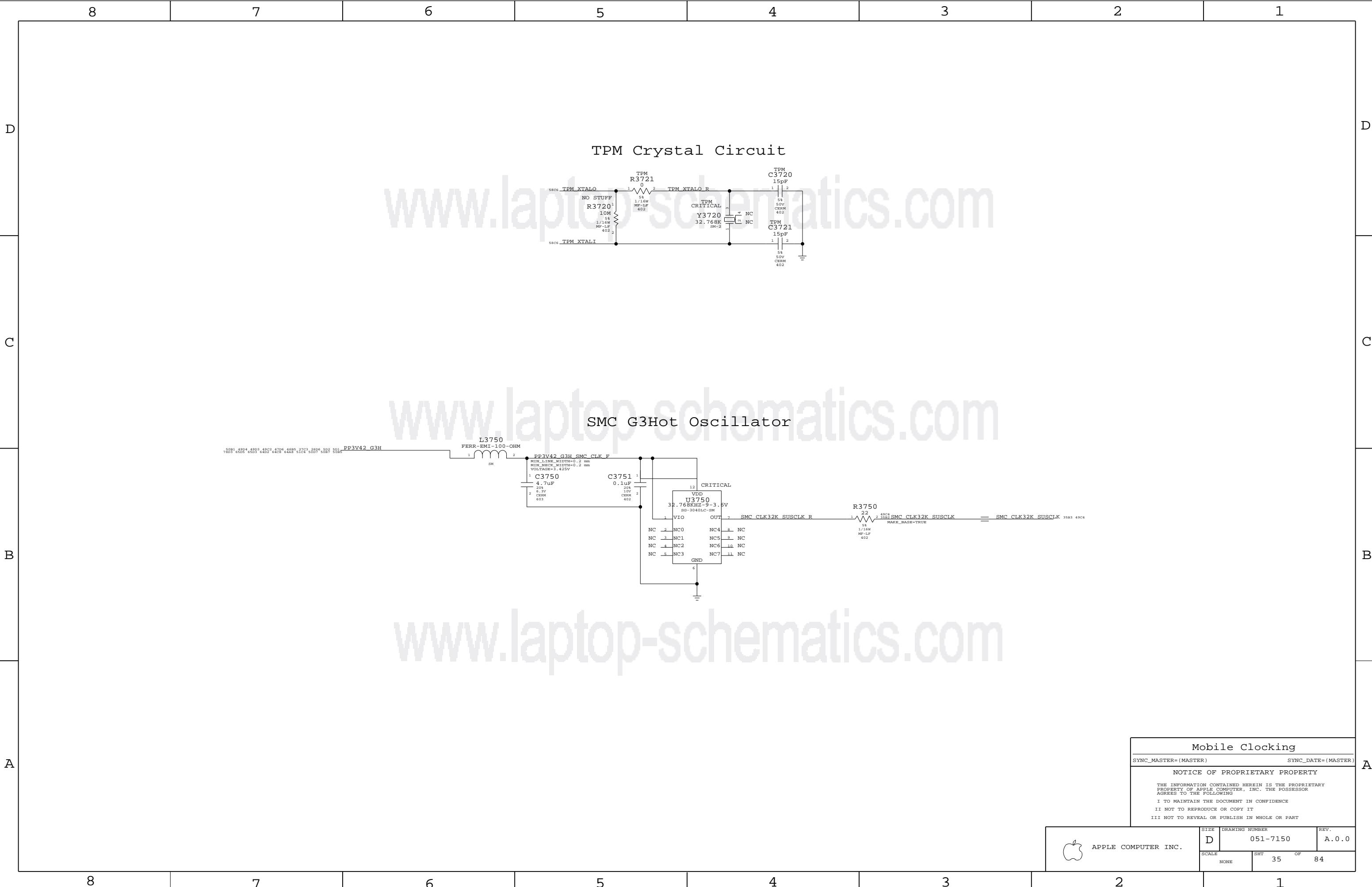
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE
II NOT TO REPRODUCE OR COPY IT
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-7150	A.0.0
SCALE	SHT	OF
NONE	34	84



Mobile Clocking

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

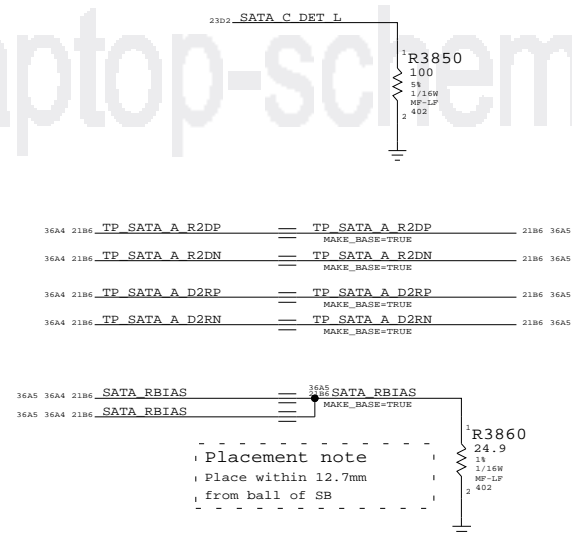
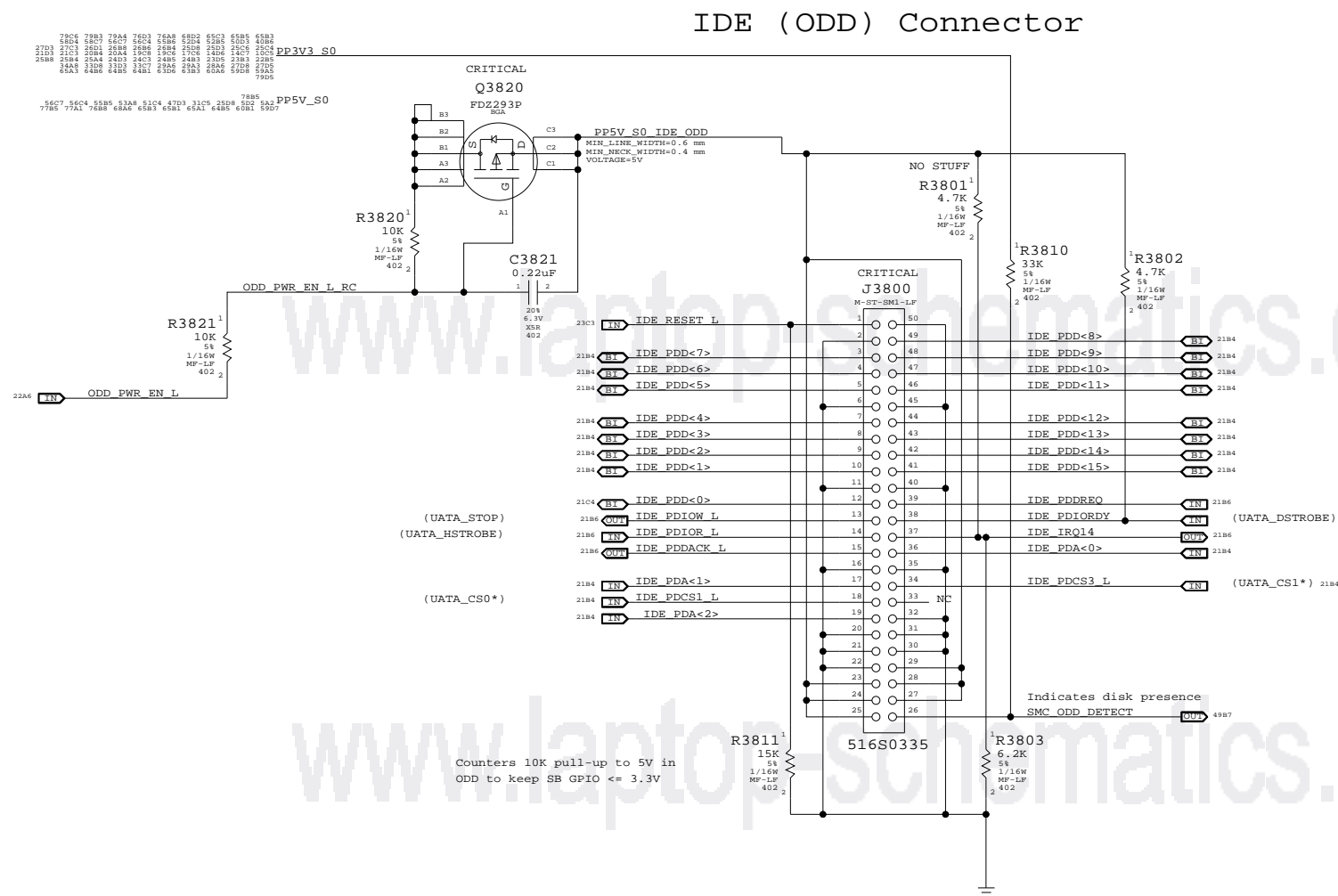
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE


II NOT TO REPRODUCE OR COPY IT

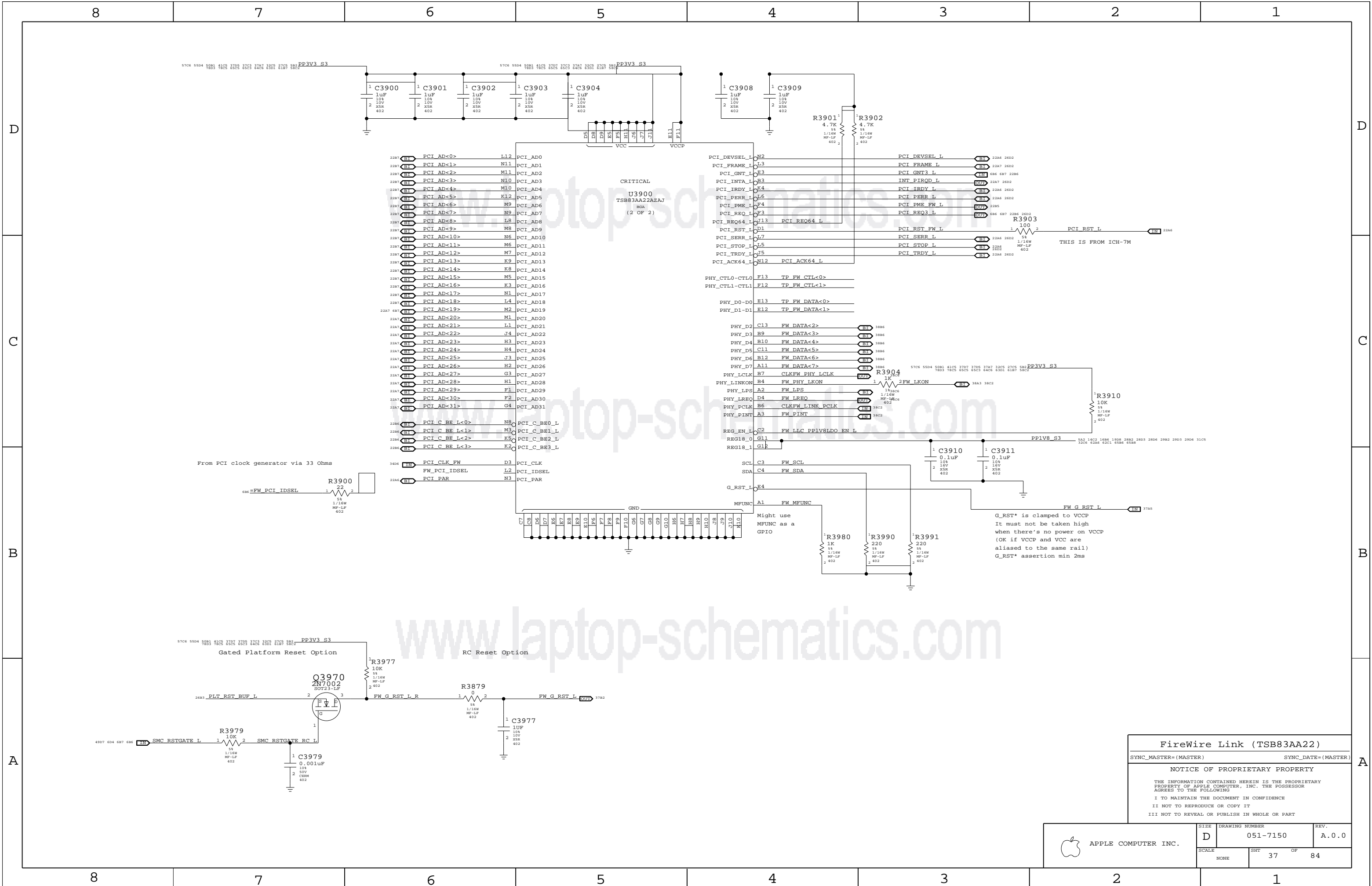
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

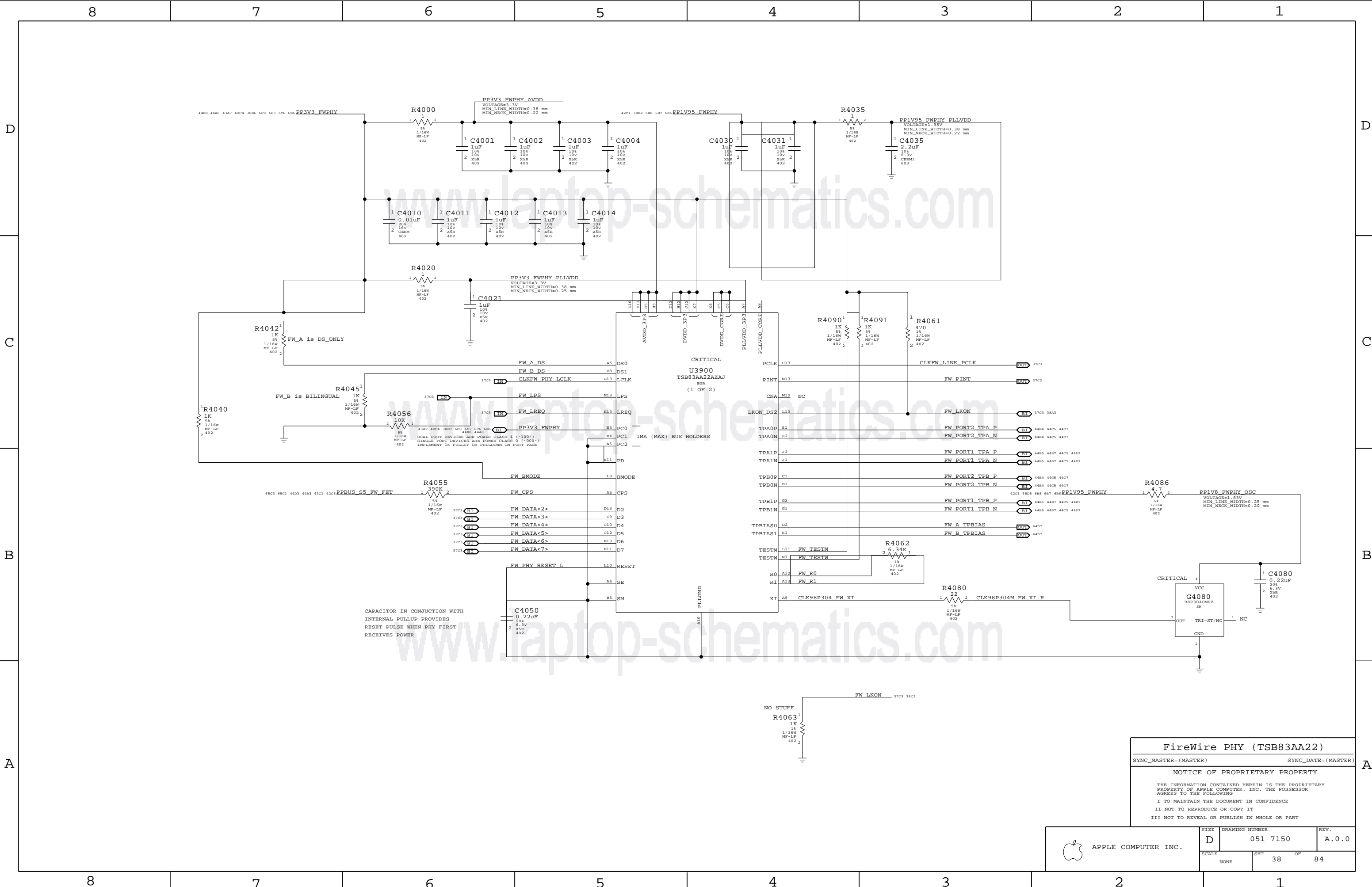
APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 35	OF 84



PATA Connector	
SYNC_MASTER={MASTER}	SYNC_DATE={MASTER}
NOTICE OF PROPRIETARY PROPERTY	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING	
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE	
II NOT TO REPRODUCE OR COPY IT	
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	

 APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
	SCALE	SHT	OF
	NONE	36	84





FireWire PHY (TSB83AA22)

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 38	OF 84

ELECTRICAL_CONSTRAINT_SET	NET_TYPE			
	SPACING	PHYSICAL		
PROVIDED	ENETCONN	ENET_100D	ENETCONN_P<0>	40B3
	ENETCONN	ENET_100D	ENETCONN_N<0>	40C3
	ENETCONN	ENET_100D	ENETCONN_P<1>	40C3
	ENETCONN	ENET_100D	ENETCONN_N<1>	40C3
BY	ENETCONN	ENET_100D	ENETCONN_P<2>	40C3
	ENETCONN	ENET_100D	ENETCONN_N<2>	40C3
	ENETCONN	ENET_100D	ENETCONN_P<3>	40C3
	ENETCONN	ENET_100D	ENETCONN_N<3>	40B3
ETHERNET	ENETCONN	ENET_100D		
PHY	ENETCONN	ENET_100D		

Page Notes

Power aliases required by this page:

- =PP2V5_ENET
- =GND_CHASSIS_ENET

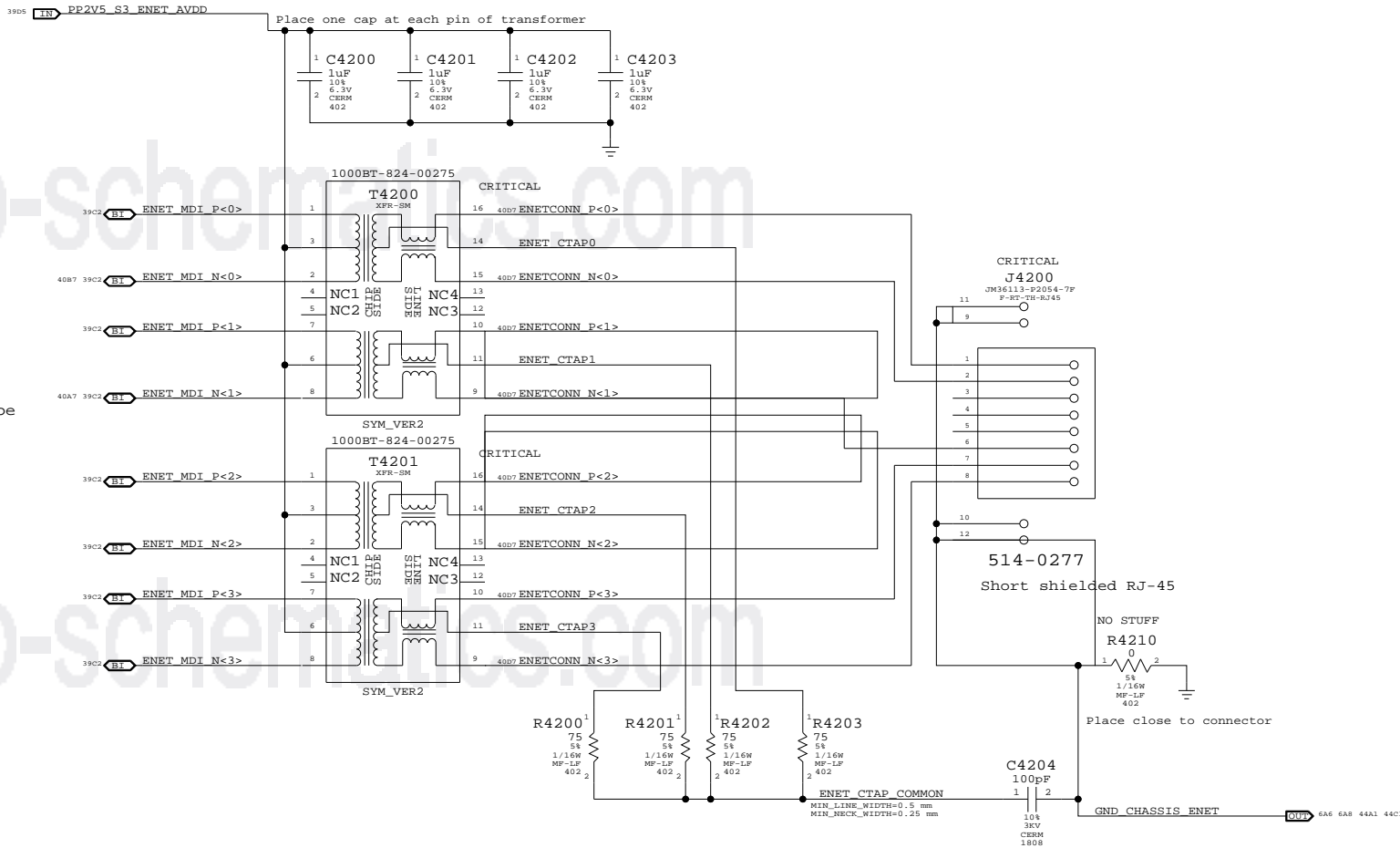
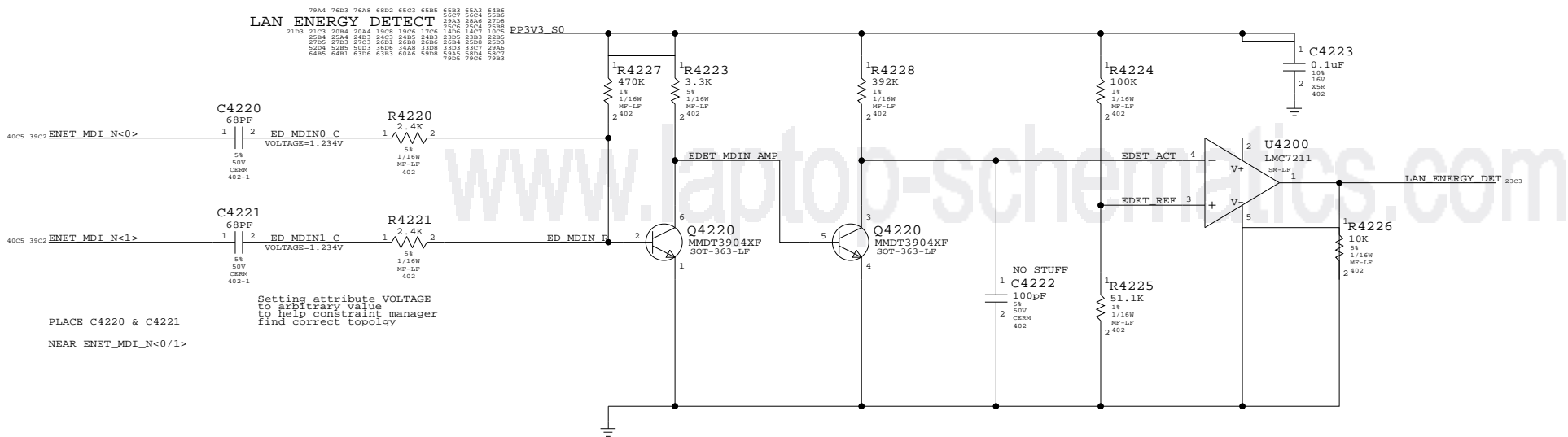
Signal aliases required by this page:

(NONE)

BOM options provided by this page:

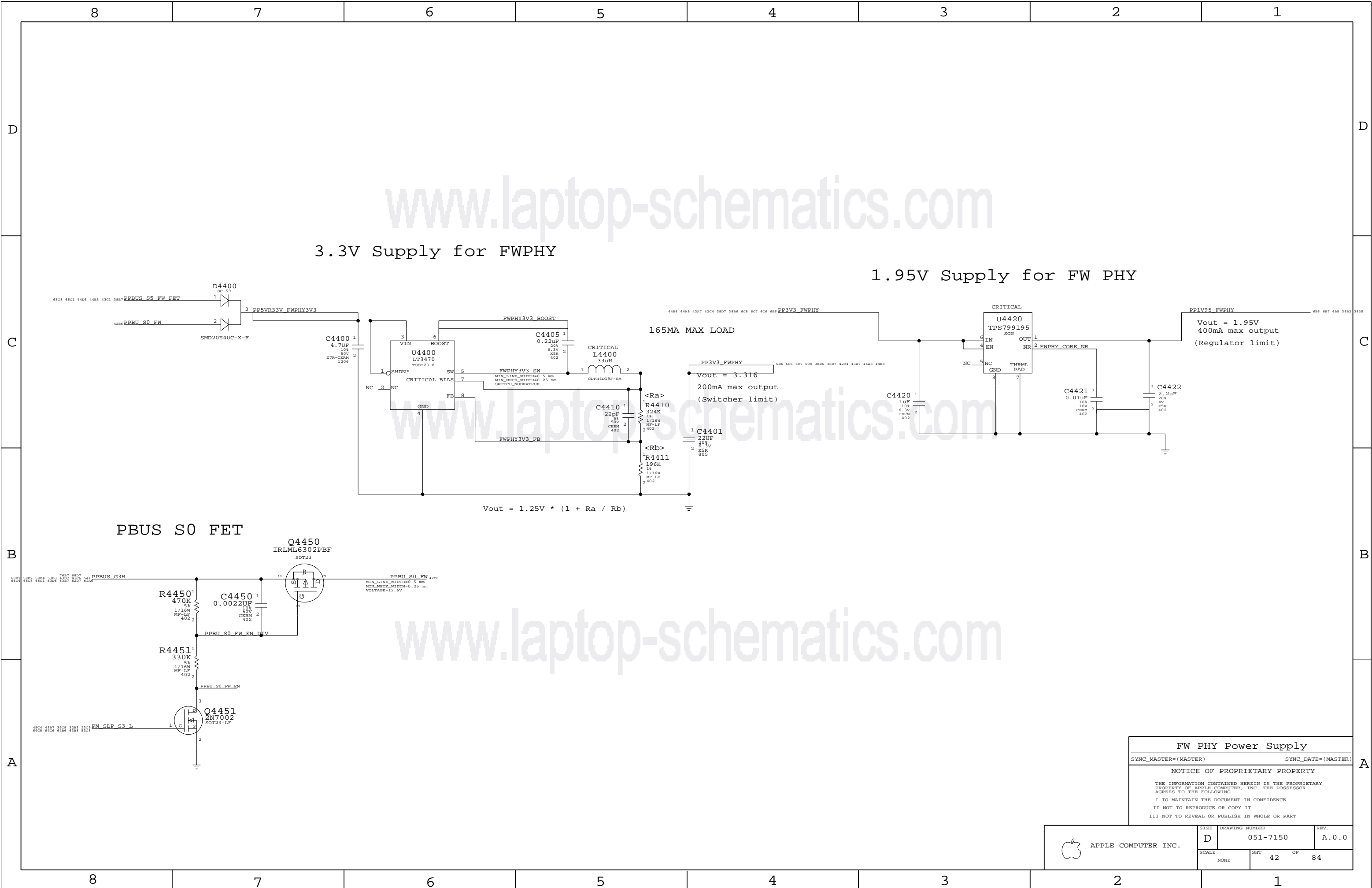
(NONE)

Transformers should be mirrored on opposite sides of the board



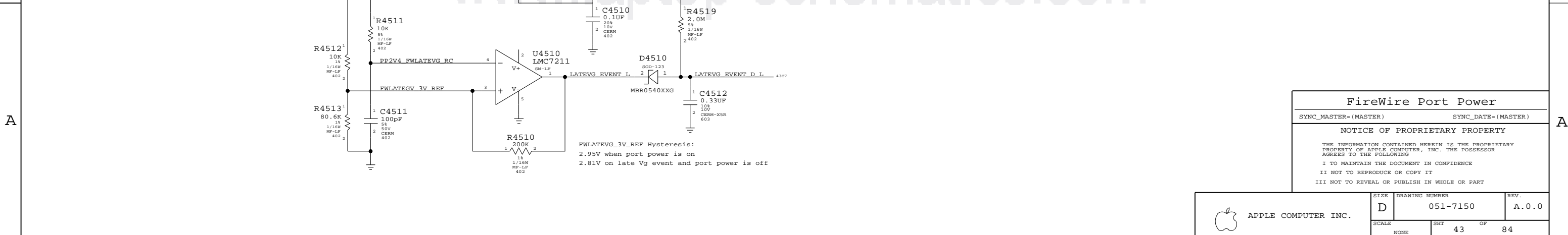
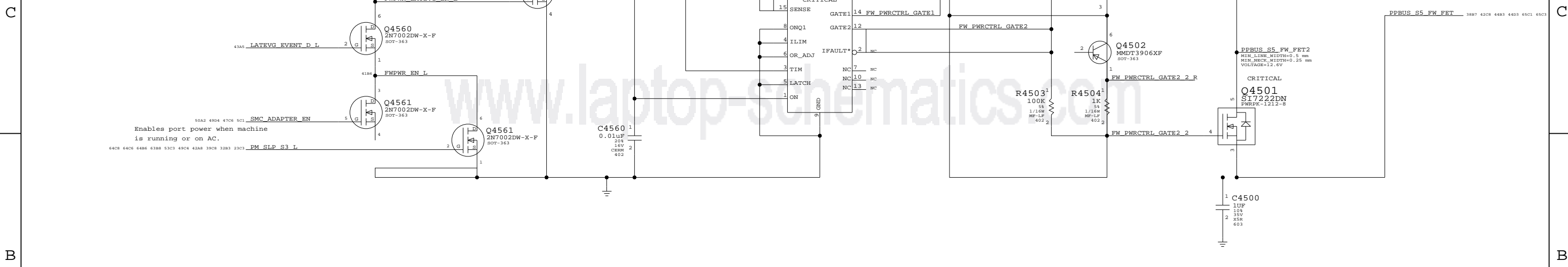
Ethernet Connector	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
NOTICE OF PROPRIETARY PROPERTY	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING	
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE	
II NOT TO REPRODUCE OR COPY IT	
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE	NONE	SHT	40 OF 84



8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

BOM options provided by this page:
(NONE)



III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

SIZE	DRAWING NUMBER	REV.
D	051-7150	A.0.0
SCALE	SHT	OF
NONE	43	84

AREF needs to be isolated from all local grounds per 1394b spec

When a bilingual device is connected to a beta-only device, there is no DC path between them (to avoid ground offset issue)

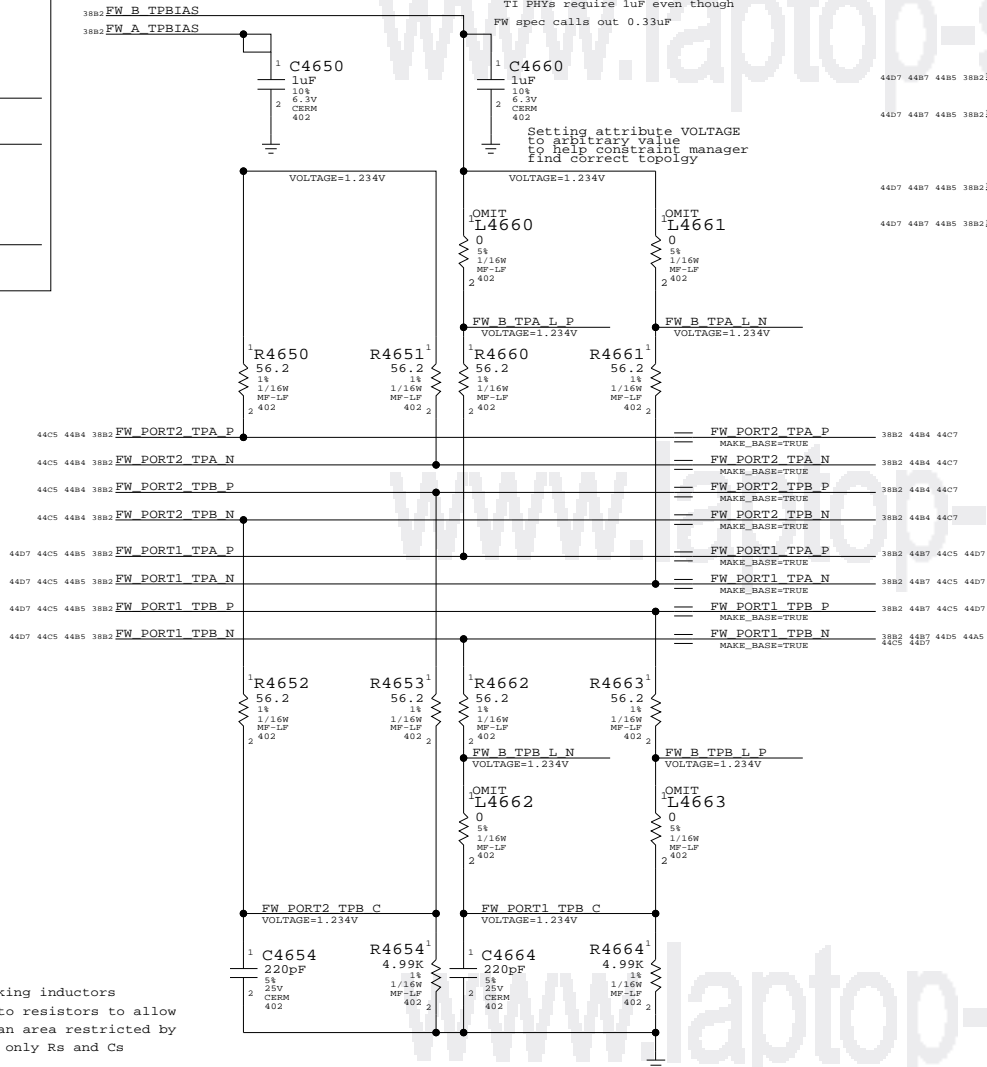
BREF should be hard-connected to logic ground for speed signaling and connection detection currents per 1394b V1.33

<p>Power aliases required by this page:</p> <ul style="list-style-type: none">- =PPFW_PORT1- =PP3V3_S5_FWLATEVG- =GND_CHASSIS_FW_PORT1 <p>Signal aliases required by this page:</p> <p>(NONE)</p> <p>NOTE: This page is expected to contain the necessary aliases to map the FireWire TPA/TPB pairs to their appropriate connectors and/or to properly terminate unused signals.</p> <hr/> <p>BOM options provided by this page:</p> <p>(NONE)</p> <hr/> <p>NOTE: FireWire TPA/TPB pairs are NOT constrained on this page. It is assumed that FireWire PHY page will provide the appropriate constraints to apply to entire TPA/TPB XNets.</p> <hr/>
<p>1394b implementation based on Apple FireWire Design Guide (FWDG 0.6, 5/14/03)</p>

Place close to FireWire PHY

TI PHYS require 1uF even though
FW spec calls out 0.33uF

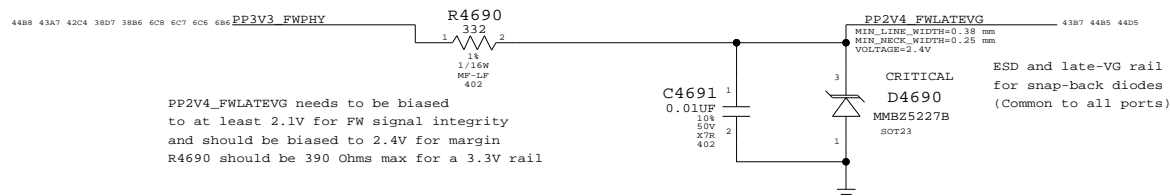
```
Setting attribute VOLTAGE
to arbitrary value
to help constraint manager
find correct topology
```



Note: The peaking inductors were changed to resistors to allow placement in an area restricted by DFM rules for only Rs and Cs

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
152S0414	4	IND, 18nH-15mA, 0402	L4660, L4661, L4662, L4663	CRITICAL	

PP2V4_FWLATEVG needs to be biased
to at least 2.1V for FW signal integrity
and should be biased to 2.4V for margin
R4690 should be 390 Ohms max for a 3.3V rail



DP2V4 FW1ATEVG

DP4620
BAV99DW-X-F
SOT-363

C4621
0.01uF
104
50V
X7R
402

DP4620
BAV99DW-X-F
SOT-363

C4620
0.01uF
104
50V
X7R
402

44B5 44A5 43B7

44D7 44B7 44B5 38B2 PW_PORT1_TPB_N

44D7 44B7 44B5 38B2 PW_PORT1_TPB_P

44D7 44B7 44B5 38B2 PW_PORT1_TPA_N

44D7 44B7 44B5 38B2 PW_PORT1_TPA_P

DP4621
BAV99DW-X-F
SOT-363

C4622
0.01uF
104
50V
X7R
402

DP4621
BAV99DW-X-F
SOT-363

C4623
0.01uF
104
50V
X7R
402

TPA_P

38B2 44B4 44C7

UE

[illegible]

PPFW_R5_PW_FET

L4620
FERR-250-OHM

1 2

PPFW_PORT1_VP

MIN_LINE_WIDTH=0.5 mm
MIN_RECT_WIDTH=0.25 mm
VOLTAGE=3.3V

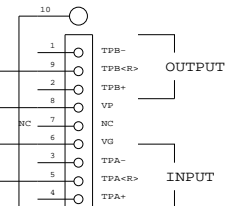
0.001uF

C4624

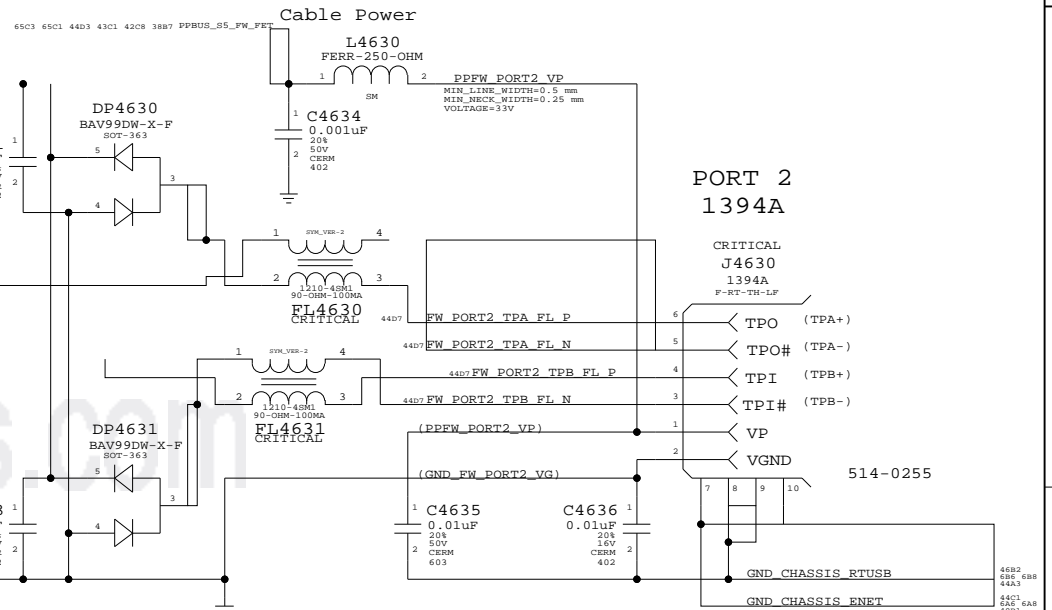
205
50V
CERM
402

Note:Trace PPFW_PORT1_VP should handle up to 5A

CRITICAL
J4620
1394B-UG31903



CRITICAL
J4630
1394A
F-RT-TH-LF



SYNC_MASTER= (MASTER)	SYNC_DATE= (MASTER)
------------------------	----------------------

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY
PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR
AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE
II NOT TO REPRODUCE OR COPY IT
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

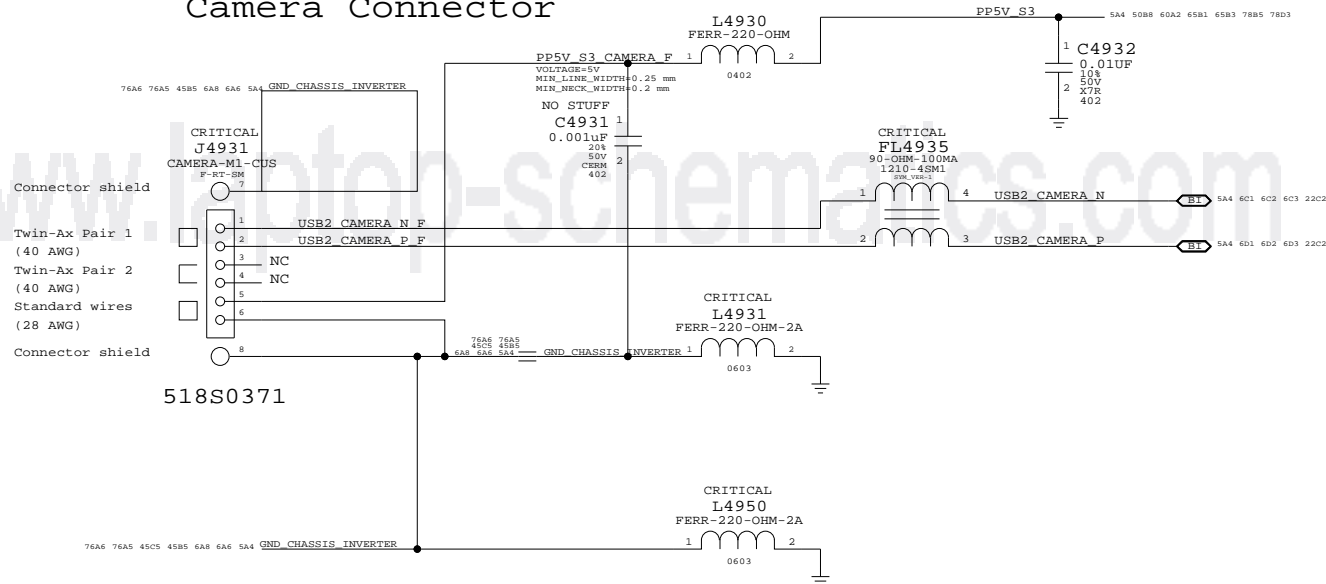
SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
SCALE NONE	SHT 44	OF 84



APPLE COMPUTER INC.

www.laptop-schematics.com

Camera Connector



Camera Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

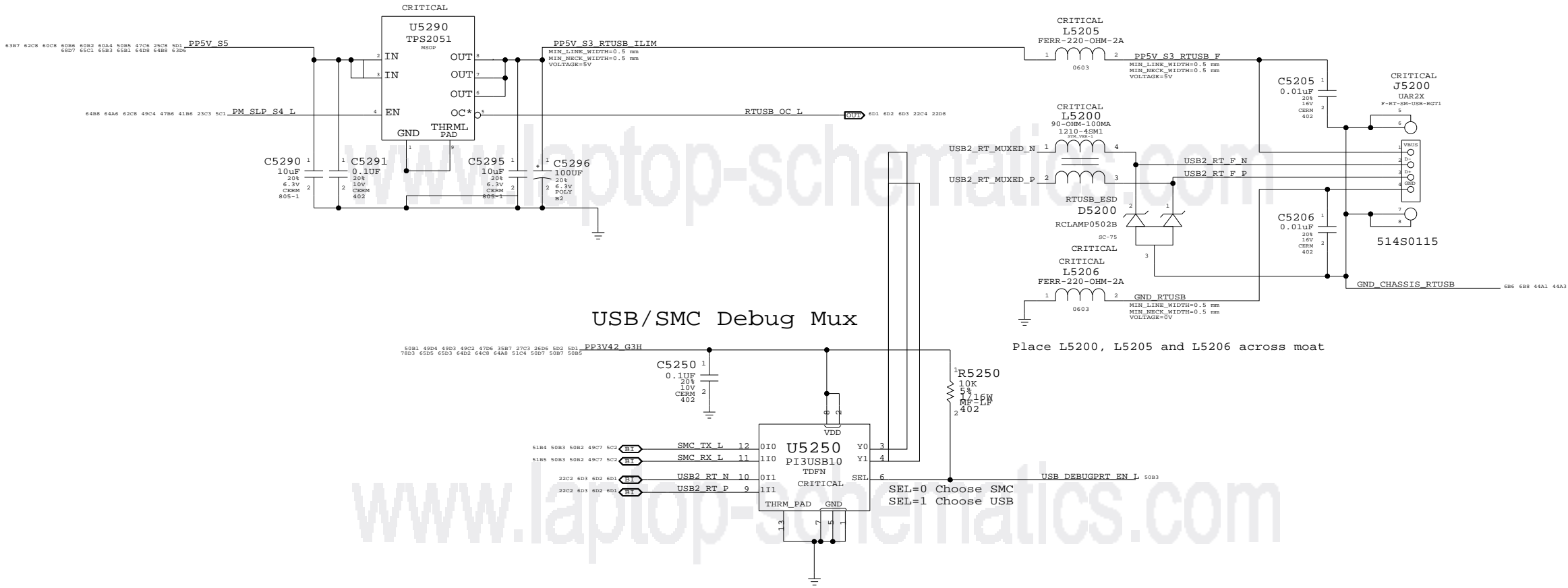
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 45	OF 84

Port Power Switch

Right USB Port



External USB Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

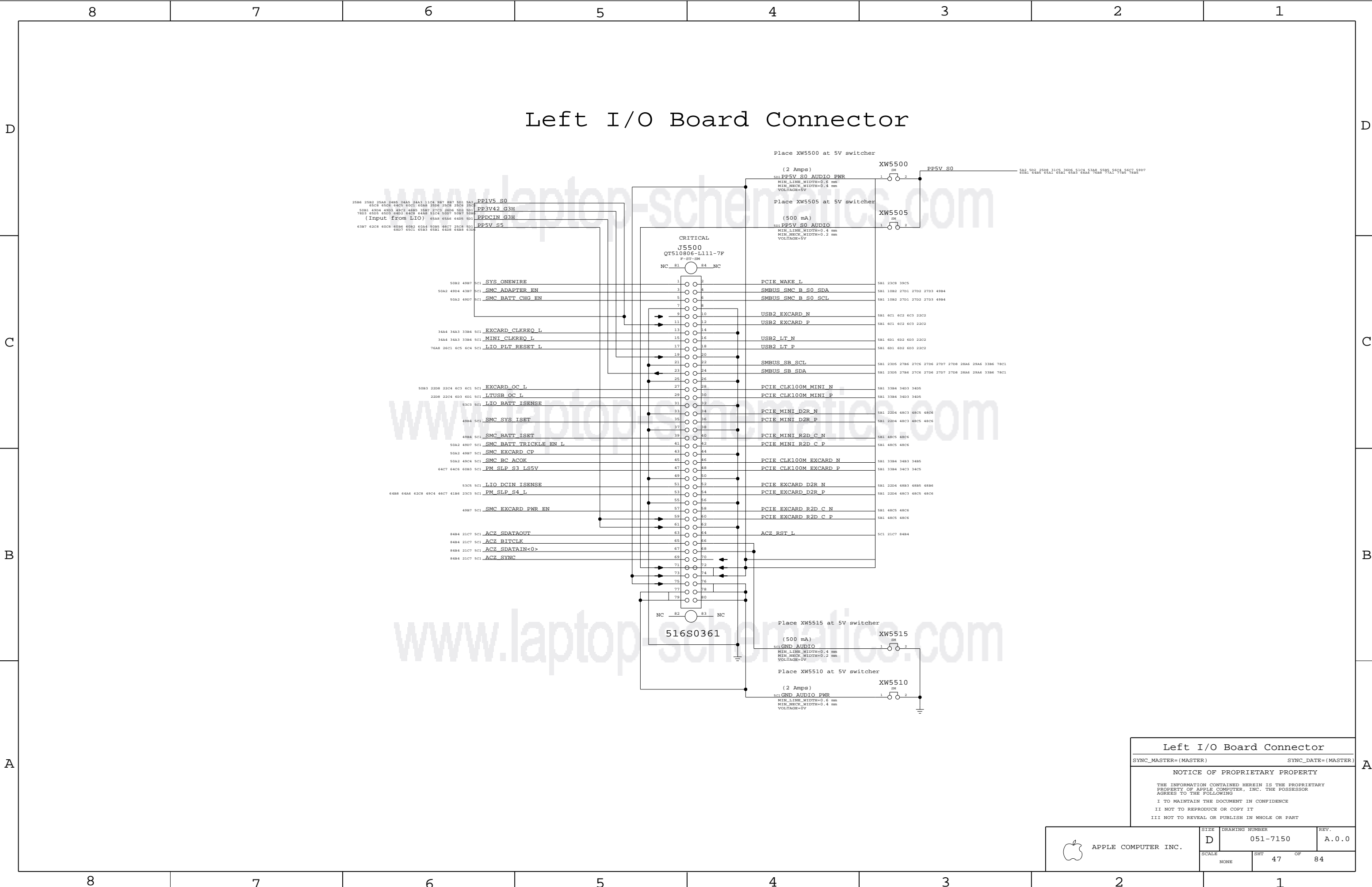
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



Left I/O Board Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

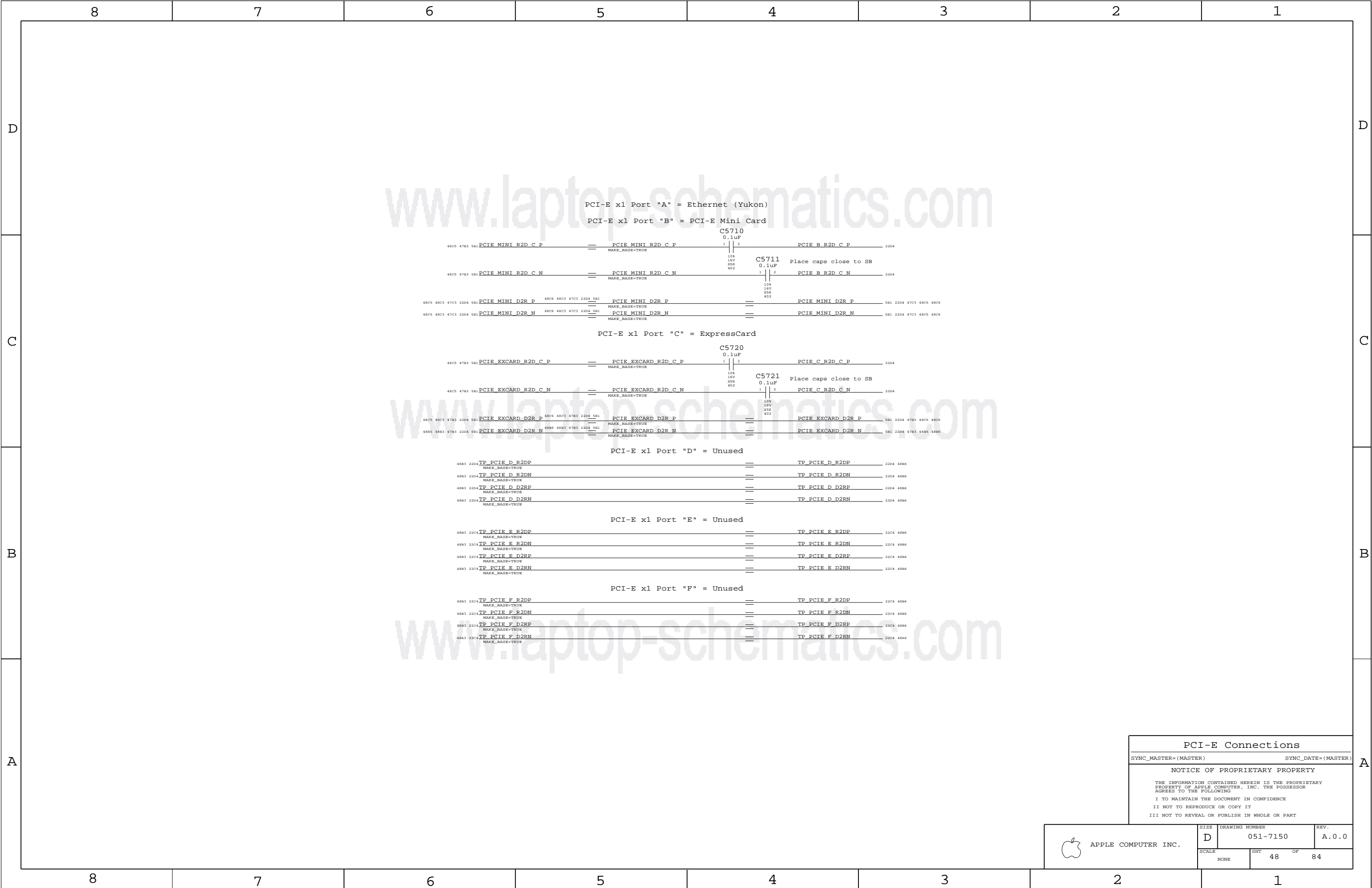
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

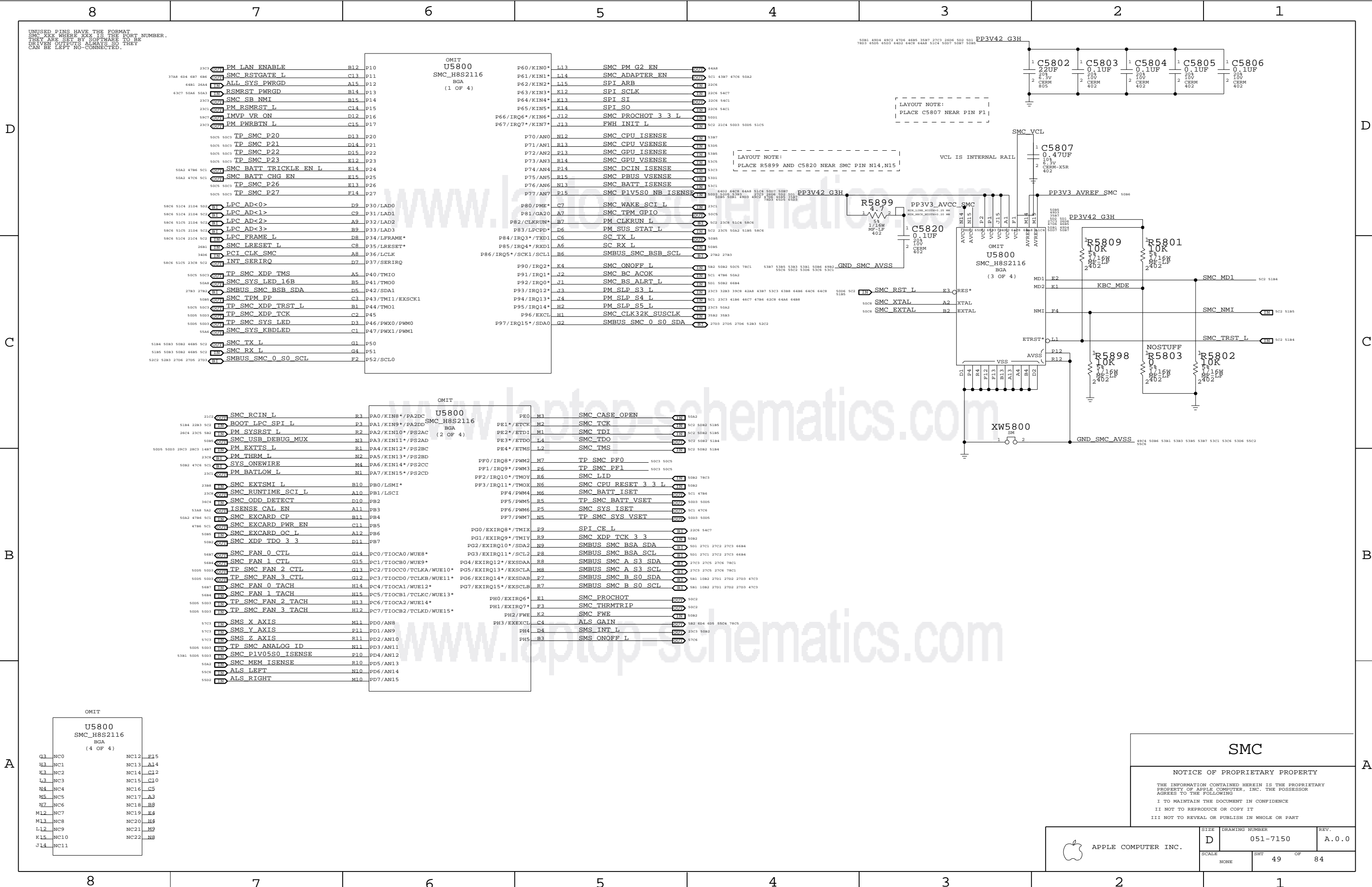
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

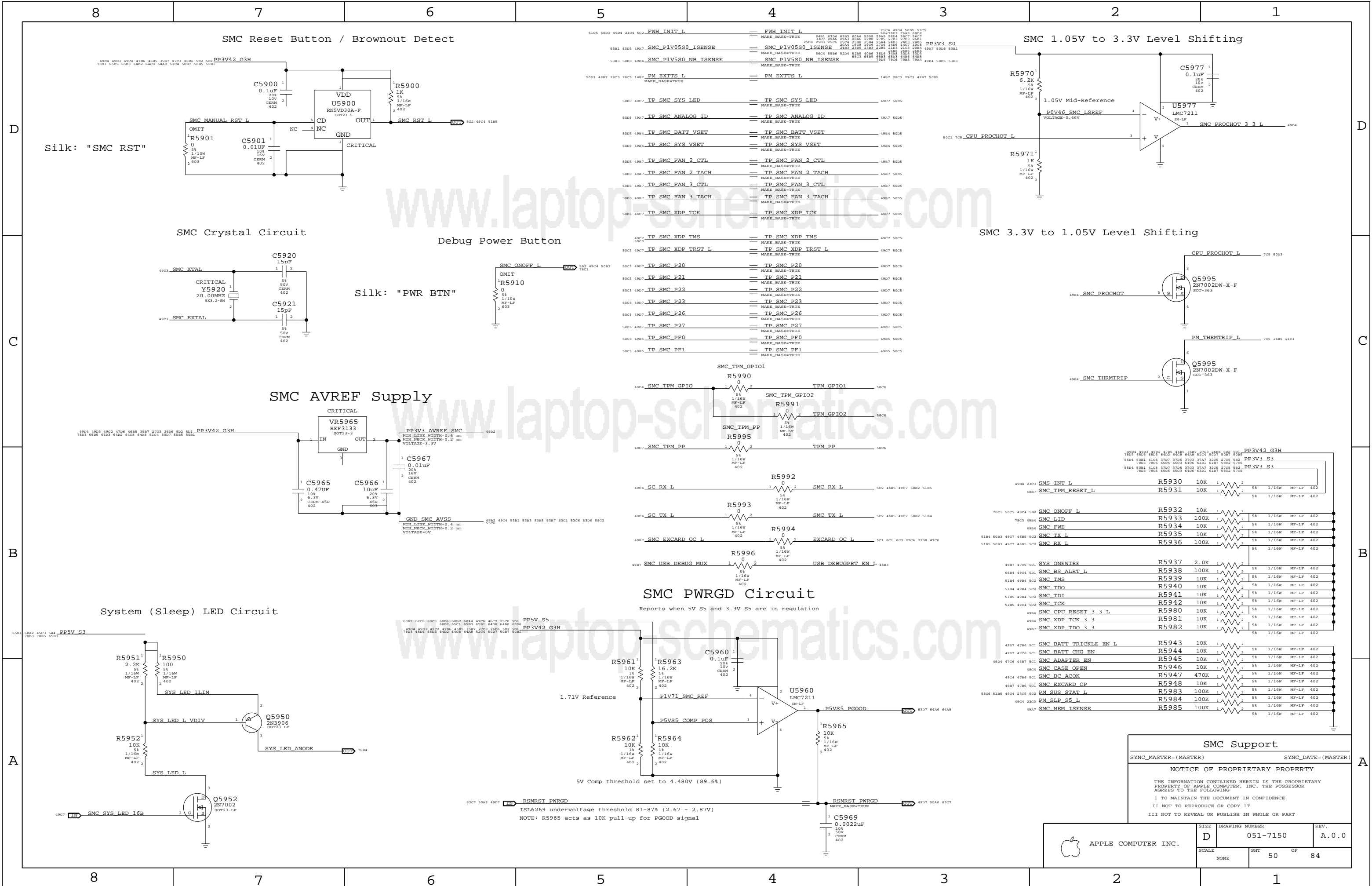
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		47	84



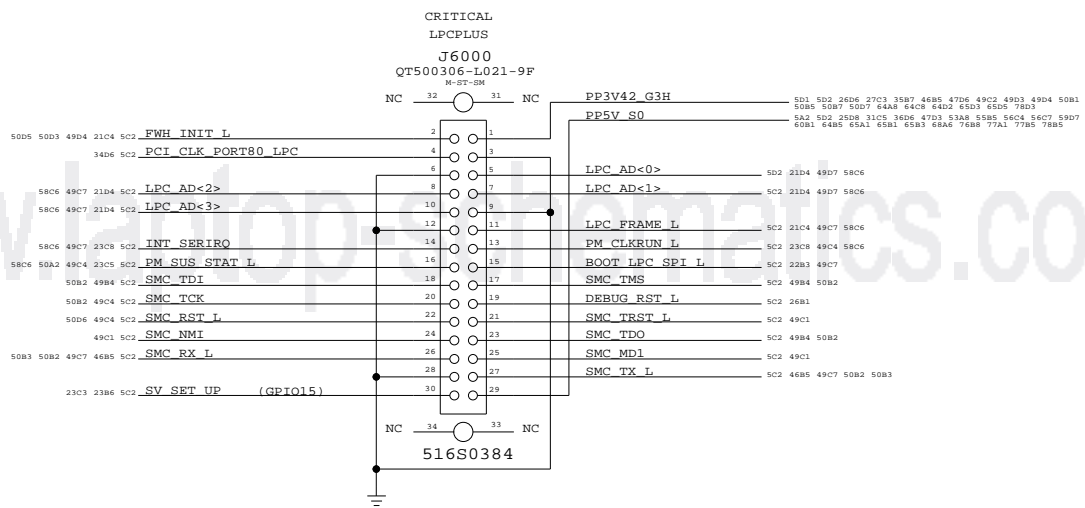




www.laptop-schematics.com

www.laptop-schematics.com

www.laptop-schematics.com



LPC+ Debug Connector

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

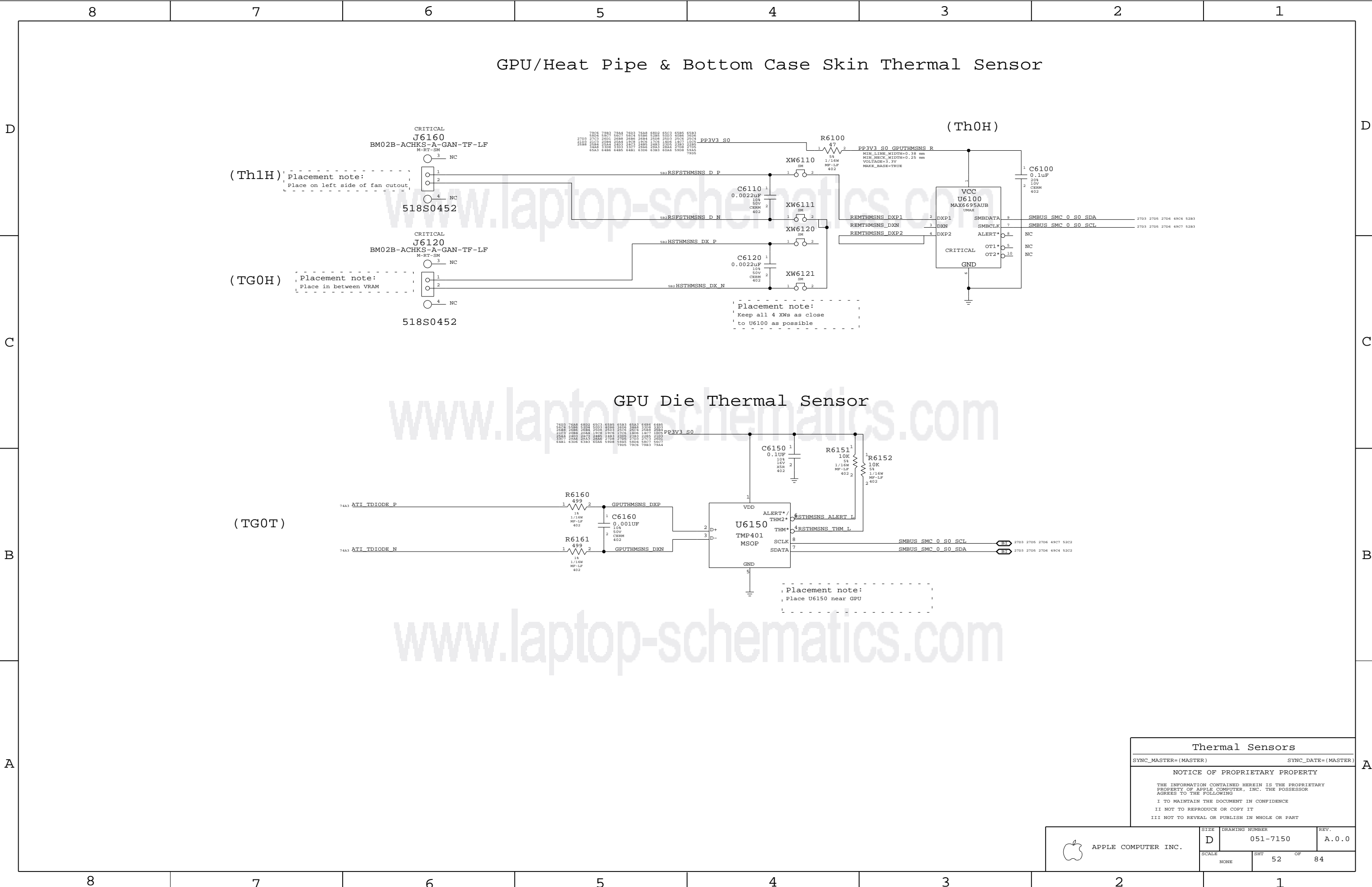
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

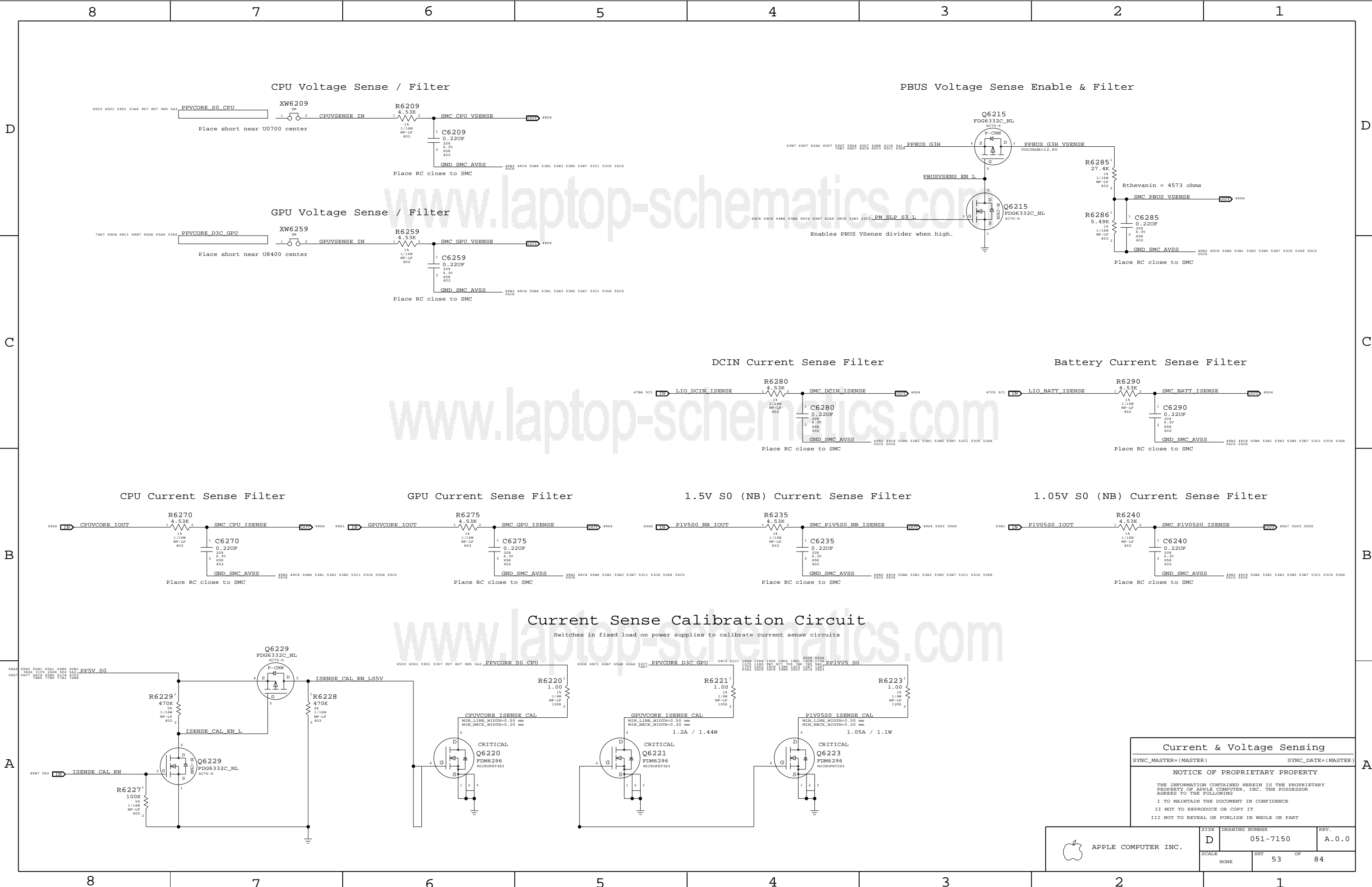
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 51	OF 84



Thermal Sensors		
SYNC_MASTER=(MASTER)		SYNC_DATE=(MASTER)
NOTICE OF PROPRIETARY PROPERTY		
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING		
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE		
II NOT TO REPRODUCE OR COPY IT		
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		

APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 52	OF 84



Current & Voltage Sensing

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

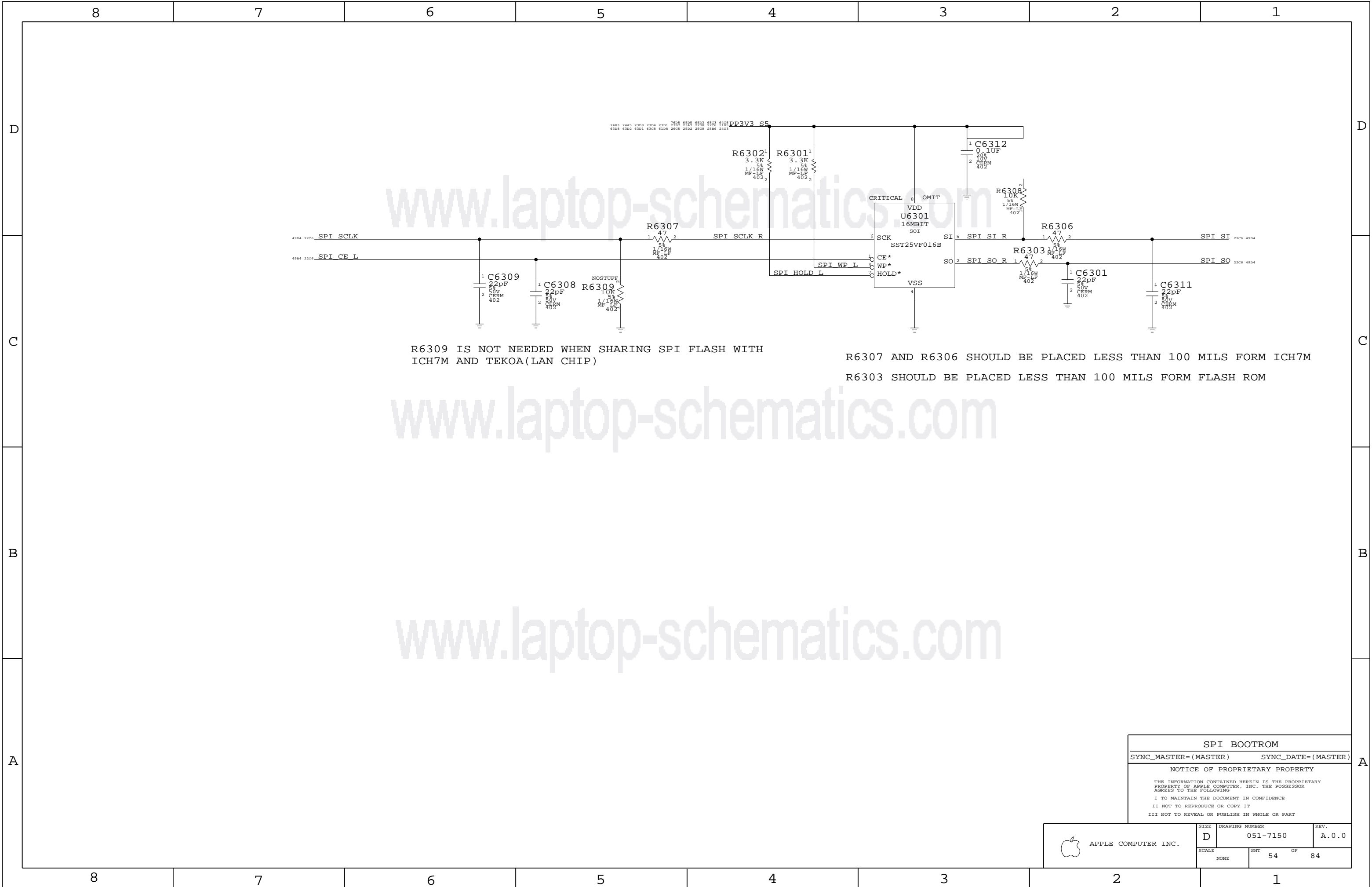
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

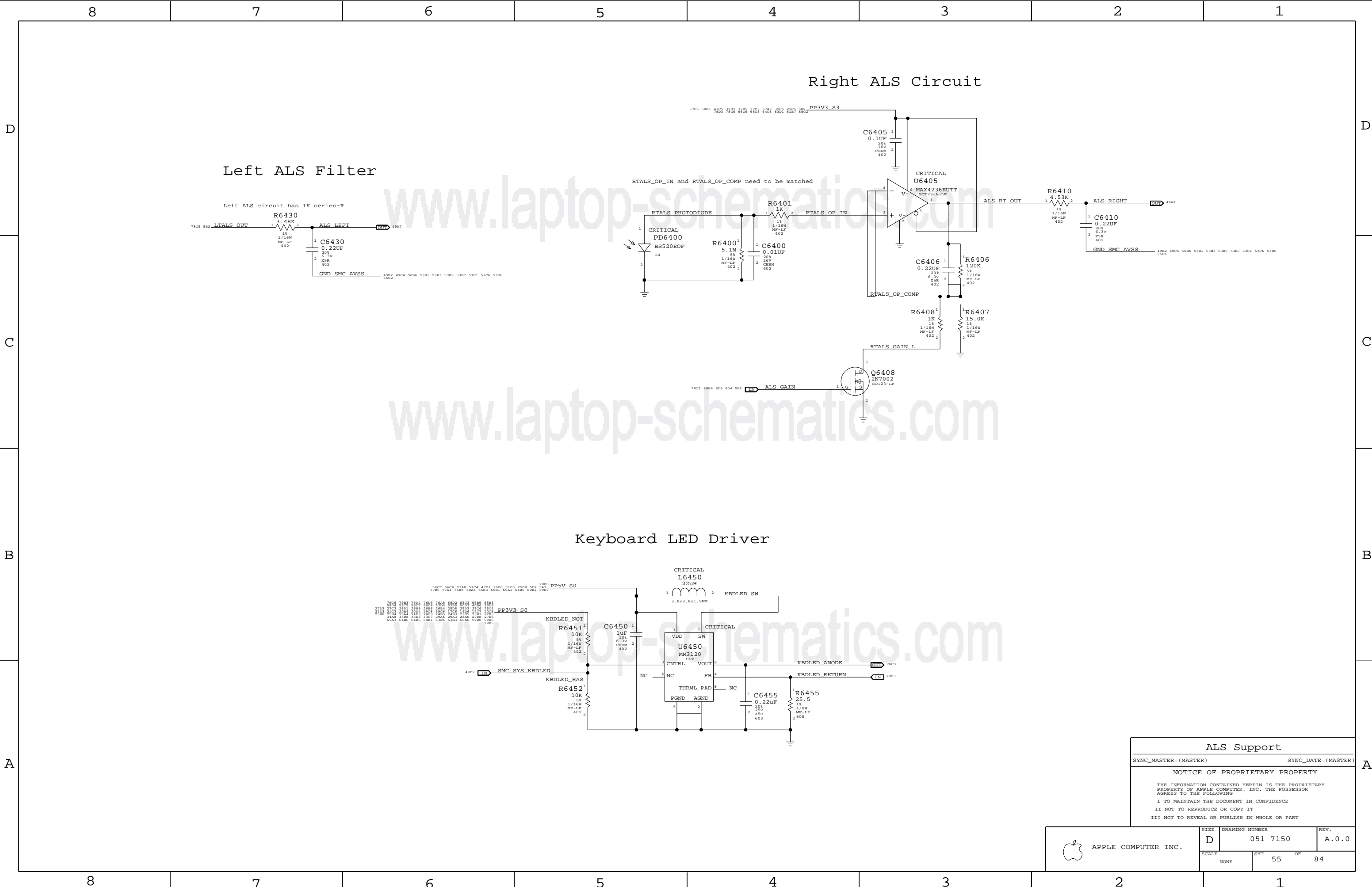
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		53	84





Left ALS Filter

Right ALS Circuit

Keyboard LED Driver

ALS Support

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY


THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

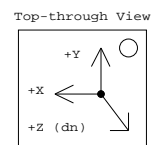
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		55	84




APPLE COMPUTER INC.

Desired orientation when
placed on board bottom-side:

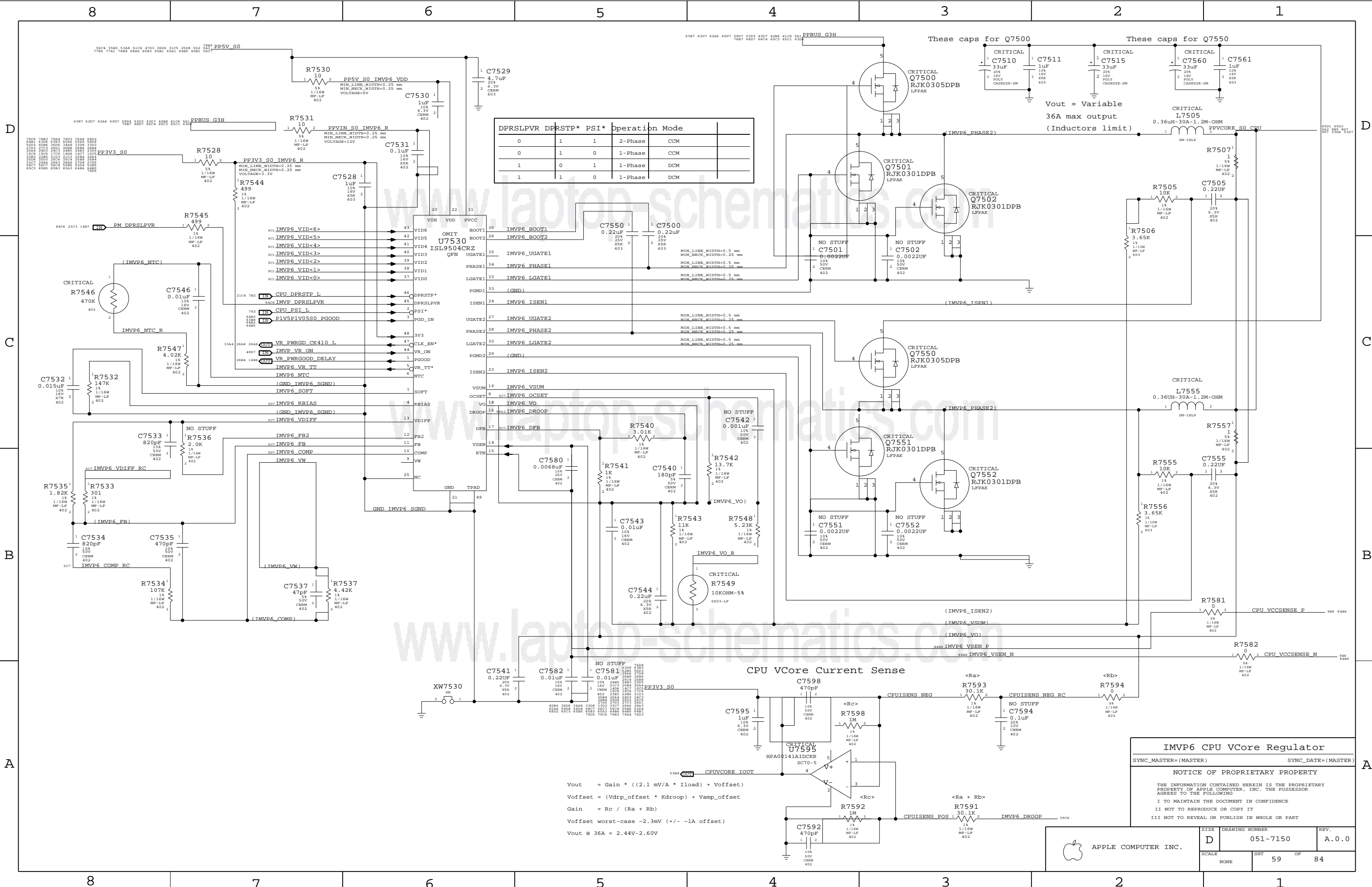


M59 placement: Bottom-side

 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
	SCALE NONE	SHT 57 OF 84	



APPLE COMPUTER INC.



DPRSLPVR	DPRSTP*	PSI*	Operation Mode	
0	1	0	2-Phase	CCM
0	1	0	1-Phase	CCM
1	0	1	1-Phase	DCM
1	1	0	1-Phase	DCM

$$V_{out} = \text{Gain} * ((2.1 \text{ mV/A} * I_{load}) + V_{offset})$$
$$V_{offset} = (V_{drp_offset} * K_{droop}) + V_{amp_offset}$$
$$\text{Gain} = R_c / (R_a + R_b)$$
$$V_{offset \text{ worst-case}} \sim 2.3\text{mV (+/- } -1\text{A offset)}$$
$$V_{out @ 36A} = 2.44\text{V} - 2.60\text{V}$$

IMVP6 CPU VCore Regulator

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

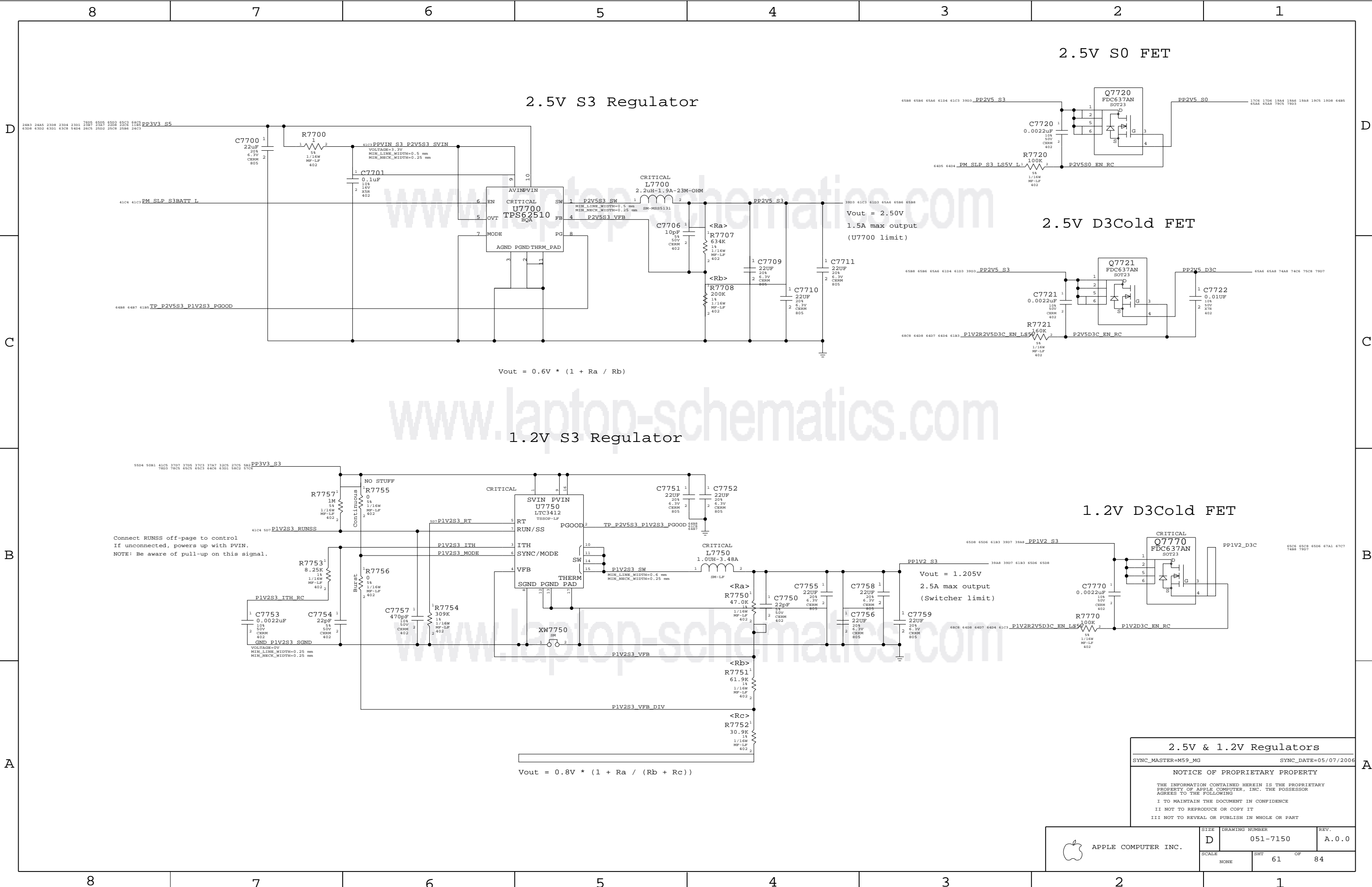
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



2.5V S3 Regulator

2.5V S0 FET

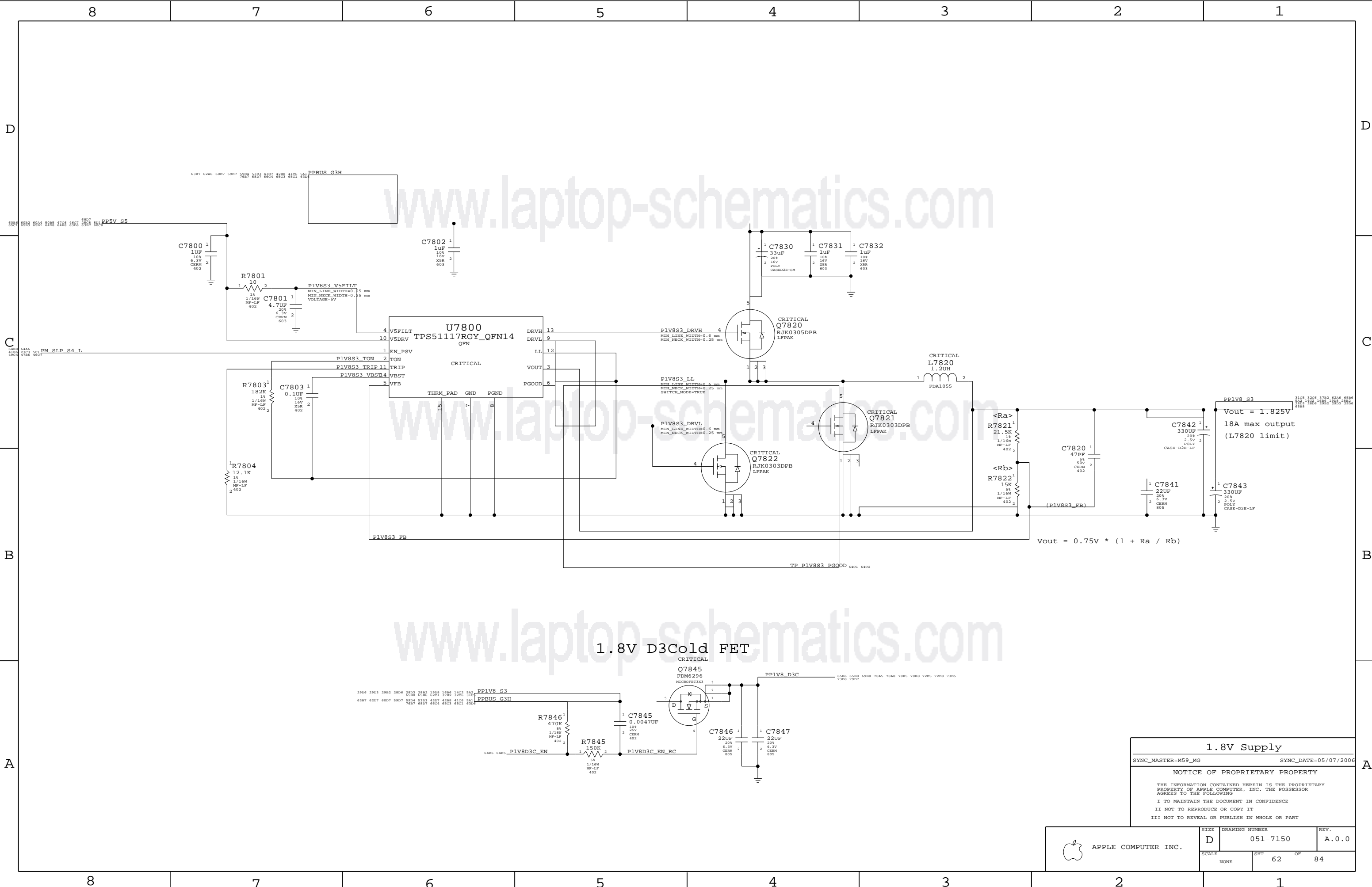
2.5V D3Cold FET

1.2V S3 Regulator

1.2V D3Cold FET

2.5V & 1.2V Regulators		
SYNC_MASTER=M59_MG		SYNC_DATE=05/07/2006
NOTICE OF PROPRIETARY PROPERTY		
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING		
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE		
II NOT TO REPRODUCE OR COPY IT		
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		61	84



D

C

B

A

D

C

B

A

1.8V Supply

SYNC_MASTER=M59_MG

SYNC_DATE=05/07/2006

NOTICE OF PROPRIETARY PROPERTY

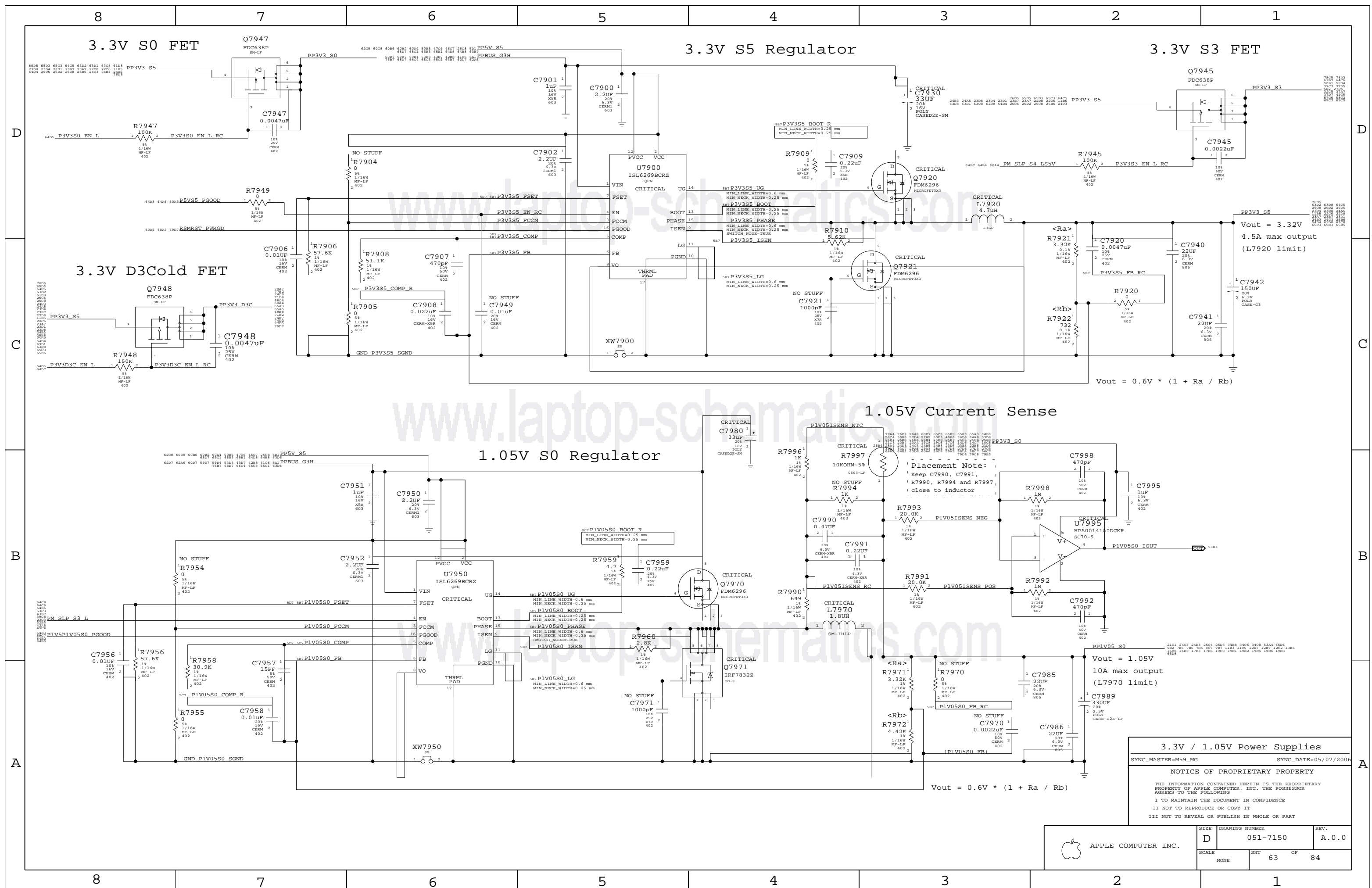
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

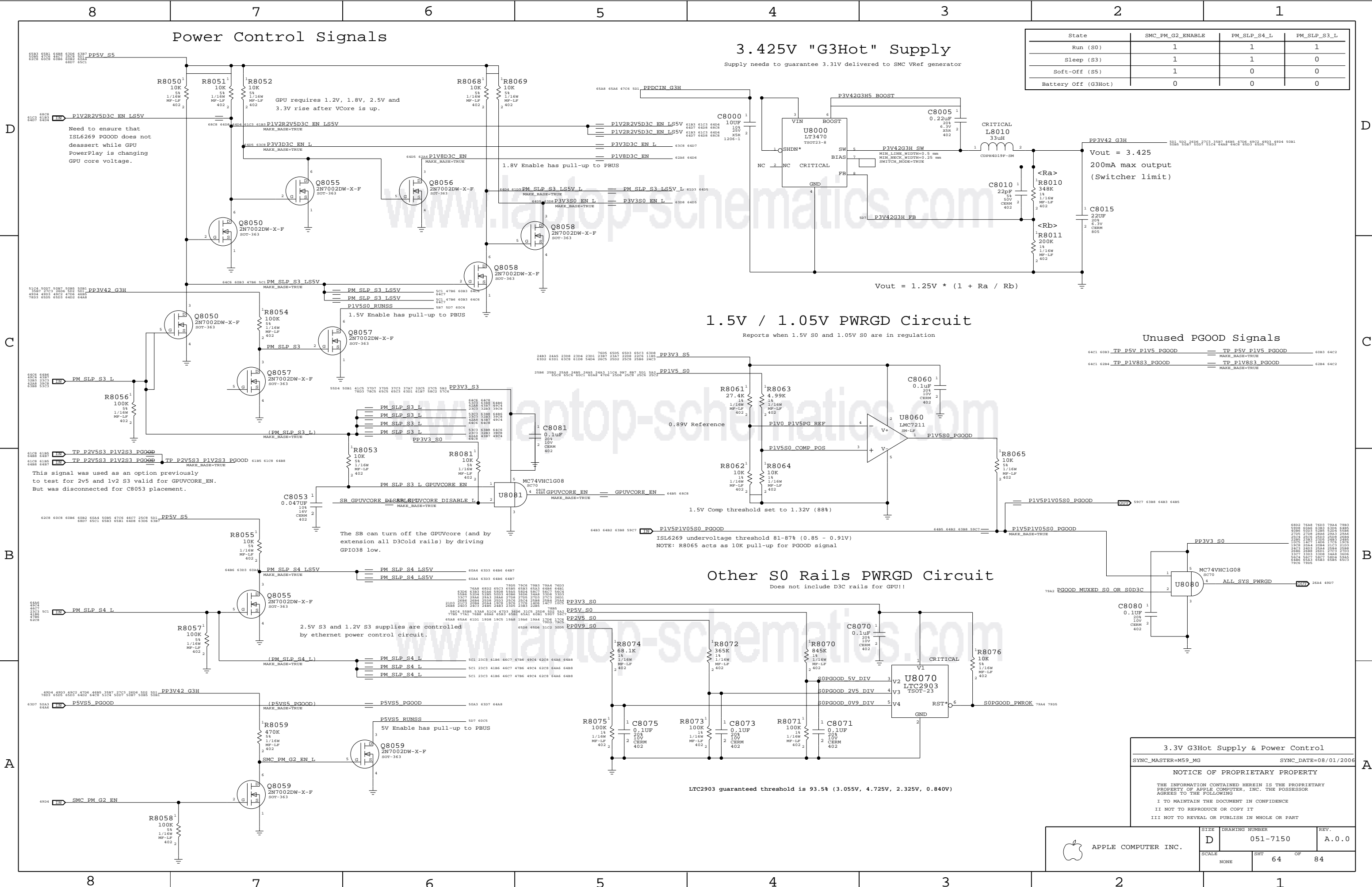
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		62	84





State	SMC_PM_G2_ENABLE	PM_SLP_S4_L	PM_SLP_S3_L
Run (S0)	1	1	1
Sleep (S3)	1	1	0
Soft-Off (S5)	1	0	0
Battery Off (G3Hot)	0	0	0

3.425V "G3Hot" Supply

Supply needs to guarantee 3.31V delivered to SMC VRef generator

Vout = 3.425
200mA max output
(Switcher limit)

$$V_{out} = 1.25V * (1 + R_a / R_b)$$

1.5V / 1.05V PWRGD Circuit

Reports when 1.5V S0 and 1.05V S0 are in regulation

Unused PGOOD Signals

64C1 60B3 TP P5V P1V5 PGOOD	TP P5V P1V5 PGOOD	60B3 64C2
64C1 62B4 TP P1V8S3 PGOOD	TP P1V8S3 PGOOD	62B4 64C2

Other S0 Rails PWRGD Circuit

Does not include D3C rails for GPU!!

3.3V G3Hot Supply & Power Control

SYNC_MASTER=M59_MG

SYNC_DATE=08/01/2006

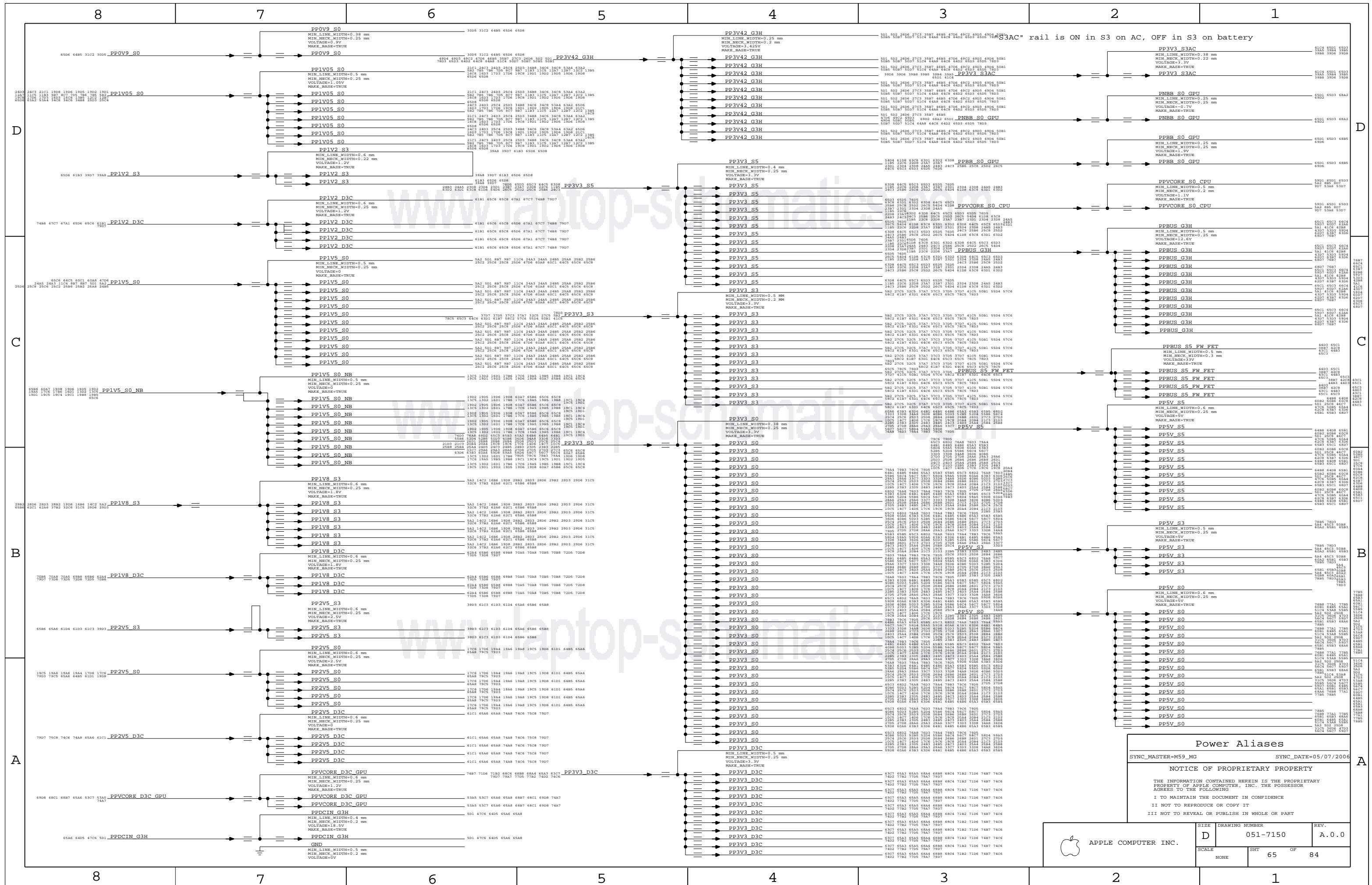
NOTICE OF PROPRIETARY PROPERTY

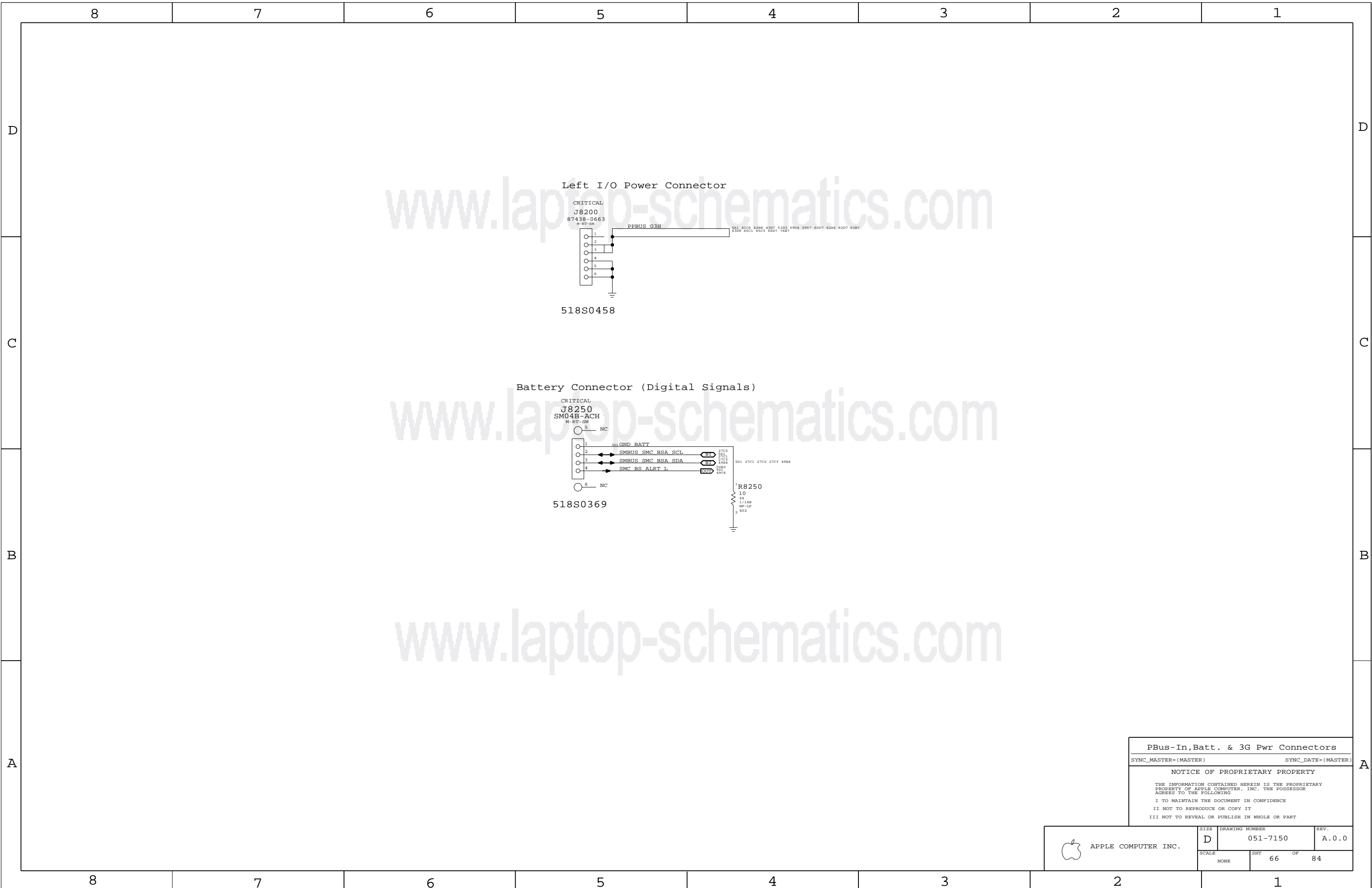
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART





PBus-In, Batt. & 3G Pwr Connectors

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

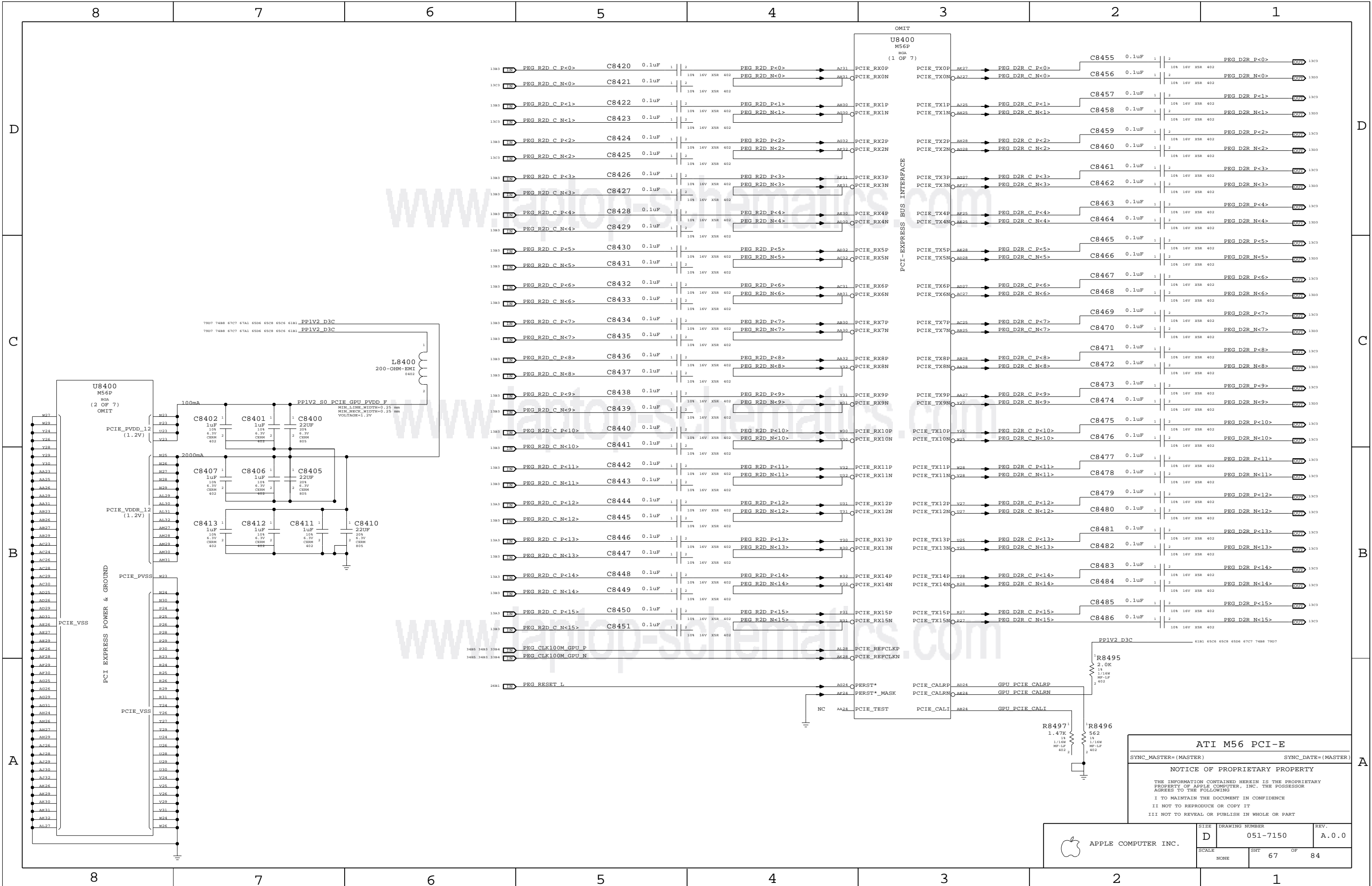
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

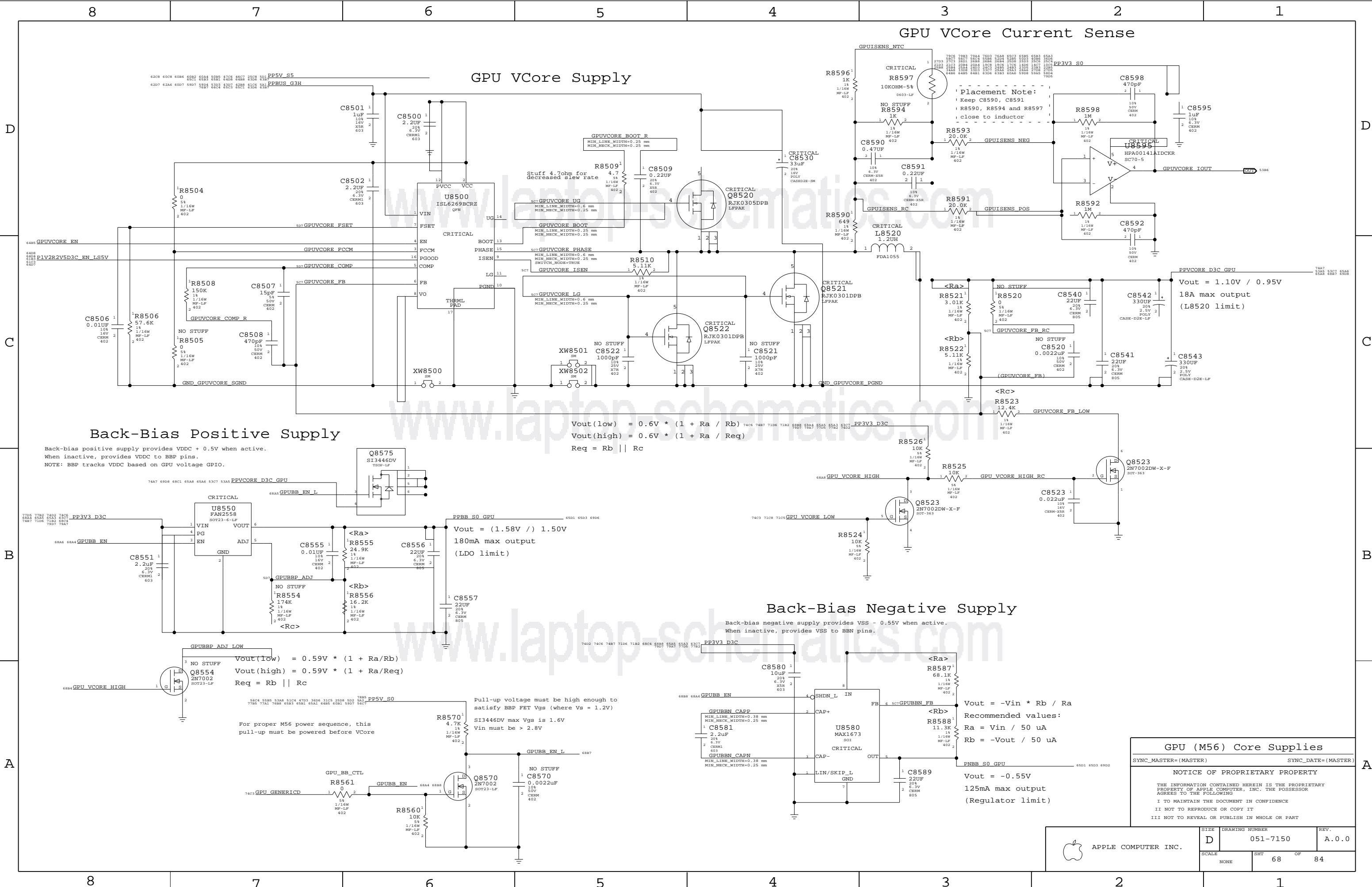
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		66	84





GPU (M56) Core Supplies		
SYNC_MASTER=(MASTER)		SYNC_DATE=(MASTER)
NOTICE OF PROPRIETARY PROPERTY		
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING		
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE		
II NOT TO REPRODUCE OR COPY IT		
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-7150	A.0.0
SCALE		SHT	OF
NONE		68	84

Page Notes

Power aliases required by this page:

```
- =PP1V5_GPU_VDD15
```

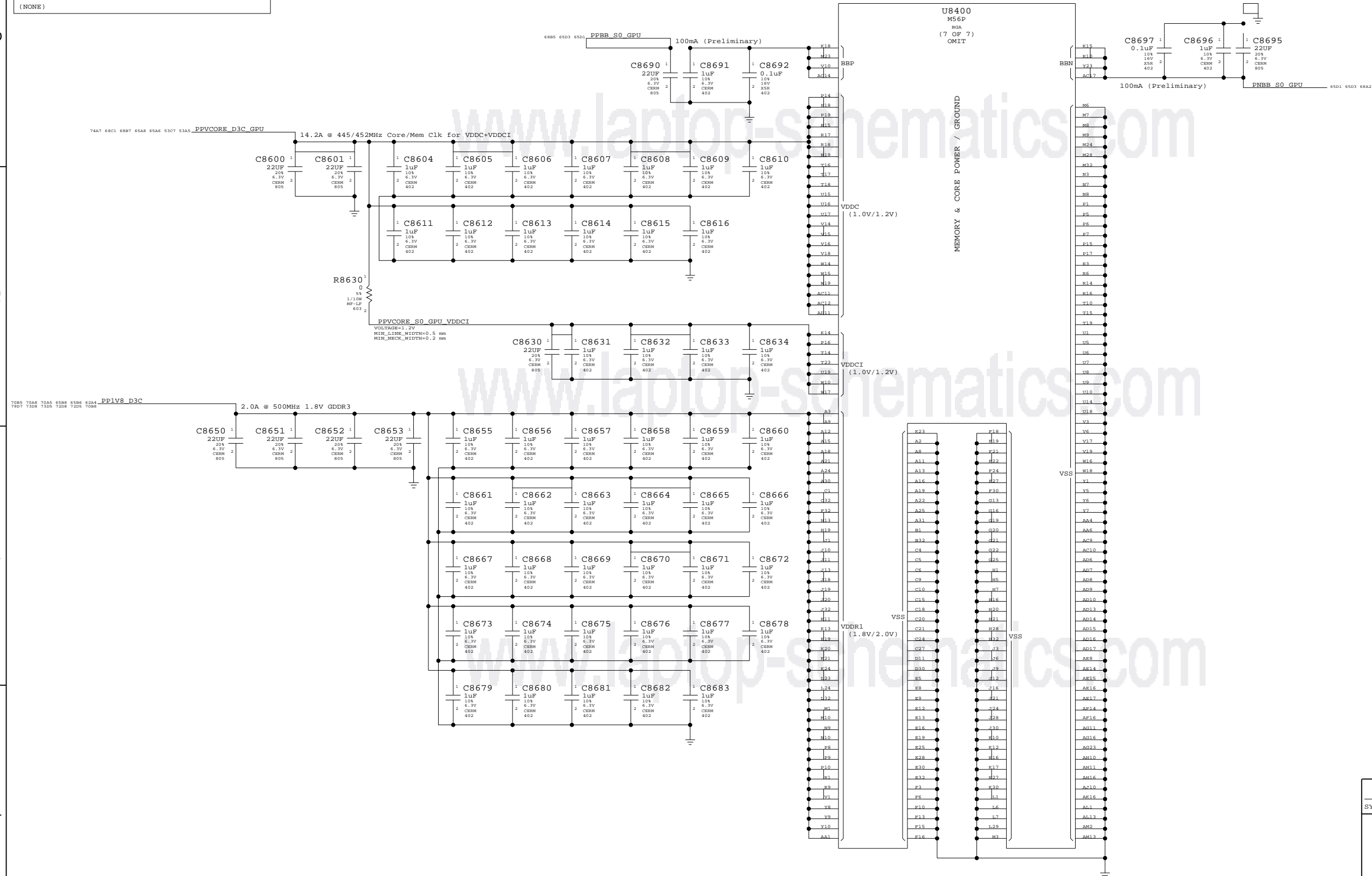
```
- =PP1VR1V3_GPU_VCORE
```

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)



ATI M56 Core Power	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
NOTICE OF PROPRIETARY PROPERTY	
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING	
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE	
II NOT TO REPRODUCE OR COPY IT	
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART	



SIZE D	DRAWING NUMBER 051-7150	REV. A.0.0
SCALE NONE	SHT 69	OF 84

Page Notes

Power aliases required by this page:
- =PP1V8R2V0_S0_FB_GPU

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

72B5	FB A DQ<0>	M31	DQA_0
72B5	FB A DQ<1>	M30	DQA_1
72B5	FB A DQ<2>	M29	DQA_2
72B5	FB A DQ<3>	M28	DQA_3
72B5	FB A DQ<4>	M27	DQA_4
72B5	FB A DQ<5>	M26	DQA_5
72B5	FB A DQ<6>	M25	DQA_6
72B5	FB A DQ<7>	M24	DQA_7
72B5	FB A DQ<8>	M23	DQA_8
72B5	FB A DQ<9>	M22	DQA_9
72B5	FB A DQ<10>	M21	DQA_10
72B5	FB A DQ<11>	M20	DQA_11
72B5	FB A DQ<12>	M19	DQA_12
72B5	FB A DQ<13>	M18	DQA_13
72B5	FB A DQ<14>	M17	DQA_14
72B5	FB A DQ<15>	M16	DQA_15
72B5	FB A DQ<16>	M15	DQA_16
72B5	FB A DQ<17>	M14	DQA_17
72B5	FB A DQ<18>	M13	DQA_18
72B5	FB A DQ<19>	M12	DQA_19
72B5	FB A DQ<20>	M11	DQA_20
72B5	FB A DQ<21>	M10	DQA_21
72B5	FB A DQ<22>	M09	DQA_22
72B5	FB A DQ<23>	M08	DQA_23
72B5	FB A DQ<24>	M07	DQA_24
72B5	FB A DQ<25>	M06	DQA_25
72B5	FB A DQ<26>	M05	DQA_26
72B5	FB A DQ<27>	M04	DQA_27
72B5	FB A DQ<28>	M03	DQA_28
72B5	FB A DQ<29>	M02	DQA_29
72B5	FB A DQ<30>	M01	DQA_30
72B5	FB A DQ<31>	M00	DQA_31
72B2	FB A DQ<32>	M23	DQA_32
72B2	FB A DQ<33>	M22	DQA_33
72B2	FB A DQ<34>	M21	DQA_34
72B2	FB A DQ<35>	M20	DQA_35
72B2	FB A DQ<36>	M19	DQA_36
72B2	FB A DQ<37>	M18	DQA_37
72B2	FB A DQ<38>	M17	DQA_38
72B2	FB A DQ<39>	M16	DQA_39
72B2	FB A DQ<40>	M15	DQA_40
72B2	FB A DQ<41>	M14	DQA_41
72B2	FB A DQ<42>	M13	DQA_42
72B2	FB A DQ<43>	M12	DQA_43
72A2	FB A DQ<44>	M11	DQA_44
72A2	FB A DQ<45>	M10	DQA_45
72A2	FB A DQ<46>	M09	DQA_46
72A2	FB A DQ<47>	M08	DQA_47
72A2	FB A DQ<48>	M07	DQA_48
72A2	FB A DQ<49>	M06	DQA_49
72A2	FB A DQ<50>	M05	DQA_50
72A2	FB A DQ<51>	M04	DQA_51
72A2	FB A DQ<52>	M03	DQA_52
72A2	FB A DQ<53>	M02	DQA_53
72A2	FB A DQ<54>	M01	DQA_54
72A2	FB A DQ<55>	M00	DQA_55
72A2	FB A DQ<56>	M23	DQA_56
72A2	FB A DQ<57>	M22	DQA_57
72A2	FB A DQ<58>	M21	DQA_58
72A2	FB A DQ<59>	M20	DQA_59
72A2	FB A DQ<60>	M19	DQA_60
72A2	FB A DQ<61>	M18	DQA_61
72A2	FB A DQ<62>	M17	DQA_62
72A2	FB A DQ<63>	M16	DQA_63

U8400
M56P
BGA
(3 OF 7)

MEMORY INTERFACE A

READ STROBE

WRITE STROBE

CLKA0

CSA0_0

CSA0_1

CKEA0

RASA0

CASA0

WEA0

ODTA0

CLKA1

CSA1_0

CSA1_1

CKEA1

RASA1

CASA1

WEA1

ODTA1

CLKA2

CSA2_0

CSA2_1

CKEA2

RASA2

CASA2

WEA2

ODTA2

CLKA3

CSA3_0

CSA3_1

CKEA3

RASA3

CASA3

WEA3

ODTA3

CLKA4

CSA4_0

CSA4_1

CKEA4

RASA4

CASA4

WEA4

ODTA4

CLKA5

CSA5_0

CSA5_1

CKEA5

RASA5

CASA5

WEA5

ODTA5

CLKA6

CSA6_0

CSA6_1

CKEA6

RASA6

CASA6

WEA6

ODTA6

CLKA7

CSA7_0

CSA7_1

CKEA7

RASA7

CASA7

WEA7

ODTA7

CLKA8

CSA8_0

CSA8_1

CKEA8

RASA8

CASA8

WEA8

ODTA8

CLKA9

CSA9_0

CSA9_1

CKEA9

RASA9

CASA9

WEA9

ODTA9

CLKA10

CSA10_0

CSA10_1

CKEA10

RASA10

CASA10

WEA10

ODTA10

CLKA11

CSA11_0

CSA11_1

CKEA11

RASA11

CASA11

WEA11

ODTA11

CLKA12

CSA12_0

CSA12_1

CKEA12

RASA12

CASA12

WEA12

ODTA12

CLKA13

CSA13_0

CSA13_1

CKEA13

RASA13

CASA13

WEA13

ODTA13

CLKA14

CSA14_0

CSA14_1

CKEA14

RASA14

CASA14

WEA14

ODTA14

CLKA15

CSA15_0

CSA15_1

CKEA15

RASA15

CASA15

WEA15

ODTA15

CLKA16

CSA16_0

CSA16_1

CKEA16

RASA16

CASA16

WEA16

ODTA16

CLKA17

CSA17_0

CSA17_1

CKEA17

RASA17

CASA17

WEA17

ODTA17

CLKA18

CSA18_0

CSA18_1

CKEA18

RASA18

CASA18

WEA18

ODTA18

CLKA19

CSA19_0

CSA19_1

CKEA19

RASA19

CASA19

WEA19

ODTA19

CLKA20

CSA20_0

CSA20_1

CKEA20

RASA20

CASA20

WEA20

ODTA20

CLKA21

CSA21_0

CSA21_1

CKEA21

RASA21

CASA21

WEA21

ODTA21

CLKA22

CSA22_0

CSA22_1

CKEA22

RASA22

CASA22

WEA22

ODTA22

CLKA23

CSA23_0

CSA23_1

CKEA23

RASA23

CASA23

WEA23

ODTA23

CLKA24

CSA24_0

CSA24_1

CKEA24

RASA24

CASA24

WEA24

ODTA24

CLKA25

CSA25_0

CSA25_1

CKEA25

RASA25

CASA25

WEA25

ODTA25

CLKA26

CSA26_0

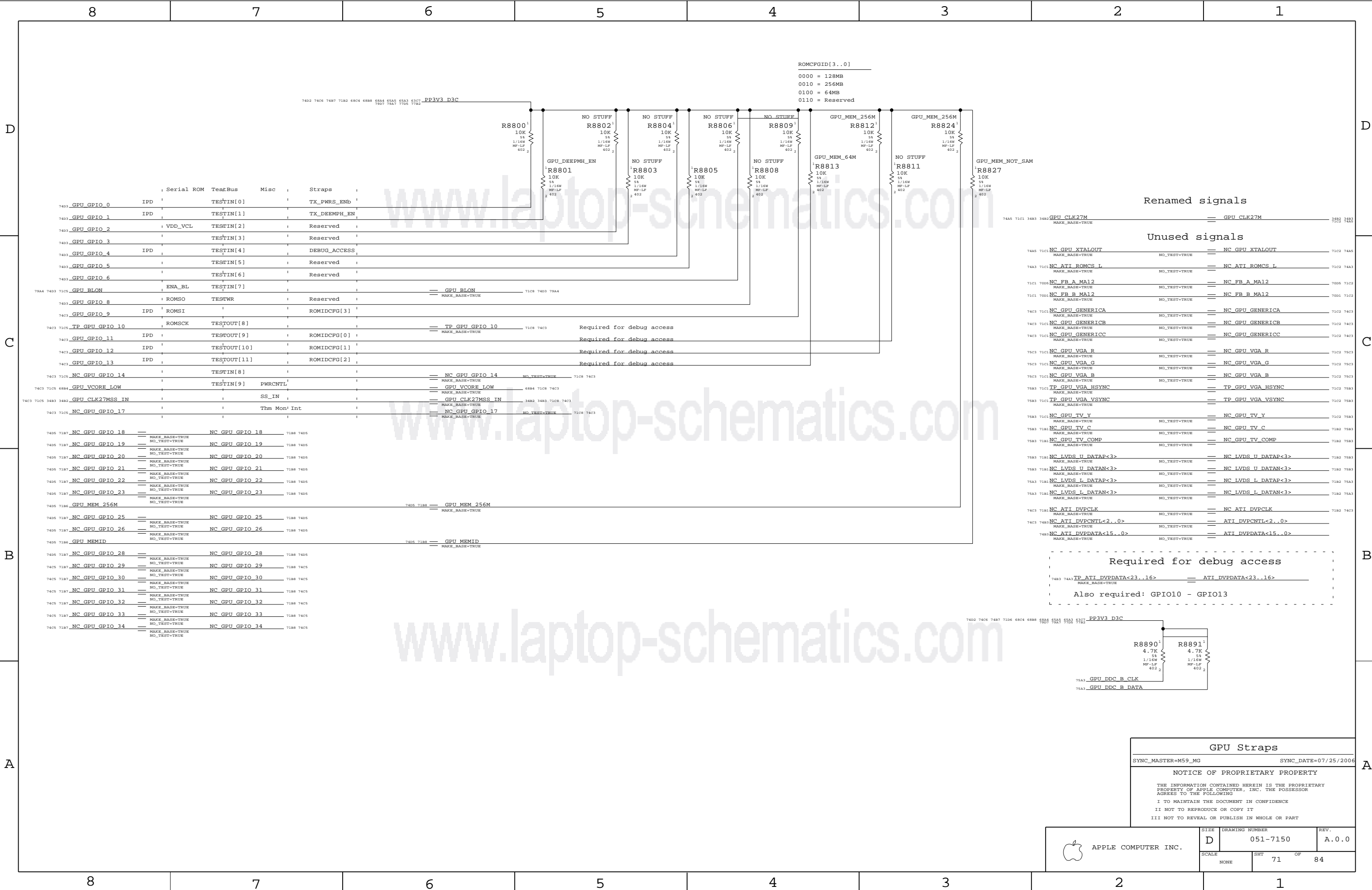
CSA26_1

CKEA26

RASA26

CASA26

WEA26



Power aliases required by this page:

- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQSignal aliases required by this page:
(NONE)BOM options provided by this page:
(NONE)

GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7150

REV.

A.0.0

SCALE

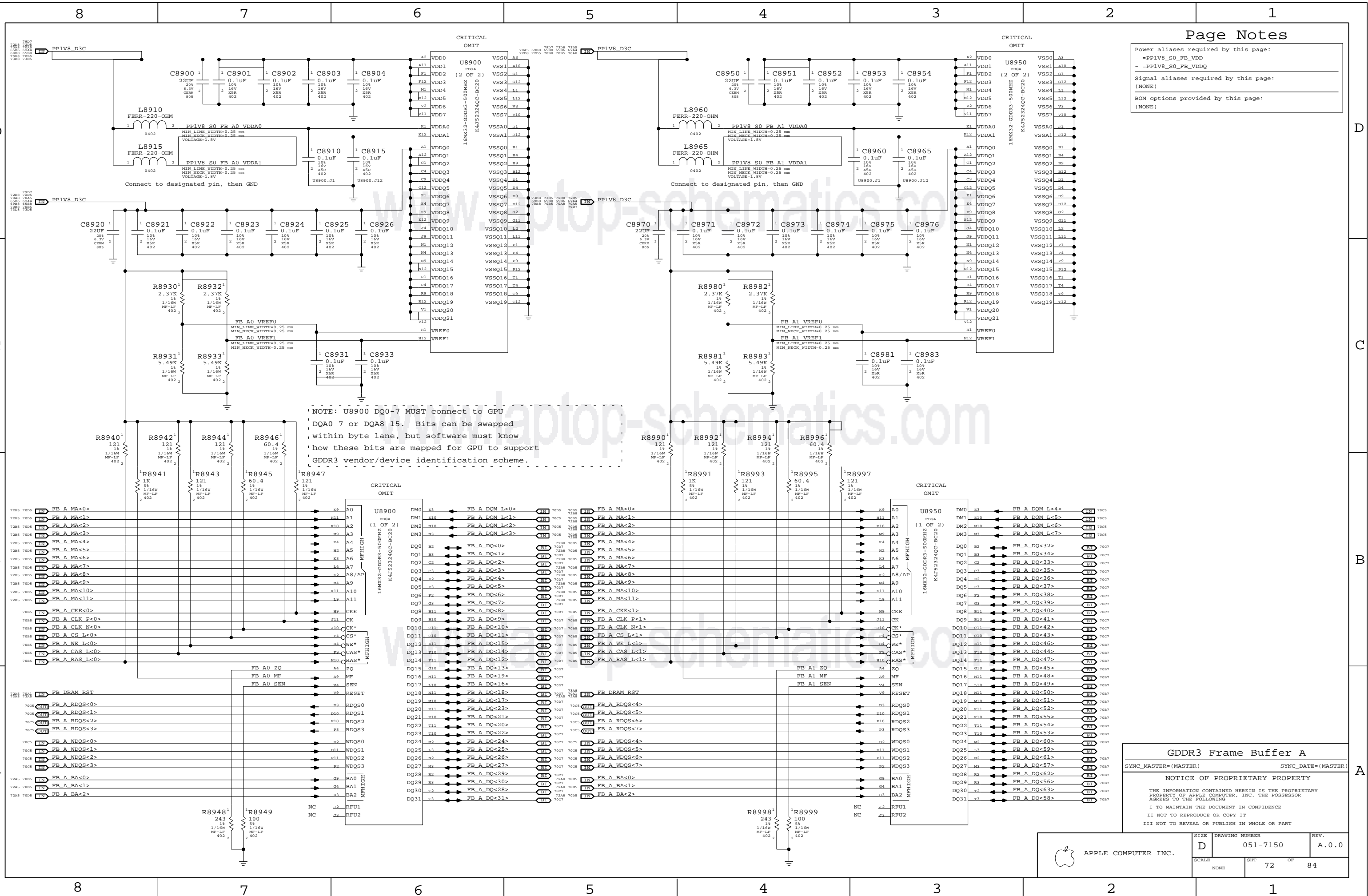
NONE

SHT

72

OF

84



Power aliases required by this page:

- =PPIV8_S0_FB_VDD
- =PPIV8_S0_FB_VDDQ

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)

GDDR3 Frame Buffer B

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7150

REV.

A.0.0

SCALE

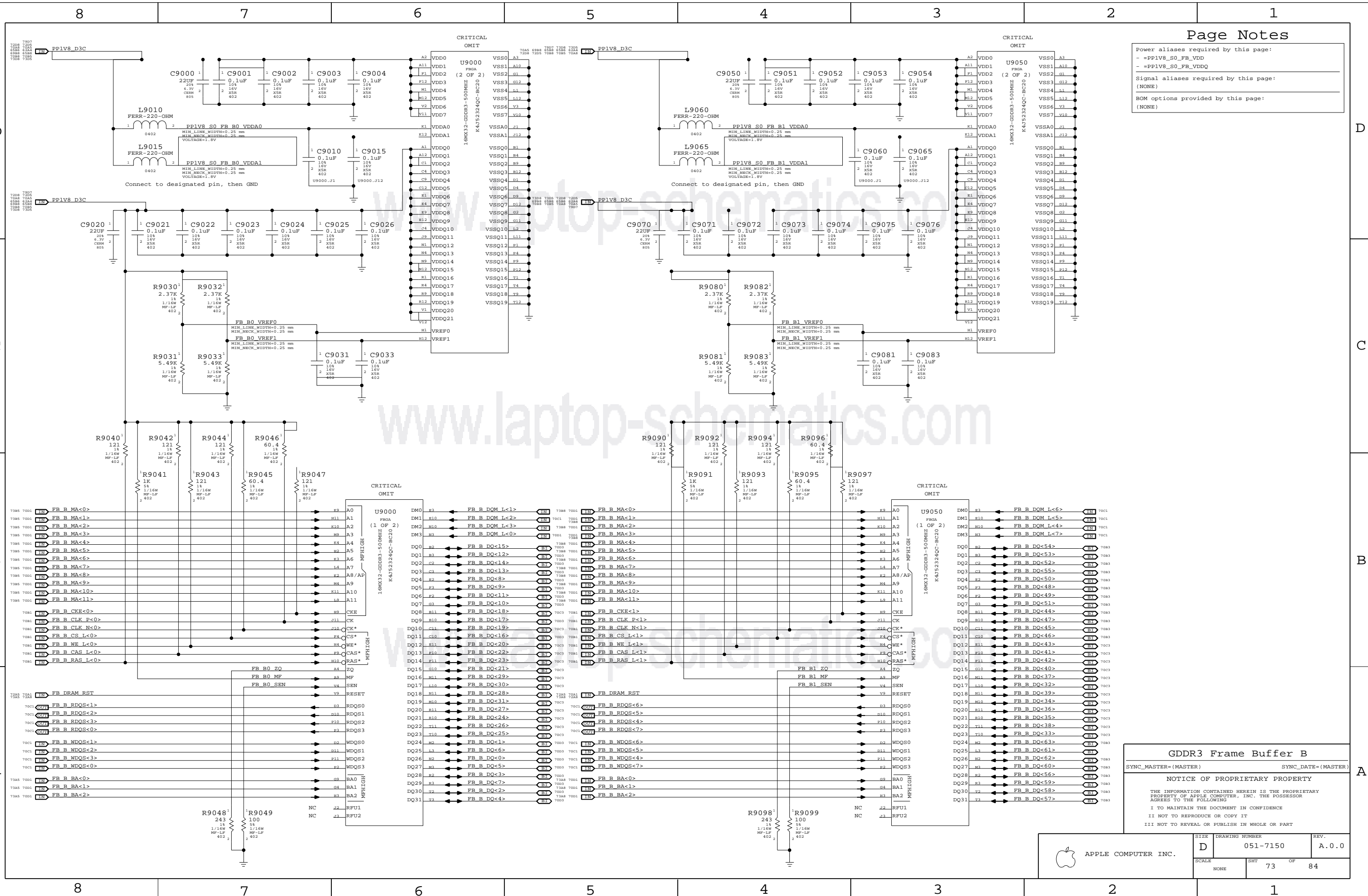
NONE

SHT

73

OF

84



Power aliases required by this page:

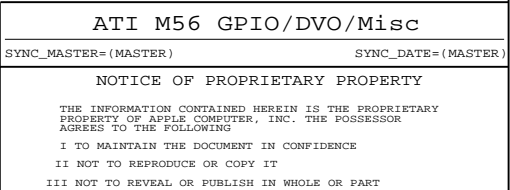
- PP3V3_GPU_GPIOS
- PP2V5_PVDD
- PP1V8_GPU_LVDS_PLL

Signal aliases required by this page:

- I2C_GPU_TMSD_SDA - I2C data line for external TMSD transmitters
- I2C_GPU_TMSD_SCL - I2C clock line for external TMSD transmitters

BOM options provided by this page:

(NONE)



Page Notes

Power aliases required by this page:

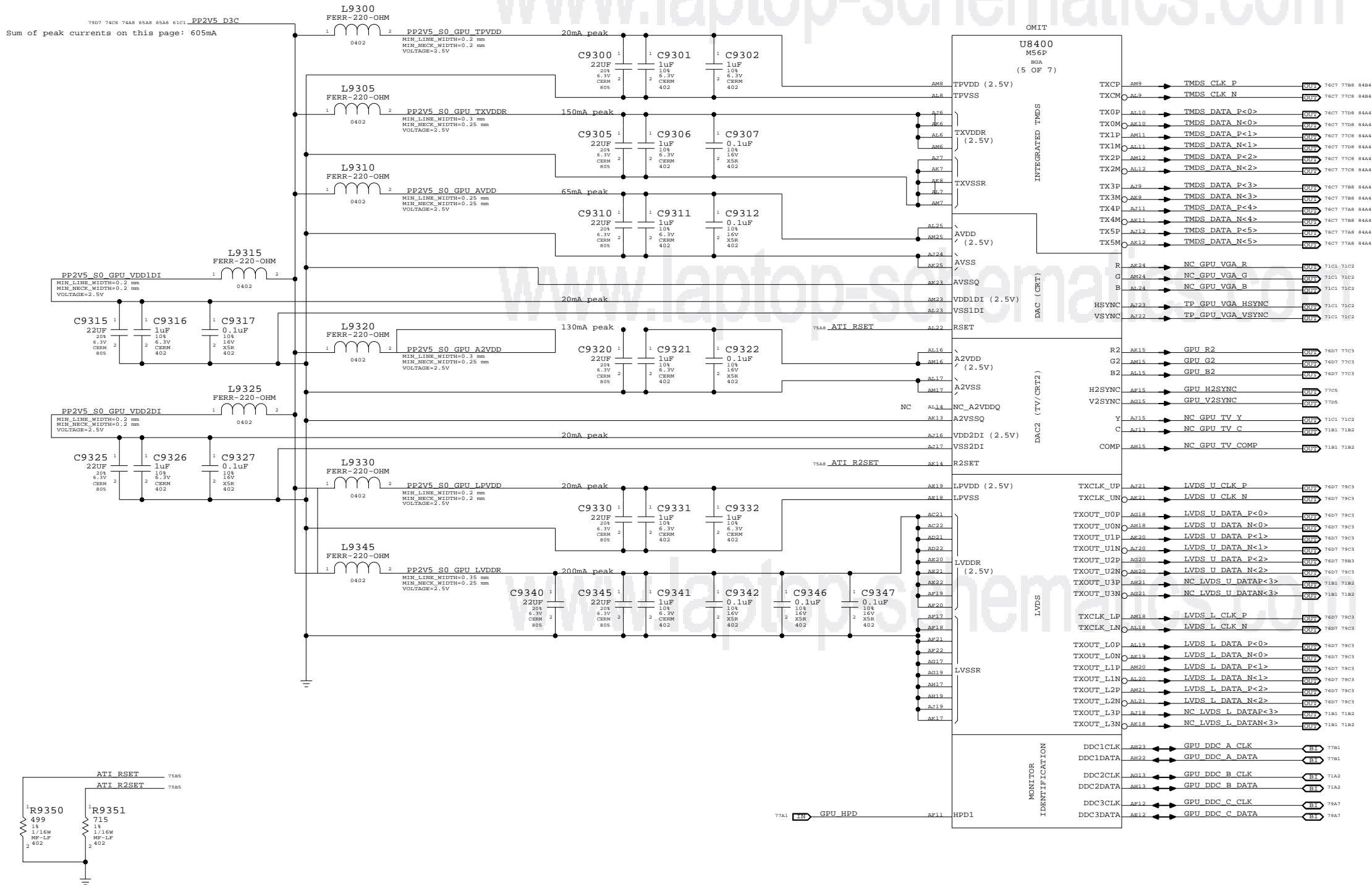
- =PP2V5_S0_GPU
- =PP1V8R2V5_S0_GPU_LVDDR

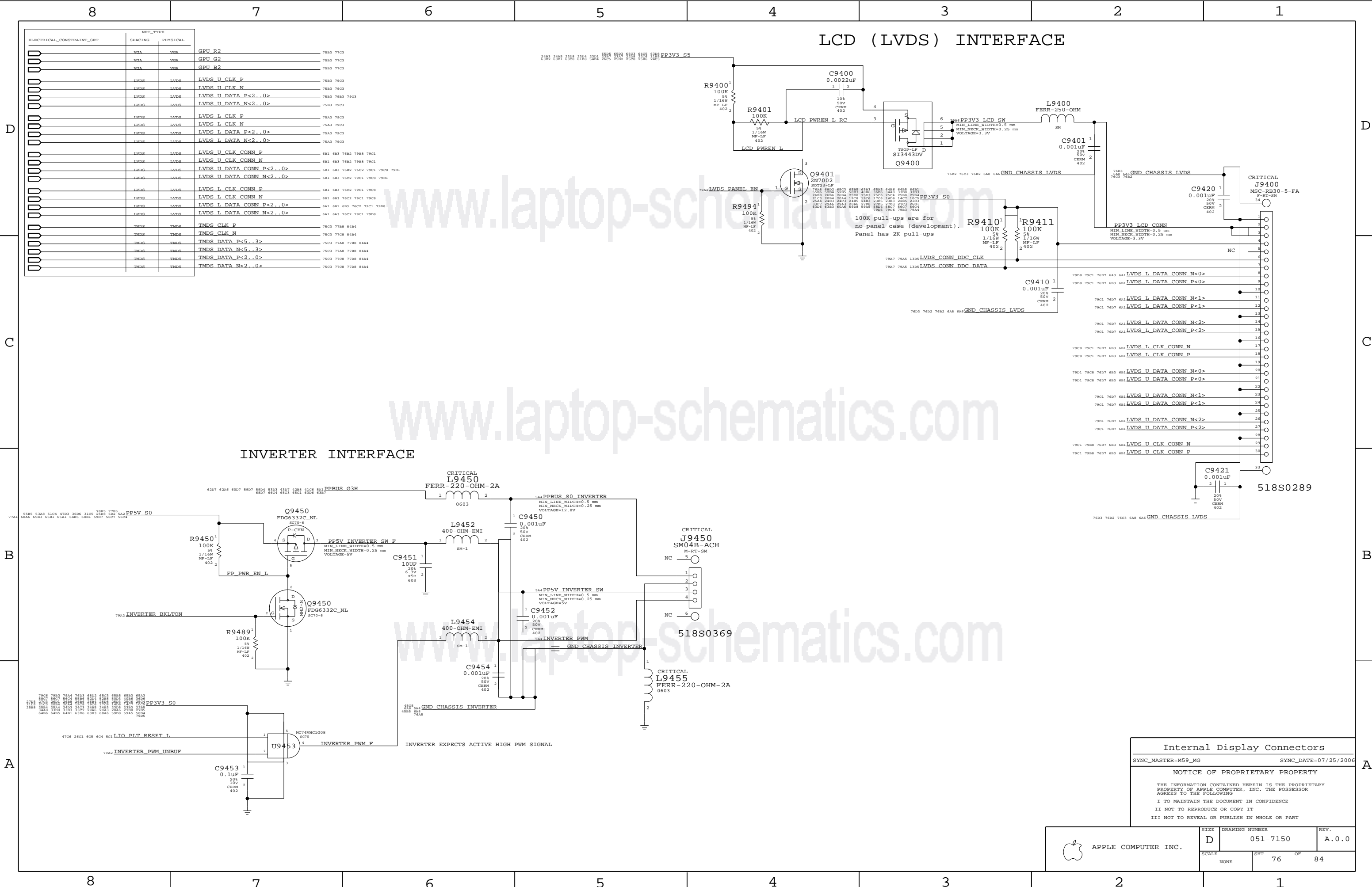
Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)





Internal Display Connectors

SYNC_MASTER=M59_MG SYNC_DATE=07/25/2006

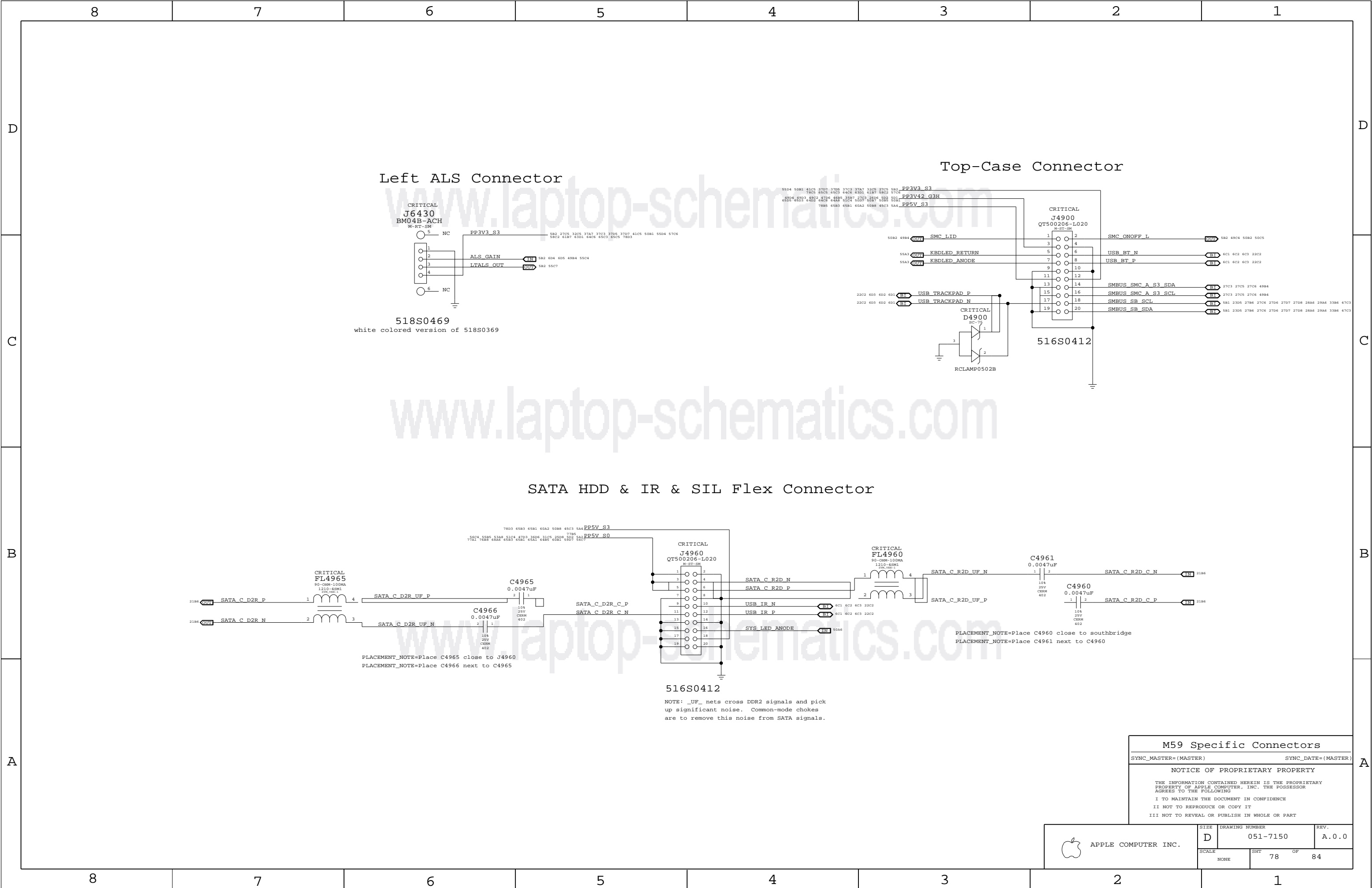
NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



8

7

6

5

4

3

2

1

LVDS Interface Pull-downs

NOTE: These parts are to counter an invalid state caused by the M56 part. Bias voltage is present on LVDS interface pins even when they should be tri-stated to meet panel power sequence requirements. Resulting pump-up in LCD panel can cause startup and long-term reliability issues. Pull-down resistors reduce the pump-up in the panel, though some voltage will still be seen on LVDS signals when they should be 0V.

D



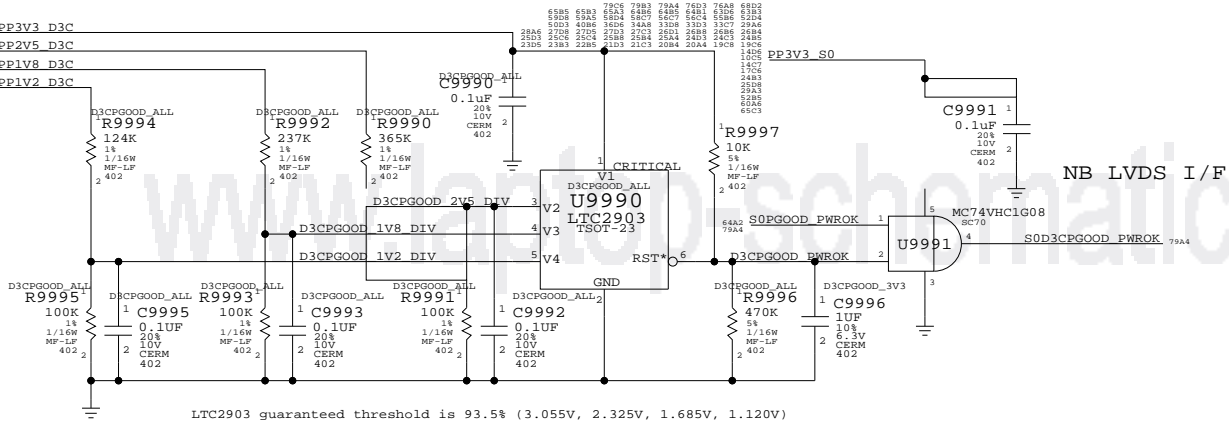
C

B

A

PGOOD Monitor for GPU Rails

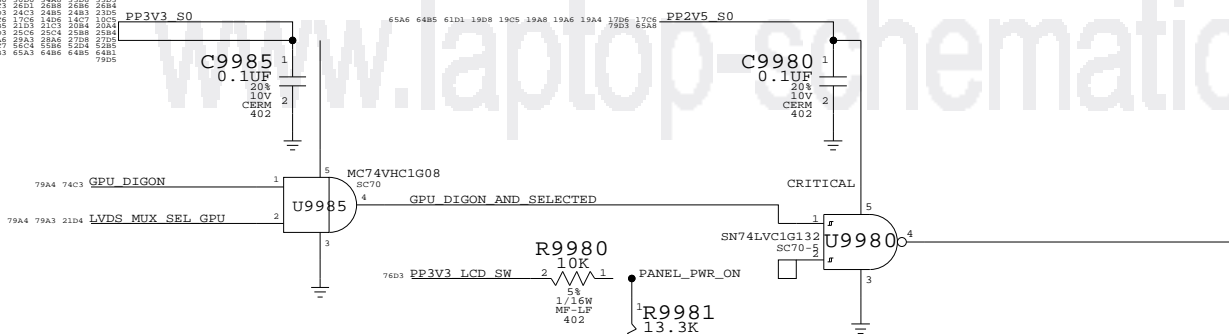
D3CPGOOD_ALL BOM option stuffs LTC2903 circuit to monitor all D3C rails to qualify D3CPGOOD.
D3CPGOOD_3V3 BOM option uses only PP3V3_D3C to qualify D3CPGOOD.



LVDS Mux Selection Qualification

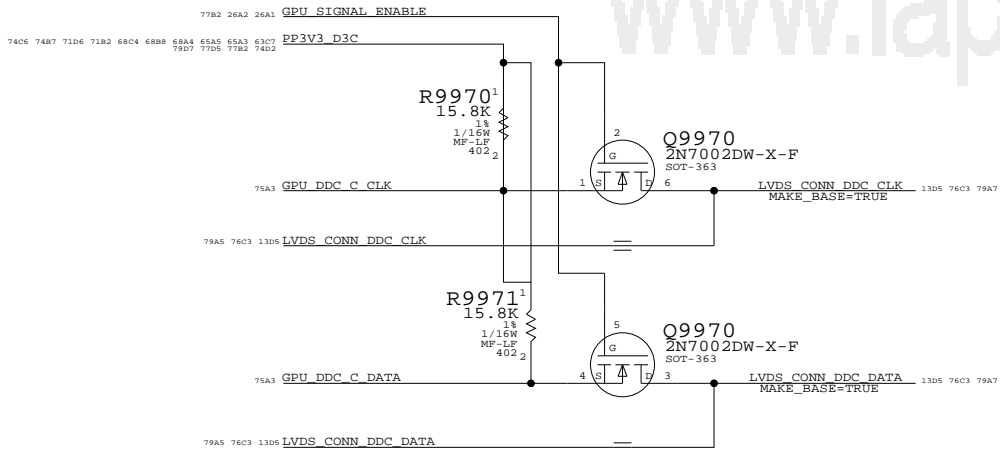
Enables the GPU LVDS path in the mux with the qualification that the GPU has turned on panel power and that the panel power has risen to (near) 3.3V. This should eliminate need for LVDS pulldowns

GPU LVDS I/F

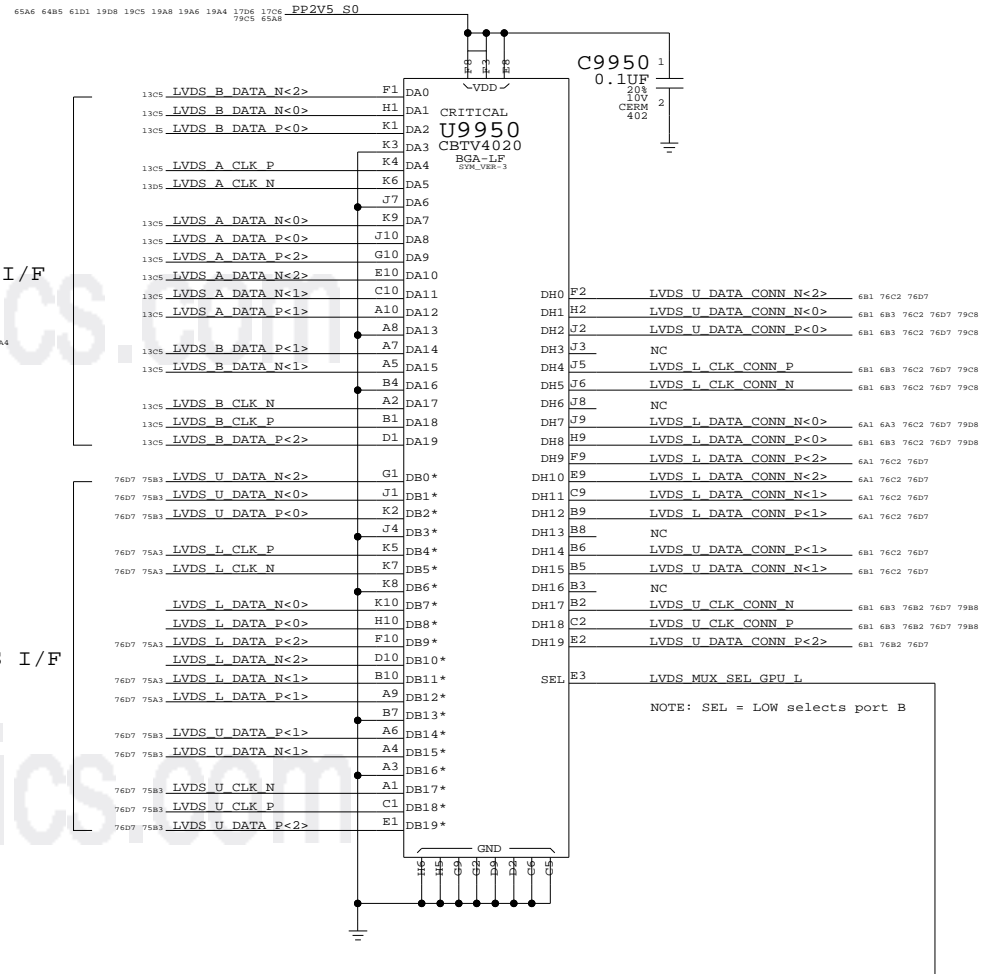


Divider set to rise to 1.88V nom/1.74V min when panel power is at 3.3V/3.315V. Schmitt trigger voltage max is 1.70V (@2.625V Vcc).
R9981 can also be used as pad for cap, creating an RC filter.

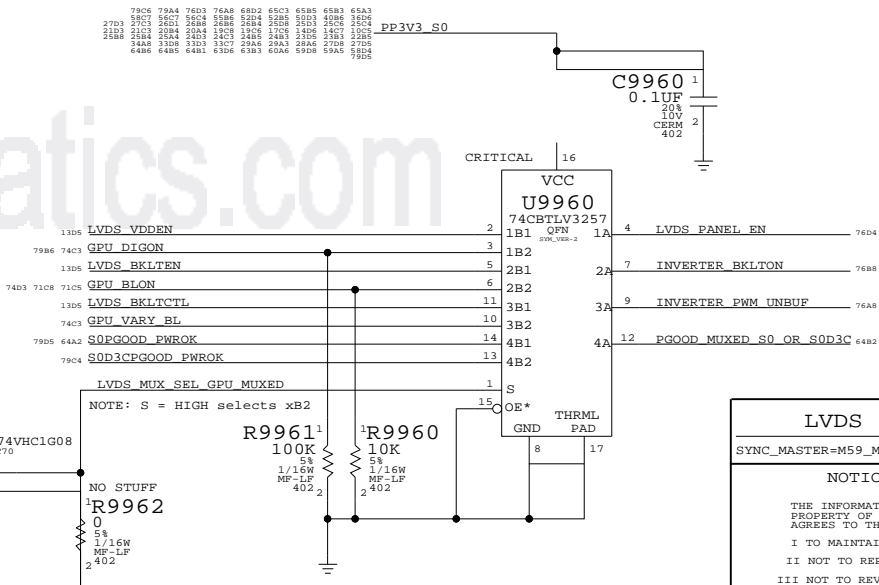
GPU DDC Pass FETs



LVDS I/F Mux



Panel/Backlight Control Mux



LVDS Interface Pull-downs

SYNC_MASTER=M59_MG SYNC_DATE=08/01/2006

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE
II NOT TO REPRODUCE OR COPY IT
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-7150 REV. A.0.0

SCALE NONE SHT 79 OF 84

8

7

6

5

4

3

2

1

Date

- Radar # - Description

DMS Release #01000

2006/05/26 - 4508681 - Release for Proto

Date

- Radar # - Description

DMS Release #04000

2006/06/30 - 4566939 - Release for EVT

Date

- Radar # - Description

DMS Release #07000

2006/08/07 - 4607952 - Release for DVT

Date

- Radar # - Description

DMS Release #0A000

2006/09/19 - 4726575 - Release for PVT

www.laptop-schematics.com

www.laptop-schematics.com

www.laptop-schematics.com

Revision History

SYNC_MASTER=N/A SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-7150

REV.

A.0.0

SCALE

NONE

SHT

80

OF

84

8

7

6

5

4

3

2

1

A

B

C

D

[illegible]

Basenet Report

Design: 1881

Date: Sep 25 10:54:06 2006

Base nets and synonyms for mib_lib.MLB(mib_lib.mib(sch...))

Base SignalSynonymsLocation([Zone])[dir()]

=FW_PCI_IDSEL

=FW_PCI_IDSEL - @mib_lib.MLB

686 3787

AC2_BITCLK

AC2_BITCLK - @mib_lib.MLB

501 2107 4786 8484

AC2_RST_P - @mib_lib.MLB

AC2_RST_P - @mib_lib.MLB

501 2107 4786 8484

AC2_SDATIN<0> - @mib_lib.MLB

AC2_SDATIN<0> - @mib_lib.MLB

501 2107 4786 8484

AC2_SDATOUT

AC2_SDATOUT - @mib_lib.MLB

501 2107 4786 8484

AC2_SYNC

AC2_SYNC - @mib_lib.MLB

501 2107 4786 8484

ALL_SYS_PWRGD

ALL_SYS_PWRGD - @mib_lib.MLB

2644 4907 6481

ALS_GAIN

ALS_GAIN - @mib_lib.MLB

582 604 605 4984 5504

ALS_LEFT

ALS_LEFT - @mib_lib.MLB

4947 5506

ALS_RIGHT

ALS_RIGHT - @mib_lib.MLB

4947 5502

ALS_RT_OUT

ALS_RT_OUT - @mib_lib.MLB

5503

ATI_DVPCNTL<2..0> - @mib_lib.MLB

ATI_DVPCNTL<2..0> - @mib_lib.MLB

7181

ATI_DVPCNTL<2..0> - @mib_lib.MLB

ATI_DVPCNTL<2..0> - @mib_lib.MLB

7182

ATI_DVPPDATA<15..0> - @mib_lib.MLB

ATI_DVPPDATA<15..0> - @mib_lib.MLB

7181

ATI_DVPPDATA<15..0> - @mib_lib.MLB

ATI_DVPPDATA<15..0> - @mib_lib.MLB

7182

ATI_DVPPDATA<23..16> - @mib_lib.MLB

ATI_DVPPDATA<23..16> - @mib_lib.MLB

7181

ATI_DVPPDATA<23..16> - @mib_lib.MLB

ATI_DVPPDATA<23..16> - @mib_lib.MLB

7182

ATI_R2SET

ATI_R2SET - @mib_lib.MLB

7548 7585

ATI_RSET

ATI_RSET - @mib_lib.MLB

7548 7585

ATI_TDIODE_N - @mib_lib.MLB

ATI_TDIODE_N - @mib_lib.MLB

5286 74A3

ATI_TDIODE_P

ATI_TDIODE_P - @mib_lib.MLB

5286 74A3

ATI_TESTEN

ATI_TESTEN - @mib_lib.MLB

74A3

ATI_VREFP

ATI_VREFP - @mib_lib.MLB

74D3

BIOS_REC

BIOS_REC - @mib_lib.MLB

23A6 23C5

BOOT_LPC_SPI_L

BOOT_LPC_SPI_L - @mib_lib.MLB

502 2283 4907 5184

CK410_27M_NONSPREAD

CK410_27M_NONSPREAD - @mib_lib.MLB

33A3 3485 3485

CK410_27M_SPREAD

CK410_27M_SPREAD - @mib_lib.MLB

33A3 3485 3485

CK410_CLK14P3M_TIMER

CK410_CLK14P3M_TIMER - @mib_lib.MLB

33A4 3488

CK410_FSR_TEST_MODE

CK410_FSR_TEST_MODE - @mib_lib.MLB

33A4 3488

CK410_IREF

CK410_IREF - @mib_lib.MLB

3386

CK410_PC11_CLK

CK410_PC11_CLK - @mib_lib.MLB

3386 34D8

CK410_PC12_CLK

CK410_PC12_CLK - @mib_lib.MLB

3386 34D8

CK410_PC13_CLK

CK410_PC13_CLK - @mib_lib.MLB

3386 34D8

CK410_PC15_FCTSEL1

CK410_PC15_FCTSEL1 - @mib_lib.MLB

3387 34A8

CK410_PC1F0_CLK

CK410_PC1F0_CLK - @mib_lib.MLB

3388 34D8

CK410_PC1F1_CLK

CK410_PC1F1_CLK - @mib_lib.MLB

3388 34D8

CK410_REF1_FCTSEL0

CK410_REF1_FCTSEL0 - @mib_lib.MLB

3384 34A8

CK410_SRC7_N

CK410_SRC7_N - @mib_lib.MLB

3384 3485

CK410_SRC7_P

CK410_SRC7_P - @mib_lib.MLB

3384 3485

CK410_SRC_CLKREQ0_L

CK410_SRC_CLKREQ0_L - @mib_lib.MLB

3384 34A4

CK410_SRC_CLKREQ0_L

CK410_SRC_CLKREQ0_L - @mib_lib.MLB

3384 34A4

CK410_USB48_FSA

CK410_USB48_FSA - @mib_lib.MLB

33A4 34C8

CK410_XTAL_IN

CK410_XTAL_IN - @mib_lib.MLB

5A7 3306

CK410_XTAL_OUT

CK410_XTAL_OUT - @mib_lib.MLB

3306

CLK98P304M_FW_XI_R

CLK98P304M_FW_XI_R - @mib_lib.MLB

3883

CLK98P304M_FW_XI

CLK98P304M_FW_XI - @mib_lib.MLB

3884

CLKFV_LINK_PCLK

CLKFV_LINK_PCLK - @mib_lib.MLB

3703 3802

CLKFV_PHY_LCLK

CLKFV_PHY_LCLK - @mib_lib.MLB

3703 3806

CLK_NB_OK_L

CLK_NB_OK_L - @mib_lib.MLB

1486 3384

CPUISENS_NEG

CPUISENS_NEG - @mib_lib.MLB

59A3

CPUISENS_NEG_RC

CPUISENS_NEG_RC - @mib_lib.MLB

59A3

CPUISENS_POS

CPUISENS_POS - @mib_lib.MLB

59A3

CPUVCORE_IOUT

CPUVCORE_IOUT - @mib_lib.MLB

5388 59A5

CPUVCORE_ISENSE_CAL

CPUVCORE_ISENSE_CAL - @mib_lib.MLB

53A6

CPUVSENSE_IN

CPUVSENSE_IN - @mib_lib.MLB

5307

CPU_A20M_L

CPU_A20M_L - @mib_lib.MLB

7808 2104 8406

CPU_BSEL<0>

CPU_BSEL<0> - @mib_lib.MLB

784 3486

CPU_BSEL<1>

CPU_BSEL<1> - @mib_lib.MLB

784 3486

CPU_BSEL<2>

CPU_BSEL<2> - @mib_lib.MLB

784 3486

CPU_BSEL_R<0>

CPU_BSEL_R<0> - @mib_lib.MLB

3407

CPU_BSEL_R<1>

CPU_BSEL_R<1> - @mib_lib.MLB

3407

CPU_BSEL_R<2>

CPU_BSEL_R<2> - @mib_lib.MLB

3407

CPU_COMP<0>

CPU_COMP<0> - @mib_lib.MLB

783 8406

CPU_COMP<1>

CPU_COMP<1> - @mib_lib.MLB

783 8406

CPU_COMP<2>

CPU_COMP<2> - @mib_lib.MLB

783 8406

CPU_COMP<3>

CPU_COMP<3> - @mib_lib.MLB

783 8406

CPU_DPRSTP_L

CPU_DPRSTP_L - @mib_lib.MLB

782 2104 5907

CPU_DPSEL_P

CPU_DPSEL_P - @mib_lib.MLB

782 2104 8406

CPU_FERR_L

CPU_FERR_L - @mib_lib.MLB

708 2101

CPU_GTLREF

CPU_GTLREF - @mib_lib.MLB

784 8406

CPU_IGNNE_L

CPU_IGNNE_L - @mib_lib.MLB

708 2104 8406

CPU_INIT_L

CPU_INIT_L - @mib_lib.MLB

705 2104 8406

CPU_INTR

CPU_INTR - @mib_lib.MLB

708 2104 8406

CPU_NMI

CPU_NMI - @mib_lib.MLB

708 2104 8406

CPU_PROCHOT_L

CPU_PROCHOT_L - @mib_lib.MLB

705 5001 5003

CPU_RST_L

CPU_RST_L - @mib_lib.MLB

782 2104 8406

CPU_PWRGD

CPU_PWRGD - @mib_lib.MLB

782 2104 8406

CPU_RCIN_L

CPU_RCIN_L - @mib_lib.MLB

2104

CPU_SMI_L

CPU_SMI_L - @mib_lib.MLB

708 2104 8406

CPU_STPCLK_L

CPU_STPCLK_L - @mib_lib.MLB

708 2104 8406

CPU_TEST1

CPU_TEST1 - @mib_lib.MLB

784

CPU_TEST2

CPU_TEST2 - @mib_lib.MLB

784

CPU_THERMD_N

CPU_THERMD_N - @mib_lib.MLB

705 1086

CPU_THERMD_P

CPU_THERMD_P - @mib_lib.MLB

705 1086

CPU_THERMTRIP_L

CPU_THERMTRIP_L - @mib_lib.MLB

8406

CPU_THERMTRIP_R

CPU_THERMTRIP_R - @mib_lib.MLB

2102

CPU_VCCSENSE_N

CPU_VCCSENSE_N - @mib_lib.MLB

886 59A1 8486

CPU_VCCSENSE_P

CPU_VCCSENSE_P - @mib_lib.MLB

886 59B1 8486

CPU_VID<0>

CPU_VID<0> - @mib_lib.MLB

887 9C2

CPU_VID<6..0>

CPU_VID<6..0> - @mib_lib.MLB

8486 8486

CPU_VID<1>

CPU_VID<1> - @mib_lib.MLB

887 9C2

CPU_VID<2>

CPU_VID<2> - @mib_lib.MLB

887 9C2

CPU_VID<3>

CPU_VID<3> - @mib_lib.MLB

887 9C2

CPU_VID<4>

CPU_VID<4> - @mib_lib.MLB

887 9C2

CPU_VID<5>

CPU_VID<5> - @mib_lib.MLB

887 9C2

CPU_VID<6>

CPU_VID<6> - @mib_lib.MLB

887 9C2

CPU_XDP_CLK_N

CPU_XDP_CLK_N - @mib_lib.MLB

1183 3304 3403 3405 8406

CPU_XDP_CLK_P

CPU_XDP_CLK_P - @mib_lib.MLB

1183 3304 3403 3405 8406

CRB_SV_DET

CRB_SV_DET - @mib_lib.MLB

2386 23C3

D3CPGOOD_IV2_DIV

D3CPGOOD_IV2_DIV - @mib_lib.MLB

7906

D3CPGOOD_IV8_DIV

D3CPGOOD_IV8_DIV - @mib_lib.MLB

7906

D3CPGOOD_2V5_DIV

D3CPGOOD_2V5_DIV - @mib_lib.MLB

7906

D3CPGOOD_PWROK

D3CPGOOD_PWROK - @mib_lib.MLB

7905

DEBUC_RST_L

DEBUC_RST_L - @mib_lib.MLB

502 26B1 5184

DMI_IRCOMP_R

DMI_IRCOMP_R - @mib_lib.MLB

2202

DMI_N2S_N<0>

DMI_N2S_N<0> - @mib_lib.MLB

1483 22D2

DMI_N2S_N<1..0>

DMI_N2S_N<1..0> - @mib_lib.MLB

5A7

DMI_N2S_N<1>

DMI_N2S_N<1> - @mib_lib.MLB

1483 22D2

DMI_N2S_N<2>

DMI_N2S_N<2> - @mib_lib.MLB

1483 22D2

DMI_N2S_N<3>

DMI_N2S_N<3> - @mib_lib.MLB

1483 22D2

DMI_N2S_P<0>

DMI_N2S_P<0> - @mib_lib.MLB

1483 22D2

DMI_N2S_P<1..0>

DMI_N2S_P<1..0> - @mib_lib.MLB

5A7

DMI_N2S_P<1>

DMI_N2S_P<1> - @mib_lib.MLB

1483 22D2

DMI_N2S_P<2>

DMI_N2S_P<2> - @mib_lib.MLB

1483 22D2

 DMI_N2S_P<3> DMI_N2S_P<3> - @mib_lib.MLB 1483 22D2 DMI_N2S_N<0> DMI_N2S_N<0> - @mib_lib.MLB 1483 22D2 DMI_N2S_N<1> DMI_N2S_N<1> - @mib_lib.MLB 1483 22D2 DMI_N2S_N<2> DMI_N2S_N<2> - @mib_lib.MLB 1483 22D2 DMI_N2S_N<3> DMI_N2S_N<3> - @mib_lib.MLB 1483 22D2 DMI_N2S_P<0> DMI_N2S_P<0> - @mib_lib.MLB 1483 22D2 DMI_N2S_P<1> DMI_N2S_P<1> - @mib_lib.MLB 1483 22D2 DMI_N2S_P<2> DMI_N2S_P<2> - @mib_lib.MLB 1483 22D2 DMI_N2S_P<3> DMI_N2S_P<3> - @mib_lib.MLB 1483 22D2 DVI_DDC_CLK DVI_DDC_CLK - @mib_lib.MLB 7782 DVI_DDC_CLK_R DVI_DDC_CLK_R - @mib_lib.MLB 7784 DVI_DDC_DATA DVI_DDC_DATA - @mib_lib.MLB 7782 DVI_DDC_DATA_R DVI_DDC_DATA_R - @mib_lib.MLB 7784 DVI_HPD DVI_HPD - @mib_lib.MLB 77A2 DVI_HPD_R DVI_HPD_R - @mib_lib.MLB 77A4 EDDET_ACT EDDET_ACT - @mib_lib.MLB 40B4 EDDET_MDINAMP EDDET_MDINAMP - @mib_lib.MLB 40B5 EDDET_REF EDDET_REF - @mib_lib.MLB 40A4 ED_MDIN0_C ED_MDIN0_C - @mib_lib.MLB 40B7 ED_MDIN1_C ED_MDIN1_C - @mib_lib.MLB 40A7 ED_MDIN_R ED_MDIN_R - @mib_lib.MLB 40A6 ENETCONN_N<0> ENETCONN_N<0> - @mib_lib.MLB 40C3 40D7 ENETCONN_N<1> ENETCONN_N<1> - @mib_lib.MLB 40C3 40D7 ENETCONN_N<2> ENETCONN_N<2> - @mib_lib.MLB 40C3 40D7 ENETCONN_N<3> ENETCONN_N<3> - @mib_lib.MLB 40C3 40D7 ENETCONN_P<0> ENETCONN_P<0> - @mib_lib.MLB 40D3 40D7 ENETCONN_P<1> ENETCONN_P<1> - @mib_lib.MLB 40C3 40D7 ENETCONN_P<2> ENETCONN_P<2> - @mib_lib.MLB 40C3 40D7 ENETCONN_P<3> ENETCONN_P<3> - @mib_lib.MLB 40C3 40D7 ENET_CLK100M_PCIE_N ENET_CLK100M_PCIE_N - @mib_lib.MLB 33A4 34C3 34C5 39C5 ENET_CLK100M_PCIE_P ENET_CLK100M_PCIE_P - @mib_lib.MLB 33A4 34C3 34C5 39C5 ENET_CTAP0 ENET_CTAP0 - @mib_lib.MLB 40C3 ENET_CTAP1 ENET_CTAP1 - @mib_lib.MLB 40C3 ENET_CTAP2 ENET_CTAP2 - @mib_lib.MLB 40C3 ENET_CTAP3 ENET_CTAP3 - @mib_lib.MLB 40C3 ENET_CTAP_COMMON ENET_CTAP_COMMON - @mib_lib.MLB 40B3 ENET_LOM_DIS_L ENET_LOM_DIS_L - @mib_lib.MLB 39B7 39C8 ENET_LOWPWR_EN ENET_LOWPWR_EN - @mib_lib.MLB 604 39B8 ENET_MDIO ENET_MDIO - @mib_lib.MLB 39C5 ENET_MD11 ENET_MD11 - @mib_lib.MLB 39B4 ENET_MD12 ENET_MD12 - @mib_lib.MLB 39B4 ENET_MD13 ENET_MD13 - @mib_lib.MLB 39B3 ENET_MD1_N<0> ENET_MD1_N<0> - @mib_lib.MLB 39C2 40A7 40C5 ENET_MD1_N<1> ENET_MD1_N<1> - @mib_lib.MLB 39C2 40A7 40C5 ENET_MD1_N<2> ENET_MD1_N<2> - @mib_lib.MLB 39C2 40C5 ENET_MD1_N<3> ENET_MD1_N<3> - @mib_lib.MLB 39C2 40B5 ENET_MD1_P<0> ENET_MD1_P<0> - @mib_lib.MLB 39C2 40D5 ENET_MD1_P<1> ENET_MD1_P<1> - @mib_lib.MLB 39C2 40C5 ENET_MD1_P<2> ENET_MD1_P<2> - @mib_lib.MLB 39C2 40C5 ENET_MD1_P<3> ENET_MD1_P<3> - @mib_lib.MLB 39C2 40C5 ENET_PU_VDD_TTL0 ENET_PU_VDD_TTL0 - @mib_lib.MLB 39A6 39C6 ENET_PU_VDD_TTL1 ENET_PU_VDD_TTL1 - @mib_lib.MLB 39A6 39B6 ENET_RSET ENET_RSET - @mib_lib.MLB 39C8 ENET_RST_L ENET_RST_L - @mib_lib.MLB 26B1 39C5 ENET_VPD_CLK ENET_VPD_CLK - @mib_lib.MLB 39A2 39C6 ENET_VPD_DATA ENET_VPD_DATA - @mib_lib.MLB 39A2 39C6 ENET_XTAL1 ENET_XTAL1 - @mib_lib.MLB 39A6 ENET_XTAL0 ENET_XTAL0 - @mib_lib.MLB 39A6 EXCARD_CLKREQ_L EXCARD_CLKREQ_L - @mib_lib.MLB 501 33B4 34A3 34A4 47C6 EXCARD_OC_L EXCARD_OC_L - @mib_lib.MLB 501 6C1 6C3 22C4 22D8 47C6 50B3 5D2 5686 FAN_IT_PWM FAN_IT_PWM - @mib_lib.MLB 502 5686 FAN_IT_TACH FAN_IT_TACH - @mib_lib.MLB 502 5686 FAN_RT_PWM FAN_RT_PWM - @mib_lib.MLB 502 56B3 FAN_RT_TACH FAN_RT_TACH - @mib_lib.MLB 502 56B3 FB_A0_MF FB_A0_MF - @mib_lib.MLB 72A7 FB_A0_SEN FB_A0_SEN - @mib_lib.MLB 72A7 FB_A0_VREF0 FB_A0_VREF0 - @mib_lib.MLB 72C7 FB_A0_VREF1 FB_A0_VREF1 - @mib_lib.MLB 72C7 FB_A0_ZQ FB_A0_ZQ - @mib_lib.MLB 72A7 FB_A1_MF FB_A1_MF - @mib_lib.MLB 72A4 FB_A1_SEN FB_A1_SEN - @mib_lib.MLB 72A4 FB_A1_VREF0 FB_A1_VREF0 - @mib_lib.MLB 72C4 FB_A1_VREF1 FB_A1_VREF1 - @mib_lib.MLB 72C4 FB_A1_ZQ FB_A1_ZQ - @mib_lib.MLB 72A4 FB_BA<0> FB_BA<0> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<1> FB_BA<1> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<2> FB_BA<2> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<3> FB_BA<3> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<4> FB_BA<4> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<5> FB_BA<5> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<6> FB_BA<6> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<7> FB_BA<7> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<8> FB_BA<8> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<9> FB_BA<9> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<10> FB_BA<10> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<11> FB_BA<11> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<12> FB_BA<12> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<13> FB_BA<13> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<14> FB_BA<14> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<15> FB_BA<15> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<16> FB_BA<16> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<17> FB_BA<17> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<18> FB_BA<18> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<19> FB_BA<19> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<20> FB_BA<20> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<21> FB_BA<21> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<22> FB_BA<22> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<23> FB_BA<23> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<24> FB_BA<24> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<25> FB_BA<25> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<26> FB_BA<26> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<27> FB_BA<27> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<28> FB_BA<28> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<29> FB_BA<29> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<30> FB_BA<30> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<31> FB_BA<31> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<32> FB_BA<32> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<33> FB_BA<33> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<34> FB_BA<34> - @mib_lib.MLB 70D5 72A5 72A8 FB_BA<35> FB_BA<35> - @mib_lib.MLB 70D5 72A5 72A8 |

 FB_A0_DQ<36> FB_A0_DQ<36> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<37> FB_A0_DQ<37> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<38> FB_A0_DQ<38> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<39> FB_A0_DQ<39> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<40> FB_A0_DQ<40> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<41> FB_A0_DQ<41> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<42> FB_A0_DQ<42> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<43> FB_A0_DQ<43> - @mib_lib.MLB 70C7 72B2 FB_A0_DQ<44> FB_A0_DQ<44> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<45> FB_A0_DQ<45> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<46> FB_A0_DQ<46> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<47> FB_A0_DQ<47> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<48> FB_A0_DQ<48> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<49> FB_A0_DQ<49> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<50> FB_A0_DQ<50> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<51> FB_A0_DQ<51> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<52> FB_A0_DQ<52> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<53> FB_A0_DQ<53> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<54> FB_A0_DQ<54> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<55> FB_A0_DQ<55> - @mib_lib.MLB 70B7 72A2 FB_A0_DQ<56> FB_A0_DQ<56> - @mib_lib.MLB 70B7 72A2 < |

D	FSB_DPWR_L	FSB_DPWR_L - @m1b.lib.MLB	7B2 12B3 84D6	FW_LKON	FW_LKON - @m1b.lib.MLB	37C3 38A3 38C2	@m1b.lib.MLB	GPU_G2	GPU_G2 - @m1b.lib.MLB	75B3 76D7 77C3	LAN_ENERGY_DET	LAN_ENERGY_DET - @m1b.lib.MLB	23C3 40A3
	FSB_DRDY_L	FSB_DRDY_L - @m1b.lib.MLB	5D5 7D5 12B3 84D6	FW_LLC_PP1V8LDO_EN_L	FW_LLC_PP1V8LDO_EN_L - @m1b.lib.MLB	37B4	GPU_GENERICD	GPU_GENERICD - @m1b.lib.MLB	68A7 74C3	LATEVG_EVENT_D_L	LATEVG_EVENT_D_L - @m1b.lib.MLB	43A5 43C7	
	FSB_DSTBN_L<0>	FSB_DSTBN_L<0> - @m1b.lib.MLB	7C4 1283	FW_LPS	FW_LPS - @m1b.lib.MLB	37C3 38C6	GPU_GPIO_0	GPU_GPIO_0 - @m1b.lib.MLB	71D8 74D3	LATEVG_EVENT_L	LATEVG_EVENT_L - @m1b.lib.MLB	43A6	
	FSB_DSTBN_L<3..0>	FSB_DSTBN_L<3..0> - @m1b.lib.MLB	5D5 84D6	FW_LREQ	FW_LREQ - @m1b.lib.MLB	37C3 38C6	GPU_GPIO_1	GPU_GPIO_1 - @m1b.lib.MLB	71C8 74D3	LCD_PWREN_L	LCD_PWREN_L - @m1b.lib.MLB	76D4	
	FSB_DSTBN_L<1>	FSB_DSTBN_L<1> - @m1b.lib.MLB	784 1283	FW_MFUNC	FW_MFUNC - @m1b.lib.MLB	37B4	GPU_GPIO_2	GPU_GPIO_2 - @m1b.lib.MLB	71C8 74D3	LCD_PWREN_L_RC	LCD_PWREN_L_RC - @m1b.lib.MLB	76D4	
	FSB_DSTBN_L<2>	FSB_DSTBN_L<2> - @m1b.lib.MLB	7C2 1283	FW_PCI_IDSEL	FW_PCI_IDSEL - @m1b.lib.MLB	37B6	GPU_GPIO_3	GPU_GPIO_3 - @m1b.lib.MLB	71C8 74D3	LIO_BATT_ISENSE	LIO_BATT_ISENSE - @m1b.lib.MLB	5C1 47C6 53C3	
	FSB_DSTBN_L<3>	FSB_DSTBN_L<3> - @m1b.lib.MLB	782 1283	FW_PHY_LKON	FW_PHY_LKON - @m1b.lib.MLB	37C4	GPU_GPIO_4	GPU_GPIO_4 - @m1b.lib.MLB	71C8 74D3	LIO_DCIN_ISENSE	LIO_DCIN_ISENSE - @m1b.lib.MLB	5C1 47B6 53C5	
	FSB_DSTBP_L<0>	FSB_DSTBP_L<0> - @m1b.lib.MLB	7C4 1283	FW_PHY_RESET_L	FW_PHY_RESET_L - @m1b.lib.MLB	38B6	GPU_GPIO_5	GPU_GPIO_5 - @m1b.lib.MLB	71C8 74D3	LIO_PLT_RESET_L	LIO_PLT_RESET_L - @m1b.lib.MLB	5C1 6C4 6C5 26C1 26C1	
	FSB_DSTBP_L<3..0>	FSB_DSTBP_L<3..0> - @m1b.lib.MLB	5D5 84D6	FW_PINT	FW_PINT - @m1b.lib.MLB	37C3 38C2	GPU_GPIO_6	GPU_GPIO_6 - @m1b.lib.MLB	71C8 74D3	LPC_AD<0>	LPC_AD<0> - @m1b.lib.MLB	5D2 21D4 49D7 51C4 58C6	
	FSB_DSTBP_L<1>	FSB_DSTBP_L<1> - @m1b.lib.MLB	784 1283	FW_PORT1_AREF	FW_PORT1_AREF - @m1b.lib.MLB	44C2	GPU_GPIO_8	GPU_GPIO_8 - @m1b.lib.MLB	71C8 74D3	LPC_AD<1>	LPC_AD<1> - @m1b.lib.MLB	5C2 21D4 49D7 51C4 58C6	
C	FSB_DSTBP_L<2>	FSB_DSTBP_L<2> - @m1b.lib.MLB	7C2 1283	FW_PORT1_TPA_N	FW_PORT1_TPA_N - @m1b.lib.MLB	38B2 44B5 44B7 44C5 44D7	GPU_GPIO_9	GPU_GPIO_9 - @m1b.lib.MLB	71C8 74D3	LPC_AD<2>	LPC_AD<2> - @m1b.lib.MLB	5C2 21D4 49C7 51C5 58C6	
	FSB_DSTBP_L<3>	FSB_DSTBP_L<3> - @m1b.lib.MLB	782 1283	FW_PORT1_TPA_P	FW_PORT1_TPA_P - @m1b.lib.MLB	38B2 44B5 44B7 44C5 44D7	GPU_GPIO_10	GPU_GPIO_10 - @m1b.lib.MLB	71C8 74D3	LPC_AD<3>	LPC_AD<3> - @m1b.lib.MLB	5C2 21D4 49C7 51C5 58C6	
	FSB_D_L<0>	FSB_D_L<0> - @m1b.lib.MLB	7C4 12D6	FW_PORT1_TPB_C	FW_PORT1_TPB_C - @m1b.lib.MLB	44B6	GPU_GPIO_11	GPU_GPIO_11 - @m1b.lib.MLB	71C8 74D3	LPC_FRAME_L	LPC_FRAME_L - @m1b.lib.MLB	5C2 21C4 49C7 51C4 58C6	
	FSB_D_L<63..0>	FSB_D_L<63..0> - @m1b.lib.MLB	5D5 84D6	FW_PORT1_TPB_N	FW_PORT1_TPB_N - @m1b.lib.MLB	38B2 44B5 44B7 44C5 44D7	GPU_GPIO_12	GPU_GPIO_12 - @m1b.lib.MLB	71C8 74D3	LTALS_OUT	LTALS_OUT - @m1b.lib.MLB	5B2 55C7 78C5	
	FSB_D_L<1>	FSB_D_L<1> - @m1b.lib.MLB	7C4 12D6	FW_PORT1_TPB_P	FW_PORT1_TPB_P - @m1b.lib.MLB	38B2 44B5 44B7 44C5 44D7	GPU_GPIO_13	GPU_GPIO_13 - @m1b.lib.MLB	71C8 74D3	LTUSB_OC_L	LTUSB_OC_L - @m1b.lib.MLB	5C1 6D1 6D3 22C4 22D8	
	FSB_D_L<2>	FSB_D_L<2> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPA_FL_N	FW_PORT2_TPA_FL_N - @m1b.lib.MLB	44B2 44D7	GPU_H2SYNC	GPU_H2SYNC - @m1b.lib.MLB	75B3 77C5	LVDS_A_CLK_N	LVDS_A_CLK_N - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<3>	FSB_D_L<3> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPA_FL_P	FW_PORT2_TPA_FL_P - @m1b.lib.MLB	44B2 44D7	GPU_HPD	GPU_HPD - @m1b.lib.MLB	75A5 77A1	LVDS_A_CLK_P	LVDS_A_CLK_P - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<4>	FSB_D_L<4> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPA_N	FW_PORT2_TPA_N - @m1b.lib.MLB	38B2 44B4 44C5 44C7	GPU_HPD_BILAT	GPU_HPD_BILAT - @m1b.lib.MLB	77A2	LVDS_A_DATA_N<0>	LVDS_A_DATA_N<0> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<5>	FSB_D_L<5> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPA_P	FW_PORT2_TPA_P - @m1b.lib.MLB	38B2 44B4 44C5 44C7	GPU_MEMID	GPU_MEMID - @m1b.lib.MLB	71B6 71B8 74D5	LVDS_A_DATA_N<1>	LVDS_A_DATA_N<1> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<6>	FSB_D_L<6> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPB_C	FW_PORT2_TPB_C - @m1b.lib.MLB	44B7	GPU_MEMTEST	GPU_MEMTEST - @m1b.lib.MLB	70A3	LVDS_A_DATA_N<2>	LVDS_A_DATA_N<2> - @m1b.lib.MLB	13C5 79D3	
B	FSB_D_L<7>	FSB_D_L<7> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPB_FL_N	FW_PORT2_TPB_FL_N - @m1b.lib.MLB	44B2 44D7	GPU_MEM_256M	GPU_MEM_256M - @m1b.lib.MLB	71B6 71B8 74D5	LVDS_A_DATA_N<3>	LVDS_A_DATA_N<3> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<8>	FSB_D_L<8> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPB_FL_P	FW_PORT2_TPB_FL_P - @m1b.lib.MLB	44B2 44D7	GPU_MVREFD0	GPU_MVREFD0 - @m1b.lib.MLB	70B7	LVDS_A_DATA_P<0>	LVDS_A_DATA_P<0> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<9>	FSB_D_L<9> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPB_N	FW_PORT2_TPB_N - @m1b.lib.MLB	38B2 44B4 44C5 44C7	GPU_MVREFD1	GPU_MVREFD1 - @m1b.lib.MLB	70B3	LVDS_A_DATA_P<1>	LVDS_A_DATA_P<1> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<10>	FSB_D_L<10> - @m1b.lib.MLB	7C4 12D6	FW_PORT2_TPB_P	FW_PORT2_TPB_P - @m1b.lib.MLB	38B2 44B4 44C5 44C7	GPU_MVREFS0	GPU_MVREFS0 - @m1b.lib.MLB	70B7	LVDS_A_DATA_P<2>	LVDS_A_DATA_P<2> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<11>	FSB_D_L<11> - @m1b.lib.MLB	7C4 12D6	FW_PWCCTRL_GATE1	FW_PWCCTRL_GATE1 - @m1b.lib.MLB	43C4	GPU_MVREFS1	GPU_MVREFS1 - @m1b.lib.MLB	70B3	LVDS_B_CLK_N	LVDS_B_CLK_N - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<12>	FSB_D_L<12> - @m1b.lib.MLB	7C4 12D6	FW_PWCCTRL_GATE2	FW_PWCCTRL_GATE2 - @m1b.lib.MLB	43C4	GPU_PCIE_CALI	GPU_PCIE_CALI - @m1b.lib.MLB	67A3	LVDS_B_CLK_P	LVDS_B_CLK_P - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<13>	FSB_D_L<13> - @m1b.lib.MLB	7C4 12C6	FW_PWCCTRL_GATE2_1	FW_PWCCTRL_GATE2_1 - @m1b.lib.MLB	43C3	GPU_PCIE_CALIN	GPU_PCIE_CALIN - @m1b.lib.MLB	67A3	LVDS_B_DATA_N<0>	LVDS_B_DATA_N<0> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<14>	FSB_D_L<14> - @m1b.lib.MLB	7C4 12C6	FW_PWCCTRL_GATE2_1_R	FW_PWCCTRL_GATE2_1_R - @m1b.lib.MLB	43C2	GPU_PCIE_CALINP	GPU_PCIE_CALINP - @m1b.lib.MLB	67A3	LVDS_B_DATA_N<1>	LVDS_B_DATA_N<1> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<15>	FSB_D_L<15> - @m1b.lib.MLB	7C4 12C6	FW_PWCCTRL_GATE2_2	FW_PWCCTRL_GATE2_2 - @m1b.lib.MLB	43B3	GPU_R2	GPU_R2 - @m1b.lib.MLB	75B3 76D7 77C3	LVDS_B_DATA_N<2>	LVDS_B_DATA_N<2> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<16>	FSB_D_L<16> - @m1b.lib.MLB	7C4 12C6	FW_PWCCTRL_GATE2_2_R	FW_PWCCTRL_GATE2_2_R - @m1b.lib.MLB	43C2	GPU_SIGNAL_ENABLE	GPU_SIGNAL_ENABLE - @m1b.lib.MLB	26A1 26A1 26A2 77B2 79A7	LVDS_B_DATA_P<0>	LVDS_B_DATA_P<0> - @m1b.lib.MLB	13C5 79D3	
A	FSB_D_L<17>	FSB_D_L<17> - @m1b.lib.MLB	7C4 12C6	FW_R0	FW_R0 - @m1b.lib.MLB	38B4	GPU_TEST_MCLK	GPU_TEST_MCLK - @m1b.lib.MLB	70A3	LVDS_B_DATA_P<1>	LVDS_B_DATA_P<1> - @m1b.lib.MLB	13C5 79D3	
	FSB_D_L<18>	FSB_D_L<18> - @m1b.lib.MLB	784 12C6	FW_R1	FW_R1 - @m1b.lib.MLB	38B4	GPU_TEST_YCLK	GPU_TEST_YCLK - @m1b.lib.MLB	70A3	LVDS_CONN_DDC_CLK	LVDS_CONN_DDC_CLK - @m1b.lib.MLB	13D5 79A4	
	FSB_D_L<19>	FSB_D_L<19> - @m1b.lib.MLB	784 12C6	FW_SCL	FW_SCL - @m1b.lib.MLB	37B4	GPU_V2SYNC	GPU_V2SYNC - @m1b.lib.MLB	75B3 77D5	LVDS_CONN_DDC_DATA	LVDS_CONN_DDC_DATA - @m1b.lib.MLB	13D5 79A4	
	FSB_D_L<20>	FSB_D_L<20> - @m1b.lib.MLB	784 12C6	FW_SDA	FW_SDA - @m1b.lib.MLB	37B4	GPU_VARY_BL	GPU_VARY_BL - @m1b.lib.MLB	74C3 79A4	LVDS_IBO	LVDS_IBO - @m1b.lib.MLB	13D5 19D3	
	FSB_D_L<21>	FSB_D_L<21> - @m1b.lib.MLB	784 12C6	FW_TESTM	FW_TESTM - @m1b.lib.MLB	38B4	GPU_VCORE_HIGH	GPU_VCORE_HIGH - @m1b.lib.MLB	68A8 68B4	LVDS_I_CLK_CONN_N	LVDS_I_CLK_CONN_N - @m1b.lib.MLB	6B1 6B3 76C2 76D7 79C1	
	FSB_D_L<22>	FSB_D_L<22> - @m1b.lib.MLB	784 12C6	FW_TESTW	FW_TESTW - @m1b.lib.MLB	38B4	GPU_VCORE_LOW	GPU_VCORE_LOW - @m1b.lib.MLB	68B3	LVDS_I_CLK_CONN_P	LVDS_I_CLK_CONN_P - @m1b.lib.MLB	6B1 6B3 76C2 76D7 79C1	
	FSB_D_L<23>	FSB_D_L<23> - @m1b.lib.MLB	784 12C6	GND_AUDIO	GND_AUDIO - @m1b.lib.MLB	5C1 47A4	HSTMSNS_DK_N	HSTMSNS_DK_N - @m1b.lib.MLB	68B4 71C5 71C8 74C3	LVDS_L_CLK_N	LVDS_L_CLK_N - @m1b.lib.MLB	75A3 76D7 79C3	
	FSB_D_L<24>	FSB_D_L<24> - @m1b.lib.MLB	784 12C6	GND_BATT	GND_BATT - @m1b.lib.MLB	5D1 66B5	HSTMSNS_DK_P	HSTMSNS_DK_P - @m1b.lib.MLB	5B2 52C5	LVDS_L_CLK_P	LVDS_L_CLK_P - @m1b.lib.MLB	75A3 76D7 79C3	
	FSB_D_L<25>	FSB_D_L<25> - @m1b.lib.MLB	784 12C6	GND_CHASSIS_BATTCONN	GND_CHASSIS_BATTCONN - @m1b.lib.MLB	6A5	IDE_IRQ14	IDE_IRQ					

	8				7				6				5				4				3				2				1			
D	MEM_A_A<12>	MEM_A_A<12> - @mlb.lib.MLB	15B5	28C6	MEM_B_DM<3>	MEM_B_DM<3> - @mlb.lib.MLB	15C1	29C6	NB_CFG<6>	NB_CFG<6> - @mlb.lib.MLB	6D6	14C6	P1V05S0_COMP_R	P1V05S0_COMP_R - @mlb.lib.MLB	5C7	63A7	MEM_A_A<13>	MEM_A_A<13> - @mlb.lib.MLB	15B5	28B3	MEM_B_DM<4>	MEM_B_DM<4> - @mlb.lib.MLB	15C1	29B3	MEM_A_A<15..14>	MEM_A_A<15..14> - @mlb.lib.MLB	6D6	14C6	MEM_A_A<13>	MEM_A_A<13> - @mlb.lib.MLB	15B5	28B3
	MEM_A_BS<0>	MEM_A_BS<0> - @mlb.lib.MLB	15D5	28B6	MEM_B_DM<5>	MEM_B_DM<5> - @mlb.lib.MLB	15C1	29A6	NB_CFG<7>	NB_CFG<7> - @mlb.lib.MLB	6D6	14C6	P1V05S0_FB	P1V05S0_FB - @mlb.lib.MLB	5B7	63A7	MEM_A_BS<1>	MEM_A_BS<1> - @mlb.lib.MLB	15D5	28B6	MEM_B_DM<6>	MEM_B_DM<6> - @mlb.lib.MLB	15C1	29A6	MEM_A_BS<2..0>	MEM_A_BS<2..0> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<2>	MEM_A_BS<2> - @mlb.lib.MLB	15D5	28B6
	MEM_A_BS<1>	MEM_A_BS<1> - @mlb.lib.MLB	15D5	28B3	MEM_B_DM<7>	MEM_B_DM<7> - @mlb.lib.MLB	15C1	29A3	NB_CFG<8>	NB_CFG<8> - @mlb.lib.MLB	6D6	14C6	P1V05S0_FB_RC	P1V05S0_FB_RC - @mlb.lib.MLB	5B7	63A7	MEM_A_BS<2>	MEM_A_BS<2> - @mlb.lib.MLB	15D5	28B3	MEM_B_DM<8>	MEM_B_DM<8> - @mlb.lib.MLB	15C1	29A3	MEM_A_BS<3>	MEM_A_BS<3> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<3>	MEM_A_BS<3> - @mlb.lib.MLB	15D5	28B3
	MEM_A_BS<2>	MEM_A_BS<2> - @mlb.lib.MLB	15D5	28C6	MEM_B_DM<9>	MEM_B_DM<9> - @mlb.lib.MLB	15C1	29A6	NB_CFG<9>	NB_CFG<9> - @mlb.lib.MLB	6D6	14C6	P1V05S0_FCCM	P1V05S0_FCCM - @mlb.lib.MLB	63B7		MEM_A_BS<4>	MEM_A_BS<4> - @mlb.lib.MLB	15D5	28B6	MEM_B_DM<10>	MEM_B_DM<10> - @mlb.lib.MLB	15C1	29A3	MEM_A_BS<4>	MEM_A_BS<4> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<4>	MEM_A_BS<4> - @mlb.lib.MLB	15D5	28B6
C	MEM_A_CAS_L	MEM_A_CAS_L - @mlb.lib.MLB	15D5	28B6	MEM_B_DM<11>	MEM_B_DM<11> - @mlb.lib.MLB	15C1	29B3	NB_CFG<10>	NB_CFG<10> - @mlb.lib.MLB	6D6	14C6	P1V05S0_FSET	P1V05S0_FSET - @mlb.lib.MLB	5B7	5D7 63B7	MEM_A_CAS_L	MEM_A_CAS_L - @mlb.lib.MLB	15D5	28B6	MEM_B_DM<12>	MEM_B_DM<12> - @mlb.lib.MLB	15C1	29B3	MEM_A_BS<5>	MEM_A_BS<5> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<5>	MEM_A_BS<5> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<0>	MEM_A_DM<0> - @mlb.lib.MLB	15D5	28D3	MEM_B_DM<13>	MEM_B_DM<13> - @mlb.lib.MLB	15C1	29D3	NB_CFG<11..10>	NB_CFG<11..10> - @mlb.lib.MLB	6D6	14C6	P1V05S0_IOUT	P1V05S0_IOUT - @mlb.lib.MLB	53B3	63B1	MEM_A_DM<1>	MEM_A_DM<1> - @mlb.lib.MLB	15D5	28D3	MEM_B_DM<14>	MEM_B_DM<14> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<6>	MEM_A_BS<6> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<6>	MEM_A_BS<6> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<2>	MEM_A_DM<2> - @mlb.lib.MLB	15D5	28C3	MEM_B_DM<15>	MEM_B_DM<15> - @mlb.lib.MLB	15C1	29D6	NB_CFG<12>	NB_CFG<12> - @mlb.lib.MLB	6D6	14C6	P1V05S0_ISEN	P1V05S0_ISEN - @mlb.lib.MLB	5B7	63A5	MEM_A_DM<3>	MEM_A_DM<3> - @mlb.lib.MLB	15D5	28C3	MEM_B_DM<16>	MEM_B_DM<16> - @mlb.lib.MLB	15C1	29D6	MEM_A_BS<7>	MEM_A_BS<7> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<7>	MEM_A_BS<7> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<4>	MEM_A_DM<4> - @mlb.lib.MLB	15C5	28C6	MEM_B_DM<17>	MEM_B_DM<17> - @mlb.lib.MLB	15C1	29D6	NB_CFG<13..12>	NB_CFG<13..12> - @mlb.lib.MLB	6D6	14C6	P1V05S0_ISENSE_CAL	P1V05S0_ISENSE_CAL - @mlb.lib.MLB	53A4		MEM_A_DM<5>	MEM_A_DM<5> - @mlb.lib.MLB	15D5	28C6	MEM_B_DM<18>	MEM_B_DM<18> - @mlb.lib.MLB	15C1	29D6	MEM_A_BS<8>	MEM_A_BS<8> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<8>	MEM_A_BS<8> - @mlb.lib.MLB	15D5	28B6
B	MEM_A_DM<5>	MEM_A_DM<5> - @mlb.lib.MLB	15C5	28A3	MEM_B_DM<19>	MEM_B_DM<19> - @mlb.lib.MLB	15C1	29D3	NB_CFG<15..14>	NB_CFG<15..14> - @mlb.lib.MLB	6D6	14C6	P1V05S0_PHASE	P1V05S0_PHASE - @mlb.lib.MLB	5B7	63B5	MEM_A_DM<6>	MEM_A_DM<6> - @mlb.lib.MLB	15D5	28A3	MEM_B_DM<20>	MEM_B_DM<20> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<9>	MEM_A_BS<9> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<9>	MEM_A_BS<9> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<7>	MEM_A_DM<7> - @mlb.lib.MLB	15C5	28A6	MEM_B_DM<21>	MEM_B_DM<21> - @mlb.lib.MLB	15C1	29D6	NB_CFG<16>	NB_CFG<16> - @mlb.lib.MLB	6D6	20C5	P1V05S0_UG	P1V05S0_UG - @mlb.lib.MLB	5B7	63B5	MEM_A_DM<8>	MEM_A_DM<8> - @mlb.lib.MLB	15D5	28A6	MEM_B_DM<22>	MEM_B_DM<22> - @mlb.lib.MLB	15C1	29D6	MEM_A_BS<10>	MEM_A_BS<10> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<10>	MEM_A_BS<10> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<9>	MEM_A_DM<9> - @mlb.lib.MLB	15C5	28B3	MEM_B_DM<23>	MEM_B_DM<23> - @mlb.lib.MLB	15C1	29D3	NB_CFG<17>	NB_CFG<17> - @mlb.lib.MLB	6D6	14C6	P1V5P1V05S0_PGOOD	P1V5P1V05S0_PGOOD - @mlb.lib.MLB	59C7	63B8 64B2 64B3 64B5	MEM_A_DM<10>	MEM_A_DM<10> - @mlb.lib.MLB	15D5	28B3	MEM_B_DM<24>	MEM_B_DM<24> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<11>	MEM_A_BS<11> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<11>	MEM_A_BS<11> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<11>	MEM_A_DM<11> - @mlb.lib.MLB	15C5	28A3	MEM_B_DM<25>	MEM_B_DM<25> - @mlb.lib.MLB	15C1	29D3	NB_CFG<18>	NB_CFG<18> - @mlb.lib.MLB	6D6	20B5	P1V5S0_BG	P1V5S0_BG - @mlb.lib.MLB	60C4		MEM_A_DM<12>	MEM_A_DM<12> - @mlb.lib.MLB	15D5	28A3	MEM_B_DM<26>	MEM_B_DM<26> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<12>	MEM_A_BS<12> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<12>	MEM_A_BS<12> - @mlb.lib.MLB	15D5	28B6
A	MEM_A_DM<13>	MEM_A_DM<13> - @mlb.lib.MLB	15C5	28A6	MEM_B_DM<27>	MEM_B_DM<27> - @mlb.lib.MLB	15C1	29D3	NB_CFG<19>	NB_CFG<19> - @mlb.lib.MLB	6D6	20B5	P1V5S0_BOOST	P1V5S0_BOOST - @mlb.lib.MLB	60C4		MEM_A_DM<14>	MEM_A_DM<14> - @mlb.lib.MLB	15D5	28A6	MEM_B_DM<28>	MEM_B_DM<28> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<13>	MEM_A_BS<13> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<13>	MEM_A_BS<13> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<15>	MEM_A_DM<15> - @mlb.lib.MLB	15C5	28A6	MEM_B_DM<29>	MEM_B_DM<29> - @mlb.lib.MLB	15C1	29D6	NB_CFG<20>	NB_CFG<20> - @mlb.lib.MLB	6D6	20A5	P1V5S0_BOOST_RC	P1V5S0_BOOST_RC - @mlb.lib.MLB	60C3		MEM_A_DM<16>	MEM_A_DM<16> - @mlb.lib.MLB	15D5	28A6	MEM_B_DM<30>	MEM_B_DM<30> - @mlb.lib.MLB	15C1	29D6	MEM_A_BS<14>	MEM_A_BS<14> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<14>	MEM_A_BS<14> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<17>	MEM_A_DM<17> - @mlb.lib.MLB	15C5	28A6	MEM_B_DM<31>	MEM_B_DM<31> - @mlb.lib.MLB	15C1	29D3	NB_CFG<21>	NB_CFG<21> - @mlb.lib.MLB	6D6	20C5	P1V5S0_COMP_POS	P1V5S0_COMP_POS - @mlb.lib.MLB	64B4		MEM_A_DM<18>	MEM_A_DM<18> - @mlb.lib.MLB	15D5	28A6	MEM_B_DM<32>	MEM_B_DM<32> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<15>	MEM_A_BS<15> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<15>	MEM_A_BS<15> - @mlb.lib.MLB	15D5	28B6
	MEM_A_DM<19>	MEM_A_DM<19> - @mlb.lib.MLB	15C5	28B3	MEM_B_DM<33>	MEM_B_DM<33> - @mlb.lib.MLB	15C1	29D3	NB_CFG<22>	NB_CFG<22> - @mlb.lib.MLB	6D6	20C5	P1V5S0_IOTH	P1V5S0_IOTH - @mlb.lib.MLB	60C4		MEM_A_DM<20>	MEM_A_DM<20> - @mlb.lib.MLB	15D5	28B3	MEM_B_DM<34>	MEM_B_DM<34> - @mlb.lib.MLB	15C1	29D3	MEM_A_BS<16>	MEM_A_BS<16> - @mlb.lib.MLB	30B6	14C6	MEM_A_BS<16>	MEM_A_BS<16> - @mlb.lib.MLB	15D5	28B6

	8	7	6	5	4	3	2	1
D	PCI_AD<11> PCI_AD<12> PCI_AD<13> PCI_AD<14> PCI_AD<15> PCI_AD<16> PCI_AD<17> PCI_AD<18> PCI_AD<20> PCI_AD<21> PCI_AD<22> PCI_AD<23> PCI_AD<24> PCI_AD<25> PCI_AD<26> PCI_AD<27> PCI_AD<28> PCI_AD<29> PCI_AD<30> PCI_AD<31> PCI_CLK_FW PCI_CLK_PORT80_LPC PCI_CLK_SB PCI_CLK_SMC PCI_CLK_TPM PCI_C_BE_L<0> PCI_C_BE_L<1> PCI_C_BE_L<2> PCI_C_BE_L<3> PCI_DEVSSEL_L PCI_FRAME_L PCI_GNT3_L PCI_IRDY_L PCI_LOCK_L PCI_PAR PCI_PERR_L PCI_PERR_FWL PCI_REQ0_L PCI_REQ1_L PCI_REQ2_L PCI_REQ3_L PCI_REQ64_L PCI_RST_FWL PCI_RST_L PCI_RST_L PCI_SERR_L PCI_SERR_L PCI_TRDY_L PEG_CLK100M_GPU_N PEG_CLK100M_GPU_P PEG_COMP PEG_D2R_C_N<0> PEG_D2R_C_N<1> PEG_D2R_C_N<2> PEG_D2R_C_N<3> PEG_D2R_C_N<4> PEG_D2R_C_N<5> PEG_D2R_C_N<6> PEG_D2R_C_N<7> PEG_D2R_C_N<8> PEG_D2R_C_N<9> PEG_D2R_C_N<10> PEG_D2R_C_N<11> PEG_D2R_C_N<12> PEG_D2R_C_N<13> PEG_D2R_C_N<14> PEG_D2R_C_N<15> PEG_D2R_C_P<0> PEG_D2R_C_P<1> PEG_D2R_C_P<2> PEG_D2R_C_P<3> PEG_D2R_C_P<4> PEG_D2R_C_P<5> PEG_D2R_C_P<6> PEG_D2R_C_P<7> PEG_D2R_C_P<8> PEG_D2R_C_P<9> PEG_D2R_C_P<10> PEG_D2R_C_P<11> PEG_D2R_C_P<12> PEG_D2R_C_P<13> PEG_D2R_C_P<14> PEG_D2R_C_P<15> PEG_D2R_N<0> PEG_D2R_N<1> PEG_D2R_N<2> PEG_D2R_N<3> PEG_D2R_N<4> PEG_D2R_N<5> PEG_D2R_N<6> PEG_D2R_N<7> PEG_D2R_N<8> PEG_D2R_N<9> PEG_D2R_N<10> PEG_D2R_N<11> PEG_D2R_N<12> PEG_D2R_N<13> PEG_D2R_N<14> PEG_D2R_N<15> PEG_D2R_P<0> PEG_D2R_P<1> PEG_D2R_P<2> PEG_D2R_P<3> PEG_D2R_P<4> PEG_D2R_P<5> PEG_D2R_P<6> PEG_D2R_P<7> PEG_D2R_P<8> PEG_D2R_P<9> PEG_D2R_P<10> PEG_D2R_P<11> PEG_D2R_P<12> PEG_D2R_P<13> PEG_D2R_P<14> PEG_D2R_P<15> PEG_D2R_N<0> PEG_D2R_N<1> PEG_D2R_N<2> PEG_D2R_N<3> PEG_D2R_N<4> PEG_D2R_N<5> PEG_D2R_N<6> PEG_D2R_N<7> PEG_D2R_N<8> PEG_D2R_N<9> PEG_D2R_N<10> PEG_D2R_N<11> PEG_D2R_N<12> PEG_D2R_N<13> PEG_D2R_N<14> PEG_D2R_N<15> PEG_R2D_C_N<0> PEG_R2D_C_N<1> PEG_R2D_C_N<2> PEG_R2D_C_N<3> PEG_R2D_C_N<4> PEG_R2D_C_N<5> PEG_R2D_C_N<6> PEG_R2D_C_N<7> PEG_R2D_C_N<8> PEG_R2D_C_N<9>	PCI_AD<11> - @m1b.lib.MLB PCI_AD<12> - @m1b.lib.MLB PCI_AD<13> - @m1b.lib.MLB PCI_AD<14> - @m1b.lib.MLB PCI_AD<15> - @m1b.lib.MLB PCI_AD<16> - @m1b.lib.MLB PCI_AD<17> - @m1b.lib.MLB PCI_AD<18> - @m1b.lib.MLB PCI_AD<20> - @m1b.lib.MLB PCI_AD<21> - @m1b.lib.MLB PCI_AD<22> - @m1b.lib.MLB PCI_AD<23> - @m1b.lib.MLB PCI_AD<24> - @m1b.lib.MLB PCI_AD<25> - @m1b.lib.MLB PCI_AD<26> - @m1b.lib.MLB PCI_AD<27> - @m1b.lib.MLB PCI_AD<28> - @m1b.lib.MLB PCI_AD<29> - @m1b.lib.MLB PCI_AD<30> - @m1b.lib.MLB PCI_AD<31> - @m1b.lib.MLB PCI_CLK_FW - @m1b.lib.MLB PCI_CLK_PORT80_LPC - @m1b.lib.MLB PCI_CLK_SB - @m1b.lib.MLB PCI_CLK_SMC - @m1b.lib.MLB PCI_CLK_TPM - @m1b.lib.MLB PCI_C_BE_L<0> - @m1b.lib.MLB PCI_C_BE_L<1> - @m1b.lib.MLB PCI_C_BE_L<2> - @m1b.lib.MLB PCI_C_BE_L<3> - @m1b.lib.MLB PCI_DEVSSEL_L - @m1b.lib.MLB PCI_FRAME_L - @m1b.lib.MLB PCI_GNT3_L - @m1b.lib.MLB PCI_IRDY_L - @m1b.lib.MLB PCI_LOCK_L - @m1b.lib.MLB PCI_PAR - @m1b.lib.MLB PCI_PERR_L - @m1b.lib.MLB PCI_PERR_FWL - @m1b.lib.MLB PCI_REQ0_L - @m1b.lib.MLB PCI_REQ1_L - @m1b.lib.MLB PCI_REQ2_L - @m1b.lib.MLB PCI_REQ3_L - @m1b.lib.MLB PCI_REQ64_L - @m1b.lib.MLB PCI_RST_FWL - @m1b.lib.MLB PCI_RST_L - @m1b.lib.MLB PCI_RST_L - @m1b.lib.MLB PCI_SERR_L - @m1b.lib.MLB PCI_SERR_L - @m1b.lib.MLB PCI_TRDY_L - @m1b.lib.MLB PEG_CLK100M_GPU_N - @m1b.lib.MLB PEG_CLK100M_GPU_P - @m1b.lib.MLB PEG_COMP - @m1b.lib.MLB PEG_D2R_C_N<0> - @m1b.lib.MLB PEG_D2R_C_N<1> - @m1b.lib.MLB PEG_D2R_C_N<2> - @m1b.lib.MLB PEG_D2R_C_N<3> - @m1b.lib.MLB PEG_D2R_C_N<4> - @m1b.lib.MLB PEG_D2R_C_N<5> - @m1b.lib.MLB PEG_D2R_C_N<6> - @m1b.lib.MLB PEG_D2R_C_N<7> - @m1b.lib.MLB PEG_D2R_C_N<8> - @m1b.lib.MLB PEG_D2R_C_N<9> - @m1b.lib.MLB PEG_D2R_C_N<10> - @m1b.lib.MLB PEG_D2R_C_N<11> - @m1b.lib.MLB PEG_D2R_C_N<12> - @m1b.lib.MLB PEG_D2R_C_N<13> - @m1b.lib.MLB PEG_D2R_C_N<14> - @m1b.lib.MLB PEG_D2R_C_N<15> - @m1b.lib.MLB PEG_D2R_C_P<0> - @m1b.lib.MLB PEG_D2R_C_P<1> - @m1b.lib.MLB PEG_D2R_C_P<2> - @m1b.lib.MLB PEG_D2R_C_P<3> - @m1b.lib.MLB PEG_D2R_C_P<4> - @m1b.lib.MLB PEG_D2R_C_P<5> - @m1b.lib.MLB PEG_D2R_C_P<6> - @m1b.lib.MLB PEG_D2R_C_P<7> - @m1b.lib.MLB PEG_D2R_C_P<8> - @m1b.lib.MLB PEG_D2R_C_P<9> - @m1b.lib.MLB PEG_D2R_C_P<10> - @m1b.lib.MLB PEG_D2R_C_P<11> - @m1b.lib.MLB PEG_D2R_C_P<12> - @m1b.lib.MLB PEG_D2R_C_P<13> - @m1b.lib.MLB PEG_D2R_C_P<14> - @m1b.lib.MLB PEG_D2R_C_P<15> - @m1b.lib.MLB PEG_D2R_N<0> - @m1b.lib.MLB PEG_D2R_N<1> - @m1b.lib.MLB PEG_D2R_N<2> - @m1b.lib.MLB PEG_D2R_N<3> - @m1b.lib.MLB PEG_D2R_N<4> - @m1b.lib.MLB PEG_D2R_N<5> - @m1b.lib.MLB PEG_D2R_N<6> - @m1b.lib.MLB PEG_D2R_N<7> - @m1b.lib.MLB PEG_D2R_N<8> - @m1b.lib.MLB PEG_D2R_N<9> - @m1b.lib.MLB PEG_D2R_N<10> - @m1b.lib.MLB PEG_D2R_N<11> - @m1b.lib.MLB PEG_D2R_N<12> - @m1b.lib.MLB PEG_D2R_N<13> - @m1b.lib.MLB PEG_D2R_N<14> - @m1b.lib.MLB PEG_D2R_N<15> - @m1b.lib.MLB PEG_R2D_C_N<0> - @m1b.lib.MLB PEG_R2D_C_N<1> - @m1b.lib.MLB PEG_R2D_C_N<2> - @m1b.lib.MLB PEG_R2D_C_N<3> - @m1b.lib.MLB PEG_R2D_C_N<4> - @m1b.lib.MLB PEG_R2D_C_N<5> - @m1b.lib.MLB PEG_R2D_C_N<6> - @m1b.lib.MLB PEG_R2D_C_N<7> - @m1b.lib.MLB PEG_R2D_C_N<8> - @m1b.lib.MLB PEG_R2D_C_N<9> - @m1b.lib.MLB	22B7 37C6 22B7 37C6 22B7 37C6 22B7 37C6 22B7 37C6 22B7 37C6 22B7 37C6 22B7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A7 37C6 22A6 37B6 5C2 34D6 51C5 22A6 34D6 34D6 49C7 34D6 58C7 22B6 37C6 22B6 37B6 22B6 37B6 22B6 37B6 22A6 26D2 37D3 22A6 26D2 37D3 6B6 6B7 22B6 37D3 22A6 26D2 37D3 22A6 26D2 22A6 37B6 22A6 26D2 37D3 22A6 26D2 37D3 22A6 26D2 22A6 26D2 37D3 22A6 26D2 37C3 22A6 26D2 37C3 33B4 34B3 34B5 67A6 33B4 34B3 34B5 67A6 13D3 7D03 67D3 67D3 67D3 67C3 					

	8	7	6	5	4	3	2	1
D	SATA_C_R2D_C_N SATA_C_R2D_C_P SATA_C_R2D_N SATA_C_R2D_P SATA_C_R2D_UF_N SATA_C_R2D_UF_P SATA_RBIA5 SB_A2OGATE SB_ACZ_BITCLK SB_ACZ_RST_L SB_ACZ_SDATAOUT SB_ACZ_SYNC SB_CLK14P3M_TIMER SB_CLK48M_USBCCLR SB_CLK100M_DMI_N SB_CLK100M_DMI_P SB_CLK100M_SATA_N SB_CLK100M_SATA_OR_L SB_CLK100M_SATA_P SB_CRT_TVOUT_MUX SB_DVI_HPD SB_GPTIO2 SB_GPTIO3 SB_GPTIO19 SB_GPTIO21 SB_GPTIO26 SB_GPTIO29 SB_GPTIO30 SB_GPTIO31 SB_GPTIO37 SB_GPVUCORE_DISABLE_L SB_INTVRMEN SB_RTC_RST_L SB_RTC_X1 SB_RTC_X1_R SB_RTC_X2 SB_SM_INTRUDER_L SB_SPKR SC_RX_L SC_TX_L SMBUS_SB_SCL SMBUS_SB_SDA SMBUS_SMC_0_S0_SCL SMBUS_SMC_0_S0_SDA SMBUS_SMC_A_S3_SCL SMBUS_SMC_A_S3_SDA SMBUS_SMC_BSA_SCL SMBUS_SMC_BSA_SDA SMBUS_SMC_BSB_SCL SMBUS_SMC_BSB_SDA SMBUS_SMC_B_S0_SCL SMBUS_SMC_B_S0_SDA SMB_ALERT_L SMB_LINK_ALERT_L SMB_ADAPTER_EN SMB_BATT_CHG_EN SMB_BATT_ISENSE SMB_BATT_ISET SMB_BATT_TRICKLE_EN_L SMB_BC_ACOK SMB_BS_ALRT_L SMB_CASE_OPEN SMB_CLK32K_SUSCLK SMB_CLK32K_SUSCLK_R SMB_CPU_ISENSE SMB_CPU_RESET_3_3_L SMB_CPU_VSENSE SMB_DCIN_ISENSE SMB_EXCARD_CP SMB_EXCARD_OC_L SMB_EXCARD_PWR_EN SMB_EXTAL SMB_EXTSMI_L SMB_FAN_0_CTL SMB_FAN_0_TACH SMB_FAN_1_CTL SMB_FAN_1_TACH SMB_FWE SMB_GPU_ISENSE SMB_GPU_VSENSE SMB_L1D SMB_LRESET_L SMB_MANUAL_RST_L SMB_MDI SMB_MM_ISENSE SMB_NMI SMB_ODD_DETECT SMB_ONOFF_L SMB_P1V5S0_ISENSE SMB_P1V5S0_NB_ISENSE SMB_PBUS_VSENSE SMB_PM_G2_EN SMB_PM_G2_EN_L SMB_PROCHOT SMB_PROCHOT_3_3_L SMB_RCIN_L SMB_RSTGATE_L SMB_RSTGATE_RC_L SMB_RST_L SMB_RUNTIME_SCI_L SMB_RX_L SMB_SVS_NMI SMB_SVS_ISET SMB_SVS_KBDLED SMB_SVS_LED_16B SMB_TCK SMB_TDI SMB_TDO SMB_THRMTRIP SMB_TMS	SATA_C_R2D_C_N - @mlb.lib.MLB SATA_C_R2D_C_P - @mlb.lib.MLB SATA_C_R2D_N - @mlb.lib.MLB SATA_C_R2D_P - @mlb.lib.MLB SATA_C_R2D_UF_N - @mlb.lib.MLB SATA_C_R2D_UF_P - @mlb.lib.MLB SATA_RBIA5 - @mlb.lib.MLB SB_A2OGATE - @mlb.lib.MLB SB_ACZ_BITCLK - @mlb.lib.MLB SB_ACZ_RST_L - @mlb.lib.MLB SB_ACZ_SDATAOUT - @mlb.lib.MLB SB_ACZ_SYNC - @mlb.lib.MLB SB_CLK14P3M_TIMER - @mlb.lib.MLB SB_CLK48M_USBCCLR - @mlb.lib.MLB SB_CLK100M_DMI_N - @mlb.lib.MLB SB_CLK100M_DMI_P - @mlb.lib.MLB SB_CLK100M_SATA_N - @mlb.lib.MLB SB_CLK100M_SATA_OR_L - @mlb.lib.MLB SB_CLK100M_SATA_P - @mlb.lib.MLB SB_CRT_TVOUT_MUX - @mlb.lib.MLB SB_DVI_HPD - @mlb.lib.MLB SB_GPTIO2 - @mlb.lib.MLB SB_GPTIO3 - @mlb.lib.MLB SB_GPTIO19 - @mlb.lib.MLB SB_GPTIO21 - @mlb.lib.MLB SB_GPTIO26 - @mlb.lib.MLB SB_GPTIO29 - @mlb.lib.MLB SB_GPTIO30 - @mlb.lib.MLB SB_GPTIO31 - @mlb.lib.MLB SB_GPTIO37 - @mlb.lib.MLB SB_GPVUCORE_DISABLE_L - @mlb.lib.MLB SB_INTVRMEN - @mlb.lib.MLB SB_RTC_RST_L - @mlb.lib.MLB SB_RTC_X1 - @mlb.lib.MLB SB_RTC_X1_R - @mlb.lib.MLB SB_RTC_X2 - @mlb.lib.MLB SB_SM_INTRUDER_L - @mlb.lib.MLB SB_SPKR - @mlb.lib.MLB SC_RX_L - @mlb.lib.MLB SC_TX_L - @mlb.lib.MLB SMBUS_SB_SCL - @mlb.lib.MLB SMBUS_SB_SDA - @mlb.lib.MLB SMBUS_SMC_0_S0_SCL - @mlb.lib.MLB SMBUS_SMC_0_S0_SDA - @mlb.lib.MLB SMBUS_SMC_A_S3_SCL - @mlb.lib.MLB SMBUS_SMC_A_S3_SDA - @mlb.lib.MLB SMBUS_SMC_BSA_SCL - @mlb.lib.MLB SMBUS_SMC_BSA_SDA - @mlb.lib.MLB SMBUS_SMC_BSB_SCL - @mlb.lib.MLB SMBUS_SMC_BSB_SDA - @mlb.lib.MLB SMBUS_SMC_B_S0_SCL - @mlb.lib.MLB SMBUS_SMC_B_S0_SDA - @mlb.lib.MLB SMB_ALERT_L - @mlb.lib.MLB SMB_LINK_ALERT_L - @mlb.lib.MLB SMB_ADAPTER_EN - @mlb.lib.MLB SMB_BATT_CHG_EN - @mlb.lib.MLB SMB_BATT_ISENSE - @mlb.lib.MLB SMB_BATT_ISET - @mlb.lib.MLB SMB_BATT_TRICKLE_EN_L - @mlb.lib.MLB SMB_BC_ACOK - @mlb.lib.MLB SMB_BS_ALRT_L - @mlb.lib.MLB SMB_CASE_OPEN - @mlb.lib.MLB SMB_CLK32K_SUSCLK - @mlb.lib.MLB SMB_CLK32K_SUSCLK_R - @mlb.lib.MLB SMB_CPU_ISENSE - @mlb.lib.MLB SMB_CPU_RESET_3_3_L - @mlb.lib.MLB SMB_CPU_VSENSE - @mlb.lib.MLB SMB_DCIN_ISENSE - @mlb.lib.MLB SMB_EXCARD_CP - @mlb.lib.MLB SMB_EXCARD_OC_L - @mlb.lib.MLB SMB_EXCARD_PWR_EN - @mlb.lib.MLB SMB_EXTAL - @mlb.lib.MLB SMB_EXTSMI_L - @mlb.lib.MLB SMB_FAN_0_CTL - @mlb.lib.MLB SMB_FAN_0_TACH - @mlb.lib.MLB SMB_FAN_1_CTL - @mlb.lib.MLB SMB_FAN_1_TACH - @mlb.lib.MLB SMB_FWE - @mlb.lib.MLB SMB_GPU_ISENSE - @mlb.lib.MLB SMB_GPU_VSENSE - @mlb.lib.MLB SMB_L1D - @mlb.lib.MLB SMB_LRESET_L - @mlb.lib.MLB SMB_MANUAL_RST_L - @mlb.lib.MLB SMB_MDI - @mlb.lib.MLB SMB_MM_ISENSE - @mlb.lib.MLB SMB_NMI - @mlb.lib.MLB SMB_ODD_DETECT - @mlb.lib.MLB SMB_ONOFF_L - @mlb.lib.MLB SMB_P1V5S0_ISENSE - @mlb.lib.MLB SMB_P1V5S0_NB_ISENSE - @mlb.lib.MLB SMB_PBUS_VSENSE - @mlb.lib.MLB SMB_PM_G2_EN - @mlb.lib.MLB SMB_PM_G2_EN_L - @mlb.lib.MLB SMB_PROCHOT - @mlb.lib.MLB SMB_PROCHOT_3_3_L - @mlb.lib.MLB SMB_RCIN_L - @mlb.lib.MLB SMB_RSTGATE_L - @mlb.lib.MLB SMB_RSTGATE_RC_L - @mlb.lib.MLB SMB_RST_L - @mlb.lib.MLB SMB_RUNTIME_SCI_L - @mlb.lib.MLB SMB_RX_L - @mlb.lib.MLB SMB_SVS_NMI - @mlb.lib.MLB SMB_SVS_ISET - @mlb.lib.MLB SMB_SVS_KBDLED - @mlb.lib.MLB SMB_SVS_LED_16B - @mlb.lib.MLB SMB_TCK - @mlb.lib.MLB SMB_TDI - @mlb.lib.MLB SMB_TDO - @mlb.lib.MLB SMB_THRMTRIP - @mlb.lib.MLB SMB_TMS - @mlb.lib.MLB	21B6 78B1 21B6 78B1 78B4 78B4 78B3 78B3 21B6 21B6 36A4 36A5 36A5 21C4 21C6 84B4 21C6 84B4 21C6 84B4 21C6 84B4 23D3 34A6 23D3 34C6 22C2 33B4 34C3 34C5 22C2 33B4 34C3 34C5 5A7 21B6 33B4 34C3 34C5 23C2 33B4 5A7 21B6 33B4 34C3 34C5 22B4 22A6 77A1 77A1 22A6 26D2 22A6 26C2 23D3 23D3 23C7 22C4 22D8 22C4 22D8 22D8 6C5 22C4 22C4 22D8 23D3 23C3 64B6 64B7 21D6 21D6 26D4 21D6 26C8 26C7 21D6 26C8 21D6 26D4 23C5 49C4 50B5 49C4 50B5 5B1 23D5 27B6 27C6 27C6 27D6 27D6 27D7 27D8 28A6 29A6 33B6 47C3 78C1 5B1 23D5 27B6 27C6 27C6 27D6 27D6 27D7 27D8 28A6 29A6 33B6 47C3 78C1 27D3 27D3 27D5 27D6 49C4 52B3 52C2 27C3 27C5 27C6 49B4 78C1 27C3 27C5 27C6 49B4 78C1 5D1 27C1 27C2 27C3 49B4 66B4 5D1 27C1 27C2 27C3 49B4 66B4 27B3 27B3 49C4 27B2 27B3 49C7 5B1 10B2 27D1 27D1 27D2 27D3 47C3 49B4 5B1 10B2 27D1 27D1 27D2 27D3 47C3 49B4 23C5 23D5 5C1 43B7 47C6 49D4 50A2 5C1 47C6 49D7 50A2 49D4 53C1 5C1 47B6 49B4 5C1 47B6 49D7 50A2 5C1 47B6 49C4 50A2 5D1 49C4 50B2 66B4 49C4 50A2 35B2 35B3 49C4 35B4 49D4 53B7 49B4 50B2 49D4 53D5 49D4 53C3 5C1 47B6 49B7 50A2 49B7 50B5 5C1 47B6 49B7 49C3 50B8 23B8 49B7 49B7 56B7 49B7 56B7 49B7 56B4 49B7 56B4 49B4 50B2 49D4 53B5 49D4 53C5 49B4 50B2 78C3 26B1 49C7 50D7 5C2 49C1 51B4 49A7 50A2 5C2 49C1 51B5 36C4 49B7 5B2 49C4 50B2 50C5 78C1 49A7 50D3 50D5 53B1 49D4 50D3 50D5 53B3 49D4 53D1 49D4 64A8 49D4 50C2 49D4 50D1 21C2 49C7 6B6 6D7 6D4 37A8 49D7 6D5 37A7 5C2 49C4 50D6 51B5 23C8 49B7 5C2 46B5 49C7 50B2 50B3 51B5 23C3 49D7 5C1 47C6 49B4 49C7 55B 49C7 50A8 5C2 49C4 50B2 51B5 5C2 49B4 50B2 51B5 5C2 49B4 50B2 51B4 49B4 50C2 5C2 49B4 50B2 51B4	SMC_TPM_GPIO SMC_TPM_PP SMC_TPM_RESET_L SMC_TRST_L SMC_TX_L SMC_USB_DEBUG_MUX SMC_VCL SMC_WAKE_SCI_L SMC_XDP_TCK_3_3 SMC_XDP_TDO_3_3 SMC_XTAL SMLINK<0> SMLINK<1> SMS_INT_L SMS_ONOFF_L SMS_X_AXIS SMS_Y_AXIS SMS_Z_AXIS SODIMM_A_SAI SPI_ARB SPI_CE_L SPI_HOLD_L SPI_SCLK SPI_SCLK_R SPI_SI SPI_SI_R SPI_SO SPI_SO_R SPI_WP_L SV_SET_UP SYS_LED_ANODE SYS_LED_ILIM SYS_LED_L SYS_LED_L_VDIV SYS_ONEMIRE THRM_ALERT THRM_ALERT_L THRM_CPU_DX_N THRM_CPU_DX_P TMD5_CLK_F_N TMD5_CLK_F_P TMD5_CLK_N TMD5_CLK_P TMD5_CLK_R_N TMD5_CLK_R_P TMD5_DATA_F_N<0> TMD5_DATA_F_N<2..0> TMD5_DATA_F_N<5..0> TMD5_DATA_F_N<5..0> TMD5_DATA_F_N<1> TMD5_DATA_F_N<2> TMD5_DATA_F_N<3> TMD5_DATA_F_N<5..3> TMD5_DATA_F_N<4> TMD5_DATA_F_N<5> TMD5_DATA_F_P<0> TMD5_DATA_F_P<2..0> TMD5_DATA_F_P<5..0> TMD5_DATA_F_P<5..3> TMD5_DATA_F_P<1> TMD5_DATA_F_P<2> TMD5_DATA_F_P<3> TMD5_DATA_F_P<5..3> TMD5_DATA_F_P<4> TMD5_DATA_F_P<5> TMD5_DATA_N<0> TMD5_DATA_N<2..0> TMD5_DATA_N<1> TMD5_DATA_N<2> TMD5_DATA_N<3> TMD5_DATA_N<5..3> TMD5_DATA_N<4> TMD5_DATA_N<5> TMD5_DATA_P<0> TMD5_DATA_P<2..0> TMD5_DATA_P<1> TMD5_DATA_P<2> TMD5_DATA_P<3> TMD5_DATA_P<5..3> TMD5_DATA_P<4> TMD5_DATA_P<5> TMD5_DATA_RL<0> TMD5_DATA_RL<1> TMD5_DATA_RL<2> TMD5_DATA_RL<3> TMD5_DATA_RL<4> TMD5_DATA_RL<5> TPM_BADD TPM_GPTIO1 TPM_GPTIO2 TPM_LRESET_L TPM_PP TPM_RST_L TPM_XTALI TPM_XTALO TPM_XTALO_R TPS73115_NR TP_AZ_DOCK_RST_L TP_CK410_PCI4_CLK TP_CPU_CPUSLP_L TP_CPU_SPARE3 TP_CPU_SPARE5 TP_CPU_SPARE6 TP_CPU_SPARE7 TP_CRT_DDC_CLK TP_CRT_DDC_DATA TP_FB_A_ODT<0> TP_FB_A_ODT<1> TP_FB_B_ODT<0> TP_FB_B_ODT<1> TP_FW_CTL<0> TP_FW_CTL<1> TP_FW_DATA<0> TP_FW_DATA<1> TP_GPU_GPTIO_10 TP_GPU_VGA_HSYNC TP_GPU_VGA_VSYNC TP_LVDS_CLKCTLA TP_LVDS_CLKCTLB	SMC_TPM_GPIO - @mlb.lib.MLB SMC_TPM_PP - @mlb.lib.MLB SMC_TPM_RESET_L - @mlb.lib.MLB SMC_TRST_L - @mlb.lib.MLB SMC_TX_L - @mlb.lib.MLB SMC_USB_DEBUG_MUX - @mlb.lib.MLB SMC_VCL - @mlb.lib.MLB SMC_WAKE_SCI_L - @mlb.lib.MLB SMC_XDP_TCK_3_3 - @mlb.lib.MLB SMC_XDP_TDO_3_3 - @mlb.lib.MLB SMC_XTAL - @mlb.lib.MLB SMLINK<0> - @mlb.lib.MLB SMLINK<1> - @mlb.lib.MLB SMS_INT_L - @mlb.lib.MLB SMS_ONOFF_L - @mlb.lib.MLB SMS_X_AXIS - @mlb.lib.MLB SMS_Y_AXIS - @mlb.lib.MLB SMS_Z_AXIS - @mlb.lib.MLB SODIMM_A_SAI - @mlb.lib.MLB SPI_ARB - @mlb.lib.MLB SPI_CE_L - @mlb.lib.MLB SPI_HOLD_L - @mlb.lib.MLB SPI_SCLK - @mlb.lib.MLB SPI_SCLK_R - @mlb.lib.MLB SPI_SI - @mlb.lib.MLB SPI_SI_R - @mlb.lib.MLB SPI_SO - @mlb.lib.MLB SPI_SO_R - @mlb.lib.MLB SPI_WP_L - @mlb.lib.MLB SV_SET_UP - @mlb.lib.MLB SYS_LED_ANODE - @mlb.lib.MLB SYS_LED_ILIM - @mlb.lib.MLB SYS_LED_L - @mlb.lib.MLB SYS_LED_L_VDIV - @mlb.lib.MLB SYS_ONEMIRE - @mlb.lib.MLB THRM_ALERT - @mlb.lib.MLB THRM_ALERT_L - @mlb.lib.MLB THRM_CPU_DX_N - @mlb.lib.MLB THRM_CPU_DX_P - @mlb.lib.MLB TMD5_CLK_F_N - @mlb.lib.MLB TMD5_CLK_F_P - @mlb.lib.MLB TMD5_CLK_N - @mlb.lib.MLB TMD5_CLK_P - @mlb.lib.MLB TMD5_CLK_R_N - @mlb.lib.MLB TMD5_CLK_R_P - @mlb.lib.MLB TMD5_DATA_F_N<0> - @mlb.lib.MLB TMD5_DATA_F_N<2..0> - @mlb.lib.MLB TMD5_DATA_F_N<5..0> - @mlb.lib.MLB TMD5_DATA_F_N<5..0> - @mlb.lib.MLB TMD5_DATA_F_N<1> - @mlb.lib.MLB TMD5_DATA_F_N<2> - @mlb.lib.MLB TMD5_DATA_F_N<3> - @mlb.lib.MLB TMD5_DATA_F_N<5..3> - @mlb.lib.MLB TMD5_DATA_F_N<4> - @mlb.lib.MLB TMD5_DATA_F_N<5> - @mlb.lib.MLB TMD5_DATA_F_P<0> - @mlb.lib.MLB TMD5_DATA_F_P<2..0> - @mlb.lib.MLB TMD5_DATA_F_P<5..0> - @mlb.lib.MLB TMD5_DATA_F_P<5..3> - @mlb.lib.MLB TMD5_DATA_F_P<1> - @mlb.lib.MLB TMD5_DATA_F_P<2> - @mlb.lib.MLB TMD5_DATA_F_P<3> - @mlb.lib.MLB TMD5_DATA_F_P<5..3> - @mlb.lib.MLB TMD5_DATA_F_P<4> - @mlb.lib.MLB TMD5_DATA_F_P<5> - @mlb.lib.MLB TMD5_DATA_N<0> - @mlb.lib.MLB TMD5_DATA_N<2..0> - @mlb.lib.MLB TMD5_DATA_N<1> - @mlb.lib.MLB TMD5_DATA_N<2> - @mlb.lib.MLB TMD5_DATA_N<3> - @mlb.lib.MLB TMD5_DATA_N<5..3> - @mlb.lib.MLB TMD5_DATA_N<4> - @mlb.lib.MLB TMD5_DATA_N<5> - @mlb.lib.MLB TMD5_DATA_P<0> - @mlb.lib.MLB TMD5_DATA_P<2..0> - @mlb.lib.MLB TMD5_DATA_P<1> - @mlb.lib.MLB TMD5_DATA_P<2> - @mlb.lib.MLB TMD5_DATA_P<3> - @mlb.lib.MLB TMD5_DATA_P<5..3> - @mlb.lib.MLB TMD5_DATA_P<4> - @mlb.lib.MLB TMD5_DATA_P<5> - @mlb.lib.MLB TMD5_DATA_RL<0> - @mlb.lib.MLB TMD5_DATA_RL<1> - @mlb.lib.MLB TMD5_DATA_RL<2> - @mlb.lib.MLB TMD5_DATA_RL<3> - @mlb.lib.MLB TMD5_DATA_RL<4> - @mlb.lib.MLB TMD5_DATA_RL<5> - @mlb.lib.MLB TPM_BADD - @mlb.lib.MLB TPM_GPTIO1 - @mlb.lib.MLB TPM_GPTIO2 - @mlb.lib.MLB TPM_LRESET_L - @mlb.lib.MLB TPM_PP - @mlb.lib.MLB TPM_RST_L - @mlb.lib.MLB TPM_XTALI - @mlb.lib.MLB TPM_XTALO - @mlb.lib.MLB TPM_XTALO_R - @mlb.lib.MLB TPS73115_NR - @mlb.lib.MLB TP_AZ_DOCK_RST_L - @mlb.lib.MLB TP_CK410_PCI4_CLK - @mlb.lib.MLB TP_CPU_CPUSLP_L - @mlb.lib.MLB TP_CPU_SPARE3 - @mlb.lib.MLB TP_CPU_SPARE5 - @mlb.lib.MLB TP_CPU_SPARE6 - @mlb.lib.MLB TP_CPU_SPARE7 - @mlb.lib.MLB TP_CRT_DDC_CLK - @mlb.lib.MLB TP_CRT_DDC_DATA - @mlb.lib.MLB TP_FB_A_ODT<0> - @mlb.lib.MLB TP_FB_A_ODT<1> - @mlb.lib.MLB TP_FB_B_ODT<0> - @mlb.lib.MLB TP_FB_B_ODT<1> - @mlb.lib.MLB TP_FW_CTL<0> - @mlb.lib.MLB TP_FW_CTL<1> - @mlb			

8			7			6			5			4			3			2			1																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																				
D	Title: Cref Part Report Design: mlb Date: Sep 25 10:54:06 2006																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								
	C0600	CAP_402	mlb[6B7]	C0602	CAP_402	mlb[6A8]	C0610	CAP_402	mlb[6A4]	C0611	CAP_402	mlb[6A4]	C0612	CAP_402	mlb[6A4]	C0613	CAP_402	mlb[6A4]	C0614	CAP_402	mlb[6A3]	C0615	CAP_402	mlb[6A3]	C0630	CAP_402	mlb[6A5]	C0631	CAP_402	mlb[6A5]	C0900	CAP_805	mlb[9D7]	C0901	CAP_805	mlb[9D6]	C0902	CAP_805	mlb[9D6]	C0903	CAP_805	mlb[9D6]	C0904	CAP_805	mlb[9D6]	C0905	CAP_805	mlb[9D5]	C0906	CAP_805	mlb[9D5]	C0907	CAP_805	mlb[9D5]	C0908	CAP_805	mlb[9D4]	C0909	CAP_805	mlb[9D4]	C0910	CAP_805	mlb[9C7]	C0911	CAP_805	mlb[9C6]	C0912	CAP_805	mlb[9C6]	C0913	CAP_805	mlb[9C6]	C0914	CAP_805	mlb[9C6]	C0915	CAP_805	mlb[9C5]	C0916	CAP_805	mlb[9C5]	C0917	CAP_805	mlb[9C5]	C0918	CAP_805	mlb[9C4]	C0919	CAP_805	mlb[9C4]	C0935	CAP_P_3P_D2T	mlb[9A7]	C0936	CAP_402	mlb[9A7]	C0937	CAP_402	mlb[9A6]	C0938	CAP_402	mlb[9A6]	C0939	CAP_402	mlb[9A6]	C0940	CAP_402	mlb[9A5]	C0941	CAP_402	mlb[9A5]	C0950	CAP_P_3P_D2T	mlb[9C7]	C0952	CAP_P_3P_D2T	mlb[9C6]	C0953	CAP_P_3P_D2T	mlb[9C6]	C0954	CAP_P_3P_D2T	mlb[9C5]	C0980	CAP_603	mlb[9B7]	C0981	CAP_402	mlb[9B7]	C1001	CAP_402	mlb[10B6]	C1002	CAP_402	mlb[10C4]	C1100	CAP_402	mlb[11A3]	C1211	CAP_402	mlb[12C3]	C1226	CAP_402	mlb[12B6]	C1236	CAP_402	mlb[12A6]	C1415	CAP_402	mlb[14C3]	C1416	CAP_402	mlb[14C2]	C1610	CAP_402	mlb[16B5]	C1611	CAP_402	mlb[16B4]	C1612	CAP_402	mlb[16B4]	C1613	CAP_402	mlb[16B8]	C1614	CAP_402	mlb[16B8]	C1615	CAP_402	mlb[16B6]	C1620	CAP_603	mlb[16B5]	C1621	CAP_603	mlb[16B5]	C1711	CAP_402	mlb[17A3]	C1712	CAP_402	mlb[17A3]	C1713	CAP_402	mlb[17B3]	C1900	CAP_P_3P_D2T	mlb[19C8]	C1902	CAP_603	mlb[19C7]	C1903	CAP_603	mlb[19C7]	C1904	CAP_402	mlb[19C7]	C1905	CAP_402	mlb[19C6]	C1906	CAP_402	mlb[19C6]	C1907	CAP_402	mlb[19C6]	C1914	CAP_603	mlb[19C5]	C1915	CAP_402	mlb[19C5]	C1916	CAP_402	mlb[19C4]	C1918	CAP_402	mlb[19C3]	C1934	CAP_805	mlb[19B7]	C1935	CAP_402	mlb[19B7]	C1936	CAP_805	mlb[19A7]	C1937	CAP_402	mlb[19A7]	C1950	CAP_402	mlb[19A8]	C1951	CAP_402	mlb[19A7]	C1952	CAP_603	mlb[19A7]	C1953	CAP_402	mlb[19A7]	C1954	CAP_402	mlb[19A6]	C1965	CAP_603	mlb[19C8]	C1966	CAP_603	mlb[19C7]	C1967	CAP_402	mlb[19C7]	C1970	CAP_P_CASE-B2	mlb[19B4]	C1971	CAP_603	mlb[19B4]	C1972	CAP_603	mlb[19B3]	C1975	CAP_603	mlb[19A3]	C1976	CAP_402	mlb[19A3]	C1990	CAP_603	mlb[19A5]	C1991	CAP_402	mlb[19A5]	C1992	CAP_603	mlb[19A4]	C1993	CAP_402	mlb[19A4]	C1994	CAP_402	mlb[19A3]	C1995	CAP_402	mlb[19A3]	C2500	CAP_P_CASE-B2	mlb[25B8]	C2501	CAP_402	mlb[25A6]	C2502	CAP_402	mlb[25D4]	C2503	CAP_402	mlb[25D8]	C2504	CAP_402	mlb[25C8]	C2505	CAP_402	mlb[25B7]	C2506	CAP_402	mlb[25B7]	C2507	CAP_402	mlb[25B7]	C2508	CAP_603	mlb[25A6]	C2509	CAP_402	mlb[25B8]	C2510	CAP_402	mlb[25C1]	C2511	CAP_402	mlb[25D6]	C2512	CAP_402	mlb[25B1]	C2513	CAP_402	mlb[25C6]	C2514	CAP_402	mlb[25C6]	C2515	CAP_402	mlb[25B6]	C2516	CAP_P_CASE-C2	mlb[25D3]	C2517	CAP_402	mlb[25D6]	C2518	CAP_402	mlb[25D4]	C2519	CAP_402	mlb[25D3]	C2520	CAP_402	mlb[25B6]	C2521	CAP_402	mlb[25C3]	C2522	CAP_402	mlb[25B3]	C2523	CAP_402	mlb[25B4]	C2524	CAP_603	mlb[25B3]	C2525	CAP_402	mlb[25B3]	C2526	CAP_402	mlb[25A4]	C2527	CAP_402	mlb[25A3]	C2528	CAP_402	mlb[25A3]	C2529	CAP_402	mlb[25A3]	C2530	CAP_402	mlb[25A3]	C2531	CAP_402	mlb[25D1]	C2532	CAP_402	mlb[25C1]	C2533	CAP_402	mlb[25C1]	C2534	CAP_402	mlb[25D1]	C2605	CAP_402	mlb[26D4]	C2607	CAP_402	mlb[26B5]	C2608	CAP_402	mlb[26C7]	C2609	CAP_402	mlb[26C7]	C2610	CAP_402	mlb[26D4]	C2611	CAP_402	mlb[26B7]	C2680	CAP_402	mlb[26B3]	C2685	CAP_402	mlb[26A3]	C2689	CAP_402	mlb[26A2]	C2800	CAP_402	mlb[28D6]	C2801	CAP_603	mlb[28D6]	C2808	CAP_603	mlb[28B2]	C2809	CAP_603	mlb[28B2]	C2810	CAP_402	mlb[28B2]	C2811	CAP_402	mlb[28B2]	C2812	CAP_402	mlb[28B1]	C2813	CAP_402	mlb[28B1]	C2814	CAP_402	mlb[28B2]	C2815	CAP_402	mlb[28B2]	C2816	CAP_402	mlb[28B1]	C2817	CAP_402	mlb[28B1]	C2818	CAP_402	mlb[28B2]	C2819	CAP_402	mlb[28B2]	C2820	CAP_402	mlb[28B1]	C2821	CAP_402	mlb[28B1]	C2900	CAP_402	mlb[29D6]	C2901	CAP_603	mlb[29D6]	C2908	CAP_603	mlb[29B2]	C2909	CAP_603	mlb[29B2]	C2910	CAP_402	mlb[29B2]	C2911	CAP_402	mlb[29B2]	C2912	CAP_402	mlb[29B1]	C2913	CAP_402	mlb[29B1]	C2914	CAP_402	mlb[29B2]	C2915	CAP_402	mlb[29B2]	C2916	CAP_402	mlb[29B1]	C2917	CAP_402	mlb[29B1]	C2918	CAP_402	mlb[29B2]	C2919	CAP_402	mlb[29B2]	C2920	CAP_402	mlb[29B1]	C2921	CAP_402	mlb[29B1]	C3000	CAP_402	mlb[30D4]	C3002	CAP_402	mlb[30D4]	C3005	CAP_402	mlb[30D4]	C3007	CAP_402	mlb[30D4]	C3010	CAP_402	mlb[30C4]	C3011	CAP_402	mlb[30C4]	C3030	CAP_402	mlb[30C4]	C3031	CAP_402	mlb[30C4]	C3032	CAP_402	mlb[30C4]	C3033	CAP_402	mlb[30C4]	C3034	CAP_402	mlb[30C4]	C3035	CAP_402	mlb[30C4]	C3036	CAP_402	mlb[30B4]	C3037	CAP_402	mlb[30B4]	C3038	CAP_402	mlb[30B4]	C3039	CAP_402	mlb[30B4]	C3050	CAP_402	mlb[30B4]	C3051	CAP_402	mlb[30B4]	C3052	CAP_402	mlb[30B4]	C3053	CAP_402	mlb[30B4]	C3054	CAP_402	mlb[30A4]	C3055	CAP_402	mlb[30A4]	C3056	CAP_402	mlb[30A4]	C3057	CAP_402	mlb[30A4]	C3058	CAP_402	mlb[30A4]	C3059	CAP_402	mlb[30A4]	C3101	CAP_603	mlb[31C5]	C3102	CAP_402	mlb[31B4]	C3104	CAP_603	mlb[31C5]	C3105	CAP_805	mlb[31B3]	C3106	CAP_805	mlb[31B3]	C3200	CAP_402	mlb[32C5]	C3205	CAP_402	mlb[32B5]	C3301	CAP_402	mlb[33D6]	C3302	CAP_402	mlb[33D6]	C3303	CAP_402	mlb[33D6]	C3304	CAP_402	mlb[33D5]	C3305	CAP_402	mlb[33D4]	C3306	CAP_402	mlb[33D4]	C3307	CAP_402	mlb[33C4]	C3308	CAP_402	mlb[33D4]	C3309	CAP_603	mlb[33B4]	C3310	CAP_402	mlb[33D3]	C3311	CAP_402	mlb[33C6]	C3312	CAP_603	mlb[33C6]	C3314	CAP_402	mlb[33D8]	C3315	CAP_402	mlb[33D6]	C3316	CAP_603	mlb[33D7]	C3317	CAP_603	mlb[33D4]	C3389	CAP_402

8			7			6			5			4			3			2			1																																																																																																																																																																																																																																																																																																																																																															
D	C7750	CAP_402	mlb[61B4]	C7751	CAP_805	mlb[61B4]	C7752	CAP_805	mlb[61B4]	C7753	CAP_402	mlb[61B7]	C7754	CAP_402	mlb[61B7]	C7755	CAP_805	mlb[61B4]	C7756	CAP_805	mlb[61B4]	C7757	CAP_402	mlb[61B6]	C7758	CAP_805	mlb[61B3]	C7759	CAP_805	mlb[61B3]	C7770	CAP_402	mlb[61B2]	C7800	CAP_402	mlb[62C7]	C7801	CAP_603	mlb[62C7]	C7802	CAP_603	mlb[62C6]	C7803	CAP_402	mlb[62C7]	C7820	CAP_402	mlb[62B2]	C7830	CAP_P_CASED2E-SM	mlb[62C4]	C7831	CAP_603	mlb[62C4]	C7832	CAP_603	mlb[62C4]	C7841	CAP_805	mlb[62B2]	C7842	CAP_P_CASE-D2E-LF	mlb[62B1]	C7843	CAP_P_CASE-D2E-LF	mlb[62B1]	C7845	CAP_402	mlb[62A5]	C7846	CAP_805	mlb[62A4]	C7847	CAP_805	mlb[62A4]	C7900	CAP_603	mlb[63D5]	C7901	CAP_603	mlb[63D5]	C7902	CAP_603	mlb[63D5]	C7906	CAP_402	mlb[63C7]	C7907	CAP_402	mlb[63C6]	C7908	CAP_402	mlb[63C6]	C7909	CAP_402	mlb[63D4]	C7920	CAP_402	mlb[63C2]	C7921	CAP_402	mlb[63C4]	C7930	CAP_P_CASED2E-SM	mlb[63D3]	C7940	CAP_805	mlb[63C2]	C7941	CAP_805	mlb[63C1]	C7942	CAP_P_CASE-C3	mlb[63C1]	C7945	CAP_402	mlb[63D1]	C7947	CAP_402	mlb[63D7]	C7948	CAP_402	mlb[63C7]	C7949	CAP_402	mlb[63C6]	C7950	CAP_603	mlb[63B6]	C7951	CAP_603	mlb[63B6]	C7952	CAP_603	mlb[63B6]	C7956	CAP_402	mlb[63A8]	C7957	CAP_402	mlb[63A7]	C7958	CAP_402	mlb[63A7]	C7959	CAP_402	mlb[63B5]	C7970	CAP_402	mlb[63A3]	C7971	CAP_402	mlb[63A5]	C7980	CAP_P_CASED2E-SM	mlb[63B4]	C7985	CAP_805	mlb[63A3]	C7986	CAP_805	mlb[63A2]	C7989	CAP_P_CASE-D2E-LF	mlb[63A2]	C7990	CAP_402	mlb[63B4]	C7991	CAP_402	mlb[63B3]	C7992	CAP_402	mlb[63B2]	C7995	CAP_402	mlb[63B2]	C7998	CAP_402	mlb[63B2]	C8000	CAP_1206-1	mlb[64D4]	C8005	CAP_402	mlb[64D3]	C8010	CAP_402	mlb[64D3]	C8015	CAP_805	mlb[64D4]	C8053	CAP_805	mlb[64B7]	C8060	CAP_402	mlb[64C3]	C8070	CAP_402	mlb[64B3]	C8071	CAP_402	mlb[64A4]	C8073	CAP_402	mlb[64A4]	C8075	CAP_402	mlb[64A5]	C8080	CAP_402	mlb[64B2]	C8081	CAP_402	mlb[64B5]	C8400	CAP_805	mlb[67C7]	C8401	CAP_402	mlb[67C7]	C8402	CAP_402	mlb[67C7]	C8405	CAP_805	mlb[67B7]	C8406	CAP_402	mlb[67B7]	C8407	CAP_402	mlb[67B7]	C8410	CAP_805	mlb[67B6]	C8411	CAP_402	mlb[67B7]	C8412	CAP_402	mlb[67B7]	C8413	CAP_402	mlb[67B7]	C8420	CAP_402	mlb[67D5]	C8421	CAP_402	mlb[67D5]	C8422	CAP_402	mlb[67D5]	C8423	CAP_402	mlb[67D5]	C8424	CAP_402	mlb[67D5]	C8425	CAP_402	mlb[67D5]	C8426	CAP_402	mlb[67D5]	C8427	CAP_402	mlb[67D5]	C8428	CAP_402	mlb[67D5]	C8429	CAP_402	mlb[67C5]	C8430	CAP_402	mlb[67C5]	C8431	CAP_402	mlb[67C5]	C8432	CAP_402	mlb[67C5]	C8433	CAP_402	mlb[67C5]	C8434	CAP_402	mlb[67C5]	C8435	CAP_402	mlb[67C5]	C8436	CAP_402	mlb[67C5]	C8437	CAP_402	mlb[67C5]	C8438	CAP_402	mlb[67C5]	C8439	CAP_402	mlb[67C5]	C8440	CAP_402	mlb[67B5]	C8441	CAP_402	mlb[67B5]	C8442	CAP_402	mlb[67B5]	C8443	CAP_402	mlb[67B5]	C8444	CAP_402	mlb[67B5]	C8445	CAP_402	mlb[67B5]	C8446	CAP_402	mlb[67B5]	C8447	CAP_402	mlb[67B5]	C8448	CAP_402	mlb[67B5]	C8449	CAP_402	mlb[67B5]	C8450	CAP_402	mlb[67B5]	C8451	CAP_402	mlb[67B5]	C8455	CAP_402	mlb[67D2]	C8456	CAP_402	mlb[67D2]	C8457	CAP_402	mlb[67D2]	C8458	CAP_402	mlb[67D2]	C8459	CAP_402	mlb[67D2]	C8460	CAP_402	mlb[67D2]	C8461	CAP_402	mlb[67D2]	C8462	CAP_402	mlb[67D2]	C8463	CAP_402	mlb[67D2]	C8464	CAP_402	mlb[67C2]
	C8465	CAP_402	mlb[67C2]	C8466	CAP_402	mlb[67C2]	C8467	CAP_402	mlb[67C2]	C8468	CAP_402	mlb[67C2]	C8469	CAP_402	mlb[67C2]	C8470	CAP_402	mlb[67C2]	C8471	CAP_402	mlb[67C2]	C8472	CAP_402	mlb[67C2]	C8473	CAP_402	mlb[67C2]	C8474	CAP_402	mlb[67C2]	C8475	CAP_402	mlb[67C2]	C8476	CAP_402	mlb[67B2]	C8477	CAP_402	mlb[67B2]	C8478	CAP_402	mlb[67B2]	C8479	CAP_402	mlb[67B2]	C8480	CAP_402	mlb[67B2]	C8481	CAP_805	mlb[67B2]	C8482	CAP_402	mlb[67B2]	C8483	CAP_402	mlb[67B2]	C8484	CAP_402	mlb[67B2]	C8485	CAP_402	mlb[67B2]	C8486	CAP_402	mlb[67B2]	C8500	CAP_603	mlb[68D6]	C8501	CAP_603	mlb[68D6]	C8502	CAP_603	mlb[68D6]	C8506	CAP_402	mlb[68C8]	C8507	CAP_402	mlb[68C7]	C8508	CAP_402	mlb[68C7]	C8509	CAP_402	mlb[68D5]	C8520	CAP_402	mlb[68C2]	C8521	CAP_402	mlb[68C4]	C8522	CAP_402	mlb[68C5]	C8523	CAP_402	mlb[68B2]	C8530	CAP_P_CASED2E-SM	mlb[68D4]	C8540	CAP_805	mlb[68C2]	C8541	CAP_805	mlb[68C2]	C8542	CAP_P_CASE-D2E-LF	mlb[68C2]	C8543	CAP_P_CASE-D2E-LF	mlb[68C2]	C8551	CAP_603	mlb[68B8]	C8555	CAP_402	mlb[68B7]	C8556	CAP_805	mlb[68B6]	C8557	CAP_805	mlb[68B6]	C8570	CAP_402	mlb[68A5]	C8580	CAP_603	mlb[68A4]	C8581	CAP_603	mlb[68A4]	C8589	CAP_805	mlb[68A3]	C8590	CAP_402	mlb[68D3]	C8591	CAP_402	mlb[68D3]	C8592	CAP_402	mlb[68C2]	C8595	CAP_402	mlb[68D2]	C8598	CAP_402	mlb[68D2]	C8600	CAP_805	mlb[69C7]	C8601	CAP_805	mlb[69C7]	C8604	CAP_402	mlb[69C7]	C8605	CAP_402	mlb[69C6]	C8606	CAP_402	mlb[69C6]	C8607	CAP_402	mlb[69C6]	C8608	CAP_402	mlb[69C5]	C8609	CAP_402	mlb[69C5]	C8610	CAP_402	mlb[69C5]	C8611	CAP_402	mlb[69C7]	C8612	CAP_402	mlb[69C6]	C8613	CAP_402	mlb[69C6]	C8614	CAP_402	mlb[69C6]	C8615	CAP_402	mlb[69C5]	C8616	CAP_402	mlb[69C5]	C8630	CAP_805	mlb[69C6]	C8631	CAP_402	mlb[69C6]	C8632	CAP_402	mlb[69C5]	C8633	CAP_402	mlb[69C5]	C8634	CAP_402	mlb[69C5]	C8650	CAP_805	mlb[69B7]	C8651	CAP_805	mlb[69B7]	C8652	CAP_805	mlb[69B7]	C8653	CAP_805	mlb[69B6]	C8655	CAP_402	mlb[69B6]	C8656	CAP_402	mlb[69B6]	C8657	CAP_402	mlb[69B6]	C8658	CAP_402	mlb[69B5]	C8659	CAP_402	mlb[69B5]	C8660	CAP_402	mlb[69B5]	C8661	CAP_402	mlb[69B6]	C8662	CAP_402	mlb[69B6]	C8663	CAP_402	mlb[69B6]	C8664	CAP_402	mlb[69B5]	C8665	CAP_402	mlb[69B5]	C8666	CAP_402	mlb[69B5]	C8667	CAP_402	mlb[69B6]	C8668	CAP_402	mlb[69B6]	C8669	CAP_402	mlb[69B6]	C8670	CAP_402	mlb[69B5]	C8671	CAP_402	mlb[69B5]	C8672	CAP_402	mlb[69B5]	C8673	CAP_402	mlb[69B6]	C8674	CAP_402	mlb[69B6]	C8675	CAP_402	mlb[69B6]	C8676	CAP_402	mlb[69B5]	C8677	CAP_402	mlb[69B5]	C8678	CAP_402	mlb[69B5]	C8679	CAP_402	mlb[69A6]	C8680	CAP_402	mlb[69A6]	C8681	CAP_402	mlb[69A6]	C8682	CAP_402	mlb[69A5]	C8683	CAP_402	mlb[69A5]	C8690	CAP_805	mlb[69D5]	C8691	CAP_402	mlb[69D5]	C8692	CAP_402	mlb[69D5]	C8695	CAP_805	mlb[69D2]	C8696	CAP_402	mlb[69D3]	C8697	CAP_402	mlb[69D3]	C8711	CAP_402	mlb[70B7]	C8713	CAP_402	mlb[70B4]	C8715	CAP_402	mlb[70A7]	C8716	CAP_402	mlb[70A6]	C8721	CAP_402	mlb[70B4]	C8723	CAP_402	mlb[70B4]	C8725	CAP_402	mlb[70A4]	C8726	CAP_402	mlb[70A3]	C8900	CAP_805	mlb[72D7]	C8901	CAP_402	mlb[72D7]	C8902	CAP_402	mlb[72D7]	C8903	CAP_402	mlb[72D7]	C8904	CAP_402	mlb[72D6]	C8910	CAP_402	mlb[72D7]
	C8915	CAP_402	mlb[72D6]	C8920	CAP_805	mlb[72C8]	C8921	CAP_402	mlb[72C8]	C8922	CAP_402	mlb[72C7]	C8923	CAP_402	mlb[72C7]	C8924	CAP_402	mlb[72C7]	C8925	CAP_402	mlb[72C7]	C8926	CAP_402	mlb[72C6]	C8931	CAP_402	mlb[72C7]	C8933	CAP_402	mlb[72C6]	C8950	CAP_805	mlb[72D4]	C8951	CAP_402	mlb[72D4]	C8952	CAP_402	mlb[72D4]	C8953	CAP_402	mlb[72D3]	C8954	CAP_402	mlb[72D3]	C8960	CAP_402	mlb[72D3]	C8965	CAP_402	mlb[72D3]	C8970	CAP_805	mlb[72C5]	C8971	CAP_402	mlb[72C4]	C8972	CAP_402	mlb[72C4]	C8973	CAP_402	mlb[72C4]	C8974	CAP_402	mlb[72C4]	C8975	CAP_402	mlb[72C3]	C8976	CAP_402	mlb[72C3]	C8981	CAP_402	mlb[72C3]	C8983	CAP_402	mlb[72C3]	C9000	CAP_805	mlb[73D7]	C9001	CAP_402	mlb[73D7]	C9002	CAP_402	mlb[73D7]	C9003	CAP_402	mlb[73D7]	C9004	CAP_402	mlb[73D6]	C9010	CAP_402	mlb[73D7]	C9015	CAP_402	mlb[73D6]	C9020	CAP_805	mlb[73C8]	C9021	CAP_402	mlb[73C8]	C9022	CAP_402	mlb[73C7]	C9023	CAP_402	mlb[73C7]	C9024	CAP_402	mlb[73C7]	C9025	CAP_402	mlb[73C7]	C9026	CAP_402	mlb[73C6]	C9031	CAP_402	mlb[73C7]	C9033	CAP_402	mlb[73C6]	C9050	CAP_805	mlb[73D4]	C9051	CAP_402	mlb[73D4]	C9052	CAP_402	mlb[73D4]	C9053	CAP_402	mlb[73D3]	C9054	CAP_402	mlb[73D3]	C9060	CAP_402	mlb[73D3]	C9065	CAP_402	mlb[73D3]	C9070	CAP_805	mlb[73C5]	C9071	CAP_402	mlb[73C4]	C9072	CAP_402	mlb[73C4]	C9073	CAP_402	mlb[73C4]	C9074	CAP_402	mlb[73C4]	C9075	CAP_402	mlb[73C3]	C9076	CAP_402	mlb[73C3]	C9081	CAP_402	mlb[73C3]	C9083	CAP_402	mlb[73C3]	C9100	CAP_805	mlb[74C5]	C9101	CAP_402	mlb[74C5]	C9102	CAP_402	mlb[74C5]	C9103	CAP_402	mlb[74C5]	C9110	CAP_805	mlb[74C5]	C9111	CAP_402	mlb[74C5]	C9112	CAP_402	mlb[74C5]	C9115	CAP_805	mlb[74B5]	C9116	CAP_402	mlb[74B5]	C9117	CAP_402	mlb[74B5]	C9120	CAP_805	mlb[74B5]	C9121	CAP_402	mlb[74B5]	C9122	CAP_402	mlb[74B5]	C9125	CAP_805	mlb[74B5]	C9126	CAP_402	mlb[74B5]	C9127	CAP_402	mlb[74B5]	C9130	CAP_805	mlb[74B5]	C9131	CAP_402	mlb[74B6]	C9132	CAP_402	mlb[74B5]	C9135	CAP_805	mlb[74A6]	C9136	CAP_402	mlb[74A6]	C9137	CAP_402	mlb[74A5]	C9140	CAP_805	mlb[74A6]	C9141	CAP_402	mlb[74A6]	C9142	CAP_402	mlb[74A5]	C9191	CAP_402	mlb[74D2]	C9300	CAP_805	mlb[75C6]	C9301	CAP_402	mlb[75C6]	C9302	CAP_402	mlb[75C5]	C9305	CAP_805	mlb[75C6]	C9306	CAP_402	mlb[75C6]	C9307	CAP_402	mlb[75C5]	C9310	CAP_805	mlb[75C6]	C9311	CAP_402	mlb[75C6]	C9312	CAP_402	mlb[75C5]	C9315	CAP_805	mlb[75B8]	C9316	CAP_402	mlb[75B8]	C9317	CAP_402	mlb[75B7]	C9320	CAP_805	mlb[75B6]	C9321	CAP_402	mlb[75B6]	C9322	CAP_402	mlb[75B5]	C9325	CAP_805	mlb[75B8]	C9326	CAP_402	mlb[75B8]	C9327	CAP_402	mlb[75B7]	C9330	CAP_805	mlb[75B6]	C9331	CAP_402	mlb[75B6]	C9332	CAP_402	mlb[75B5]	C9340	CAP_805	mlb[75A6]	C9341	CAP_402	mlb[75A6]	C9342	CAP_402	mlb[75A5]	C9345	CAP_805	mlb[75A6]	C9346	CAP_402	mlb[75A5]	C9347	CAP_402	mlb[75A5]	C9400	CAP_805	mlb[76D4]	C9401	CAP_402	mlb[76D2]	C9410	CAP_402	mlb[76C2]	C9420	CAP_402	mlb[76D1]	C9421	CAP_402	mlb[76B1]	C9450	CAP_402	mlb[76B6]	C9451	CAP_603	mlb[76B6]	C9452	CAP_402	mlb[76B5]	C9453	CAP_402	mlb[76A7]	C9454	CAP_402	mlb[76A6]	C9503	CAP_603	mlb[77A3]	C9711	CAP_402	mlb[77B3]	C9713	CAP_402	mlb[77B3]
	C9714	CAP_402	mlb[77A3]	C9740	CAP_402	mlb[77C1]	C9741	CAP_402	mlb[77C1]	C9742	CAP_402	mlb[77C1]	C9750	CAP_402	mlb[77D5]	C9751	CAP_402	mlb[77C5]	C9950	CAP_402	mlb[79D2]	C9960	CAP_402	mlb[79B2]	C9961	CAP_402	mlb[79A4]	C9980	CAP_402	mlb[79C4]	C9985	CAP_402	mlb[79C6]	C9990	CAP_402	mlb[79D5]	C9991	CAP_402	mlb[79D4]	C9992	CAP_402	mlb[79C5]	C9993	CAP_402	mlb[79C6]	C9995	CAP_402																																																																																																																																																																																																																																																																																																																																					

8			7			6			5			4			3			2			1		
D	L6450	IND_3.8x3.8x1.5MM	mlb[55B4]	Q8056	TRA_2N7002DW_SOT-363	mlb[64D6]	R2388	RES_402	mlb[23A3]	R3879	RES_402	mlb[37A6]	D										
	L7505	IND_SM-IHLP	mlb[59D2]	Q8057	TRA_2N7002DW_SOT-363	mlb[64C7 64C7]	R2395	RES_402	mlb[23D7]	R3900	RES_402	mlb[37B7]											
	L7555	IND_SM-IHLP	mlb[59C2]	Q8058	TRA_2N7002DW_SOT-363	mlb[64C6 64C5]	R2396	RES_402	mlb[23D6]	R3901	RES_402	mlb[37D4]											
	L7620	IND_SM-IHLP	mlb[60C7]	Q8059	TRA_2N7002DW_SOT-363	mlb[64A7 64A6]	R2397	RES_402	mlb[23D6]	R3902	RES_402	mlb[37D4]											
	L7660	IND_IHLP2525CZ-SM	mlb[60C2]	Q8520	TRA_RJK_LFPAK	mlb[68D4]	R2398	RES_402	mlb[23D8]	R3903	RES_402	mlb[37C3]											
	L7700	IND_SM-MSS5131	mlb[61D5]	Q8521	TRA_RJK_LFPAK	mlb[68C4]	R2399	RES_402	mlb[23C1]	R3904	RES_402	mlb[37C3]											
	L7750	IND_SM-LF	mlb[61B4]	Q8522	TRA_RJK_LFPAK	mlb[68C5]	R2500	RES_603	mlb[25A8]	R3910	RES_402	mlb[37C2]											
	L7820	IND_FDA1055	mlb[62C3]	Q8523	TRA_2N7002DW_SOT-363	mlb[68B2 68B3]	R2501	RES_402	mlb[25C8]	R3977	RES_402	mlb[37A6]											
	L7920	IND_IHLP	mlb[63C3]	Q8554	TRA_2N7002_SOT23-LF	mlb[68A8]	R2502	RES_402	mlb[25D8]	R3979	RES_402	mlb[37A7]											
	L7970	IND_SM-IHLP	mlb[63B4]	Q8570	TRA_2N7002_SOT23-LF	mlb[68A6]	R2600	RES_402	mlb[26D4]	R3980	RES_402	mlb[37B4]											
C	L8010	IND_CDPH4D19F-SM	mlb[64D3]	Q8575	TRA_SI3446DV_TSOP-LF	mlb[68B6]	R2606	RES_402	mlb[26D5]	R3990	RES_402	mlb[37B3]	C										
	L8400	IND_0402	mlb[67C6]	Q9400	TRA_SI3443DV_TSOP-LF	mlb[76D3]	R2607	RES_402	mlb[26D6]	R3991	RES_402	mlb[37B3]											
	L8520	IND_FDA1055	mlb[68C3]	Q9401	TRA_2N7002_SOT23-LF	mlb[76D4]	R2609	RES_402	mlb[26C7]	R4000	RES_402	mlb[38D6]											
	L8715	IND_0402	mlb[70A7]	Q9450	TRA_DUAL_MOSFET_NPCH	mlb[76B7 76B7]	R2610	RES_402	mlb[26C7]	R4020	RES_402	mlb[38C6]											
	L8725	IND_0402	mlb[70A4]		N2_SC70-6		R2611	RES_402	mlb[26B5]	R4035	RES_402	mlb[38D4]											
	L8910	IND_0402	mlb[72D8]	Q9711	TRA_2N7002DW_SOT-363	mlb[77B2 77B2]	R2612	RES_402	mlb[26A5]	R4040	RES_402	mlb[38C7]											
	L8915	IND_0402	mlb[72D8]	Q9714	TRA_2N7002DW_SOT-363	mlb[77A2 77A2]	R2622	RES_402	mlb[26A5]	R4042	RES_402	mlb[38C7]											
	L8960	IND_0402	mlb[72D4]	Q9715	TRA_2N7002DW_SOT-363	mlb[77A2]	R2623	RES_402	mlb[26D1]	R4045	RES_402	mlb[38C6]											
	L8965	IND_0402	mlb[72D4]	Q9970	TRA_2N7002DW_SOT-363	mlb[79A6 79A6]	R2624	RES_402	mlb[26D1]	R4055	RES_402	mlb[38B6]											
	L9010	IND_0402	mlb[73D8]	R0600	RES_402	mlb[6C4]	R2625	RES_402	mlb[26D1]	R4056	RES_402	mlb[38C6]											
B	L9015	IND_0402	mlb[73D8]	R0601	RES_402	mlb[6A8]	R2626	RES_402	mlb[26D1]	R4061	RES_402	mlb[38C3]	B										
	L9060	IND_0402	mlb[73D4]	R0702	RES_402	mlb[7D5]	R2627	RES_402	mlb[26D1]	R4062	RES_402	mlb[38B3]											
	L9065	IND_0402	mlb[73D4]	R0703	RES_402	mlb[7C5]	R2628	RES_402	mlb[26D1]	R4063	RES_402	mlb[38A4]											
	L9120	IND_0402	mlb[74B6]	R0704	RES_402	mlb[7C5]	R2629	RES_402	mlb[26D1]	R4080	RES_402	mlb[38B3]											
	L9125	IND_0402	mlb[74B6]	R0705	RES_402	mlb[7B4]	R2630	RES_402	mlb[26D1]	R4086	RES_402	mlb[38B2]											
	L9130	IND_0402	mlb[74B7]	R0706	RES_402	mlb[7B4]	R2631	RES_402	mlb[26D1]	R4090	RES_402	mlb[38C4]											
	L9135	IND_0402	mlb[74A7]	R0707	RES_402	mlb[7A4]	R2632	RES_402	mlb[26D1]	R4091	RES_402	mlb[38C3]											
	L9140	IND_0402	mlb[74A7]	R0712	RES_402	mlb[7A4]	R2633	RES_402	mlb[26D1]	R4101	RES_402	mlb[39B7]											
	L9300	IND_0402	mlb[75C7]	R0716	RES_402	mlb[7B2]	R2634	RES_402	mlb[26D1]	R4102	RES_402	mlb[39C8]											
	L9305	IND_0402	mlb[75C7]	R0717	RES_402	mlb[7B2]	R2636	RES_402	mlb[26D1]	R4103	RES_402	mlb[39B4]											
A	L9310	IND_0402	mlb[75C7]	R0718	RES_402	mlb[7B2]	R2637	RES_402	mlb[26D1]	R4104	RES_402	mlb[39B4]	A										
	L9315	IND_0402	mlb[75C7]	R0719	RES_402	mlb[7B2]	R2638	RES_402	mlb[26D1]	R4105	RES_402	mlb[39B4]											
	L9320	IND_0402	mlb[75B7]	R0720	RES_402	mlb[7B7]	R2639	RES_402	mlb[26D1]	R4106	RES_402	mlb[39B5]											
	L9325	IND_0402	mlb[75B7]	R0721	RES_402	mlb[7B7]	R2640	RES_402	mlb[26D1]	R4117	RES_402	mlb[39B3]											
	L9330	IND_0402	mlb[75B7]	R0722	RES_402	mlb[7A7]	R2642	RES_402	mlb[26C1]	R4118	RES_402	mlb[39B3]											
	L9345	IND_0402	mlb[75B7]	R0730	RES_402	mlb[7A4]	R2680	RES_402	mlb[26B3]	R4119	RES_402	mlb[39B3]											
	L9400	IND_SM	mlb[76D2]	R0802	RES_402	mlb[8B6]	R2681	RES_402	mlb[26B2]	R4120	RES_402	mlb[39B4]											
	L9450	IND_0603	mlb[76B6]	R0803	RES_402	mlb[8A7]	R2682	RES_402	mlb[26B2]	R4122	RES_402	mlb[39A3]											
	L9452	IND_SM-1	mlb[76B6]	R1001	RES_402	mlb[10B6]	R2683	RES_402	mlb[26B2]	R4123	RES_402	mlb[39A2]											
	L9454	IND_SM-1	mlb[76A6]	R1002	RES_402	mlb[10B6]	R2684	RES_402	mlb[26B2]	R4130	RES_402	mlb[39B6]											

8			7			6			5			4			3			2			1				
D	R5965	RES_402	mlb[50A4]	R7672	RES_402	mlb[60A7]	R8731	RES_402	mlb[70A3]	R9994	RES_402	mlb[79D6]	R9994	RES_402	mlb[79D6]	D									
	R5970	RES_402	mlb[50D2]	R7674	RES_402	mlb[60A7]	R8732	RES_402	mlb[70A3]	R9995	RES_402	mlb[79C6]	R9995	RES_402	mlb[79C6]										
	R5971	RES_402	mlb[50D2]	R7675	RES_1206	mlb[60A7]	R8733	RES_402	mlb[70A1]	R9996	RES_402	mlb[79C5]	R9996	RES_402	mlb[79C5]										
	R5980	RES_402	mlb[50B1]	R7700	RES_402	mlb[61D7]	R8800	RES_402	mlb[71D5]	R9997	RES_402	mlb[79D5]	R9997	RES_402	mlb[79D5]										
	R5981	RES_402	mlb[50B1]	R7707	RES_402	mlb[61C4]	R8801	RES_402	mlb[71D5]	RP0990	RP4K4P_SM-LF	mlb[9C2]	RP0990	RP4K4P_SM-LF	mlb[9C2]										
	R5982	RES_402	mlb[50B1]	R7708	RES_402	mlb[61C4]	R8802	RES_402	mlb[71D5]	RP0991	RP4K4P_SM-LF	mlb[9C2]	RP0991	RP4K4P_SM-LF	mlb[9C2]										
	R5983	RES_402	mlb[50A1]	R7720	RES_402	mlb[61D2]	R8803	RES_402	mlb[71D5]	RP2300	RP4K4P_SM-LF	mlb[23D5]	RP2300	RP4K4P_SM-LF	mlb[23D5]										
	R5984	RES_402	mlb[50A1]	R7721	RES_402	mlb[61C2]	R8804	RES_402	mlb[71D5]	RP3005	RP4K4P_SM-LF	mlb[30C4 30B4 30B4 30A4]	RP3005	RP4K4P_SM-LF	mlb[30C4 30B4 30B4 30A4]										
	R5985	RES_402	mlb[50A1]	R7750	RES_402	mlb[61B4]	R8805	RES_402	mlb[71D4]	RP3010	RP4K4P_SM-LF	mlb[30B4 30B4 30B4 30C4]	RP3010	RP4K4P_SM-LF	mlb[30B4 30B4 30B4 30C4]										
	R5990	RES_402	mlb[50C4]	R7751	RES_402	mlb[61A4]	R8806	RES_402	mlb[71D4]	RP3030	RP4K4P_SM-LF	mlb[30C4 30C4 30C4 30C4]	RP3030	RP4K4P_SM-LF	mlb[30C4 30C4 30C4 30C4]										
C	R5991	RES_402	mlb[50C4]	R7752	RES_402	mlb[61A4]	R8808	RES_402	mlb[71D4]	RP3032	RP4K4P_SM-LF	mlb[30C4 30C4 30D4 30D4]	RP3032	RP4K4P_SM-LF	mlb[30C4 30C4 30D4 30D4]	C									
	R5992	RES_402	mlb[50B4]	R7753	RES_402	mlb[61B7]	R8809	RES_402	mlb[71D4]	RP3034	RP4K4P_SM-LF	mlb[30C4 30C4 30C4 30C4]	RP3034	RP4K4P_SM-LF	mlb[30C4 30C4 30C4 30C4]										
	R5993	RES_402	mlb[50B4]	R7754	RES_402	mlb[61B6]	R8811	RES_402	mlb[71D3]	RP3036	RP4K4P_SM-LF	mlb[30C4 30B4 30C4 30B4]	RP3036	RP4K4P_SM-LF	mlb[30C4 30B4 30C4 30B4]										
	R5994	RES_402	mlb[50B4]	R7755	RES_402	mlb[61B6]	R8812	RES_402	mlb[71D3]	RP3050	RP4K4P_SM-LF	mlb[30B4 30B4 30B4 30A4]	RP3050	RP4K4P_SM-LF	mlb[30B4 30B4 30B4 30A4]										
	R5995	RES_402	mlb[50B4]	R7756	RES_402	mlb[61B6]	R8813	RES_402	mlb[71D4]	RP3052	RP4K4P_SM-LF	mlb[30A4 30B4 30B4 30A4]	RP3052	RP4K4P_SM-LF	mlb[30A4 30B4 30B4 30A4]										
	R5996	RES_402	mlb[50B4]	R7757	RES_402	mlb[61B7]	R8824	RES_402	mlb[71D3]	RP3054	RP4K4P_SM-LF	mlb[30B4 30B4 30B4 30A4]	RP3054	RP4K4P_SM-LF	mlb[30B4 30B4 30B4 30A4]										
	R6100	RES_402	mlb[52D4]	R7770	RES_402	mlb[61B2]	R8827	RES_402	mlb[71D3]	RP3056	RP4K4P_SM-LF	mlb[30A4 30A4 30A4 30A4]	RP3056	RP4K4P_SM-LF	mlb[30A4 30A4 30A4 30A4]										
	R6151	RES_402	mlb[52B4]	R7801	RES_402	mlb[62C7]	R8890	RES_402	mlb[71A2]	RP3058	RP4K4P_SM-LF	mlb[30B4 30B4 30D4 30D4]	RP3058	RP4K4P_SM-LF	mlb[30B4 30B4 30D4 30D4]										
	R6152	RES_402	mlb[52B3]	R7803	RES_402	mlb[62C7]	R8891	RES_402	mlb[71A1]	RP9900	RP4K4P_SM-LF	mlb[79C7 79D7 79D7 79D7]	RP9900	RP4K4P_SM-LF	mlb[79C7 79D7 79D7 79D7]										
	R6160	RES_402	mlb[52B5]	R7804	RES_402	mlb[62B7]	R8930	RES_402	mlb[72C7]	RP9901	RP4K4P_SM-LF	mlb[79C7 79C7 79C7 79C7]	RP9901	RP4K4P_SM-LF	mlb[79C7 79C7 79C7 79C7]										
B	R6161	RES_402	mlb[52B5]	R7821	RES_402	mlb[62B3]	R8931	RES_402	mlb[72C7]	RP9902	RP4K4P_SM-LF	mlb[79B7 79B7 79C7 79C7]	RP9902	RP4K4P_SM-LF	mlb[79B7 79B7 79C7 79C7]	B									
	R6209	RES_402	mlb[53D6]	R7822	RES_402	mlb[62B3]	R8932	RES_402	mlb[72C7]	RP9903	RP4K4P_SM-LF	mlb[79B7 79B7 79B7 79B7]	RP9903	RP4K4P_SM-LF	mlb[79B7 79B7 79B7 79B7]										
	R6220	RES_1206	mlb[53A5]	R7845	RES_402	mlb[62A5]	R8933	RES_402	mlb[72C7]	SH0600	SHLD_3P_EMI_SHLD-SM-LF	mlb[6A8]	SH0600	SHLD_3P_EMI_SHLD-SM-LF	mlb[6A8]										
	R6221	RES_1206	mlb[53A4]	R7846	RES_402	mlb[62A5]	R8940	RES_402	mlb[72B8]	T4200	XFR_1000BT_82400275_	mlb[40D4]	T4200	XFR-SM											
	R6223	RES_1206	mlb[53A3]	R7904	RES_402	mlb[63D6]	R8941	RES_402	mlb[72B8]	T4201	XFR_1000BT_82400275_	mlb[40C4]	T4201	XFR-SM											
	R6227	RES_402	mlb[53A8]	R7905	RES_402	mlb[63C6]	R8942	RES_402	mlb[72B7]	U0700	CPU_YONAH_BGA	mlb[7C3 7D7]	U0700	CPU_YONAH_BGA	mlb[8D8 8D4]										
	R6228	RES_402	mlb[53A7]	R7906	RES_402	mlb[63C7]	R8943	RES_402	mlb[72B7]	U1001	TMF401_MSOP	mlb[10C5]	U1001	TMF401_MSOP	mlb[10C5]										
	R6229	RES_402	mlb[53A7]	R7908	RES_402	mlb[63C6]	R8944	RES_402	mlb[72B7]	U1200	NB_945GM_BGA	mlb[12D5]	U1200	NB_945GM_BGA	mlb[12D5]										
	R6235	RES_402	mlb[53B4]	R7909	RES_402	mlb[63D4]	R8945	RES_402	mlb[72B7]	U1200	NB_945GM_BGA	mlb[13D4]	U1200	NB_945GM_BGA	mlb[13D4]										
	R6240	RES_402	mlb[53B2]	R7910	RES_402	mlb[63C4]	R8946	RES_402	mlb[72B7]	U1200	NB_945GM_BGA	mlb[14D5]	U1200	NB_945GM_BGA	mlb[14D5]										
A	R6259	RES_402	mlb[53C6]	R7920	RES_402	mlb[63C2]	R8947	RES_402	mlb[72B7]	U1200	NB_945GM_BGA	mlb[15D3 15D7]	U1200	NB_945GM_BGA	mlb[15D3 15D7]	A									
	R6270	RES_402	mlb[53B7]	R7921	RES_402	mlb[63C2]	R8948	RES_402	mlb[72A7]	U1200	NB_945GM_BGA	mlb[16D2 16C8]	U1200	NB_945GM_BGA	mlb[16D2 16C8]										
	R6275	RES_402	mlb[53B6]	R7922	RES_402	mlb[63C2]	R8949	RES_402	mlb[72A7]	U1200	NB_945GM_BGA	mlb[17D5]	U1200	NB_945GM_BGA	mlb[17D5]										
	R6280	RES_402	mlb[53C4]	R7945	RES_402	mlb[63D2]	R8980	RES_402	mlb[72C4]	U1200	NB_945GM_BGA	mlb[18D4 18D7]	U1200	NB_945GM_BGA	mlb[18D4 18D7]										
	R6285	RES_402	mlb[53D2]	R7947	RES_402	mlb[63D8]	R8981	RES_402	mlb[72C4]	U1900	LREG_TPS73115_SOT23-5	mlb[19A8]	U1900	LREG_TPS73115_SOT23-5	mlb[19A8]										
	R6286	RES_402	mlb[53C2]	R7948	RES_402	mlb[63C8]	R8982	RES_402	mlb[72C4]	U2100	SB_1CH7M_BGA	mlb[21D6]	U2100	SB_1CH7M_BGA	mlb[21D6]										
	R6290	RES_402	mlb[53C2]	R7949	RES_402	mlb[63D7]	R8983	RES_402	mlb[72C4]	U2100	SB_1CH7M_BGA	mlb[22B7 22D3]	U2100	SB_1CH7M_BGA	mlb[22B7 22D3]										
	R6301	RES_402	mlb[54D4]	R7954	RES_402	mlb[63B8]	R8990	RES_402	mlb[72B5]	U2100	SB_1CH7M_BGA	mlb[23D4]	U2100	SB_1CH7M_BGA	mlb[23D4]										
	R6302	RES_402	mlb[54D4]	R7955	RES_402	mlb[63A8]	R8991	RES_402	mlb[72B4]	U2100	SB_1CH7M_BGA	mlb[24D4 24D7]	U2100	SB_1CH7M_BGA	mlb[24D4 24D7]										
	R6303	RES_402	mlb[54C3]	R7956	RES_402	mlb[63A8]	R8992	RES_402	mlb[72B4]	U2100	SB_1CH7M_BGA	mlb[25D3]	U2100	SB_1CH7M_BGA	mlb[25D3]										

113

