

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

SCHEMATIC, MLB, "Kriolo-S"

REV

ZONE

ECN

DESCRIPTION OF CHANGE

CK
APPD

ENG
APPD

1



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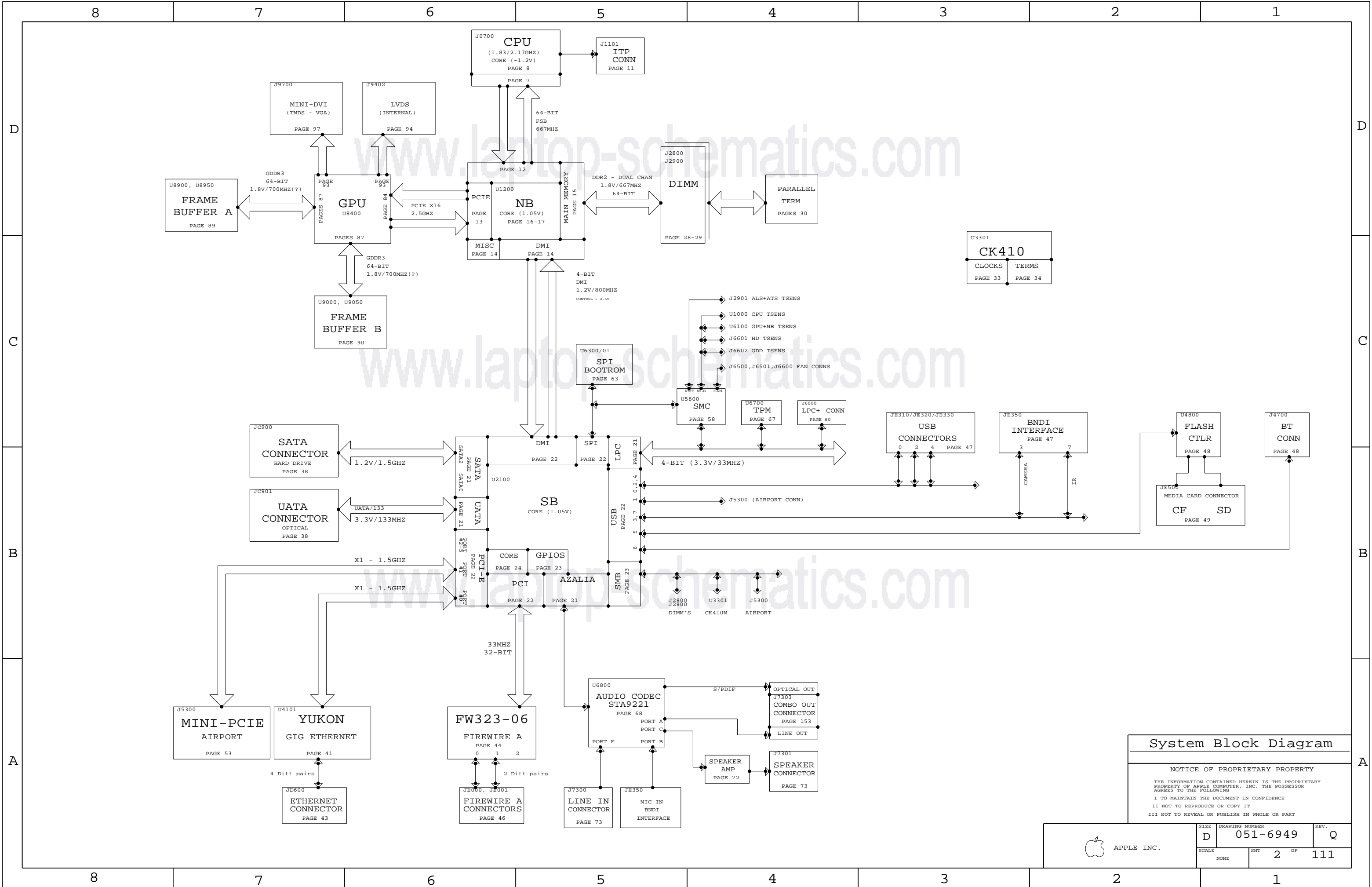
DATE _____

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9	MS	JD 9	CPU - DECAPS
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21	JD	JD 21	SB - RTC, LAN, AUDIO, ATA, CPU, LPC
22	JD	JD 22	SB - PCIE, SPI, USB, DMI, PCI
23	JD	JD 23	SB - SMB, GPIO, PM, CLKS
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25	JD	JD 25	SB - DECAPS
26	JD	JD 26	SB - MISC
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59	MS	46	SMC - SMB BUSSES, MISC
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65	MS	MS 50	SMC - FANS
66	MS	MS 51	SMC - FANS
67	JD	JD 52	SMC - TPM
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SO 72	PT	JD 54	AUDIO - INTERNAL SPEAKER AMP
SO 73	PT	JD 55	AUDIO - I/O CONN'S,EMC
SO 74	PT	JD 56	AUDIO - DETECT TRANSLATORS
RP 75	RT	RT 57	VR - CPU CORE
RP 76	RT	RT 58	VR - CPU I-V SENSE CKT
RP 77	RT	RT 59	VR - "S0" 1.2V & 2.5V (GRAFIX)
RP 78	RT	RT 60	VR - "S0" 1.8V
RP 79	RT	RT 61	VR - "S3" 1.8V
RP 80	RT	RT 62	VR - "S0" 1.5V
RP 81	RT	RT 63	VR - "S0" 1.05V
RP 83	RT	RT 64	VR - "S3" 3.3V AND 5V
JH 84	JH	JH 65	GPU - M56 PCI-E
M1 85	JH	JH 66	GPU - VCORE SUPPLY
M1 86	JH	JH 67	GPU - M56 CORE PWR
M1 87	JH	JH 68	GPU - M56 FRAME BUFFER
M1 88	JH	JH 69	GPU - MISC
M1 89	JH	JH 70	GPU - GDDR SDRAM A
M1 90	JH	JH 71	GPU - GDDR SDRAM B
M1 91	JH	JH 72	GPU - M56 GPIO,DVO,MISC
M1 92	JH	JH 73	GPU - M56 CLOCKS
M1 93	JH	JH 74	GPU - M56 VIDEO INTERFACES
JH 94	JH	JH 75	GPU - INTERNAL DISPLAY CONN'S
JH 95	JH	JH 76	GPU - TP'S
JH 96	JH	JH 77	GPU - TMDS,INVERTER,EXT VGA
JH 97	JH	JH 78	GPU - EXTERNAL DISPLAY CONN'S

2 / 7 / 08

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		RELEASE _____ / _____ SCALE _____ NONE			
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				REV. Q <div>SHT 1 OF 111</div>	



System Block Diagram

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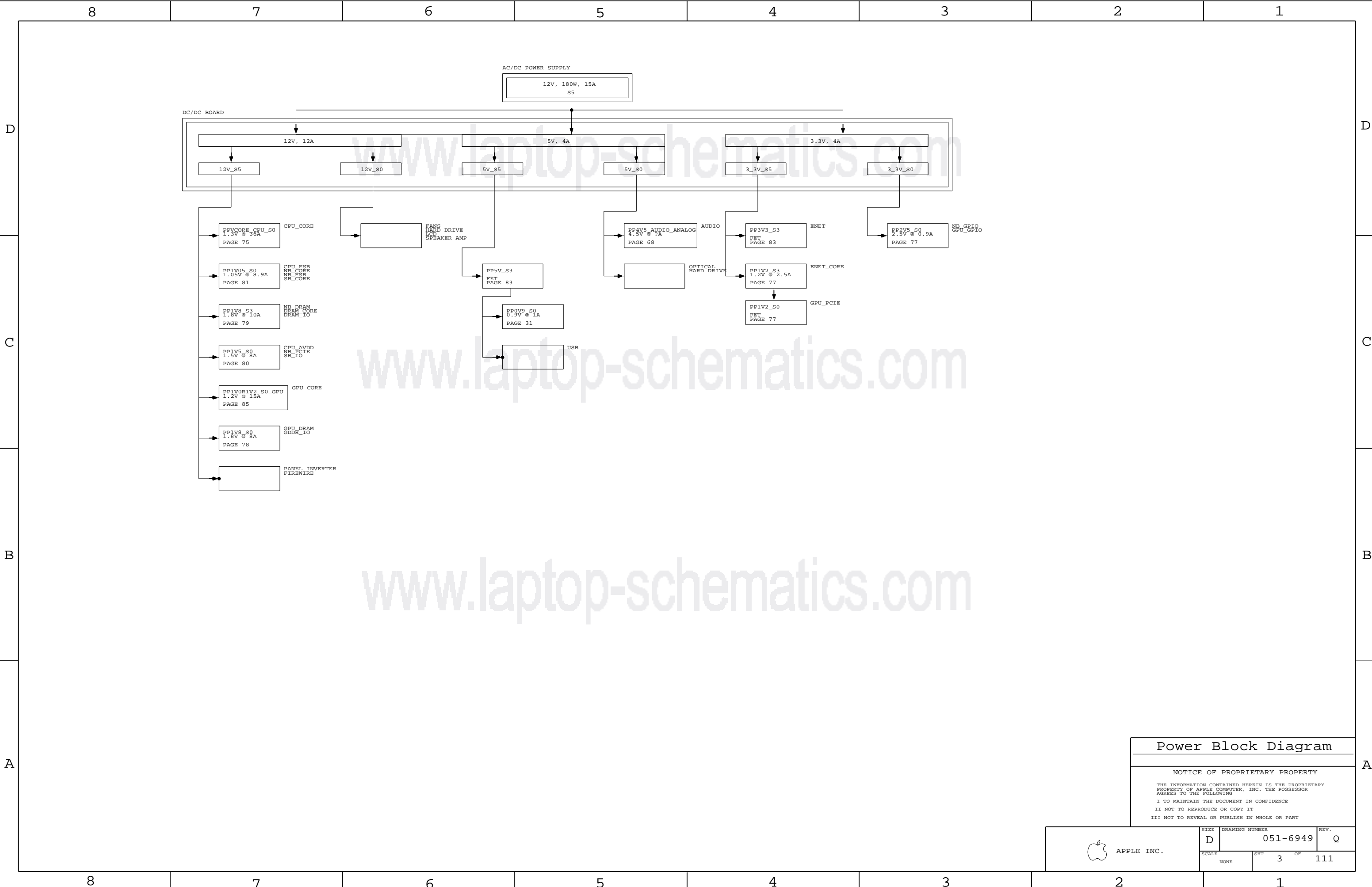
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SCALE	SHT	OF
NONE	2	111



Power Block Diagram

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D	COMMON																																																																											
	<table><tr><th>PART#</th><th>QTY</th><th>DESCRIPTION</th><th>REFERENCE DESIGNATOR(S)</th><th>CRITICAL</th><th>BOM OPTION</th></tr><tr><td>511S0025</td><td>1</td><td>IC,CPU-SKT,479BGA</td><td>J0700</td><td>CRITICAL</td><td></td></tr><tr><td>338S0269</td><td>1</td><td>IC,945GM,NORTHBRIDGE</td><td>U1200</td><td>CRITICAL</td><td></td></tr><tr><td>343S0385</td><td>1</td><td>IC,SB,652BGA</td><td>U2100</td><td>CRITICAL</td><td></td></tr><tr><td>742-0048</td><td>1</td><td>BAT,COIN,3V,220MAH,CR2032</td><td>BT2600</td><td>CRITICAL</td><td></td></tr><tr><td>359S0101</td><td>1</td><td>IC,CY28445-5,CLK GEN,68PIN QFP</td><td>U3301</td><td>CRITICAL</td><td></td></tr><tr><td>338S0270</td><td>1</td><td>IC,88E053,GIGABIT ENET XCVR,64P QFN-MD</td><td>U4101</td><td>CRITICAL</td><td></td></tr><tr><td>(335S0382) 341S1797</td><td>1</td><td>IC,ENET LAN ROM</td><td>U4102</td><td>CRITICAL</td><td></td></tr><tr><td>338S0279</td><td>1</td><td>IC,FW32306,1394A LINK,TQFP</td><td>U4400</td><td>CRITICAL</td><td></td></tr></table>															PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL		338S0269	1	IC,945GM,NORTHBRIDGE	U1200	CRITICAL		343S0385	1	IC,SB,652BGA	U2100	CRITICAL		742-0048	1	BAT,COIN,3V,220MAH,CR2032	BT2600	CRITICAL		359S0101	1	IC,CY28445-5,CLK GEN,68PIN QFP	U3301	CRITICAL		338S0270	1	IC,88E053,GIGABIT ENET XCVR,64P QFN-MD	U4101	CRITICAL		(335S0382) 341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL		338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL								
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8	7	6	5	4	3	2	1
<div>LAYOUT NOTE: PLACE NEAR J0700</div> <div><div><div>12 7 5</div><div>FSB A L<6></div><div>PP600</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L<0></div><div>PP601</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB A L<27></div><div>PP602</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L<1></div><div>PP603</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<0></div><div>PP604</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<0></div><div>PP605</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<0></div><div>PP606</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<0></div><div>PP607</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<16></div><div>PP608</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<1></div><div>PP609</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<1></div><div>PP610</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<1></div><div>PP611</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<41></div><div>PP612</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<2></div><div>PP613</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<2></div><div>PP614</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<2></div><div>PP615</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<59></div><div>PP616</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<3></div><div>PP617</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<3></div><div>PP618</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<3></div><div>PP619</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB LOCK L</div><div>PP620</div><div>OMIT P4MM</div></div><div><div>12 11 7</div><div>FSB CPURST L</div><div>PP621</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU INIT L</div><div>PP622</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU A20M L</div><div>PP623</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU IGNNE L</div><div>PP624</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU STPLCK L</div><div>PP625</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU INTR</div><div>PP626</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU NMI</div><div>PP627</div><div>OMIT P4MM</div></div><div><div>21 7</div><div>CPU SMI L</div><div>PP628</div><div>OMIT P4MM</div></div><div><div>34 7</div><div>FSB CLK CPU P</div><div>PP629</div><div>OMIT P4MM</div></div><div><div>34 7</div><div>FSB CLK CPU N</div><div>PP630</div><div>OMIT P4MM</div></div></div>	<div>LAYOUT NOTE: PLACE NEAR U1200</div> <div><div><div>12 7 5</div><div>FSB A L<6></div><div>PP631</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L<0></div><div>PP632</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB A L<27></div><div>PP633</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L<1></div><div>PP634</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<0></div><div>PP635</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<0></div><div>PP636</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<0></div><div>PP637</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<0></div><div>PP638</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<16></div><div>PP639</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<1></div><div>PP640</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<1></div><div>PP641</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<1></div><div>PP642</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<41></div><div>PP643</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<2></div><div>PP644</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<2></div><div>PP645</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<2></div><div>PP646</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L<59></div><div>PP647</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L<3></div><div>PP648</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L<3></div><div>PP649</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L<3></div><div>PP650</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB LOCK L</div><div>PP651</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB HIT L</div><div>PP652</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB HITM L</div><div>PP653</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB BNR L</div><div>PP654</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB BREQ0 L</div><div>PP655</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DBSY L</div><div>PP656</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DPNR L</div><div>PP657</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB REQ L<0></div><div>PP658</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB REQ L<1></div><div>PP659</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB REQ L<2></div><div>PP660</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB REQ L<3></div><div>PP661</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB REQ L<4></div><div>PP662</div><div>OMIT P4MM</div></div><div><div>34 12</div><div>FSB CLK NB P</div><div>PP663</div><div>OMIT P4MM</div></div><div><div>34 12</div><div>FSB CLK NB N</div><div>PP664</div><div>OMIT P4MM</div></div><div><div>75 26 14</div><div>VR_PWRGOOD_DELAY</div><div>PP665</div><div>OMIT P4MM</div></div><div><div>14</div><div>NB_RST_IN_L_R</div><div>PP666</div><div>OMIT P4MM</div></div><div><div>34 14</div><div>NB_CLK100M_GCLKIN_P</div><div>PP667</div><div>OMIT P4MM</div></div><div><div>34 14</div><div>NB_CLK100M_GCLKIN_N</div><div>PP668</div><div>OMIT P4MM</div></div><div><div>22 14</div><div>DMI_S2N_N<0></div><div>PP673</div><div>OMIT P4MM</div></div><div><div>22 14</div><div>DMI_S2N_P<0></div><div>PP674</div><div>OMIT P4MM</div></div><div><div>19 14</div><div>MEM_VREF_NB_0</div><div>PP675</div><div>OMIT P4MM</div></div><div><div>19 14</div><div>MEM_VREF_NB_1</div><div>PP676</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<7></div><div>PP677</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<14></div><div>PP678</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<16></div><div>PP679</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<25></div><div>PP680</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<39></div><div>PP681</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<47></div><div>PP682</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQ<54></div><div>PP683</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<0></div><div>PP684</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<0></div><div>PP685</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<1></div><div>PP686</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<1></div><div>PP687</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<2></div><div>PP688</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<2></div><div>PP689</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<3></div><div>PP690</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<3></div><div>PP691</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<4></div><div>PP692</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<4></div><div>PP693</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<5></div><div>PP694</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<5></div><div>PP695</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<6></div><div>PP696</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<6></div><div>PP697</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_P<7></div><div>PP698</div><div>OMIT P4MM</div></div><div><div>28 15</div><div>MEM_A_DQS_N<7></div><div>PP699</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<6></div><div>PP6A0</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<8></div><div>PP6A1</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<23></div><div>PP6A2</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<25></div><div>PP6A3</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<38></div><div>PP6A4</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<44></div><div>PP6A5</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<48></div><div>PP6A6</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQ<62></div><div>PP6A7</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<0></div><div>PP6A8</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<0></div><div>PP6A9</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<1></div><div>PP6B0</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<1></div><div>PP6B1</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<2></div><div>PP6B2</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<2></div><div>PP6B3</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<3></div><div>PP6B4</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<3></div><div>PP6B5</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<4></div><div>PP6B6</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<4></div><div>PP6B7</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<5></div><div>PP6B8</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<5></div><div>PP6B9</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<6></div><div>PP6C0</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<6></div><div>PP6C1</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_P<7></div><div>PP6C2</div><div>OMIT P4MM</div></div><div><div>29 15</div><div>MEM_B_DQS_N<7></div><div>PP6C3</div><div>OMIT P4MM</div></div></div>	<div>LAYOUT NOTE: PLACE NEAR U8400</div> <div><div><div>89 87 5</div><div>FB A DQ<0></div><div>PP8700</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<8></div><div>PP8701</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<16></div><div>PP8702</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<24></div><div>PP8703</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<32></div><div>PP8704</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<40></div><div>PP8705</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<48></div><div>PP8706</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A DQ<56></div><div>PP8707</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A MA<3></div><div>PP8708</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<0></div><div>PP8709</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<1></div><div>PP8710</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<2></div><div>PP8711</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<3></div><div>PP8712</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<4></div><div>PP8713</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<5></div><div>PP8714</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<6></div><div>PP8715</div><div>OMIT P4MM</div></div><div><div>89 87 5</div><div>FB A RDQS<7></div><div>PP8716</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<0></div><div>PP8720</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<8></div><div>PP8721</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<16></div><div>PP8722</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<24></div><div>PP8723</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<32></div><div>PP8724</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<40></div><div>PP8725</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<48></div><div>PP8726</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B DQ<56></div><div>PP8727</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B MA<3></div><div>PP8728</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<0></div><div>PP8729</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<1></div><div>PP8730</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<2></div><div>PP8731</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<3></div><div>PP8732</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<4></div><div>PP8733</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<5></div><div>PP8734</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<6></div><div>PP8735</div><div>OMIT P4MM</div></div><div><div>90 87 5</div><div>FB B RDQS<7></div><div>PP8736</div><div>OMIT P4MM</div></div><div><div>84 34</div><div>GPU_CLK100M_PCIE_P</div><div>PP8400</div><div>OMIT P4MM</div></div><div><div>84 34</div><div>GPU_CLK100M_PCIE_N</div><div>PP8401</div><div>OMIT P4MM</div></div></div>	<div>LAYOUT NOTE: PLACE NEAR U4101</div> <div><div><div>41 34</div><div>ENET_CLK100M_PCIE_P</div><div>PP4100</div><div>OMIT P4MM</div></div><div><div>41 34</div><div>ENET_CLK100M_PCIE_N</div><div>PP4101</div><div>OMIT P4MM</div></div></div>	<div>PLACE NEAR R1210 AND R1211</div> <div><div><div>12</div><div>NB_FSB_VREF</div><div>PP1200</div><div>OMIT P4MM</div></div><div><div>12</div><div>NB_FSB_VREF</div><div>PP1201</div><div>OMIT P4MM</div></div><div><div>12</div><div>NB_FSB_VREF</div><div>PP1202</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1201</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1202</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1203</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1204</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1205</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1206</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1207</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1208</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1209</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1210</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1211</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1212</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1213</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1214</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1215</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1216</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1217</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1218</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1219</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1220</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1221</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1222</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1223</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1224</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1225</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1226</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1227</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1228</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1229</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1230</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1231</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1232</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1233</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1234</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1235</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1236</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1237</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1238</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1239</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1240</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1241</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1242</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1243</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1244</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1245</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1246</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1247</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1248</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1249</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1250</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1251</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1252</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1253</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1254</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1255</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1256</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1257</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1258</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1259</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1260</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1261</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1262</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1263</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1264</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1265</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1266</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1267</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1268</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1269</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1270</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1271</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1272</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1273</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1274</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1275</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1276</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1277</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1278</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1279</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1280</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1281</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1282</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1283</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1284</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1285</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1286</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1287</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1288</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1289</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1290</div><div>OMIT P4MM</div></div><div><div>19 12 6</div><div>PP1V05_S0_FSB_NB</div><div>PP1291</div><div>OMIT P4MM</div></div><</div>			

89 87 5

FB_A_DQ<0>

PP8700

OMIT P4MM

89 87 5

FB_A_DQ<8>

PP8701

OMIT P4MM

89 87 5

FB_A_DQ<16>

PP8702

OMIT P4MM

89 87 5

FB_A_DQ<24>

PP8703

OMIT P4MM

89 87 5

FB_A_DQ<32>

PP8704

OMIT P4MM

89 87 5

FB_A_DQ<40>

PP8705

OMIT P4MM

89 87 5

FB_A_DQ<48>

PP8706

OMIT P4MM

89 87 5

FB_A_DQ<56>

PP8707

OMIT P4MM

89 87 5

FB_A_MA<3>

PP8708

OMIT P4MM

89 87 5

FB_A_RDQS<0>

PP8709

OMIT P4MM

89 87 5

FB_A_RDQS<1>

PP8710

OMIT P4MM

89 87 5

FB_A_RDQS<2>

PP8711

OMIT P4MM

89 87 5

FB_A_RDQS<3>

PP8712

OMIT P4MM

89 87 5

FB_A_RDQS<4>

PP8713

OMIT P4MM

89 87 5

FB_A_RDQS<5>

PP8714

OMIT P4MM

89 87 5

FB_A_RDQS<6>

PP8715

OMIT P4MM

89 87 5

FB_A_RDQS<7>

PP8716

OMIT P4MM

89 87 5

FB_B_DQ<0>

PP8720

OMIT P4MM

89 87 5

FB_B_DQ<8>

PP8721

OMIT P4MM

89 87 5

FB_B_DQ<16>

PP8722

OMIT P4MM

89 87 5

FB_B_DQ<24>

PP8723

OMIT P4MM

89 87 5

FB_B_DQ<32>

PP8724

OMIT P4MM

89 87 5

FB_B_DQ<40>

PP8725

OMIT P4MM

89 87 5

FB_B_DQ<48>

PP8726

OMIT P4MM

89 87 5

FB_B_DQ<56>

PP8727

OMIT P4MM

89 87 5

FB_B_MA<3>

PP8728

OMIT P4MM

89 87 5

FB_B_RDQS<0>

PP8729

OMIT P4MM

89 87 5

FB_B_RDQS<1>

PP8730

OMIT P4MM

89 87 5

FB_B_RDQS<2>

PP8731

OMIT P4MM

89 87 5

FB_B_RDQS<3>

PP8732

OMIT P4MM

89 87 5

FB_B_RDQS<4>

PP8733

OMIT P4MM

89 87 5

FB_B_RDQS<5>

PP8734

OMIT P4MM

89 87 5

FB_B_RDQS<6>

PP8735

OMIT P4MM

89 87 5

FB_B_RDQS<7>

PP8736

OMIT P4MM

84 34

GPU_CLK100M_PCIE_P

PP8400

OMIT P4MM

84 34

GPU_CLK100M_PCIE_N

PP8401

OMIT P4MM

89 87 5

FB_A_CKE<0>

PP8900

OMIT P4MM

89 87 5

FB_A_CLK_P<0>

PP8901

OMIT P4MM

89 87 5

FB_A_CLK_N<0>

PP8902

OMIT P4MM

89 87 5

FB_B_CS_L<0>

PP8903

OMIT P4MM

89 87 5

FB_A_WE_L<0>

PP8904

OMIT P4MM

89 87 5

FB_B_CAS_L<0>

PP8905

OMIT P4MM

89 87 5

FB_B_RAS_L<0>

PP8906

OMIT P4MM

89 87 5

FB_B_MA<3>

PP8907

OMIT P4MM

89 87 5

FB_A_WDQS<0>

PP8908

OMIT P4MM

89 87 5

FB_B_WDQS<0>

PP8909

OMIT P4MM

89 87 5

FB_A_WDQS<1>

PP8910

OMIT P4MM

89 87 5

FB_B_WDQS<1>

PP8911

OMIT P4MM

89 87 5

FB_A_WDQS<3>

PP8912

OMIT P4MM

89 87 5

FB_B_WDQS<2>

PP8913

OMIT P4MM

89 87 5

FB_A_DQ<0>

PP8914

OMIT P4MM

89 87 5

FB_B_DQ<8>

PP8915

OMIT P4MM

89 87 5

FB_A_DQ<16>

PP8916

OMIT P4MM

89 87 5

FB_A_DQ<24>

PP8916

OMIT P4MM

89 87 5

FB_A_CKE<1>

PP8920

OMIT P4MM

89 87 5

FB_A_CLK_P<1>

PP8921

OMIT P4MM

89 87 5

FB_B_CLK_N<1>

PP8922

OMIT P4MM

89 87 5

FB_A_CS_L<1>

PP8923

OMIT P4MM

89 87 5

FB_B_WE_L<1>

PP8924

OMIT P4MM

89 87 5

FB_A_CAS_L<1>

PP8925

OMIT P4MM

89 87 5

FB_B_RAS_L<1>

PP8926

OMIT P4MM

89 87 5

FB_A_MA<3>

PP8927

OMIT P4MM

89 87 5

FB_A_WDQS<5>

PP8928

OMIT P4MM

89 87 5

FB_B_WDQS<5>

PP8929

OMIT P4MM

89 87 5

FB_A_WDQS<6>

PP8930

OMIT P4MM

89 87 5

FB_B_WDQS<4>

PP8931

OMIT P4MM

89 87 5

FB_A_WDQS<7>

PP8932

OMIT P4MM

89 87 5

FB_B_DQ<32>

PP8933

OMIT P4MM

89 87 5

FB_A_DQ<40>

PP8934

OMIT P4MM

89 87 5

FB_B_DQ<48>

PP8935

OMIT P4MM

89 87 5

FB_A_DQ<56>

PP8936

OMIT P4MM

46 34

ENET_CLK100M_PCIE_P

PP4100

OMIT P4MM

46 34

ENET_CLK100M_PCIE_N

PP4101

OMIT P4MM

89 87 5

FB_B_CKE<0>

PP9000

OMIT P4MM

89 87 5

FB_B_CLK_P<0>

PP9001

OMIT P4MM

89 87 5

FB_B_CLK_N<0>

PP9002

OMIT P4MM

89 87 5

FB_B_CS_L<0>

PP9003

OMIT P4MM

89 87 5

FB_B_WE_L<0>

PP9004

OMIT P4MM

89 87 5

FB_B_CAS_L<0>

PP9005

OMIT P4MM

89 87 5

FB_B_RAS_L<0>

PP9006

OMIT P4MM

89 87 5

FB_B_MA<3>

PP9007

OMIT P4MM

89 87 5

FB_B_WDQS<0>

PP9008

OMIT P4MM

89 87 5

FB_B_WDQS<1>

PP9010

OMIT P4MM

89 87 5

FB_B_WDQS<3>

PP9011

OMIT P4MM

89 87 5

FB_B_WDQS<2>

PP9012

OMIT P4MM

89 87 5

FB_B_DQ<0>

PP9013

OMIT P4MM

89 87 5

FB_B_DQ<8>

PP9014

OMIT P4MM

89 87 5

FB_B_DQ<16>

PP9015

OMIT P4MM

89 87 5

FB_B_DQ<24>

PP9016

OMIT P4MM

89 87 5

FB_B_CKE<1>

PP9020

OMIT P4MM

89 87 5

FB_B_CLK_P<1>

PP9021

OMIT P4MM

89 87 5

FB_B_CLK_N<1>

PP9022

OMIT P4MM

89 87 5

FB_B_CS_L<1>

PP9023

OMIT P4MM

89 87 5

FB_B_WE_L<1>

PP9024

OMIT P4MM

89 87 5

FB_B_CAS_L<1>

PP9025

OMIT P4MM

89 87 5

FB_B_RAS_L<1>

PP9026

OMIT P4MM

89 87 5

FB_B_MA<3>

PP9027

OMIT P4MM

89 87 5

FB_B_WDQS<5>

PP9028

OMIT P4MM

89 87 5

FB_B_WDQS<6>

PP9030

OMIT P4MM

89 87 5

FB_B_WDQS<4>

PP9031

OMIT P4MM

89 87 5

FB_B_DQ<32>

PP9032

OMIT P4MM

89 87 5

FB_B_DQ<40>

PP9034

OMIT P4MM

89 87 5

FB_B_DQ<48>

PP9035

OMIT P4MM

89 87 5

FB_B_DQ<56>

PP9036

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U8400

89 87 5

FB_A_DQ<0>

PP8700

OMIT P4MM

89 87 5

FB_A_DQ<8>

PP8701

OMIT P4MM

89 87 5

FB_A_DQ<16>

PP8702

OMIT P4MM

89 87 5

FB_A_DQ<24>

PP8703

OMIT P4MM

89 87 5

FB_A_DQ<32>

PP8704

OMIT P4MM

89 87 5

FB_A_DQ<40>

PP8705

OMIT P4MM

89 87 5

FB_A_DQ<48>

PP8706

OMIT P4MM

89 87 5

FB_A_DQ<56>

PP8707

OMIT P4MM

89 87 5

FB_A_MA<3>

PP8708

OMIT P4MM

89 87 5

FB_A_RDQS<0>

PP8709

OMIT P4MM

89 87 5

FB_A_RDQS<1>

PP8710

OMIT P4MM

89 87 5

FB_A_RDQS<2>

PP8711

OMIT P4MM

89 87 5

FB_A_RDQS<3>

PP8712

OMIT P4MM

89 87 5

FB_A_RDQS<4>

PP8713

OMIT P4MM

89 87 5

FB_A_RDQS<5>

PP8714

OMIT P4MM

89 87 5

FB_A_RDQS<6>

PP8715

OMIT P4MM

89 87 5

FB_A_RDQS<7>

PP8716

OMIT P4MM

89 87 5

FB_B_DQ<0>

PP8720

OMIT P4MM

89 87 5

FB_B_DQ<8>

PP8721

OMIT P4MM

89 87 5

FB_B_DQ<16>

PP8722

OMIT P4MM

89 87 5

FB_B_DQ<24>

PP8723

OMIT P4MM

89 87 5

FB_B_DQ<32>

PP8724

OMIT P4MM

89 87 5

FB_B_DQ<40>

PP8725

OMIT P4MM

89 87 5

FB_B_DQ<48>

PP8726

OMIT P4MM

89 87 5

FB_B_DQ<56>

PP8727

OMIT P4MM

89 87 5

FB_B_MA<3>

PP8728

OMIT P4MM

89 87 5

FB_B_RDQS<0>

PP8729

OMIT P4MM

89 87 5

FB_B_RDQS<1>

PP8730

OMIT P4MM

89 87 5

FB_B_RDQS<2>

PP8731

OMIT P4MM

89 87 5

FB_B_RDQS<3>

PP8732

OMIT P4MM

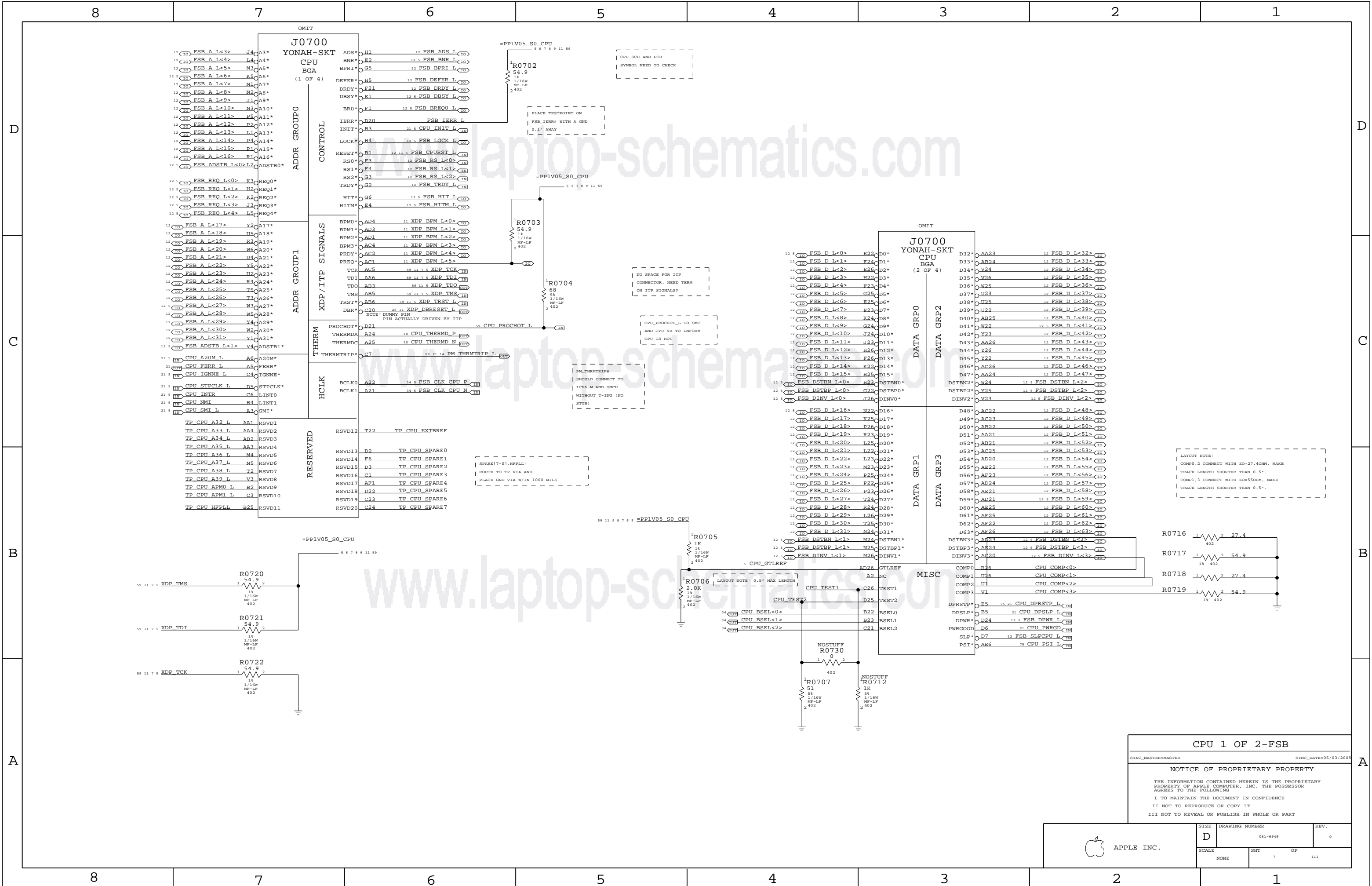
89 87 5

FB_B_RDQS<4>

PP8733

OMIT P4MM

89 87 5



CPU 1 OF 2-FSB

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

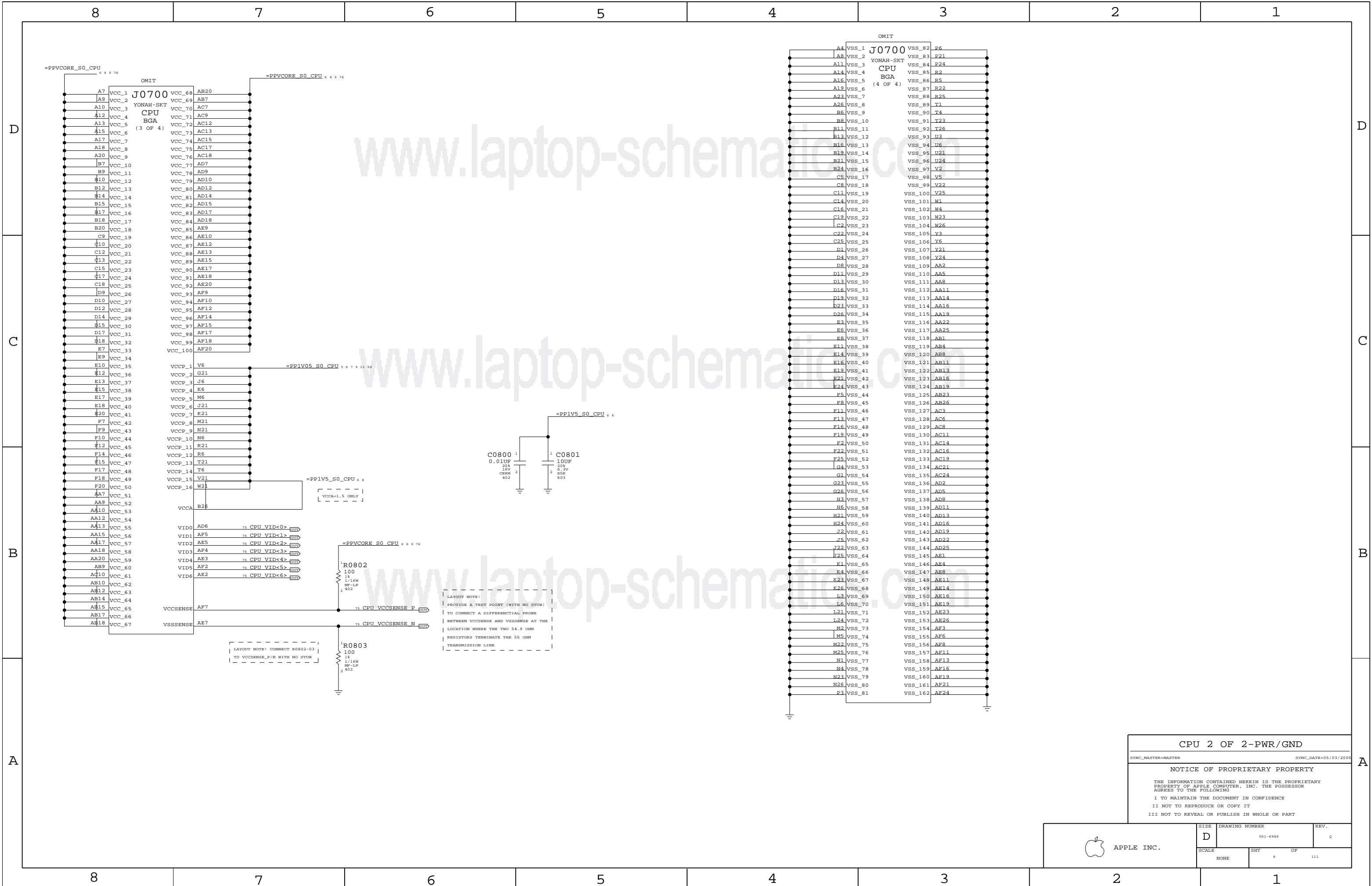
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CPU 2 OF 2-PWR/GND

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE		SHT	OF
NONE		8	111

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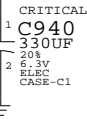
4

3

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1

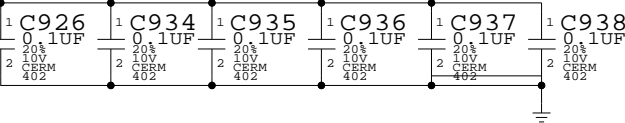
59 11 9 8 7 6 5 =PP1V05_S0_CPU



VCCP CORE DECOUPLING

59 11 9 8 7 6 5 =PP1V05_S0_CPU

PLACE INSIDE SOCKET CAVITY
ON L8 (NORTH SIDE SECONDARY)



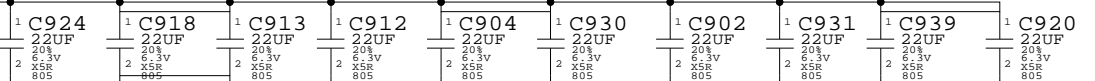
VCC CORE DECOUPLING

70 8 6 =PPVCORE_S0_CPU

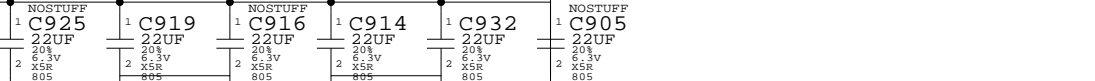
PLACE 8 INSIDE SOCKET
CAVITY ON L8 (NORTH SIDE
SECONDARY)



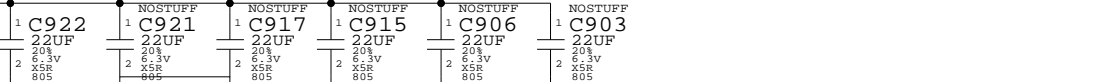
PLACE 8 INSIDE SOCKET
CAVITY ON L8 (SOUTH SIDE
SECONDARY)



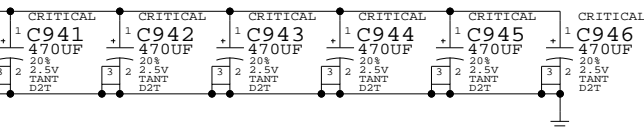
PLACE 6 INSIDE SOCKET
CAVITY ON L1 (NORTH SIDE
PRIMARY)



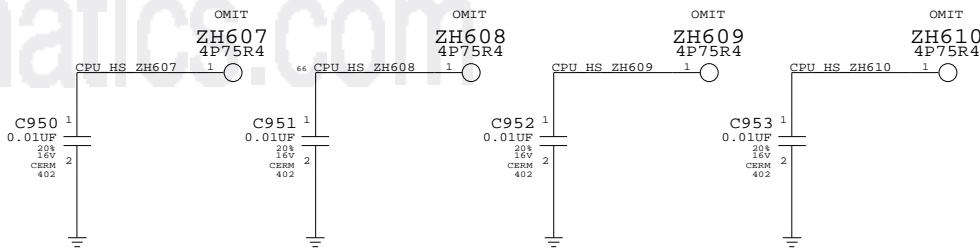
PLACE 6 INSIDE SOCKET
CAVITY ON L1 (SOUTH SIDE
PRIMARY)



SOUTH SIDE SECONDARY



CPU HEATSINK MOUNTING HOLES



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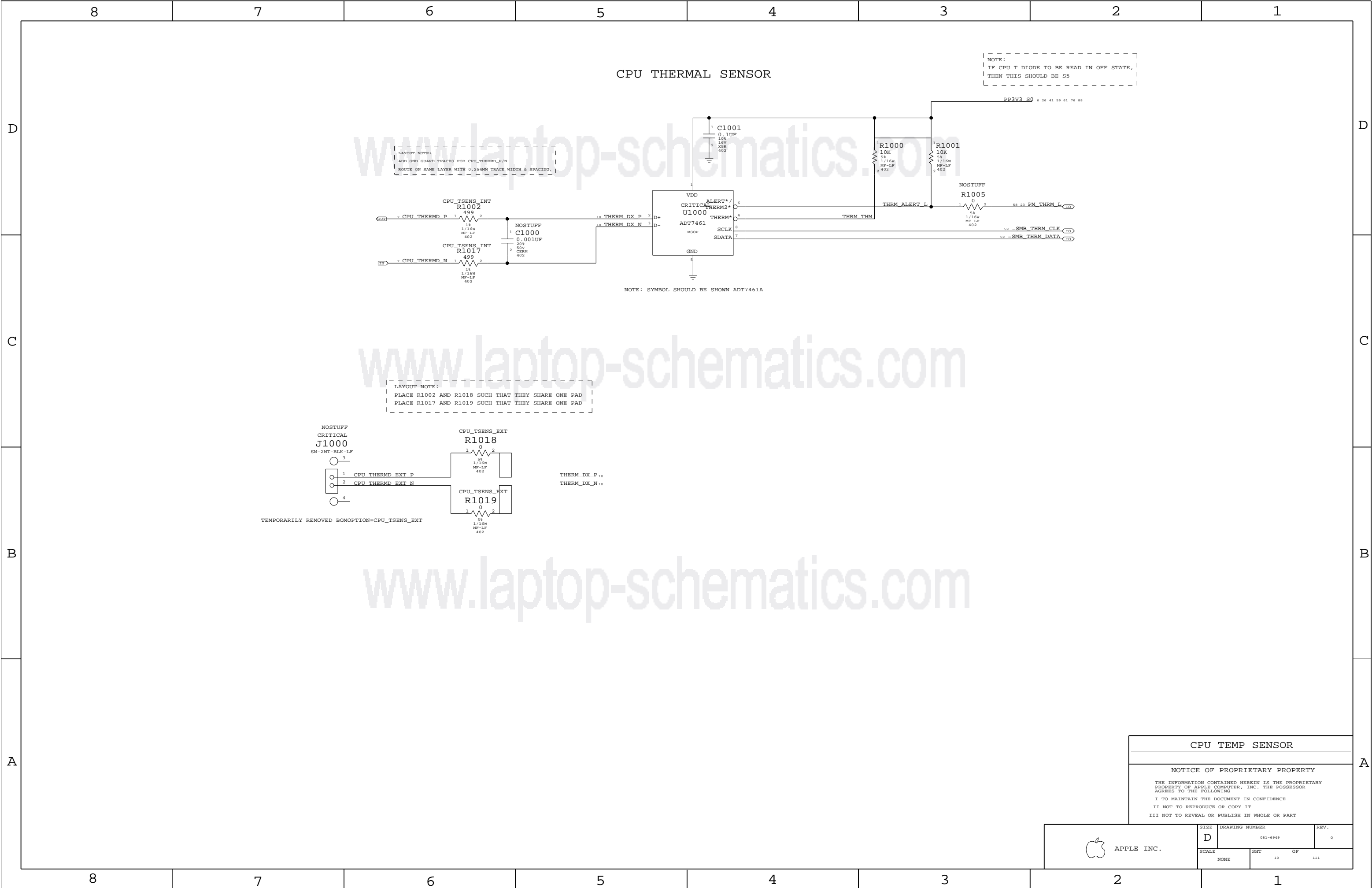
4

3

2

1

CPU DECAPS & VID<>		
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	SCALE NONE	REV. Q
		SHT 9
		OF 111



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www.laptop-schematics.com


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CPU ITP700FLEX DEBUG SUPPORT

(AND WITH RESET BUTTON)

ITP TCK SIGNAL LAYOUT NOTE:
ROUTE THE TCK SIGNAL FROM ITP700FLEX CONNECTOR'S TCK PIN TO CPU'S
TCK PIN AND THEN FORK BACK FROM CPU TCK PIN AND ROUTE BACK TO ITP700FLEX
CONNECTOR'S FBO PIN.

APPLE INC.

SIZE D	DRAWING NUMBER 051-6949	REV. Q
SCALE NONE	SHT 11 OF 111	

CPU ITP700FLEX DEBUG

SYNC_MASTER=MASTER

SYNC_DATE=5/23/05

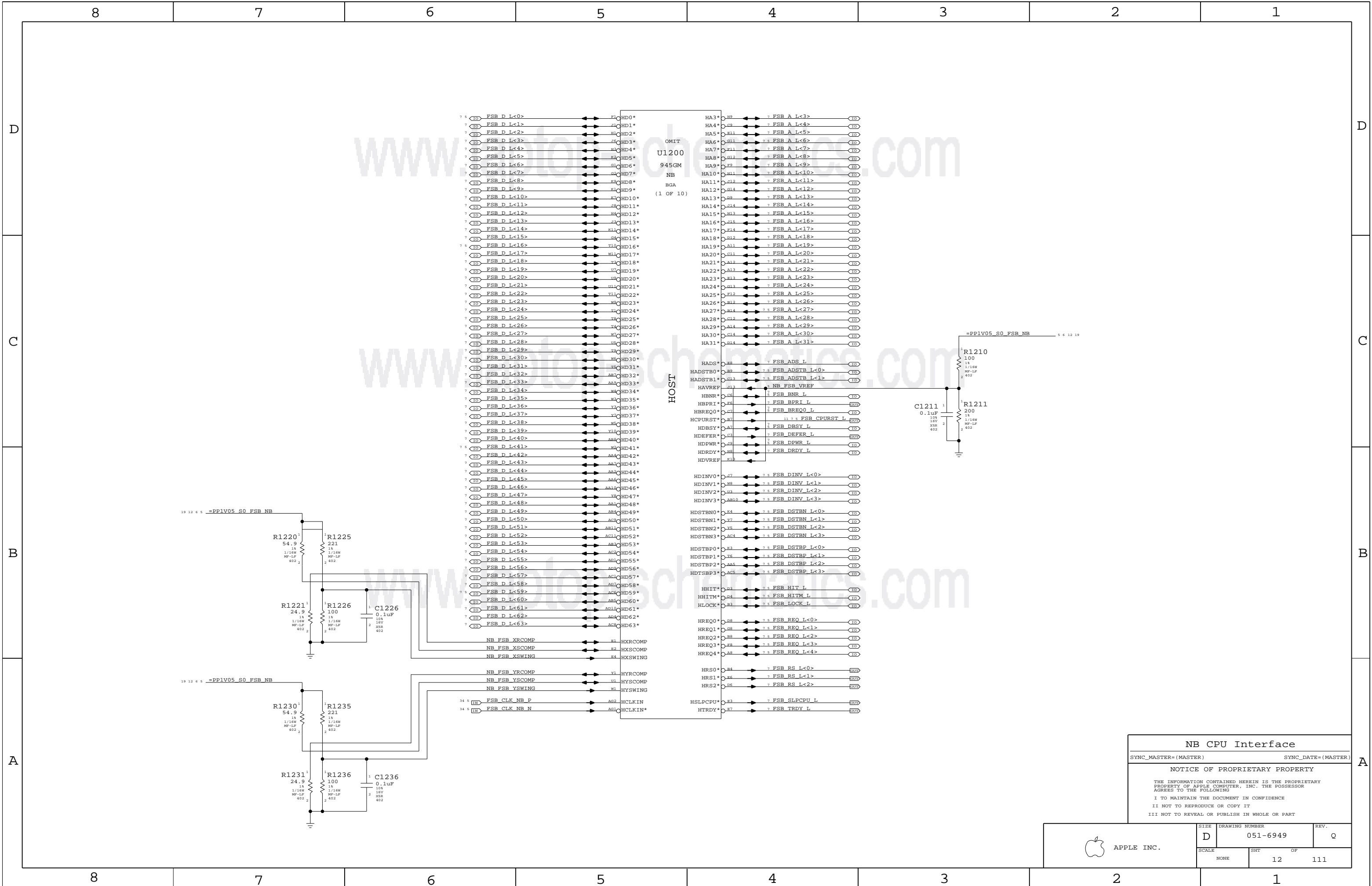
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NB CPU Interface

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

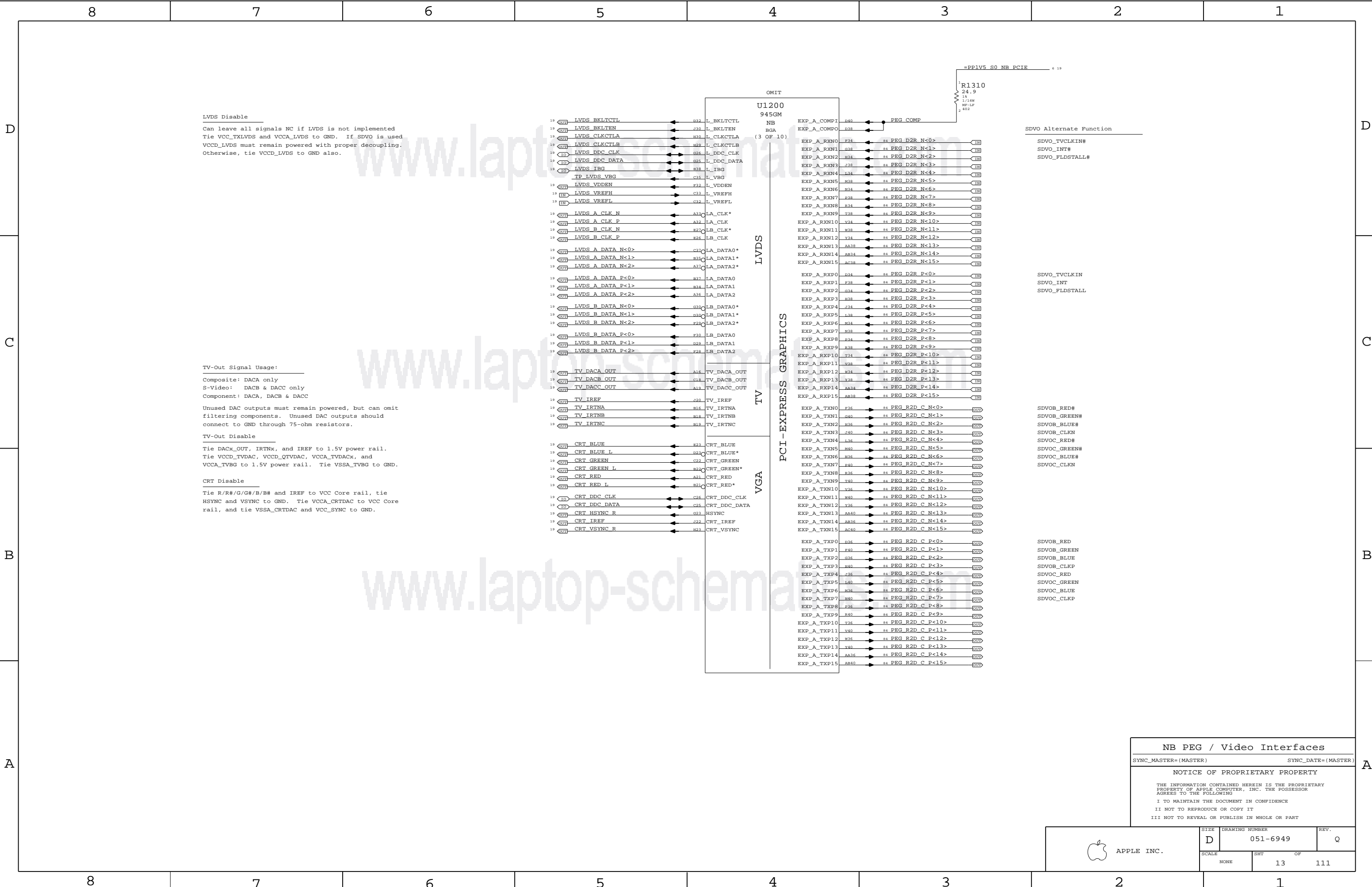
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE		SHT	OF
NONE		12	111



LVDS Disable

Can leave all signals NC if LVDS is not implemented
Tie VCC_TXLVDS and VCCA_LVDS to GND. If SDVO is used
VCCD_LVDS must remain powered with proper decoupling.
Otherwise, tie VCCD_LVDS to GND also.

TV-Out Signal Usage:

Composite: DACA only
S-Video: DACB & DACC only
Component: DACA, DACB & DACC

Unused DAC outputs must remain powered, but can omit
filtering components. Unused DAC outputs should
connect to GND through 75-ohm resistors.


TV-Out Disable

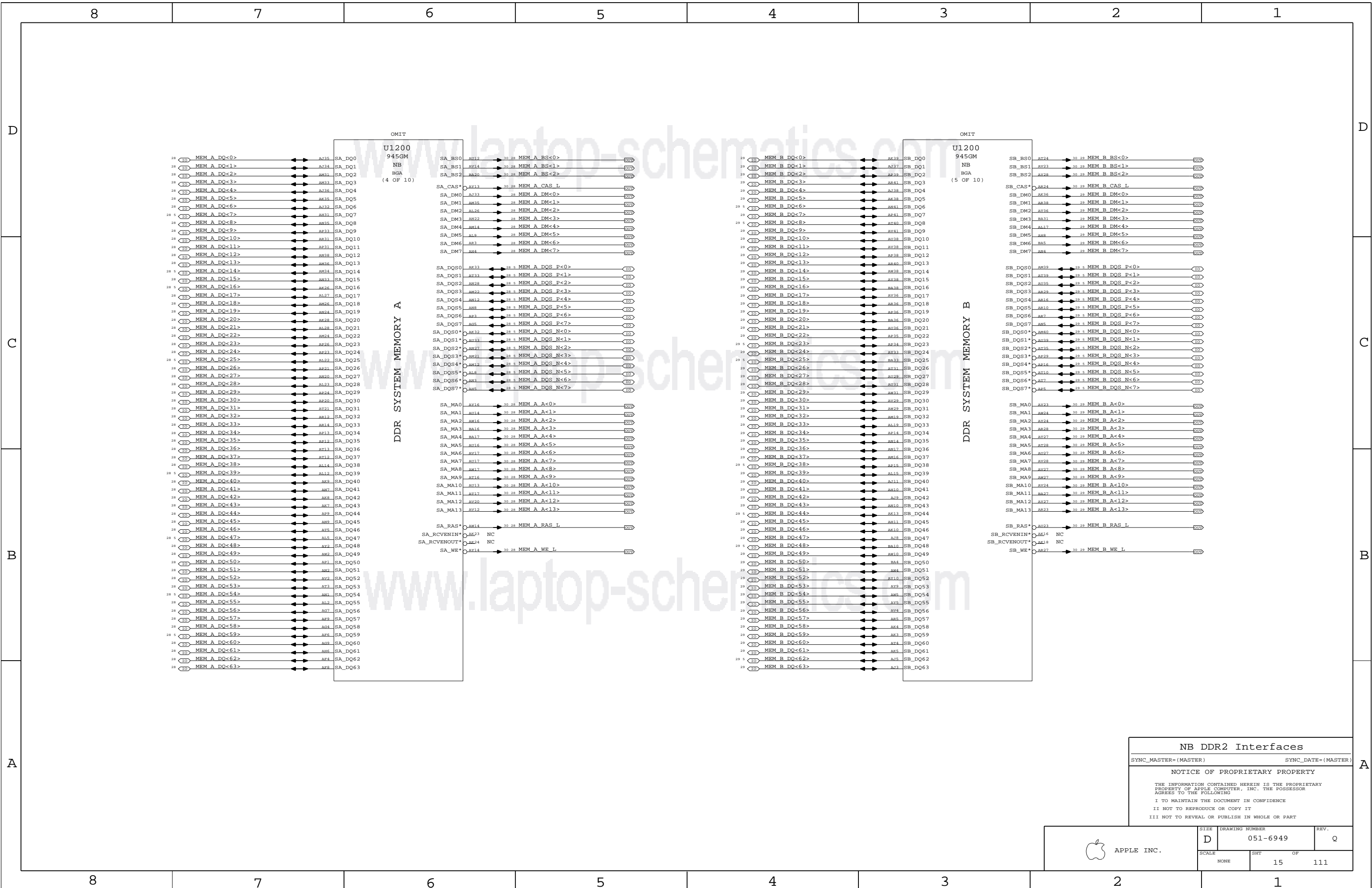
Tie DACx_OUT, IRTNx, and IREF to 1.5V power rail.
Tie VCCD_TVDAC, VCCD_QTVDAC, VCCA_TVDACx, and
VCCA_TVBG to 1.5V power rail. Tie VSSA_TVBG to GND.

CRT Disable

Tie R/R#/G/G#/B/B# and IREF to VCC Core rail, tie
HSYNC and VSYNC to GND. Tie VCCA_CRTDAC to VCC Core
rail, and tie VSSA_CRTDAC and VCC_SYNC to GND.

NB PEG / Video Interfaces	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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	SCALE NONE	SHT 13	OF 111



NB DDR2 Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

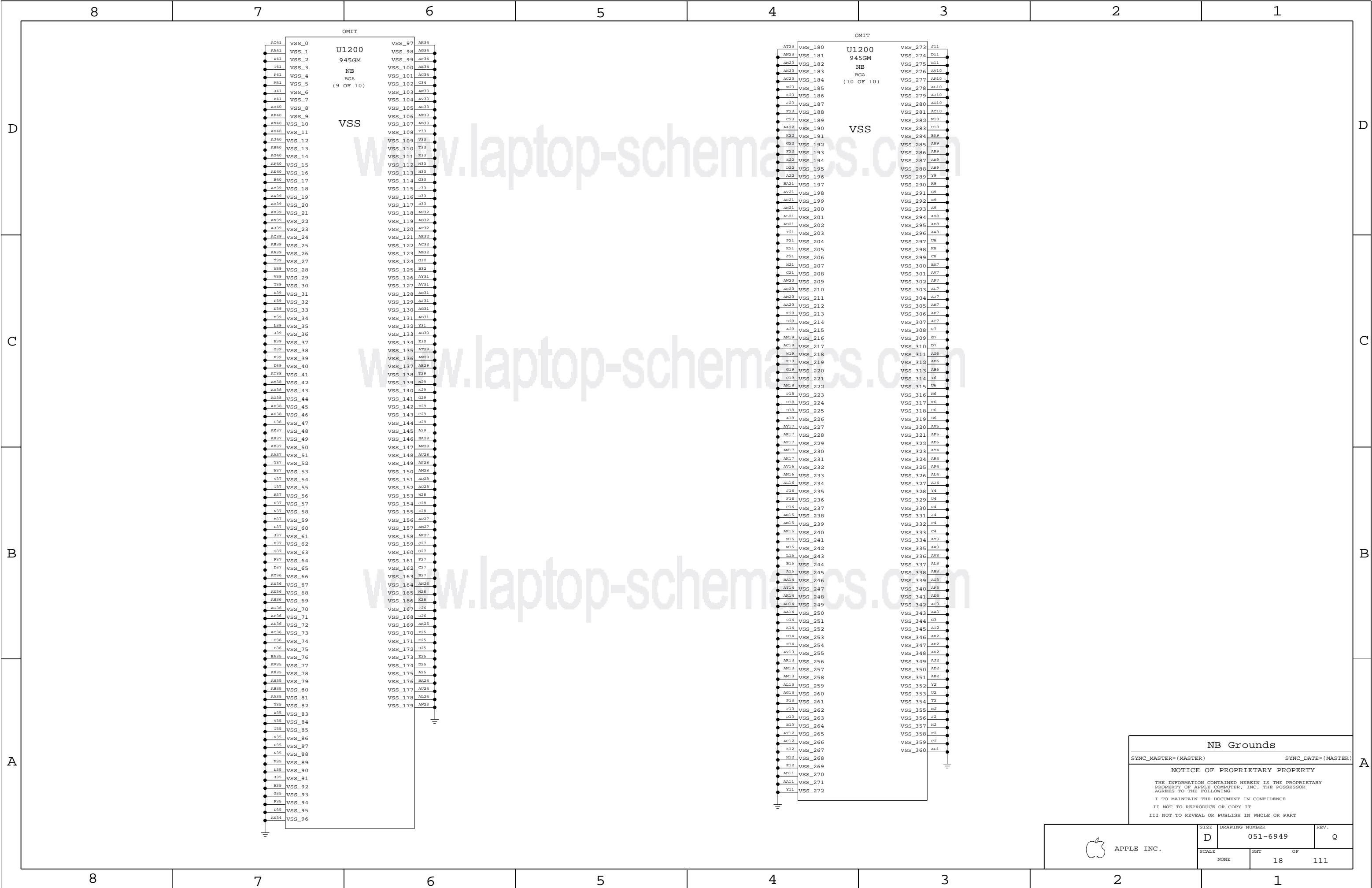
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	D	051-6949	Q
SCALE		SHT	OF
NONE		15	111



NB Grounds

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

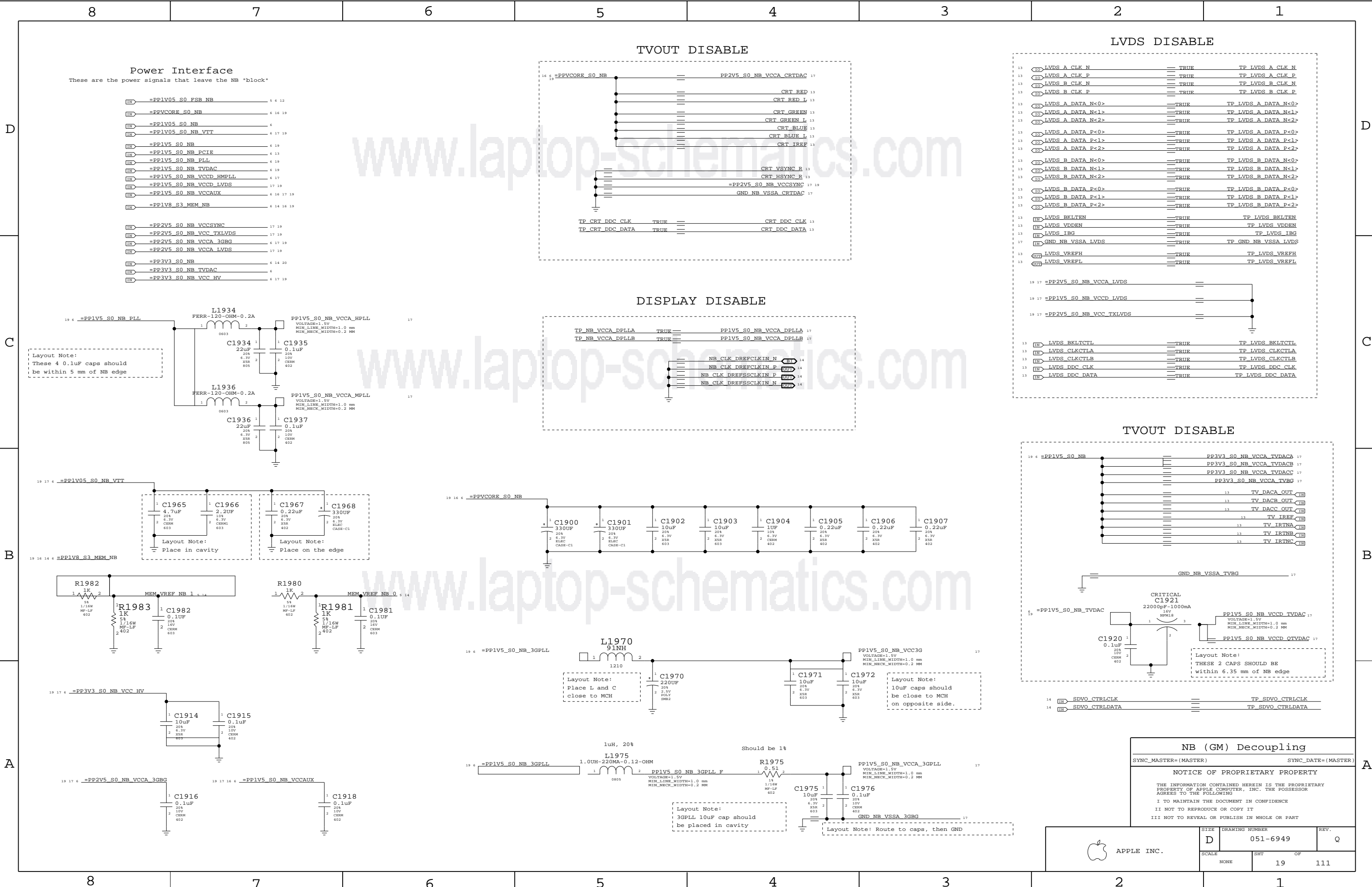
NOTICE OF PROPRIETARY PROPERTY

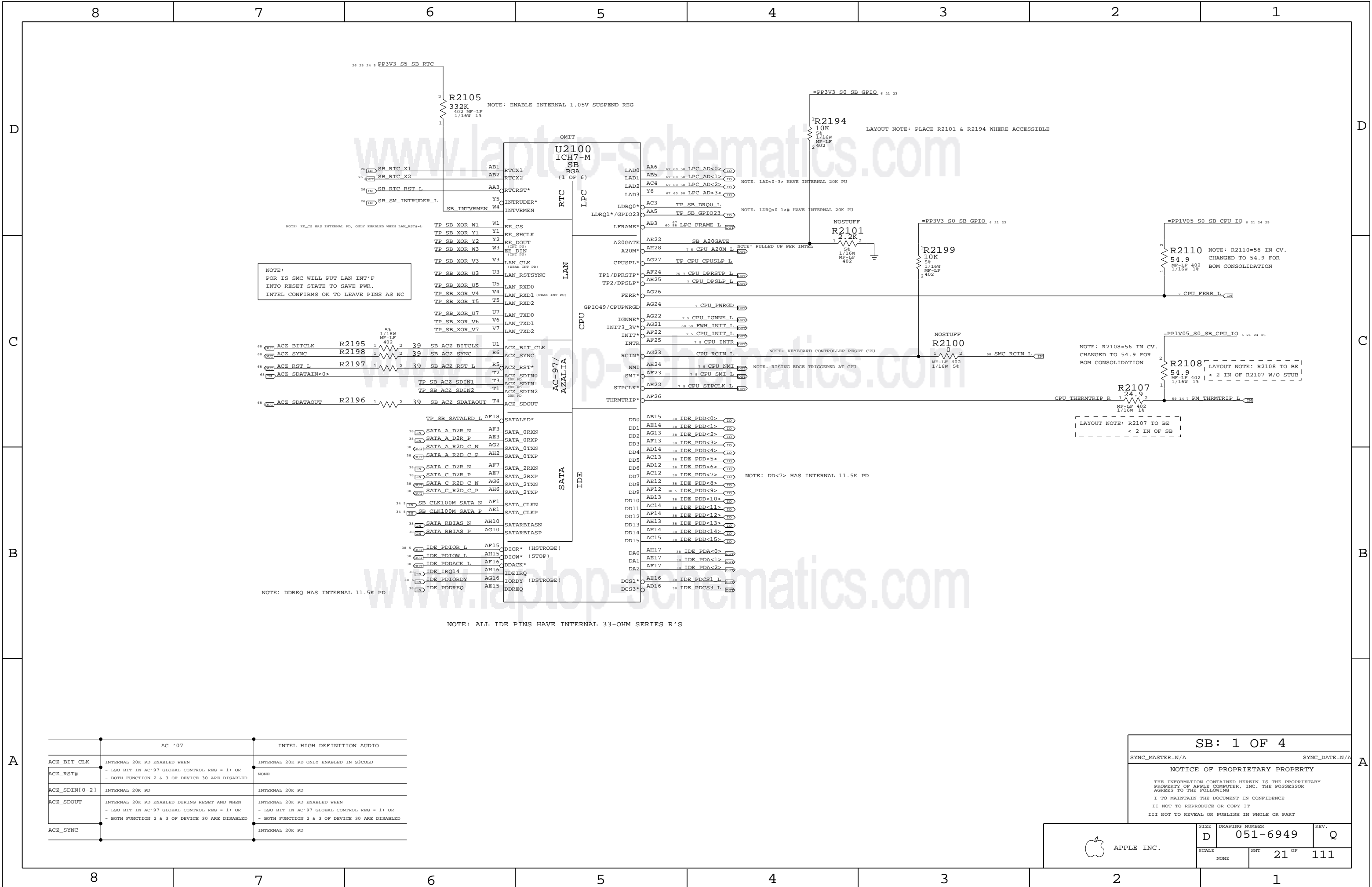
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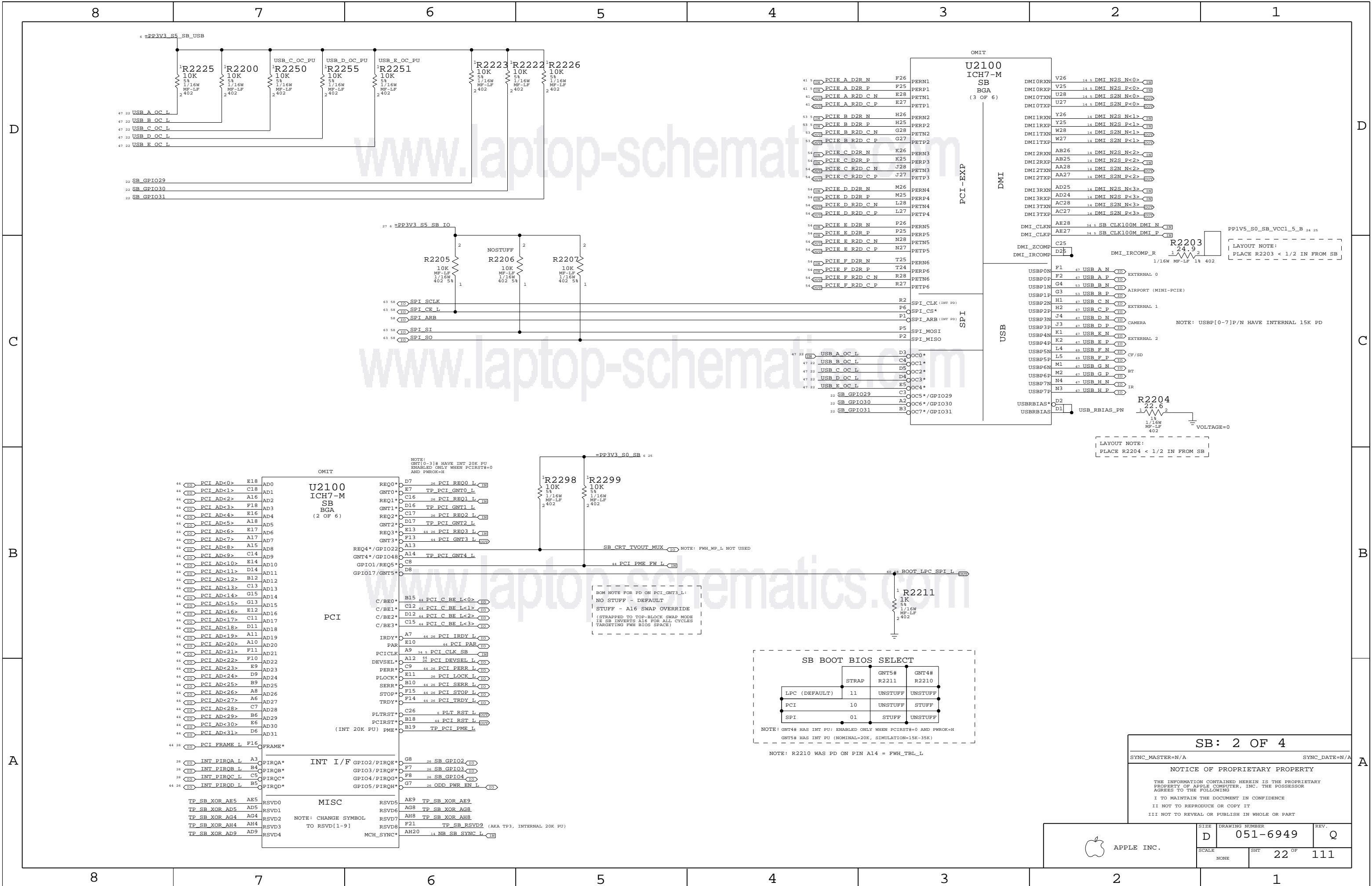
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

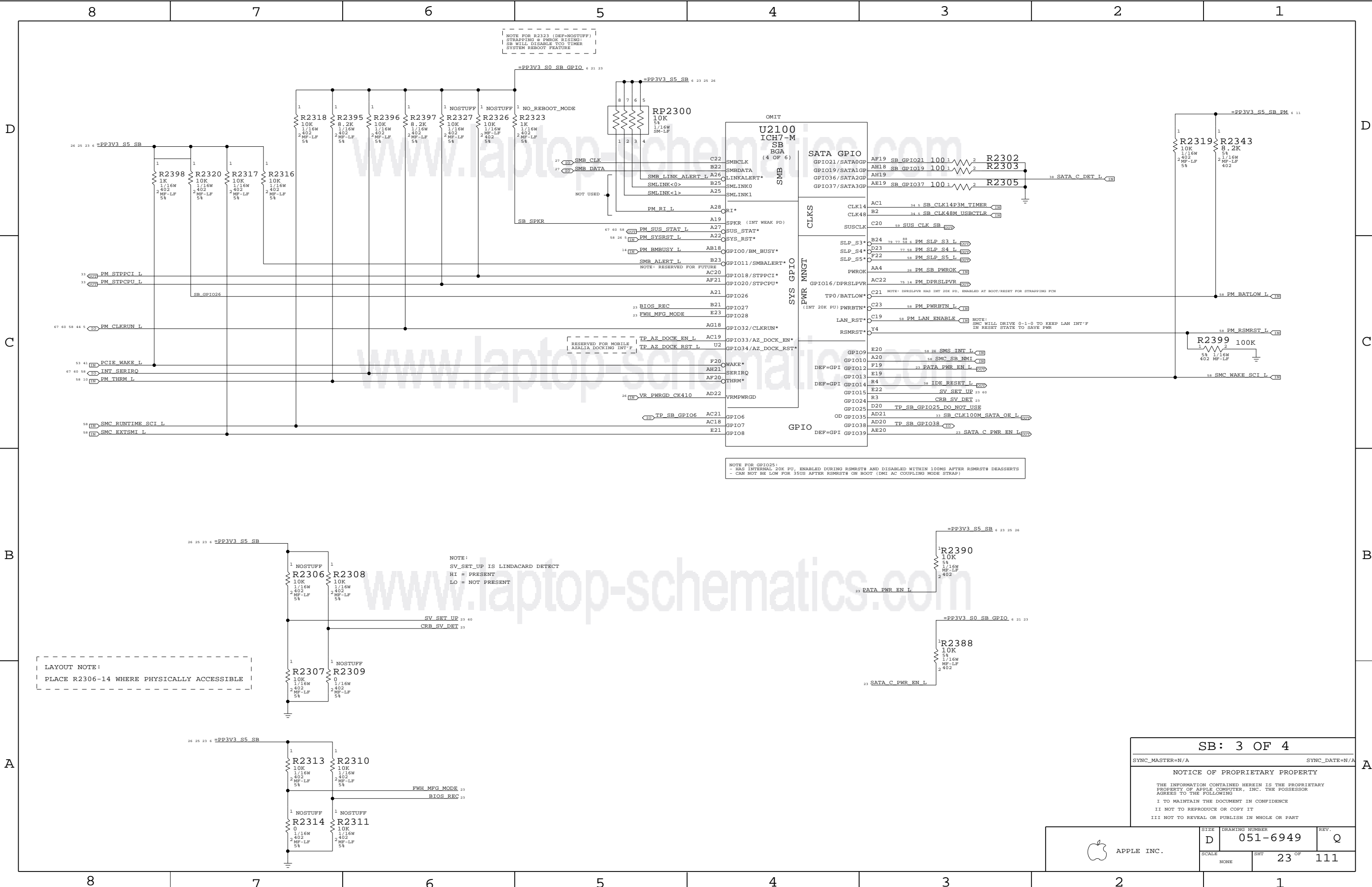
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART









NOTE FOR R2323 (DEF-NOSTUFF)
STRAPPING & PWROK RISING:
SB WILL DISABLE TCO TIMER
SYSTEM REBOOT FEATURE

NOTE FOR GPIO25:
- HAS INTERNAL 20K PU, ENABLED DURING RSMRST# AND DISABLED WITHIN 100MS AFTER RSMRST# DEASSERTS
- CAN NOT BE LOW FOR 35US AFTER RSMRST# ON BOOT (DMI AC COUPLING MODE STRAP)

LAYOUT NOTE:
PLACE R2306-14 WHERE PHYSICALLY ACCESSIBLE

SB: 3 OF 4

SYNC_MASTER=N/A SYNC_DATE=N/A

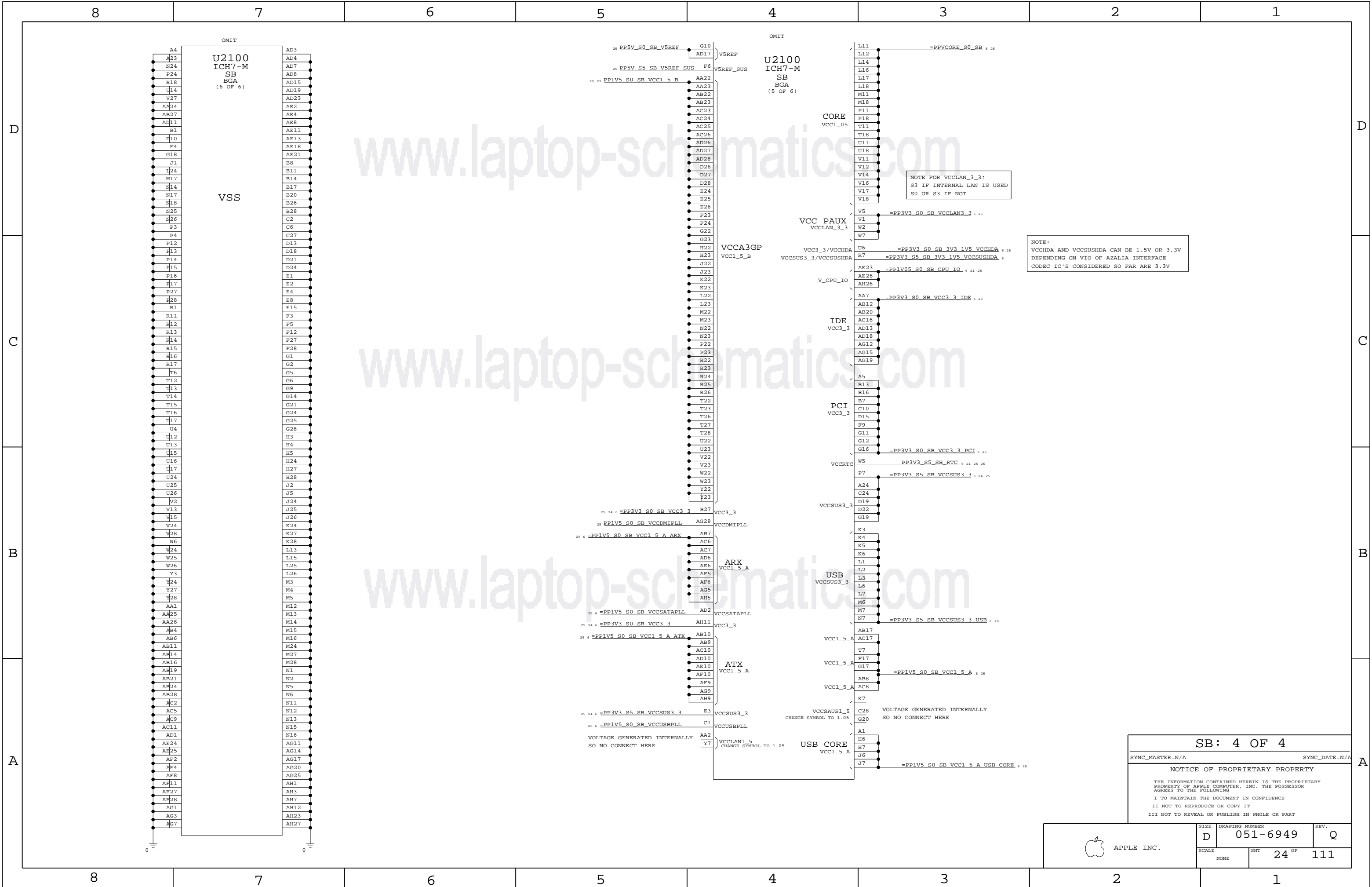
NOTICE OF PROPRIETARY PROPERTY

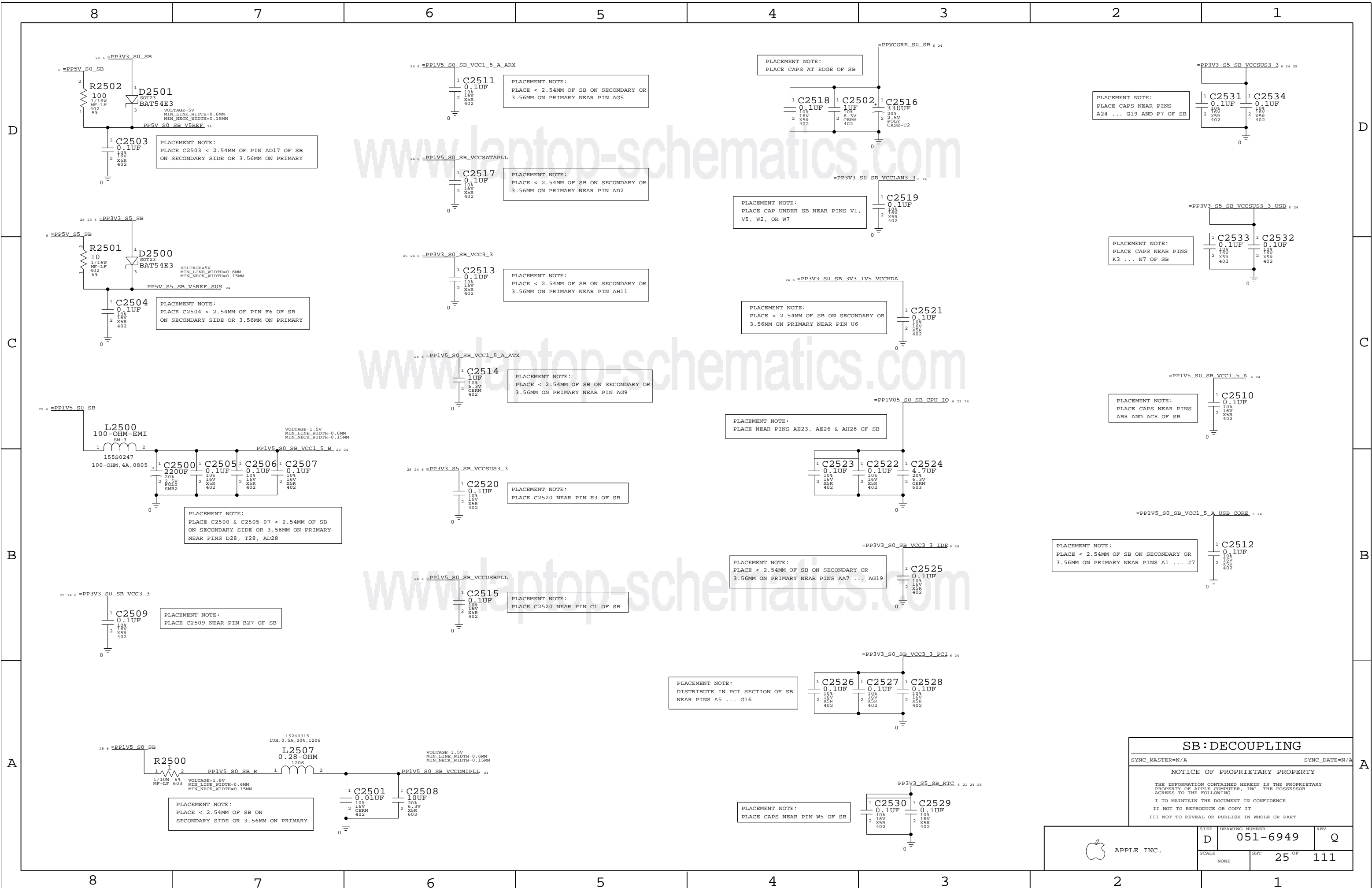
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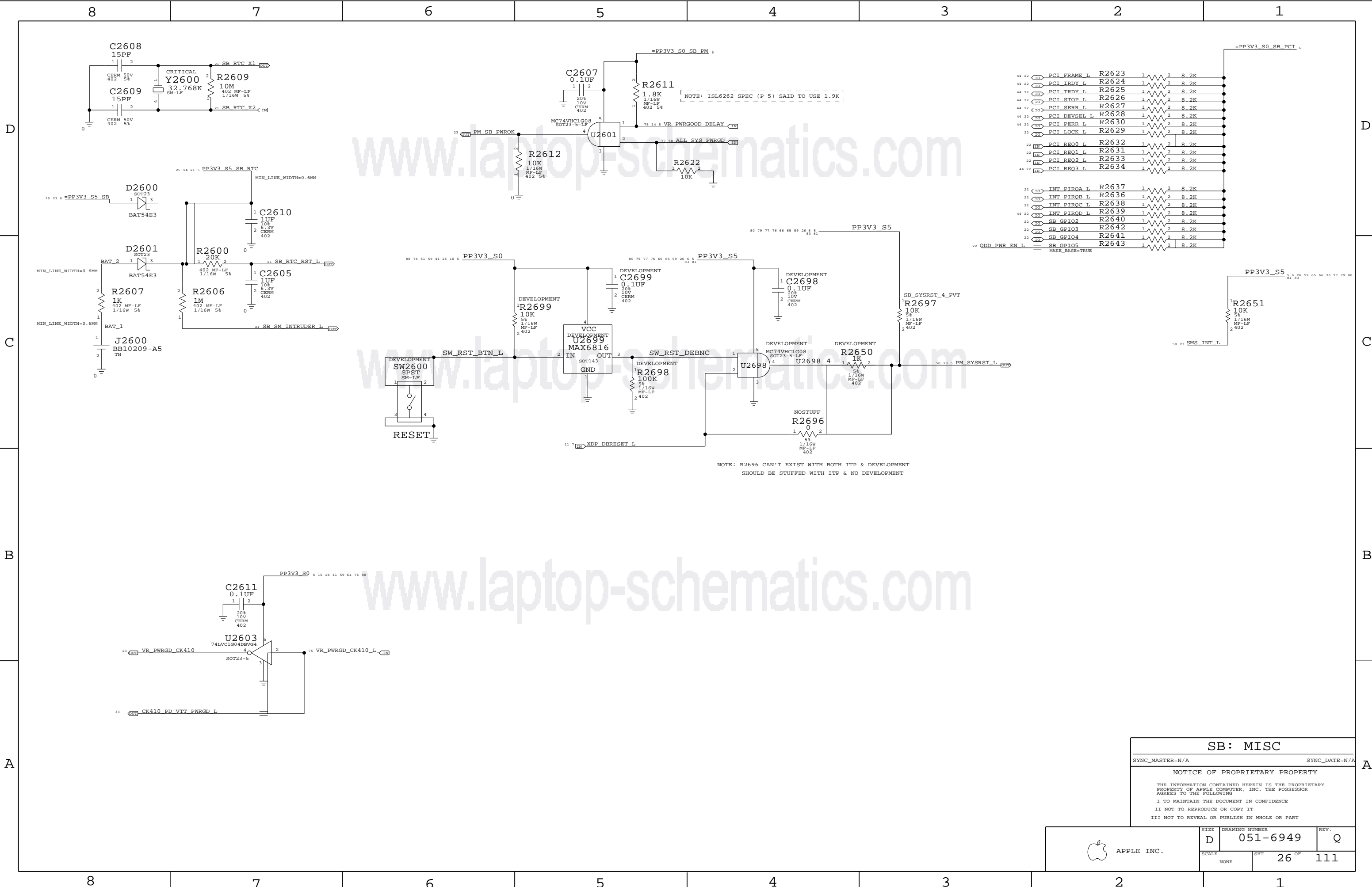
II NOT TO REPRODUCE OR COPY IT

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SB: DECOUPLING			
SYNC_MASTER=N/A		SYNC_DATE=N/A	
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE	NONE	SHT	25 OF 111



SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

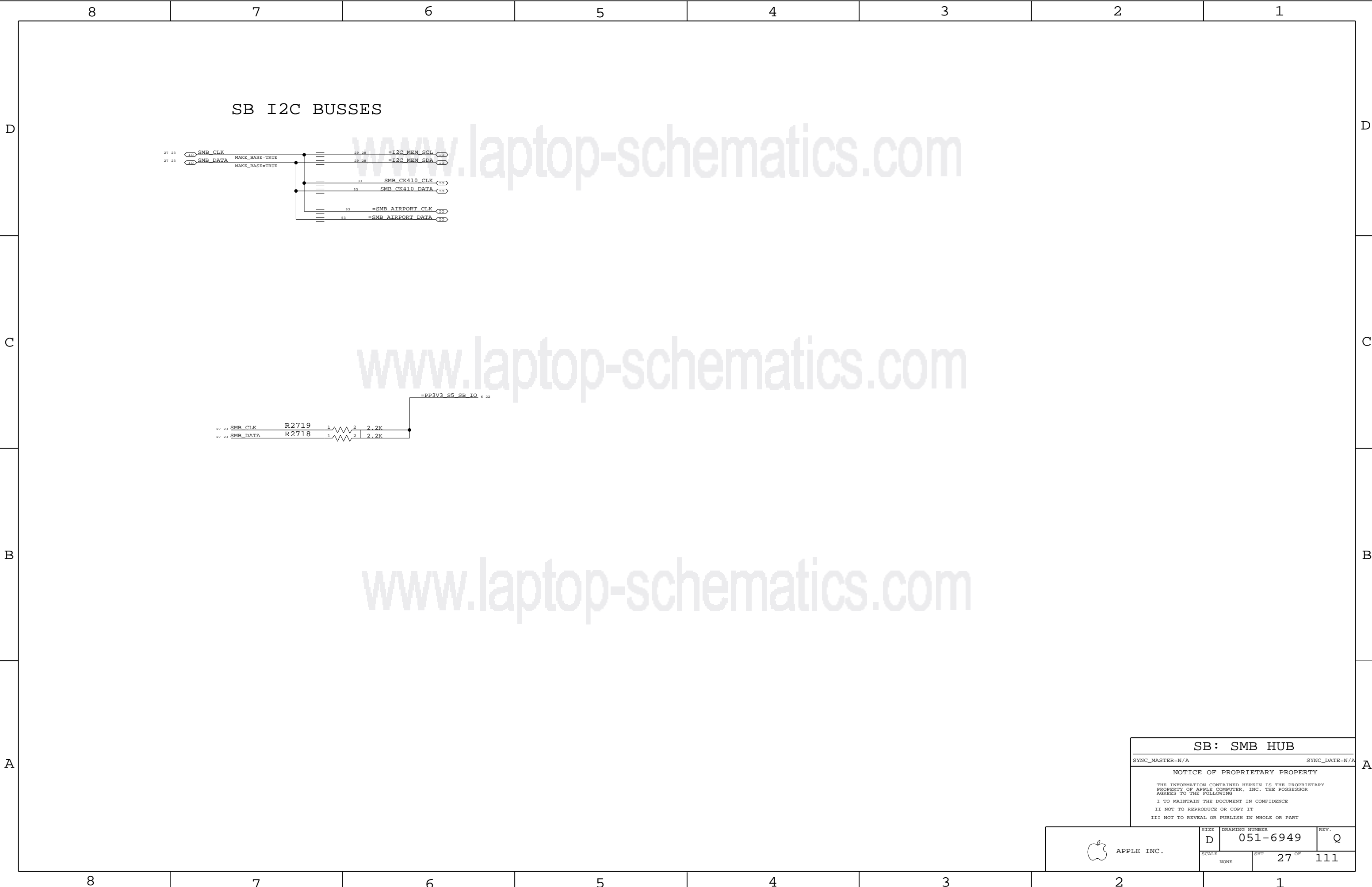
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SB: SMB HUB

SYNC_MASTER=N/A

SYNC_DATE=N/A

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APPLE INC.

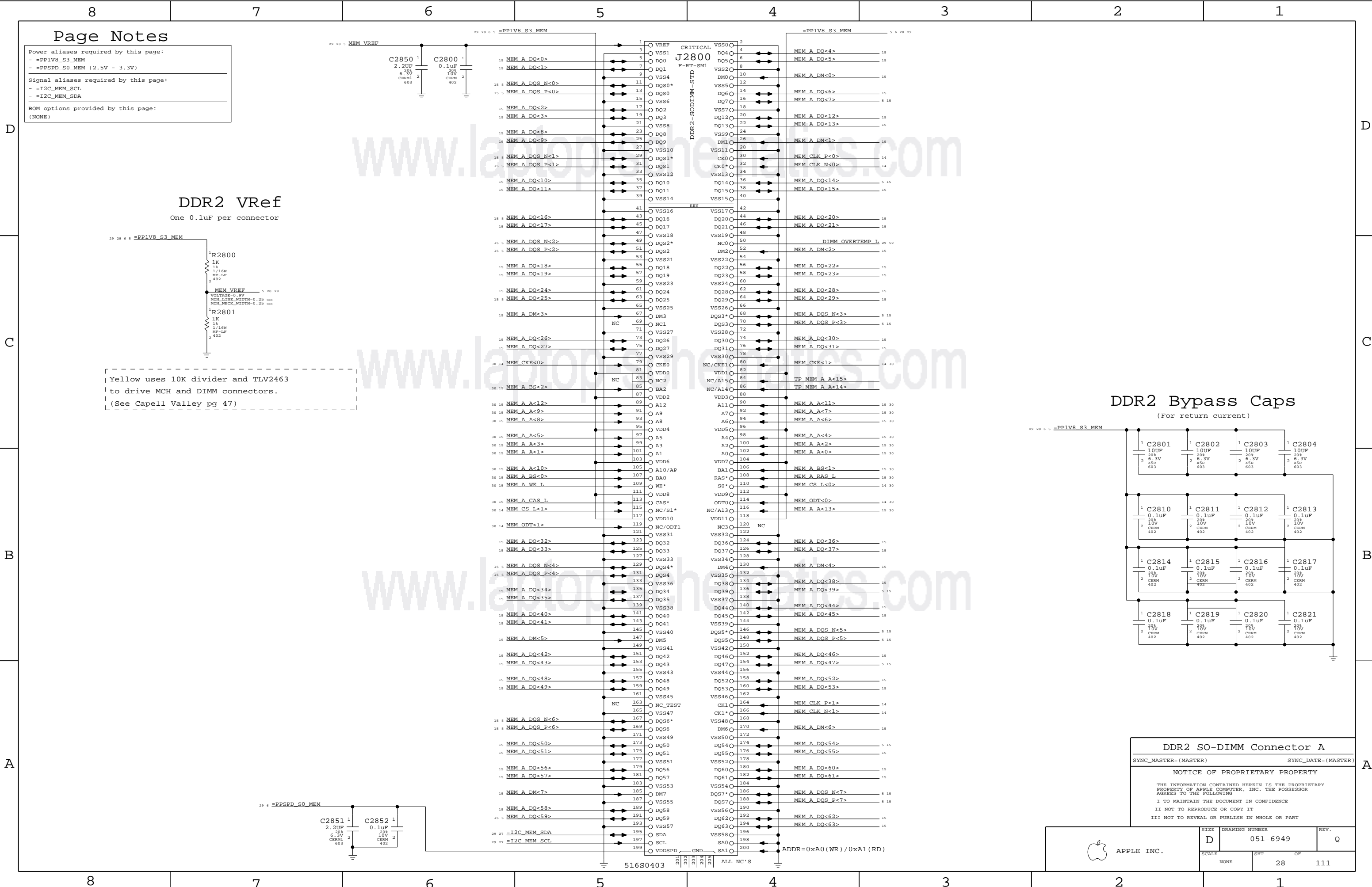
SIZE D

DRAWING NUMBER 051-6949

REV. Q

SCALE NONE

SHT 27 OF 111



Page Notes

Power aliases required by this page:

- =PPIV8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

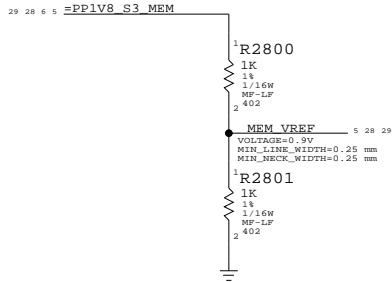
Signal aliases required by this page:

- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:

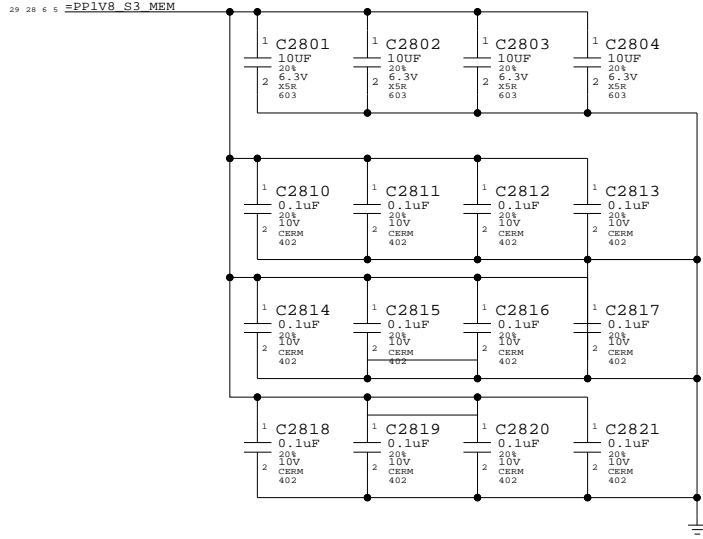
(NONE)

DDR2 VRef
One 0.1uF per connector



Yellow uses 10K divider and TLV2463 to drive MCH and DIMM connectors.
(See Capell Valley pg 47)

DDR2 Bypass Caps
(For return current)



DDR2 SO-DIMM Connector A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

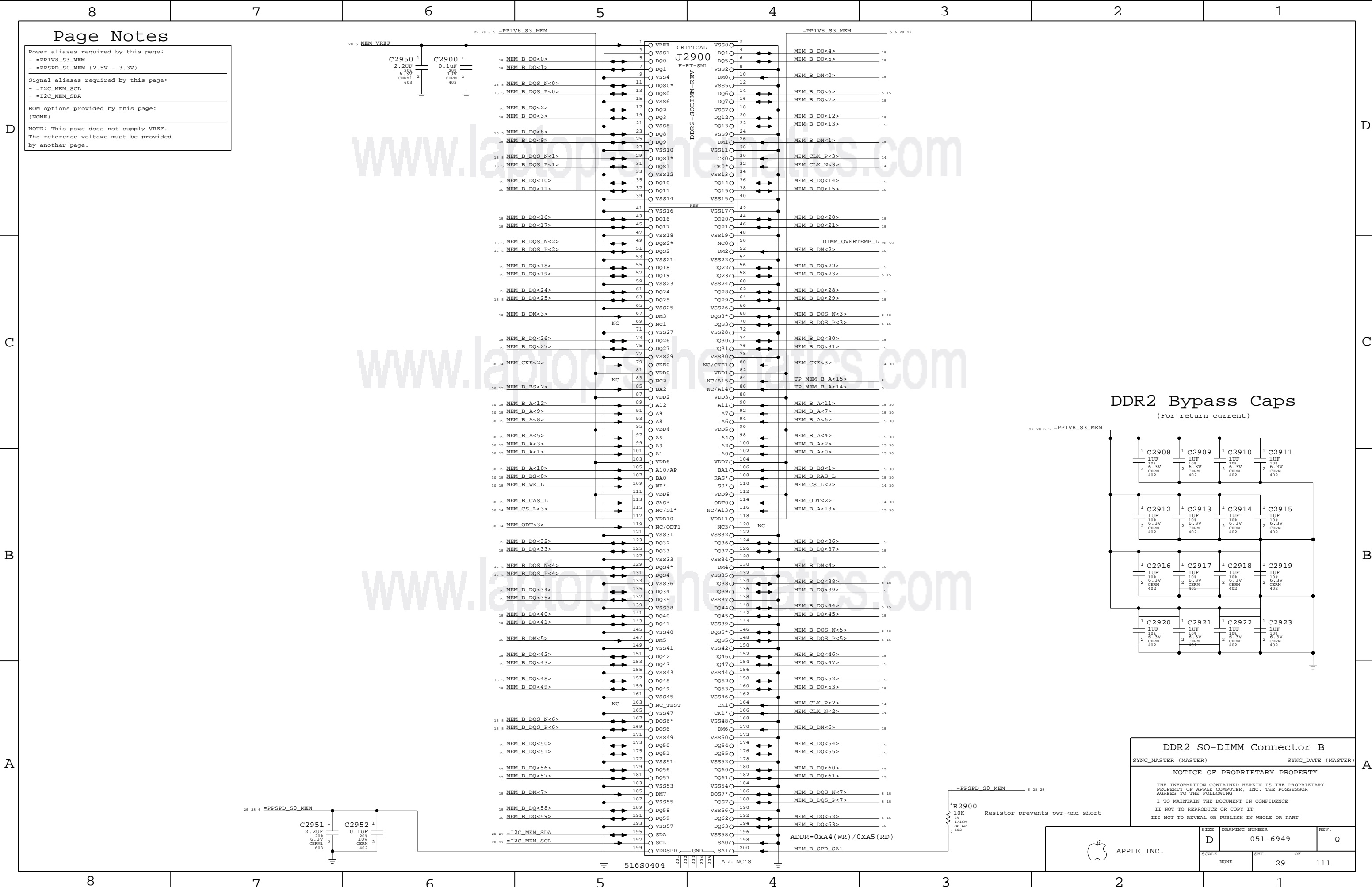
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE		SHT	OF
NONE		28	111



Page Notes

Power aliases required by this page:

- =PP1V8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

Signal aliases required by this page:

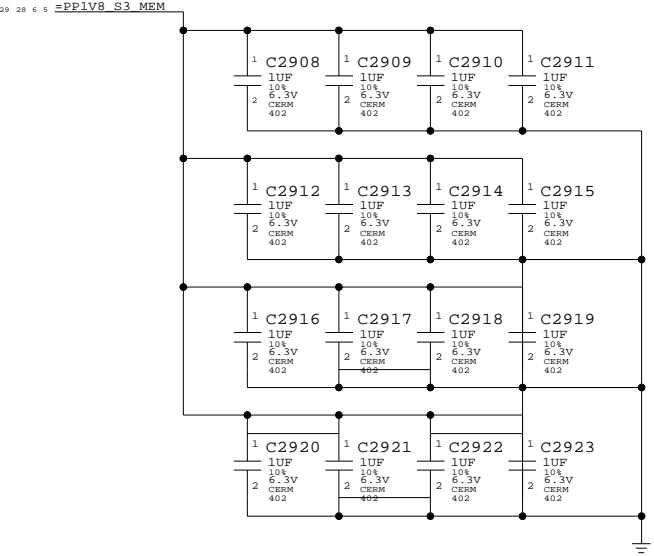
- =I2C_MEM_SCL
- =I2C_MEM_SDA

BOM options provided by this page:

(NONE)

NOTE: This page does not supply VREF. The reference voltage must be provided by another page.

DDR2 Bypass Caps
(For return current)



DDR2 SO-DIMM Connector B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

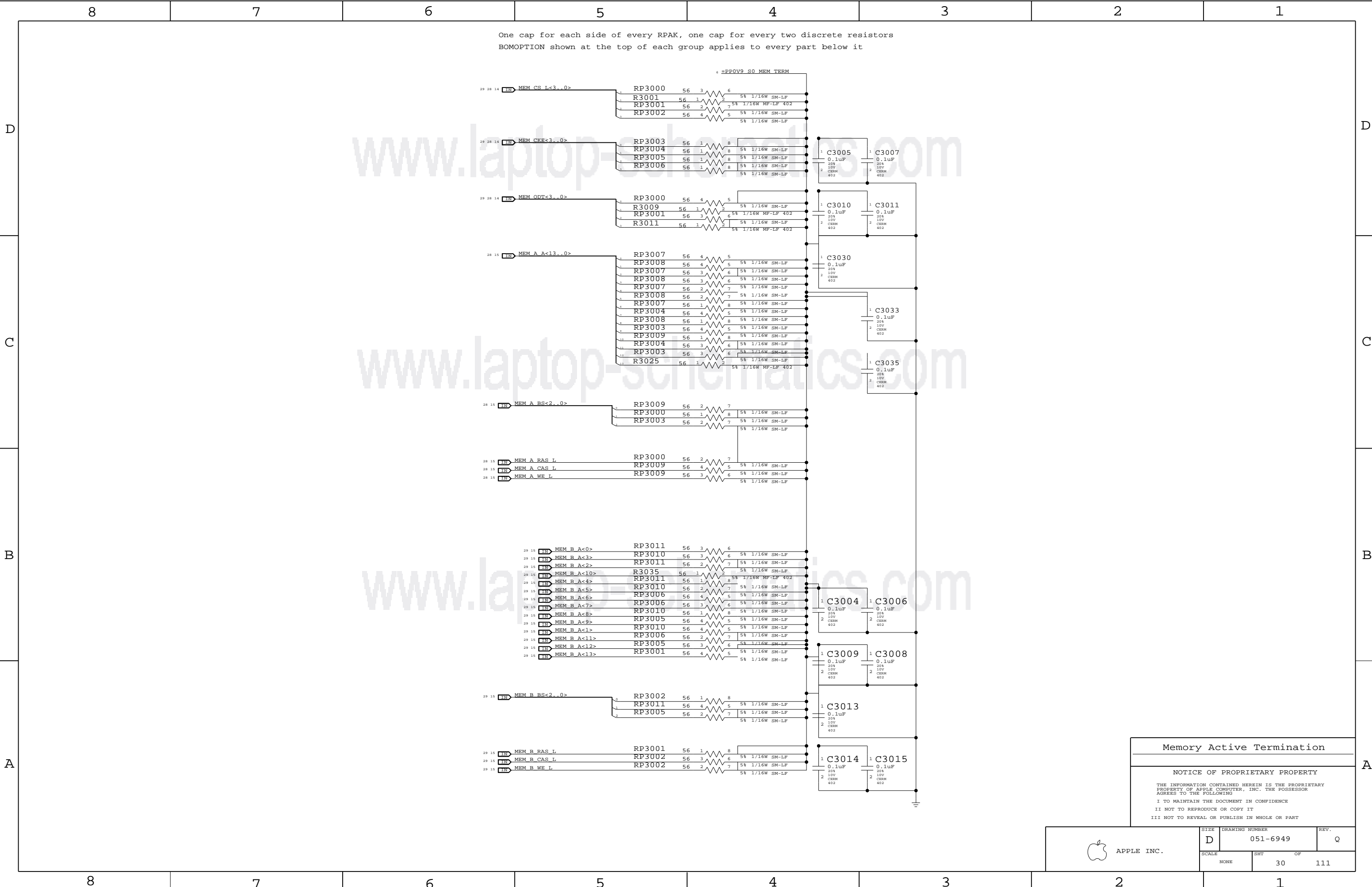
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	D	051-6949	Q
SCALE		SHT	OF
NONE		29	111



Memory Active Termination

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SIZE	DRAWING NUMBER	REV.
D	051-6949	Q
SCALE	SHT	OF
NONE	30	111

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

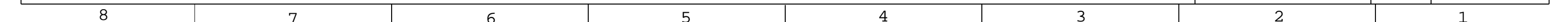
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8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---





 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT OF	
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT OF	
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111


 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT OF	
	NONE	31	111

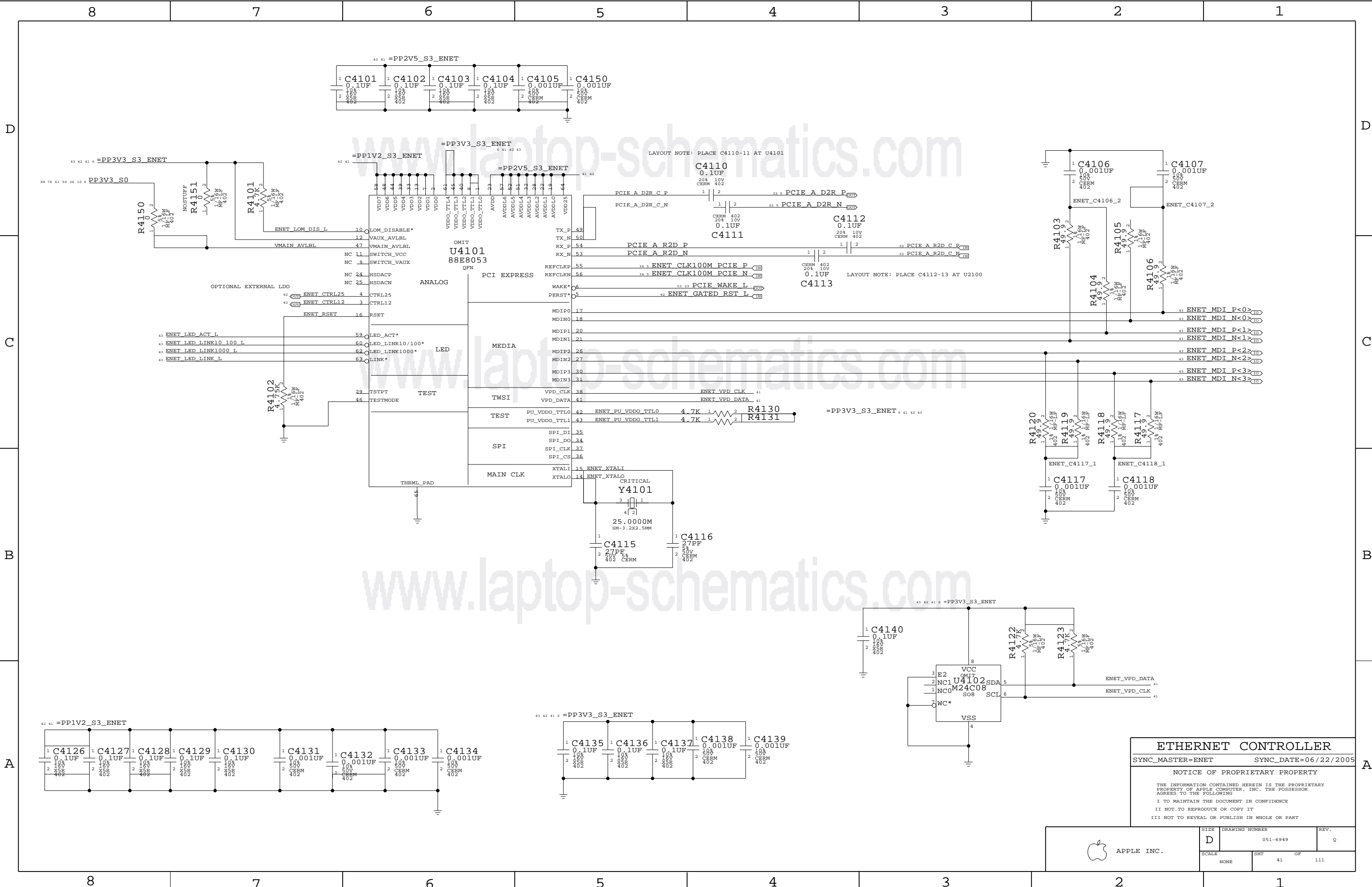
 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT OF	
	NONE	31	111

 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111

 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111

 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111

 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
	SCALE	SHT	OF
	NONE	31	111



ETHERNET CONTROLLER

SYNC_MASTER=ENET SYNC_DATE=06/22/2005

NOTICE OF PROPRIETARY PROPERTY

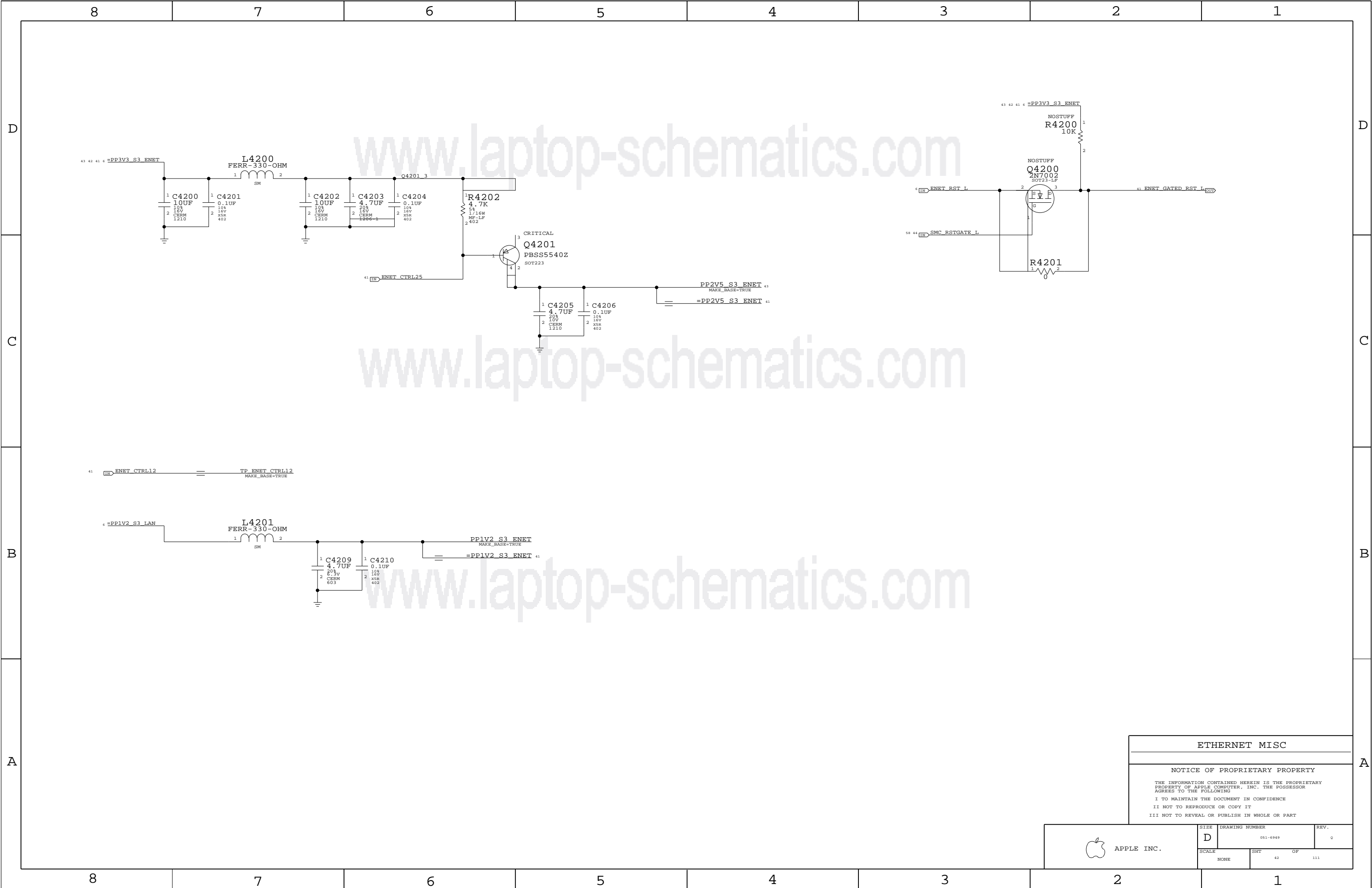
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	D	051-6949	Q
SCALE	NONE		
	SHT	41	OF 111



ETHERNET MISC

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APPLE INC.

SIZE

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DRAWING NUMBER

051-6949

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SCALE

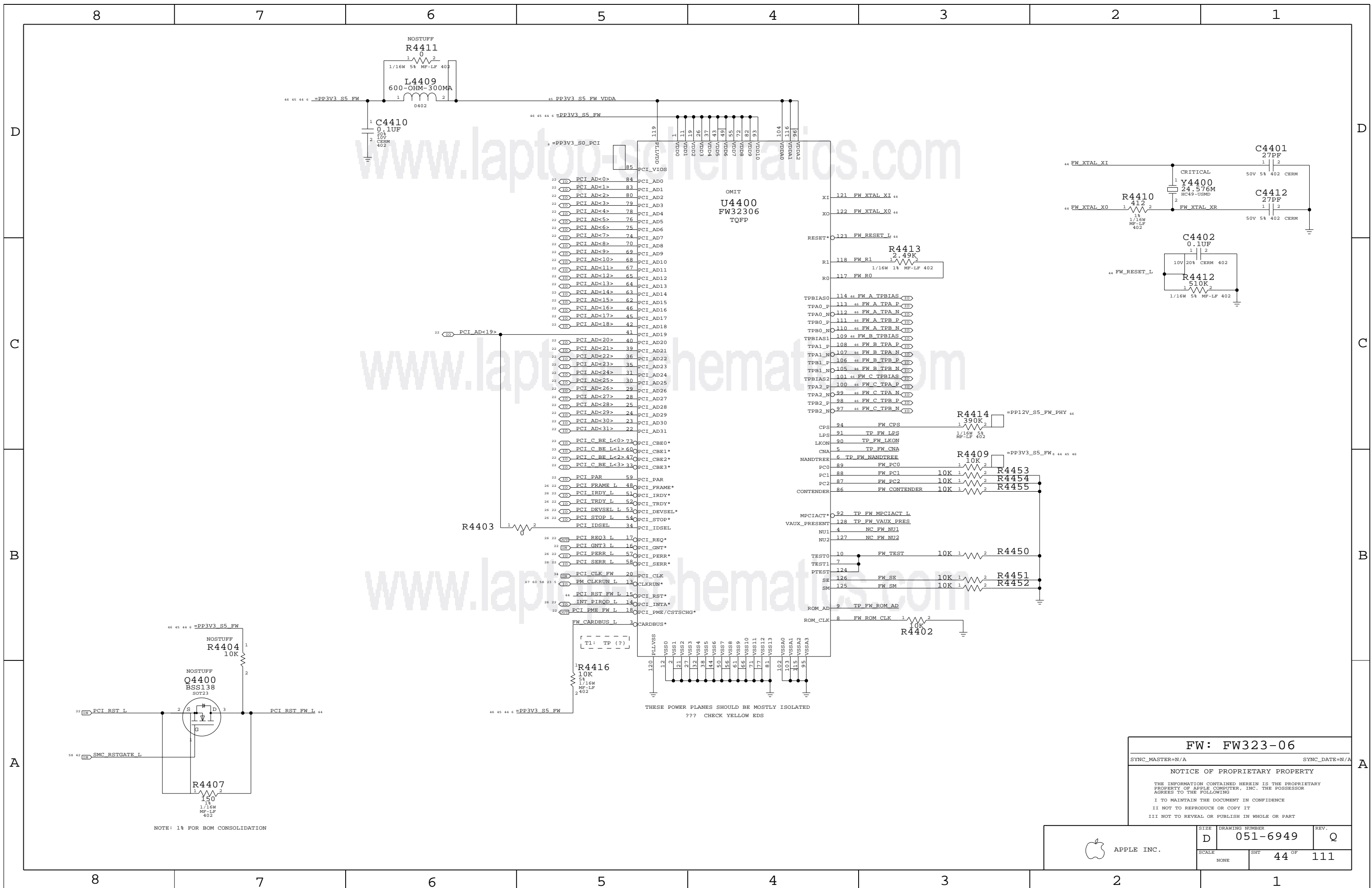
NONE

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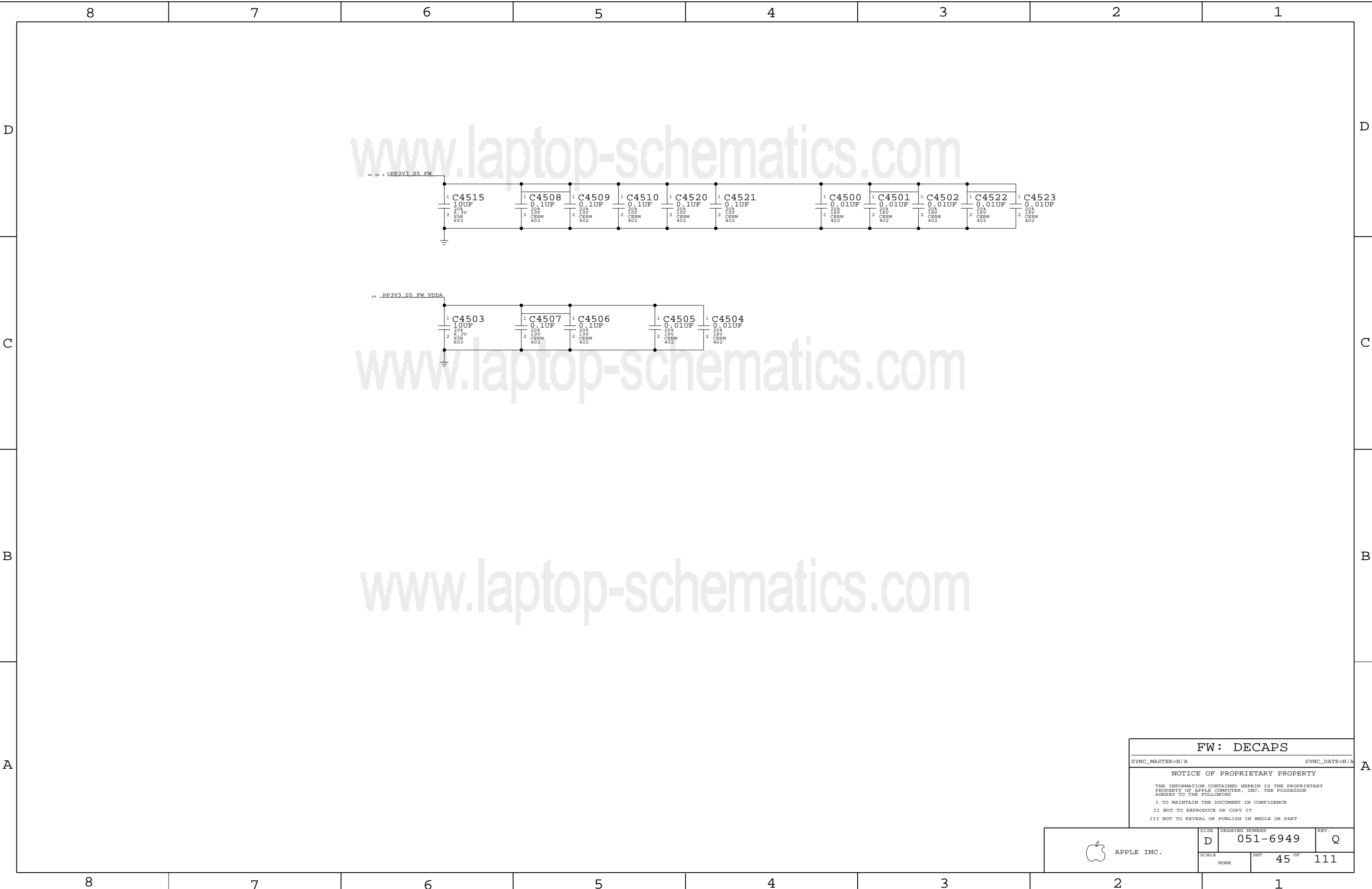
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FW: DECAPS

SYNC_MASTER=N/A SYNC_DATE=N/A

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APPLE INC.

SIZE

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DRAWING NUMBER

051-6949

REV.

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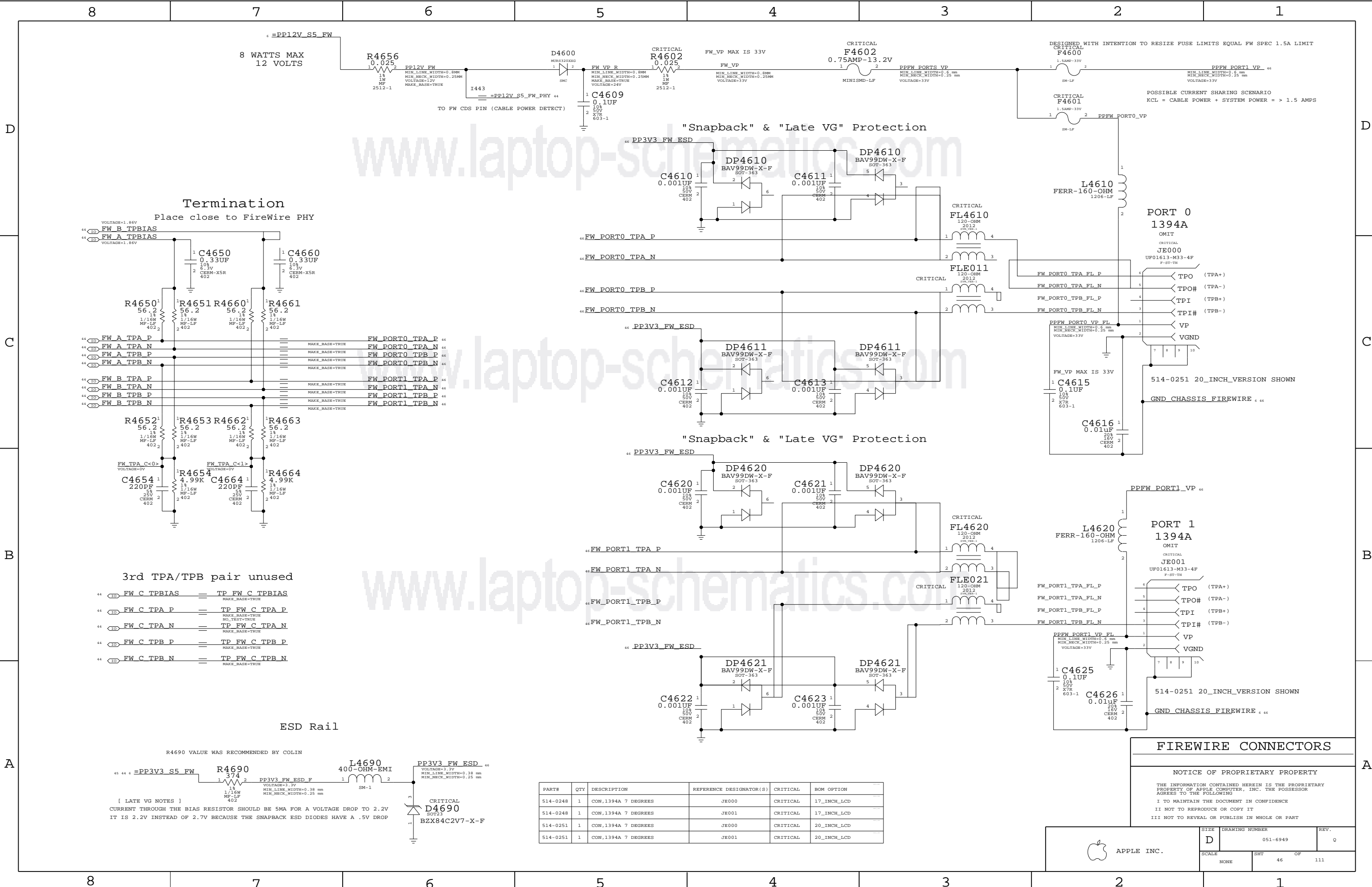
SCALE

NONE

SHT

45 OF

111



Termination

Place close to FireWire PHY

3rd TPA/TPB pair unused

ESD Rail

R4690 VALUE WAS RECOMMENDED BY COLIN

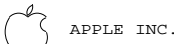
[LATE VG NOTES]
CURRENT THROUGH THE BIAS RESISTOR SHOULD BE 5MA FOR A VOLTAGE DROP TO 2.2V
IT IS 2.2V INSTEAD OF 2.7V BECAUSE THE SNAPBACK ESD DIODES HAVE A .5V DROP

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0248	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	17_INCH_LCD
514-0248	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	17_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	20_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	20_INCH_LCD

FIREWIRE CONNECTORS

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SIZE	DRAWING NUMBER	REV.
D	051-6949	Q
SCALE	SHT	OF
NONE	46	111

External USB Ports

D

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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0294	3	USB RECEPTACLE,4P,UB1123-M23-4F	JE310,JE320,JE330	CRITICAL	17_INCH_LCD
514-0295	3	USB RECEPTACLE,4P,UB1123-M33-4F	JE310,JE320,JE330	CRITICAL	20_INCH_LCD
35381370	1	IC,TPS2023DG4,ANALOG SW,8 PIN	SOIC U4700	CRITICAL	

SB HAS INTERNAL 15K PULL-DOWNS

SB HAS INTERNAL 15K PULL-DOWNS

SB HAS INTERNAL 15K PULL-DOWNS

LAYOUT NOTE:
PLACE C4743, C4797 & L4740
NEAR JE350 PIN 14 IN THE
ORDER LISTED, AND NOT ON
BOTH SIDES OF THE PIN.
PLACE C4742 CLOSED TO JE350.

FHB CONNECTOR

BLUETOOTH

USB Device Interfaces

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APPLE INC.

SIZE

DRAWING NUMBER

REV.

D

051-6949

Q

SCALE

SHT

OF

111

8		7		6		5		4		3		2		1											
D		www.laptop-schematics.com														D									
C		www.laptop-schematics.com														C									
B		www.laptop-schematics.com														B									
A																A									
8		7		6		5		4		3		2		1											
<div>APPLE INC.</div>																<div>051-6949</div>		<div>Q</div>							
<div>SCALE</div>																<div>48</div>		<div>111</div>							
<div>BLANK</div>																<div>NOTICE OF PROPRIETARY PROPERTY</div>		<div>THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING</div>		<div>I TO MAINTAIN THE DOCUMENT IN CONFIDENCE</div>		<div>II NOT TO REPRODUCE OR COPY IT</div>		<div>III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</div>	

BLANK


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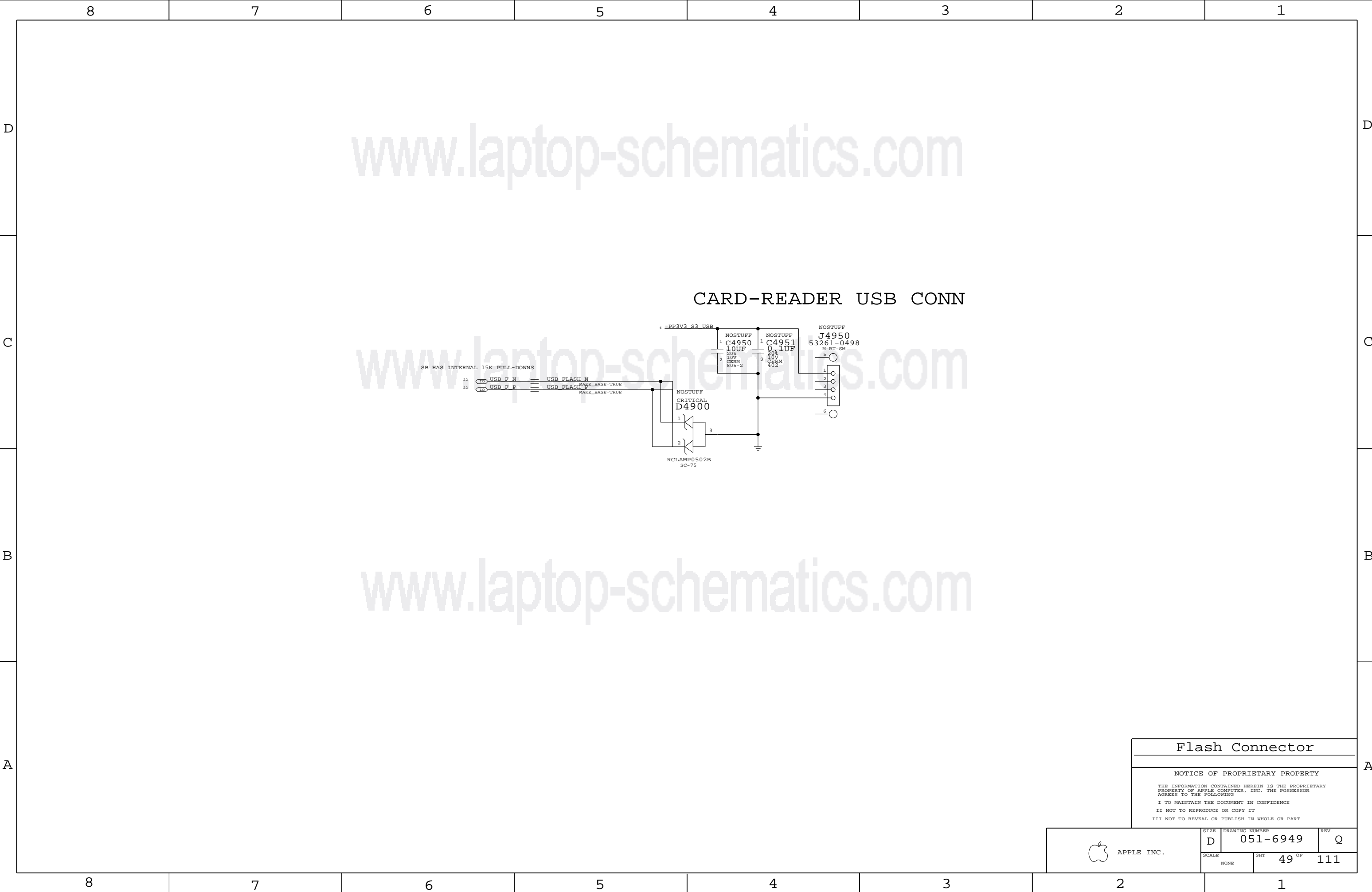
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APPLE INC.

SIZE	D	DRAWING NUMBER	051-6949	REV.	Q
SCALE	NONE	SHT	48 OF	111	



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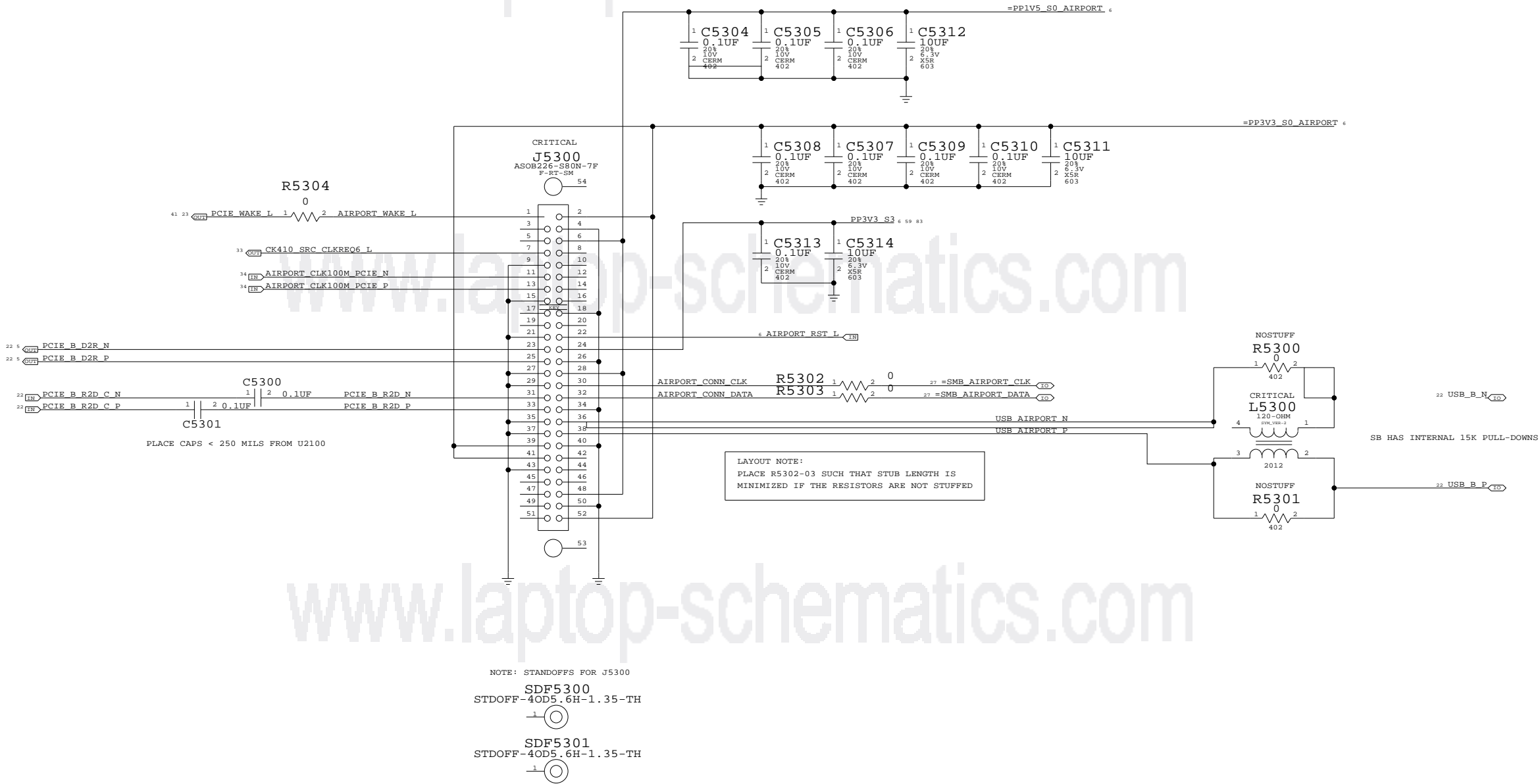
A

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AIRPORT CONN

SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

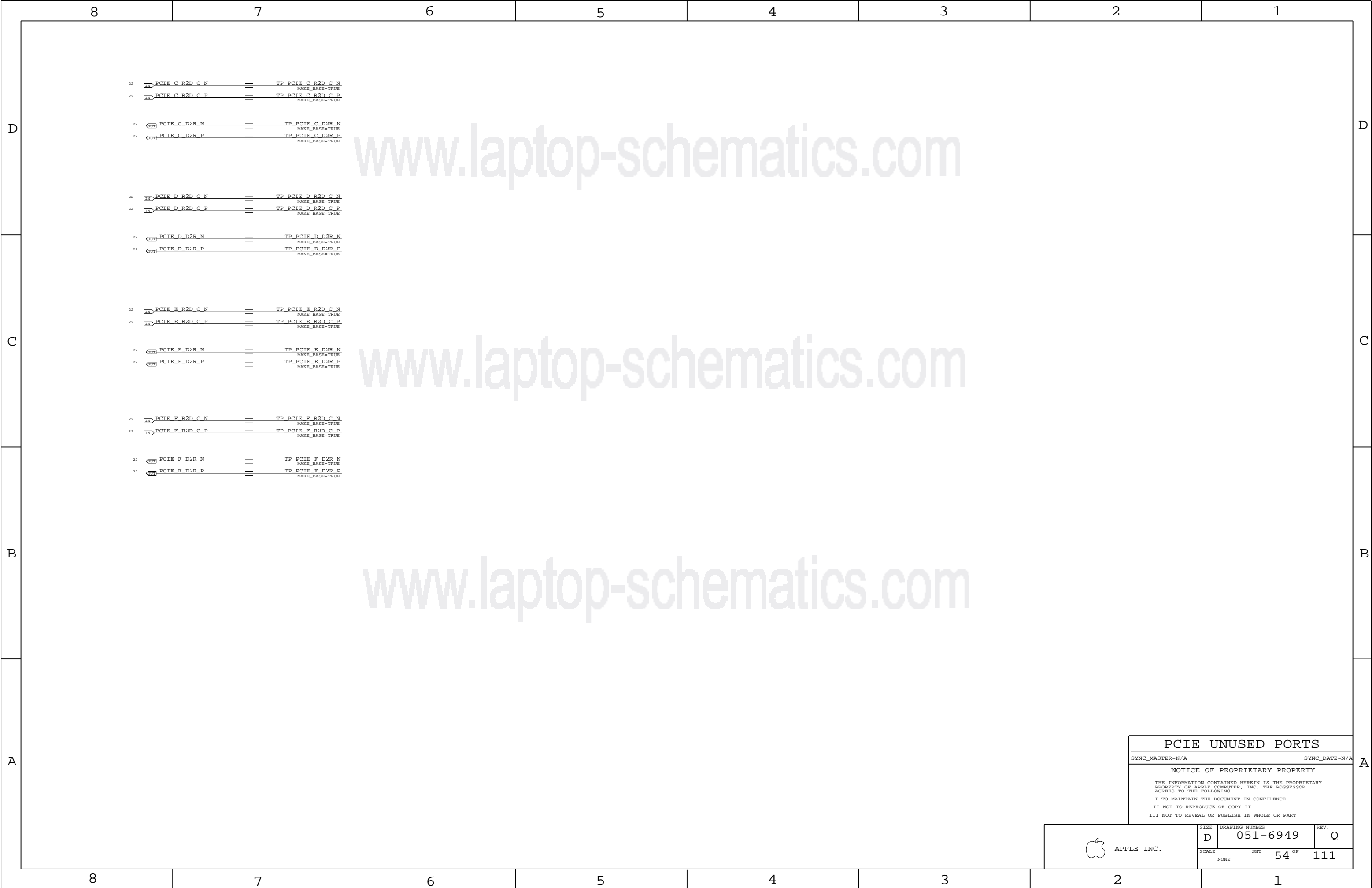
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

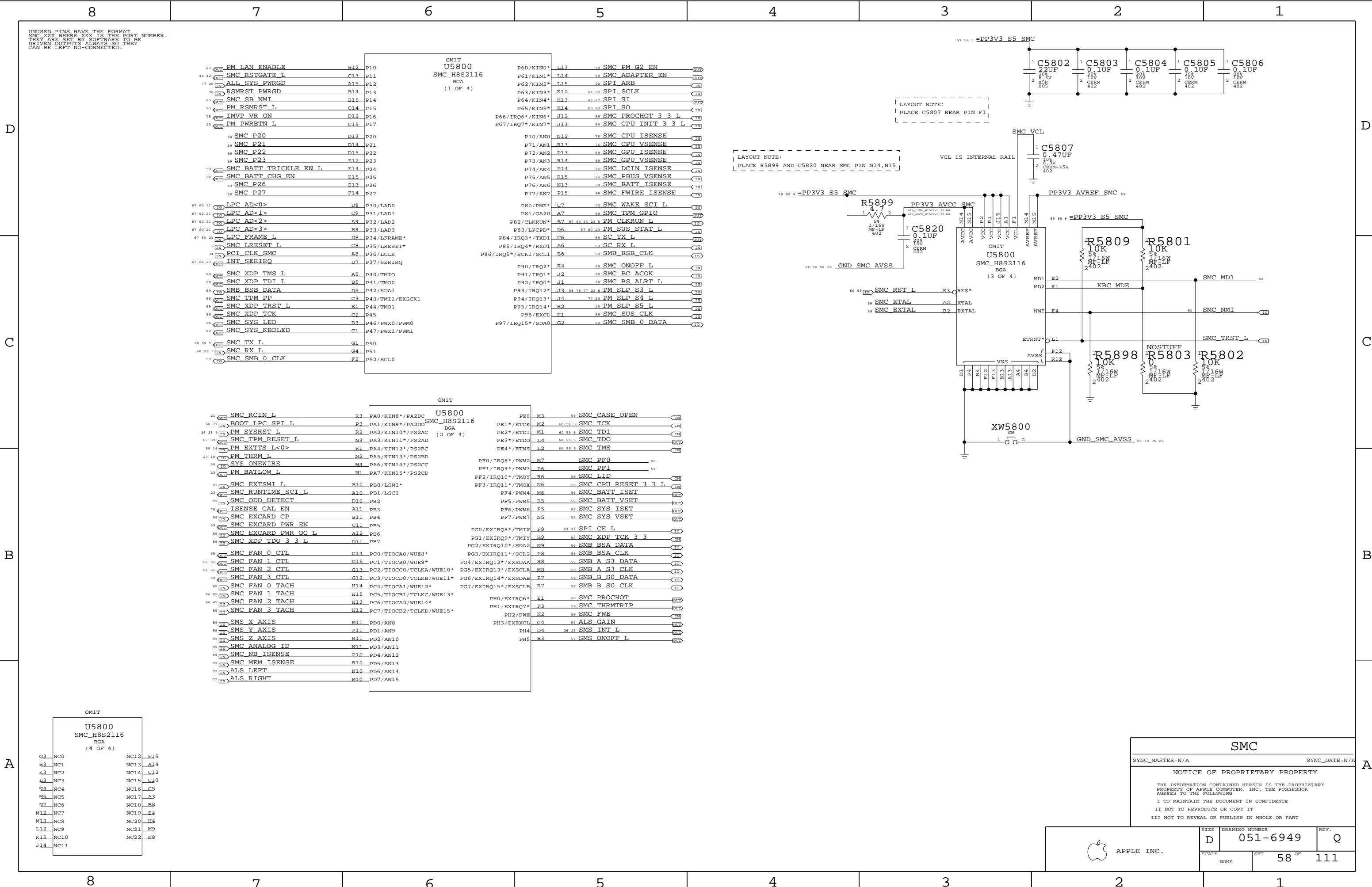
I TO MAINTAIN THE DOCUMENT IN CONFIDENCE

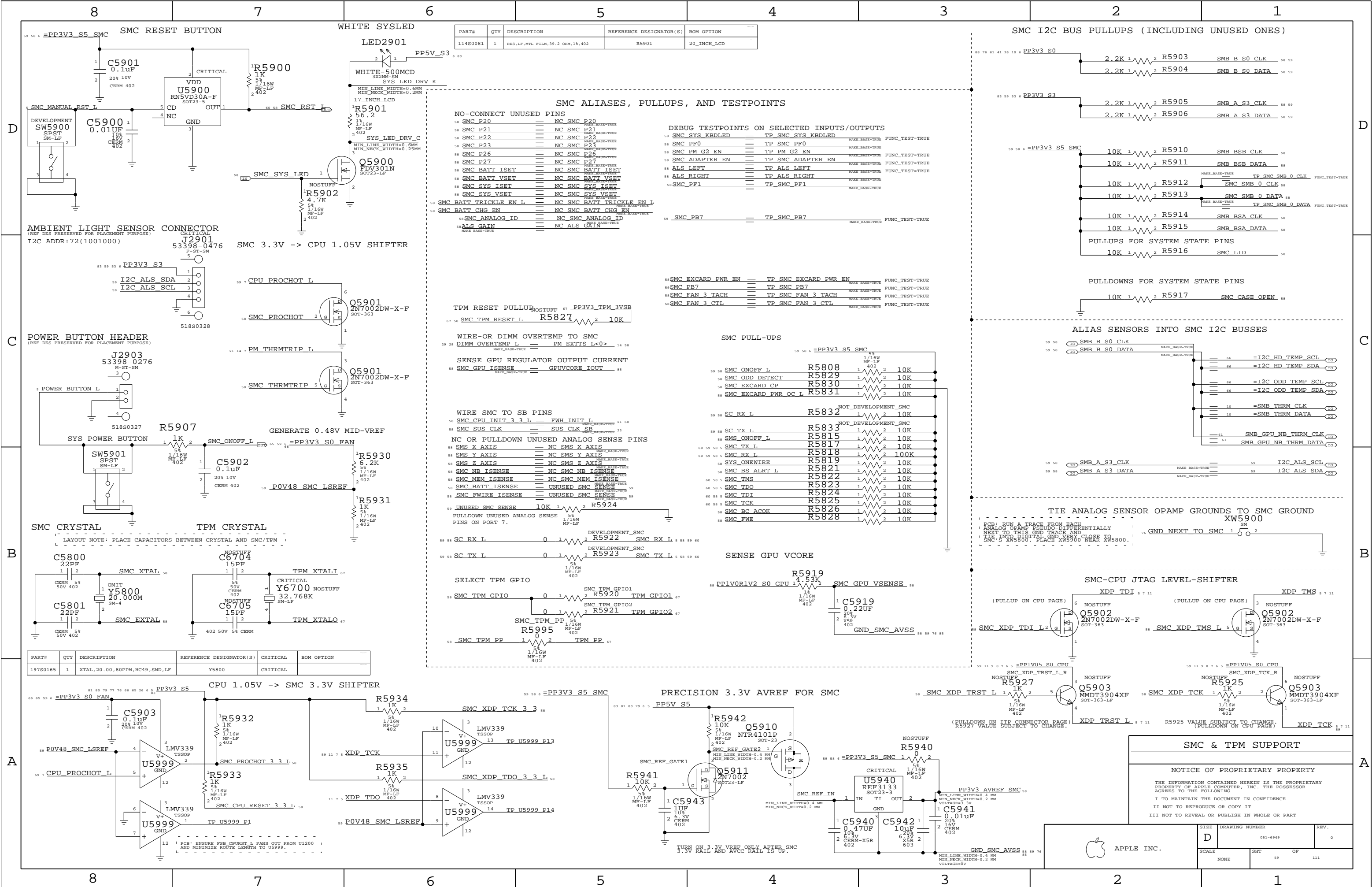
II NOT TO REPRODUCE OR COPY IT

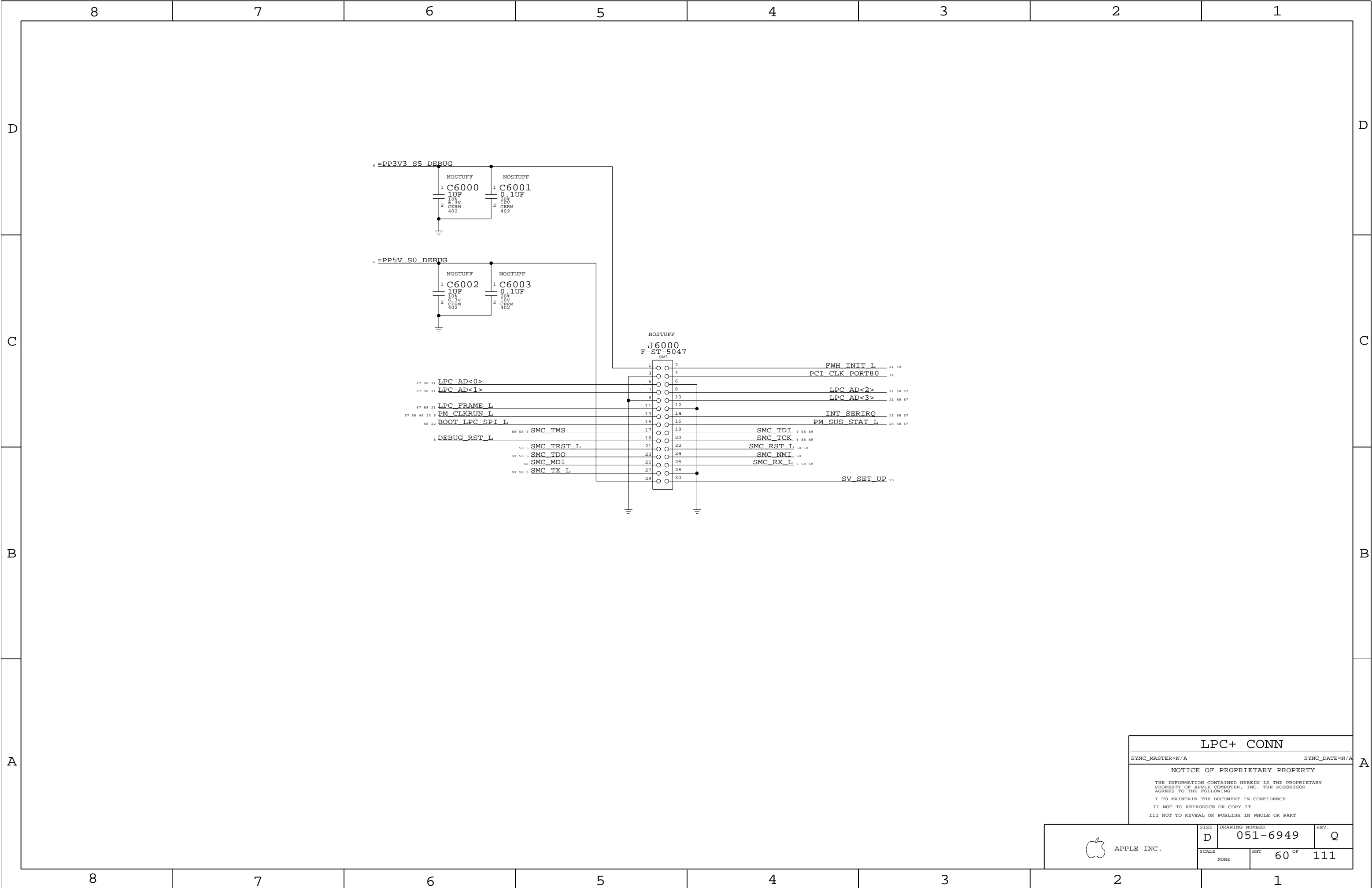
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

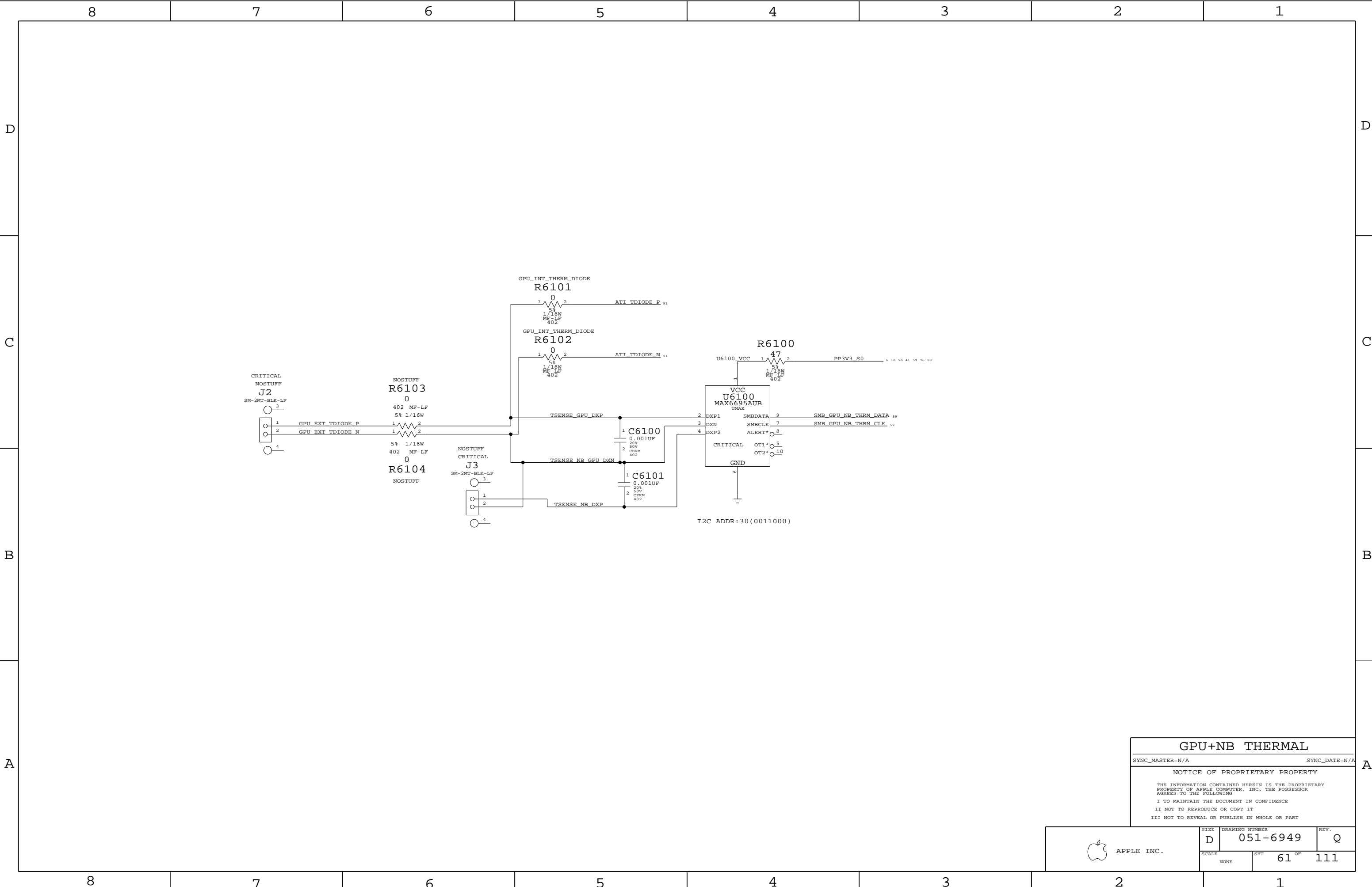
APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE		SHT	53 OF 111
NONE			

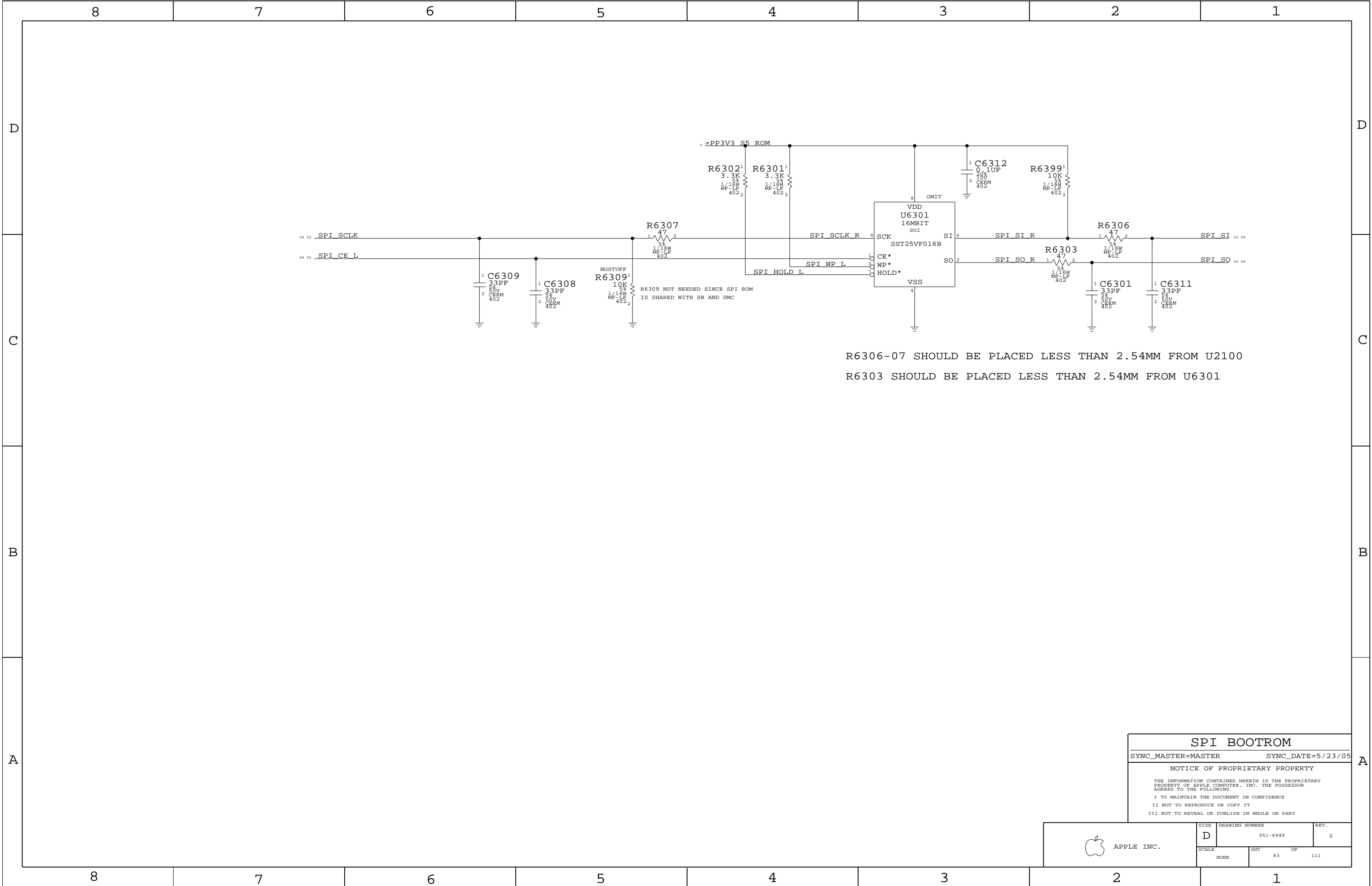


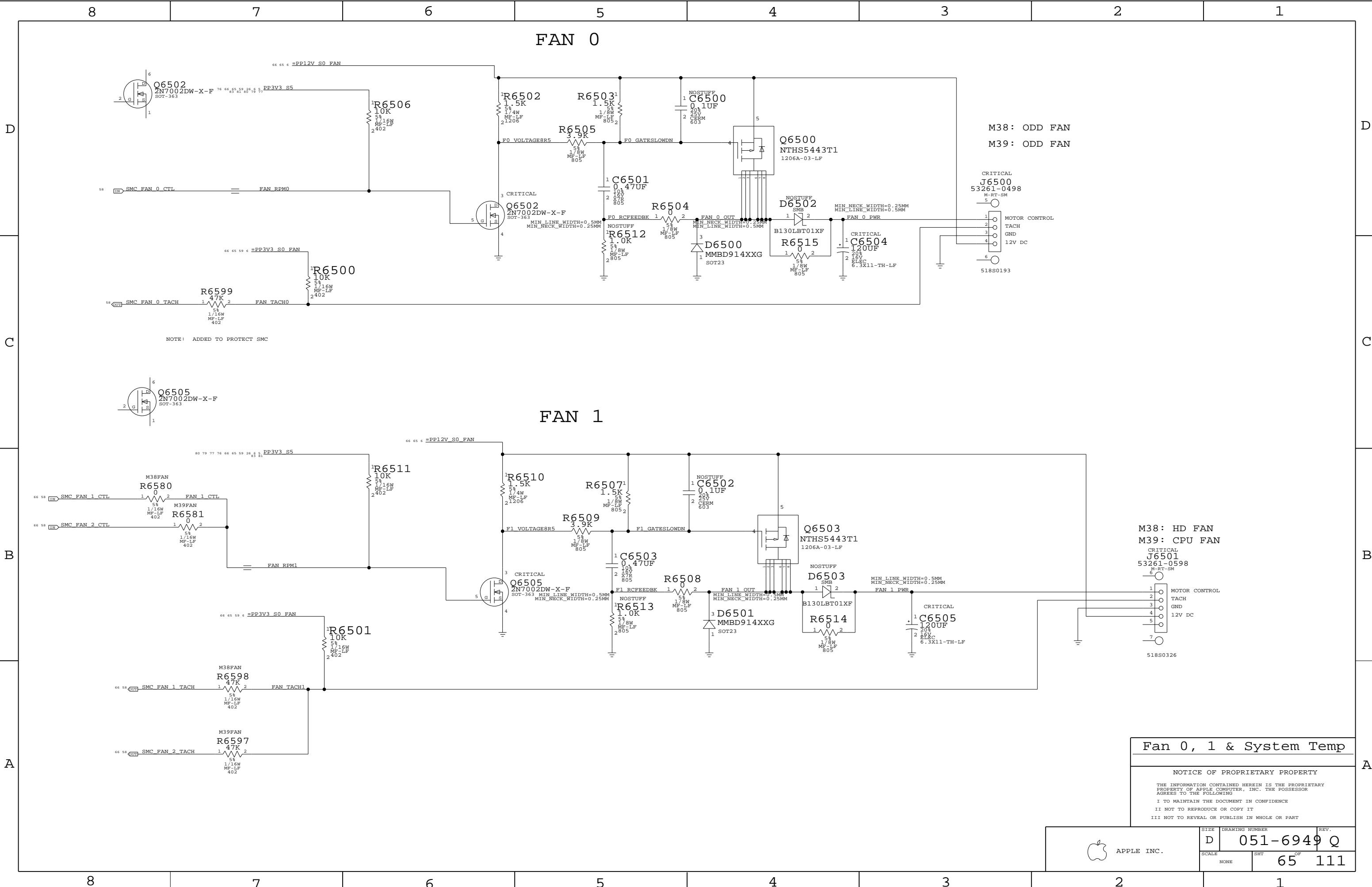












Fan 0, 1 & System Temp

NOTICE OF PROPRIETARY PROPERTY

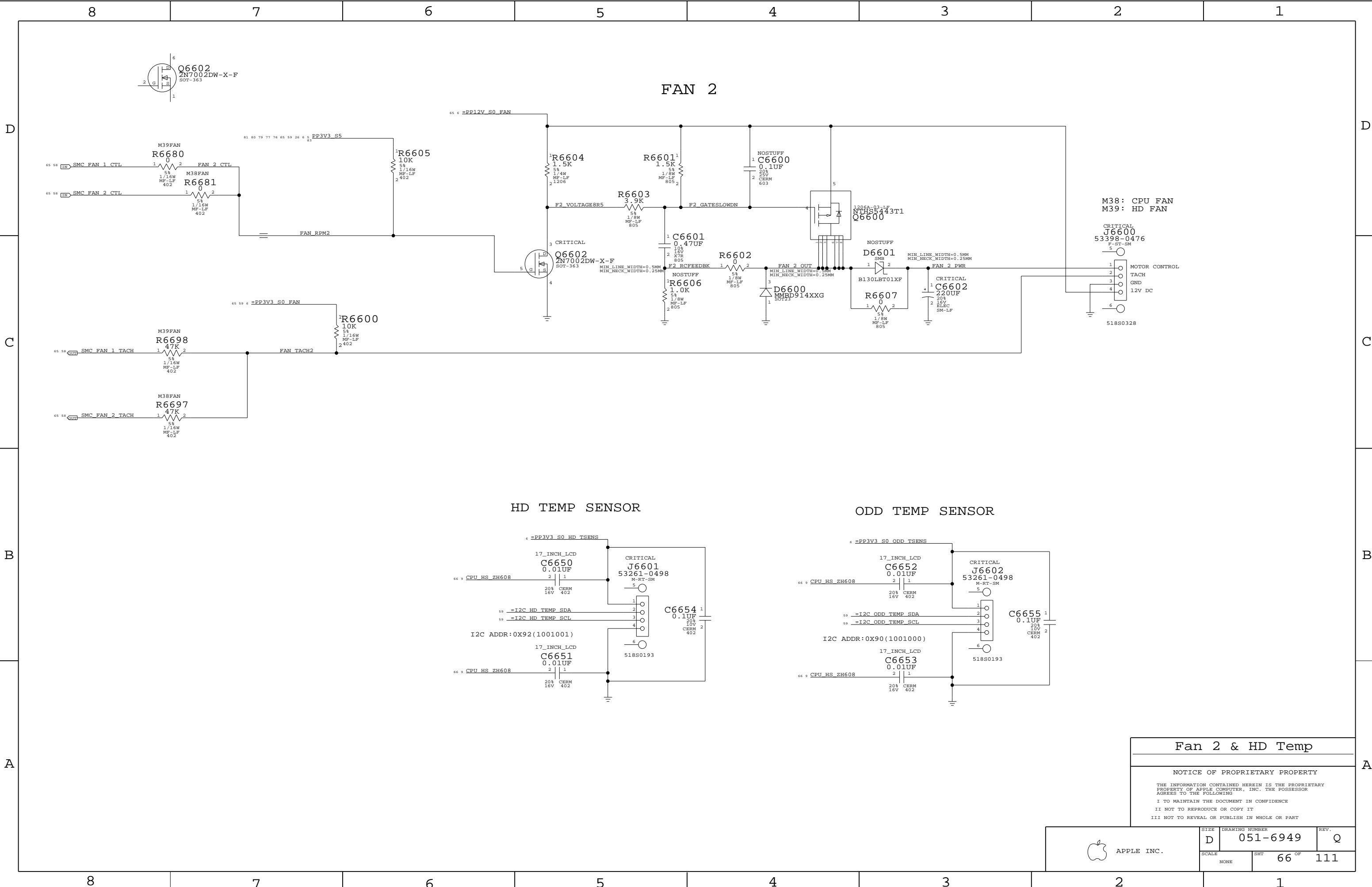
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING

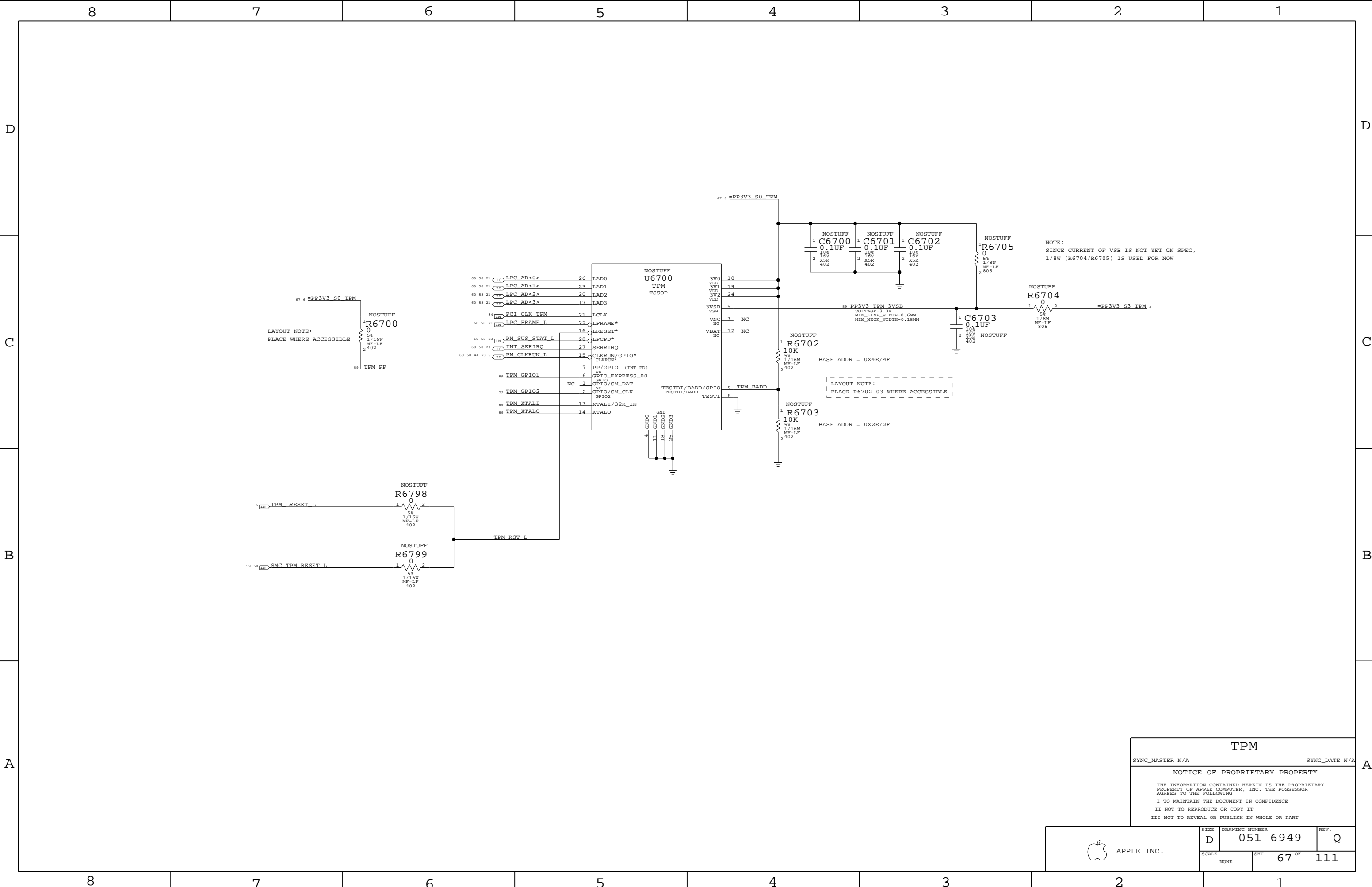
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE	SHT		REV.
	NONE		65 OF 111





TPM

SYNC_MASTER=N/A SYNC_DATE=N/A

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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE		SHT	67 OF 111
NONE			

D

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B

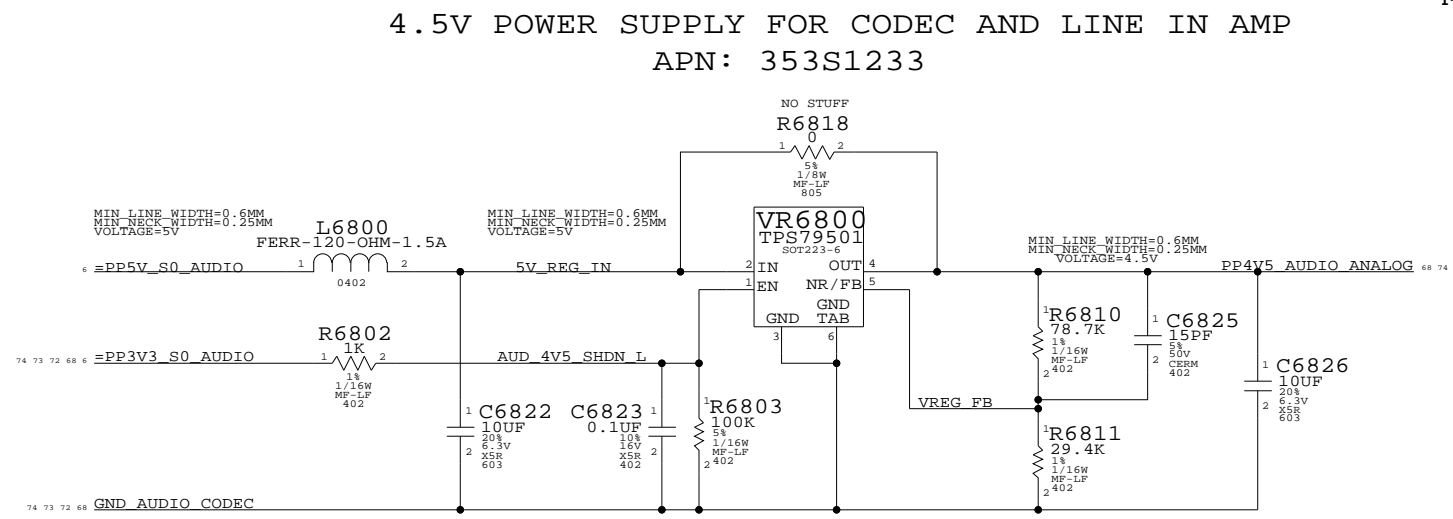
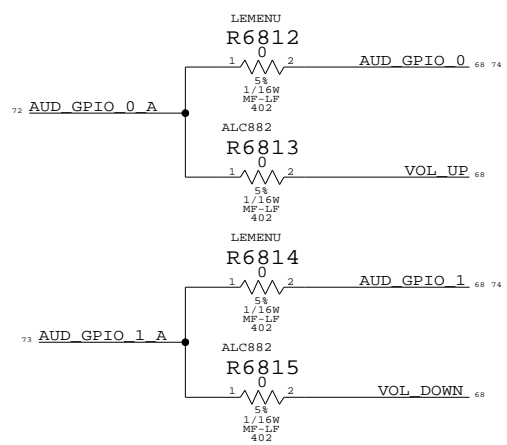
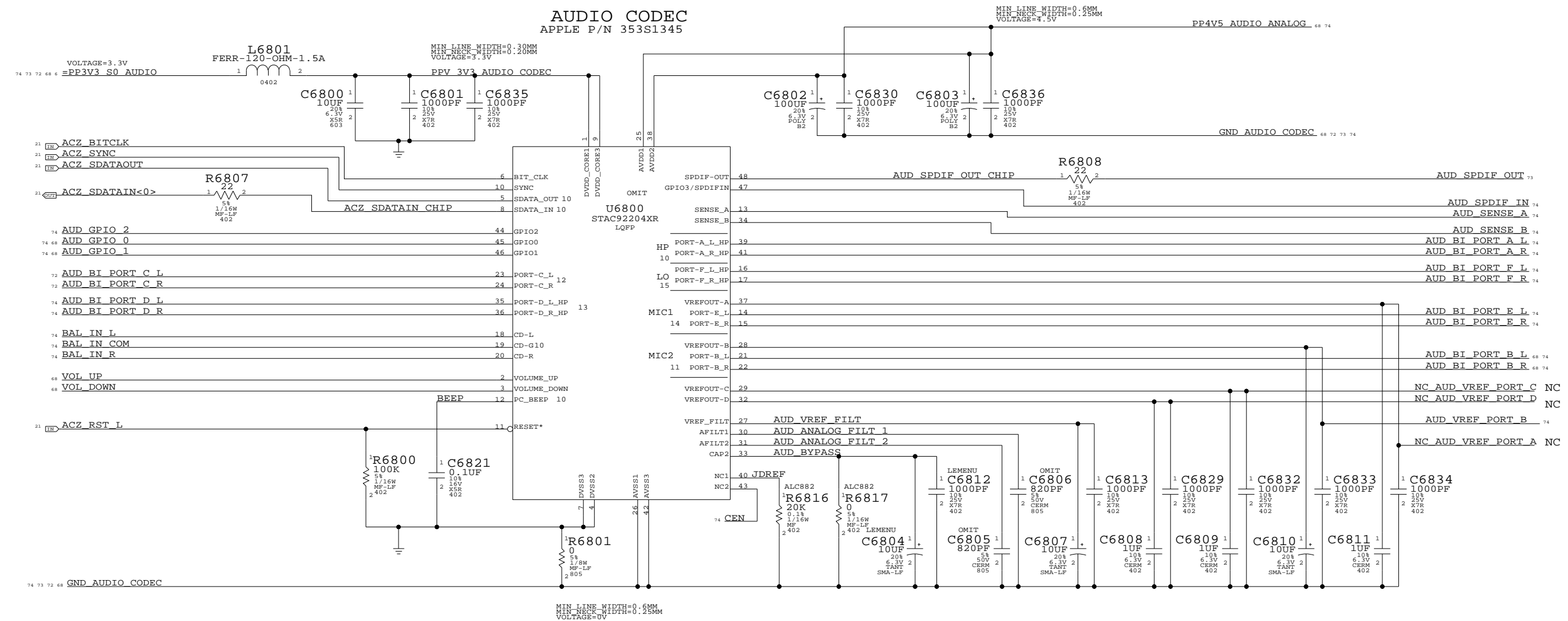
A

D

C

B

A



AUDIO: CODEC

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005


NOTICE OF PROPRIETARY PROPERTY

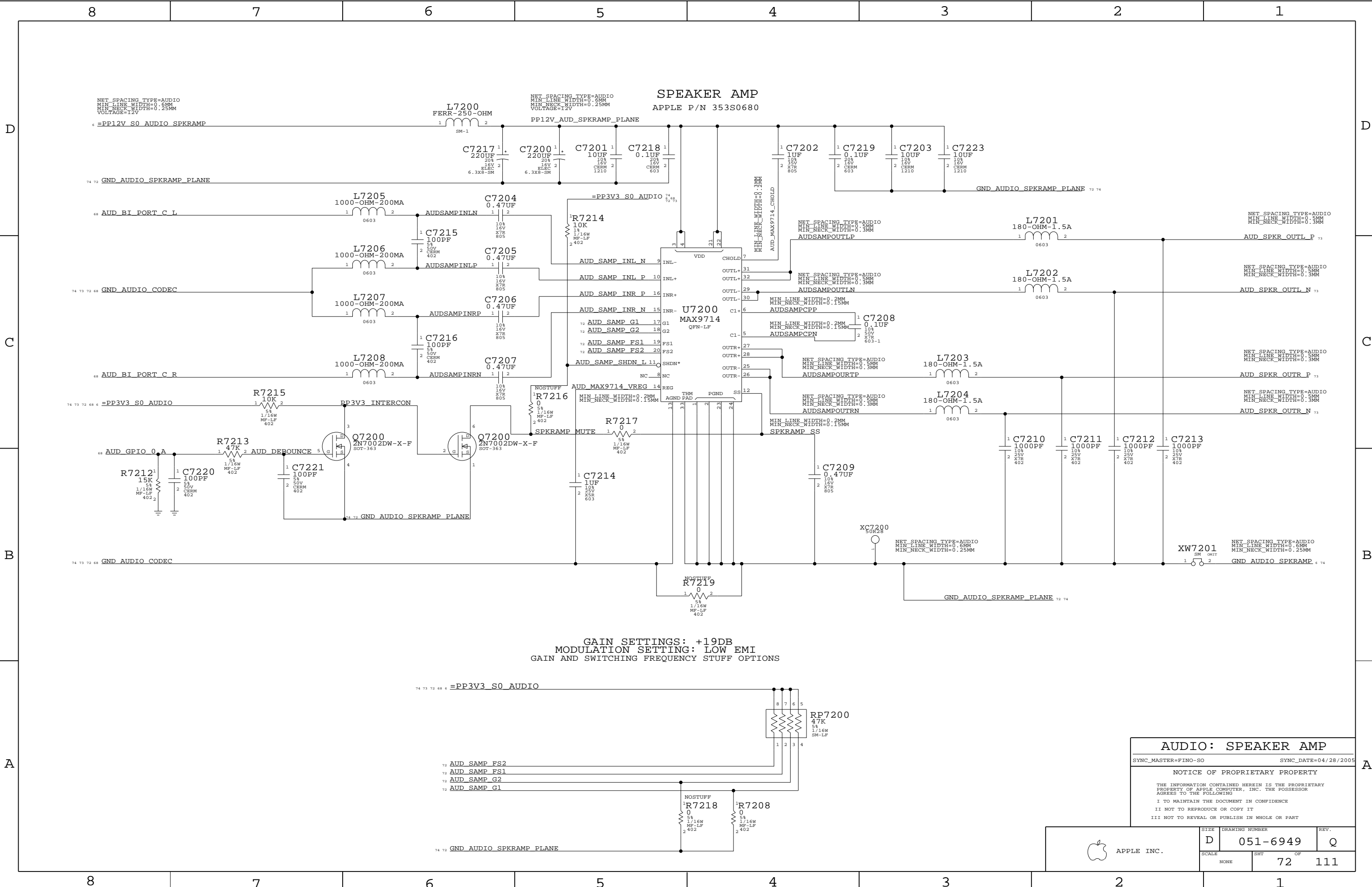
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 APPLE INC.	SIZE D	DRAWING NUMBER 051-6949	REV. Q
	SCALE NONE	SHT 68	OF 111



GAIN SETTINGS: +19DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

AUDIO: SPEAKER AMP

SYNC_MASTER=FINO-SO

SYNC_DATE=04/28/2005


NOTICE OF PROPRIETARY PROPERTY

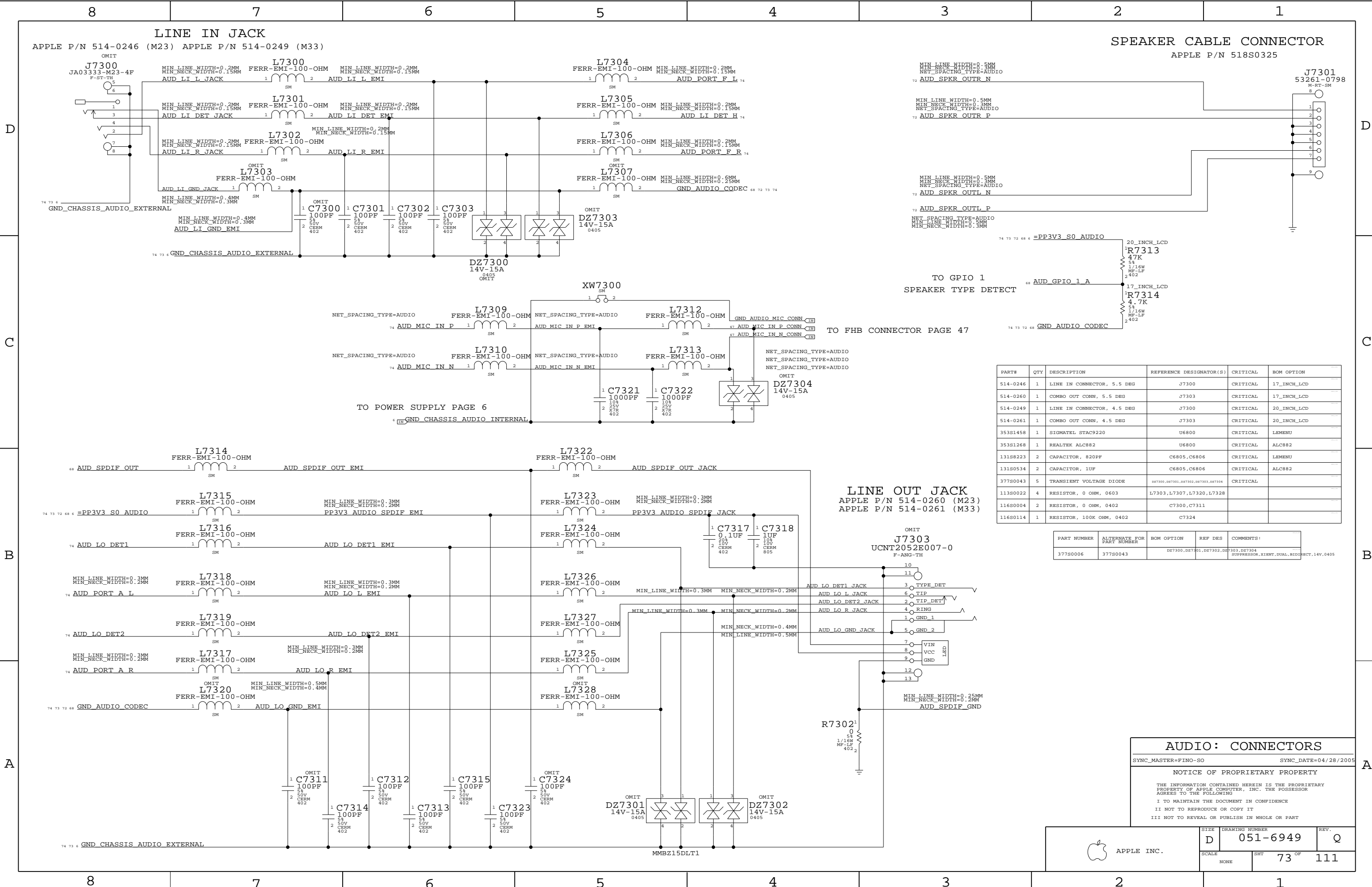
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 APPLE INC.	SIZE	DRAWING NUMBER		REV.
	D	051-6949		Q
SCALE		SHT	OF	
NONE		72	111	



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0246	1	LINE IN CONNECTOR, 5.5 DEG	J7300	CRITICAL	17_INCH_LCD
514-0260	1	COMBO OUT CONN, 5.5 DEG	J7303	CRITICAL	17_INCH_LCD
514-0249	1	LINE IN CONNECTOR, 4.5 DEG	J7300	CRITICAL	20_INCH_LCD
514-0261	1	COMBO OUT CONN, 4.5 DEG	J7303	CRITICAL	20_INCH_LCD
353S1458	1	SIGMATEL STAC9220	U6800	CRITICAL	LEMENU
353S1268	1	REALTEK ALC882	U6800	CRITICAL	ALC882
131S8223	2	CAPACITOR, 820PF	C6805,C6806	CRITICAL	LEMENU
131S0534	2	CAPACITOR, 1UF	C6805,C6806	CRITICAL	ALC882
377S0043	5	TRANSIENT VOLTAGE DIODE	DZ7300,DZ7301,DZ7302,DZ7303,DZ7304	CRITICAL	
113S0022	4	RESISTOR, 0 OHM, 0603	L7303,L7307,L7320,L7328		
116S0004	2	RESISTOR, 0 OHM, 0402	C7300,C7311		
116S0114	1	RESISTOR, 100K OHM, 0402	C7324		

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S0006	377S0043		DZ7300,DZ7301,DZ7302,DZ7303,DZ7304	SUPPRESSOR,XIENT,DUAL,BIDIRECT,14V,0405

AUDIO: CONNECTORS

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

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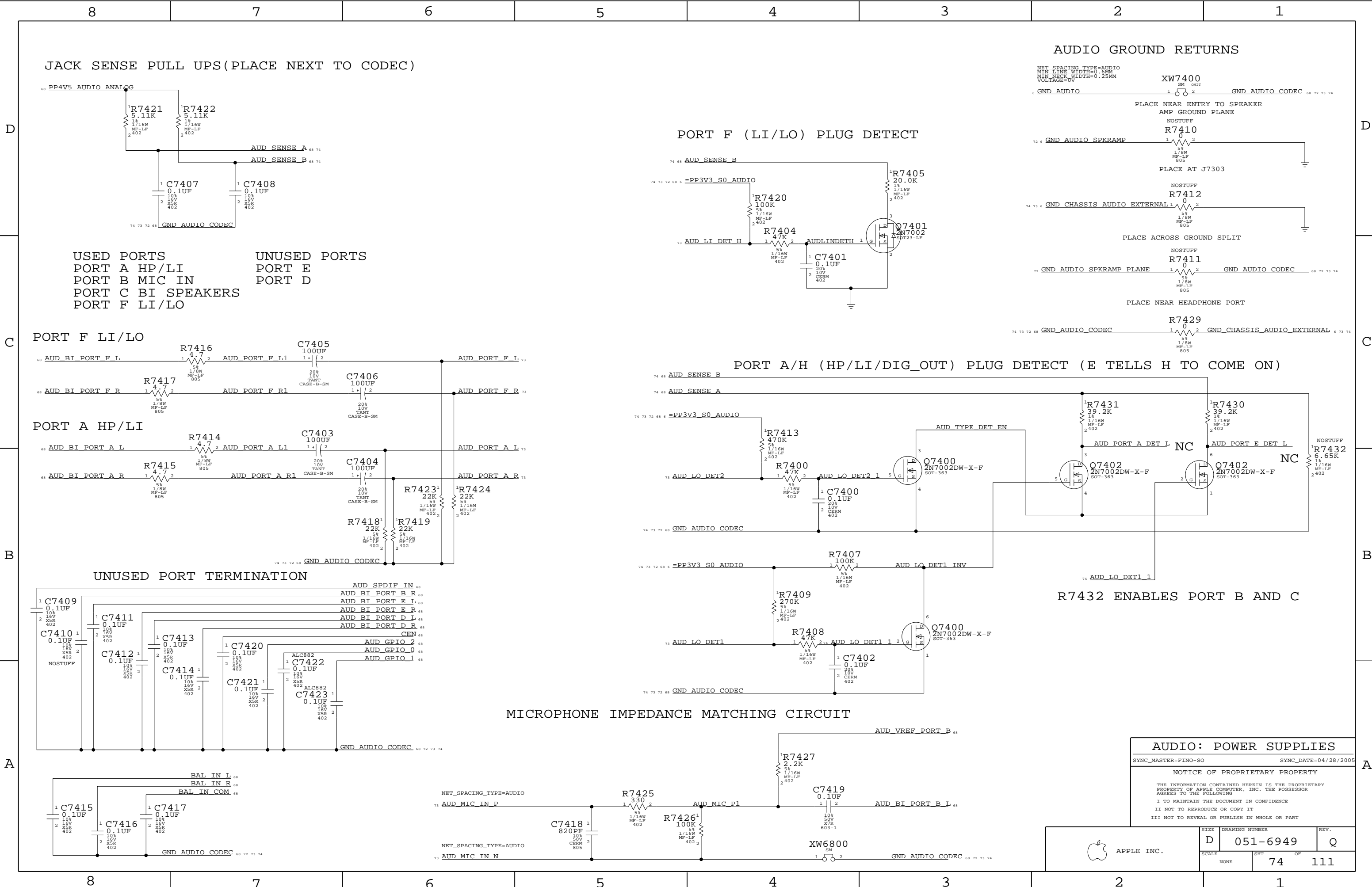
SIZE D

DRAWING NUMBER 051-6949

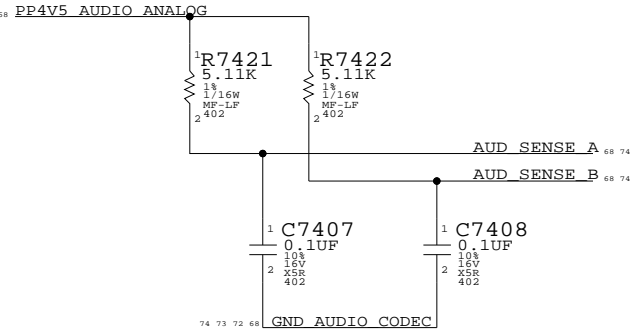
REV. Q

SCALE NONE

SHT 73 OF 111



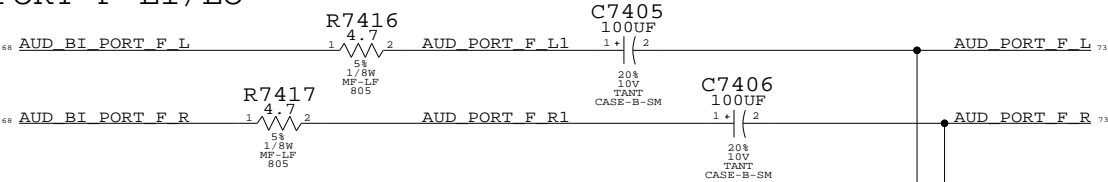
JACK SENSE PULL UPS(PLACE NEXT TO CODEC)



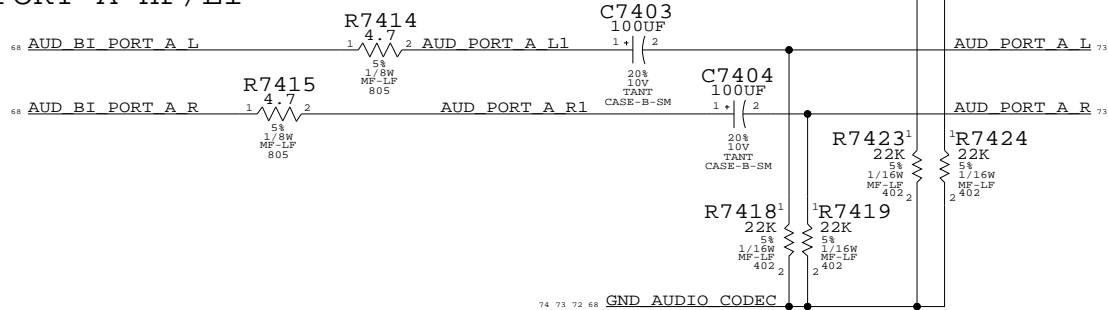
USED PORTS
PORT A HP/LI
PORT B MIC IN
PORT C BI SPEAKERS
PORT F LI/LO

UNUSED PORTS
PORT E
PORT D

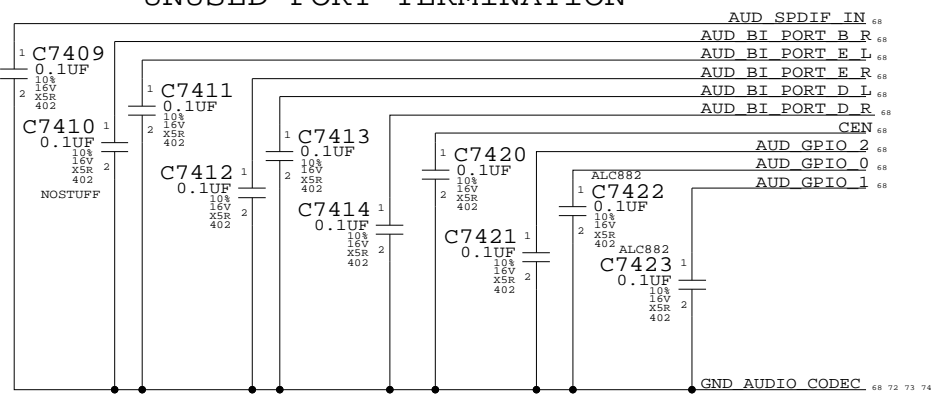
PORT F LI/LO



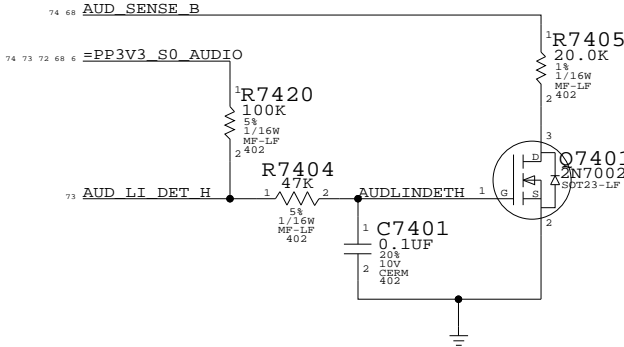
PORT A HP/LI



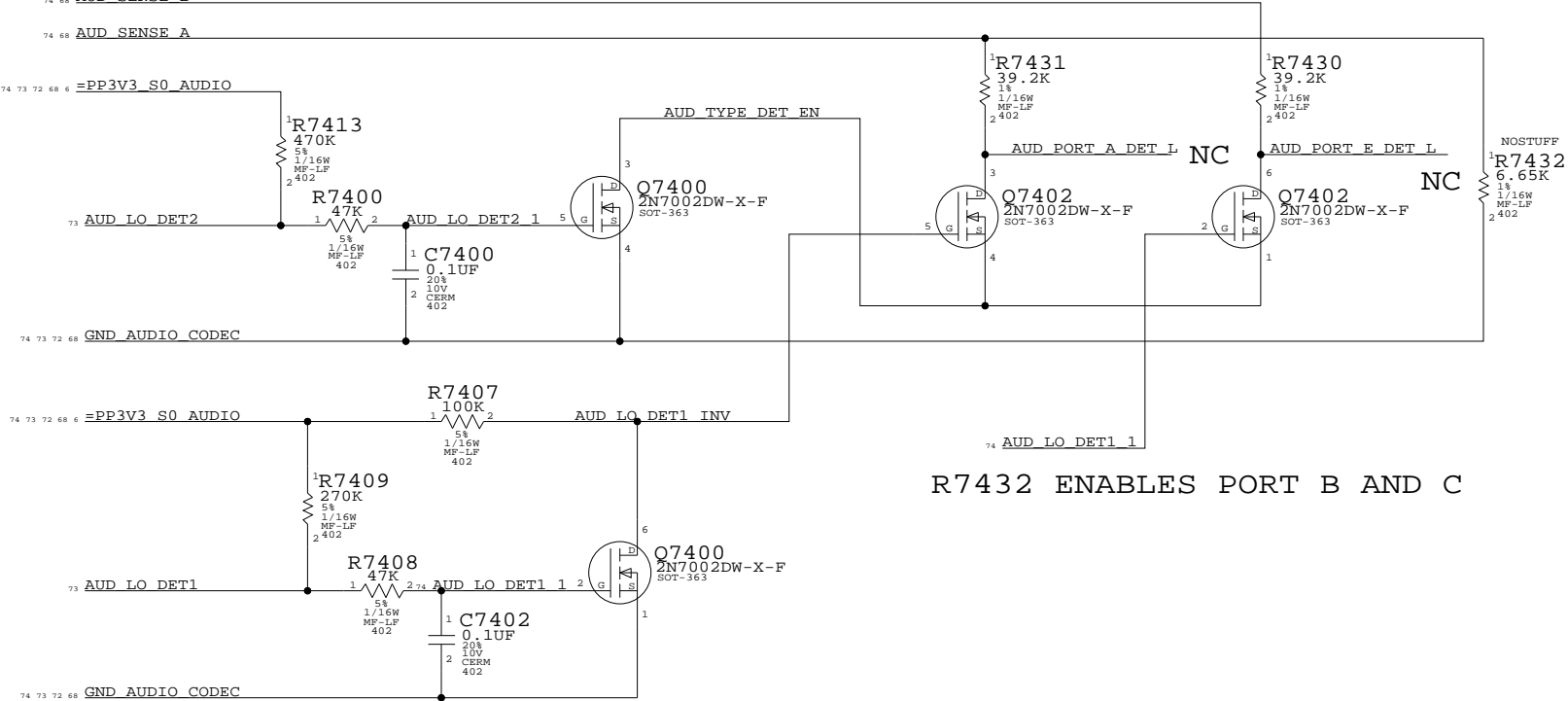
UNUSED PORT TERMINATION



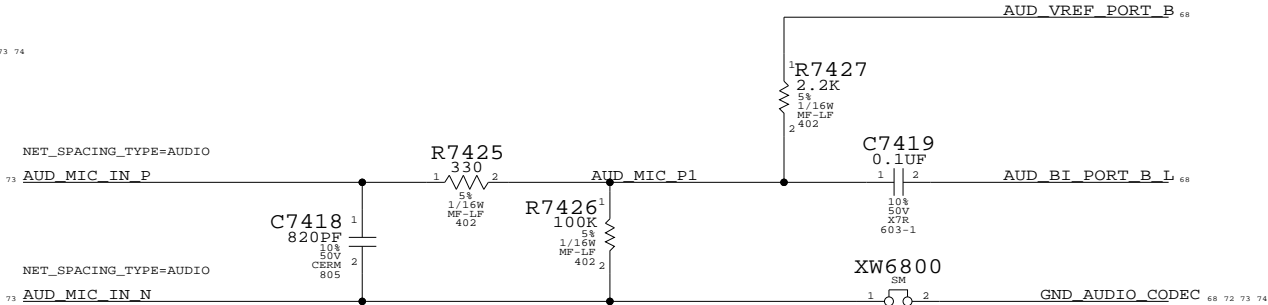
PORT F (LI/LO) PLUG DETECT



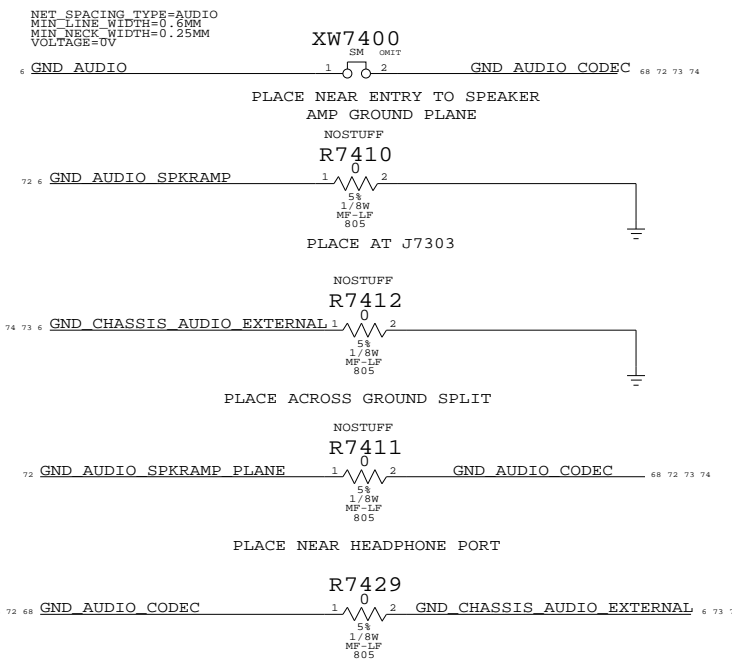
PORT A/H (HP/LI/DIG_OUT) PLUG DETECT (E TELLS H TO COME ON)



MICROPHONE IMPEDANCE MATCHING CIRCUIT



AUDIO GROUND RETURNS



R7432 ENABLES PORT B AND C

AUDIO: POWER SUPPLIES

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

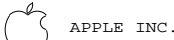
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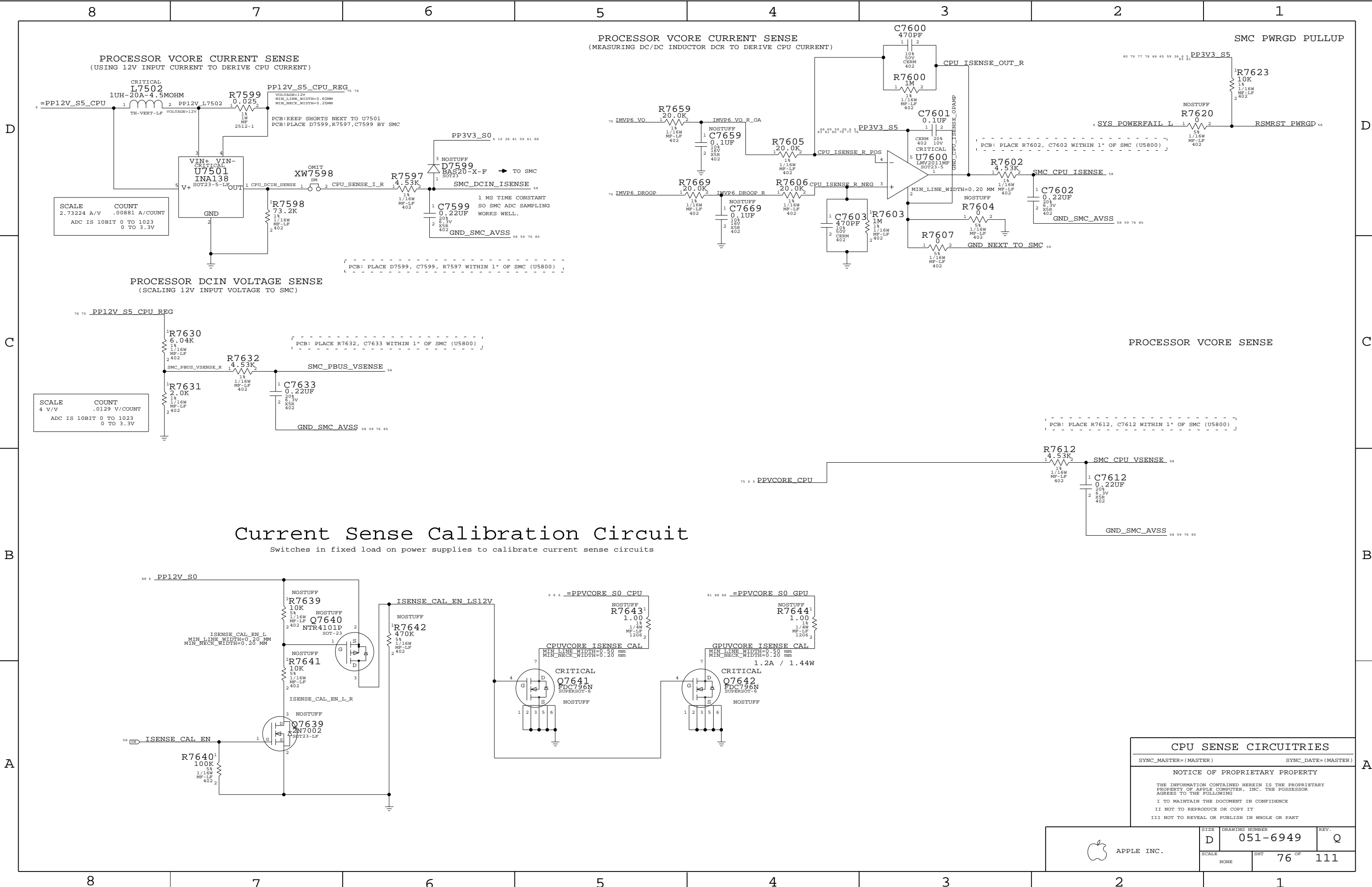
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APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	Q
SCALE	SHT	OF
NONE	74	111



Current Sense Calibration Circuit

Switches in fixed load on power supplies to calibrate current sense circuits

CPU SENSE CIRCUITRIES

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)


NOTICE OF PROPRIETARY PROPERTY

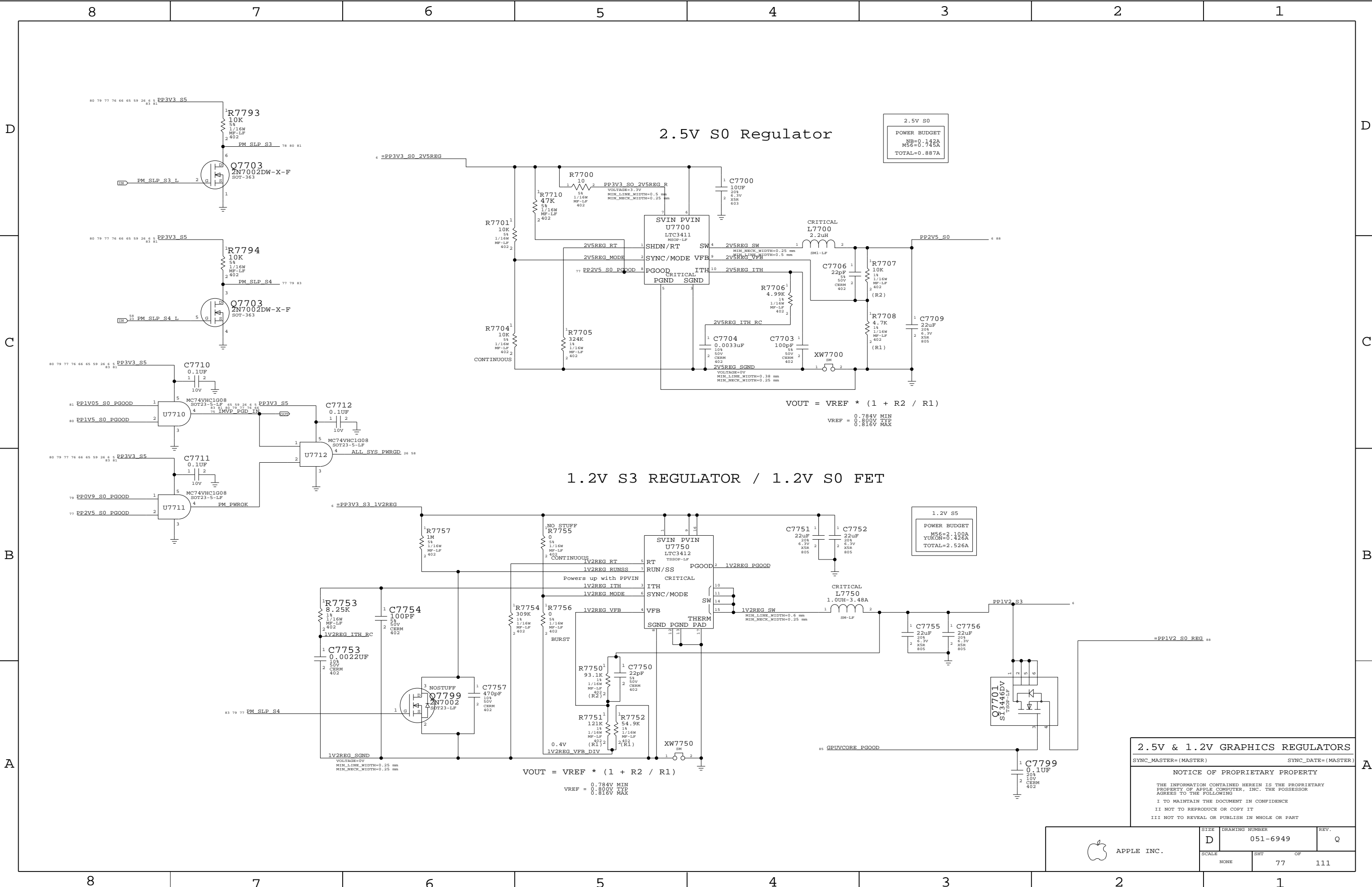
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 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE	SHT	76 ^{OF}	111
NONE			



2.5V & 1.2V GRAPHICS REGULATORS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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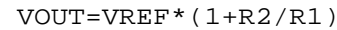
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APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	Q
SCALE	SHT	OF
NONE	77	111

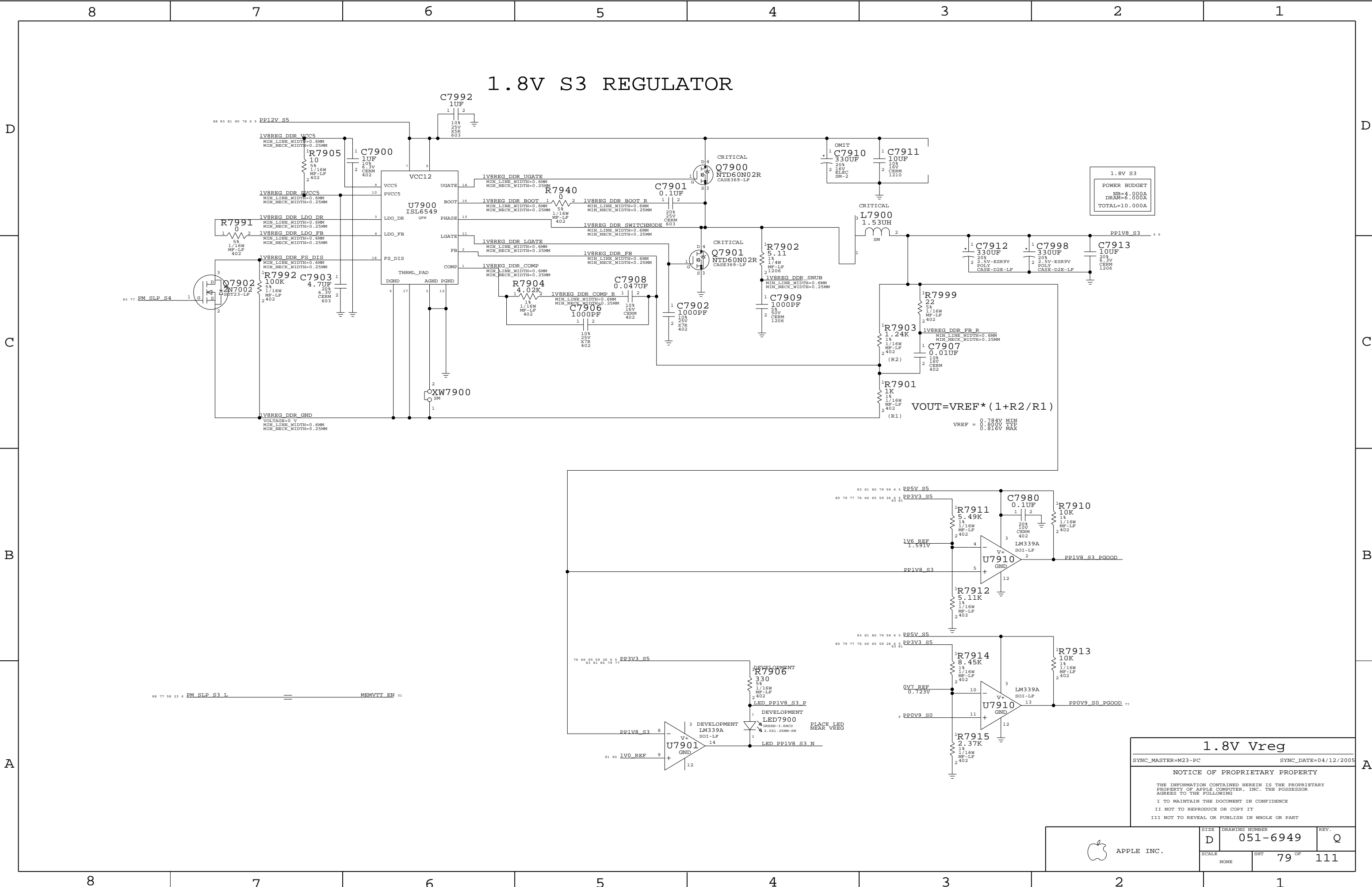
1.8V S0 REGULATOR



VREF = 0.784V MIN
0.800V TYP
0.816V MAX

 APPLE INC.

TRUE



1.8V S3 REGULATOR

1.8V S3	
POWER BUDGET	
NB=	4.000A
DSM=	6.000A
TOTAL=	10.000A

$$VOUT = VREF * (1 + R2 / R1)$$

$$VREF = 0.784V \text{ MIN}$$

$$0.800V \text{ TYP}$$

$$0.816V \text{ MAX}$$

1.8V Vreg

SYNC_MASTER=M23-PC

SYNC_DATE=04/12/2005

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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE		SHT	79 OF 111
NONE			

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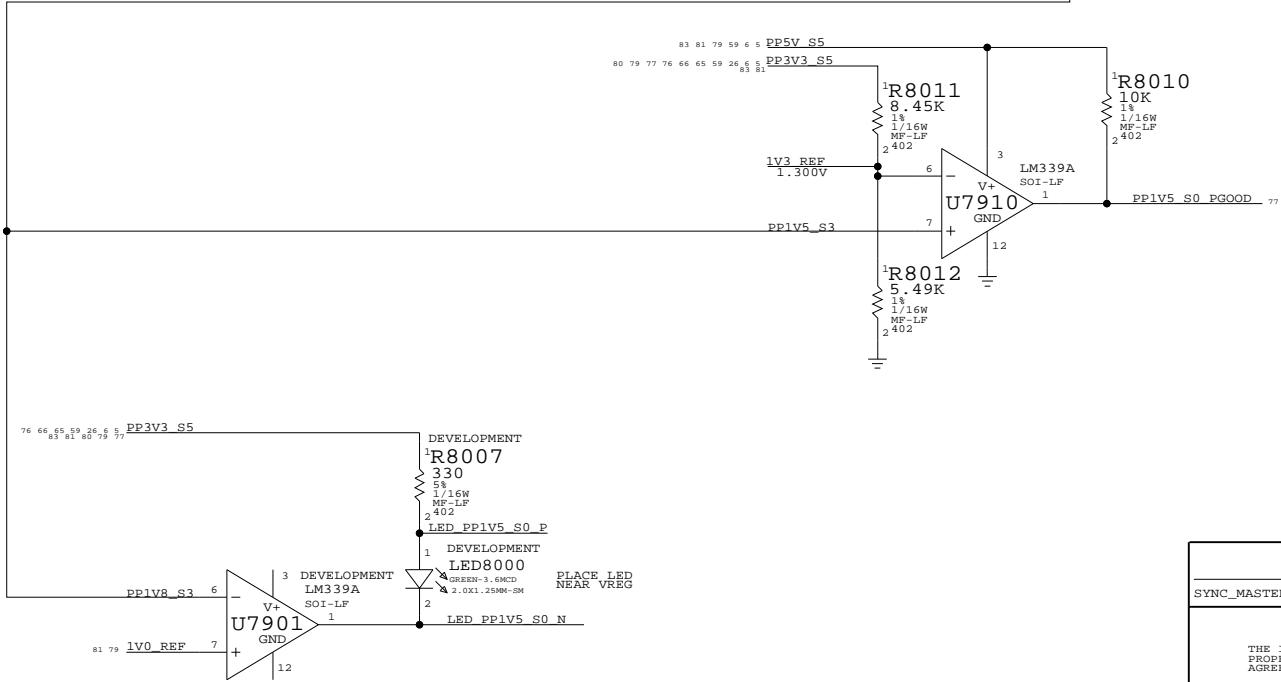
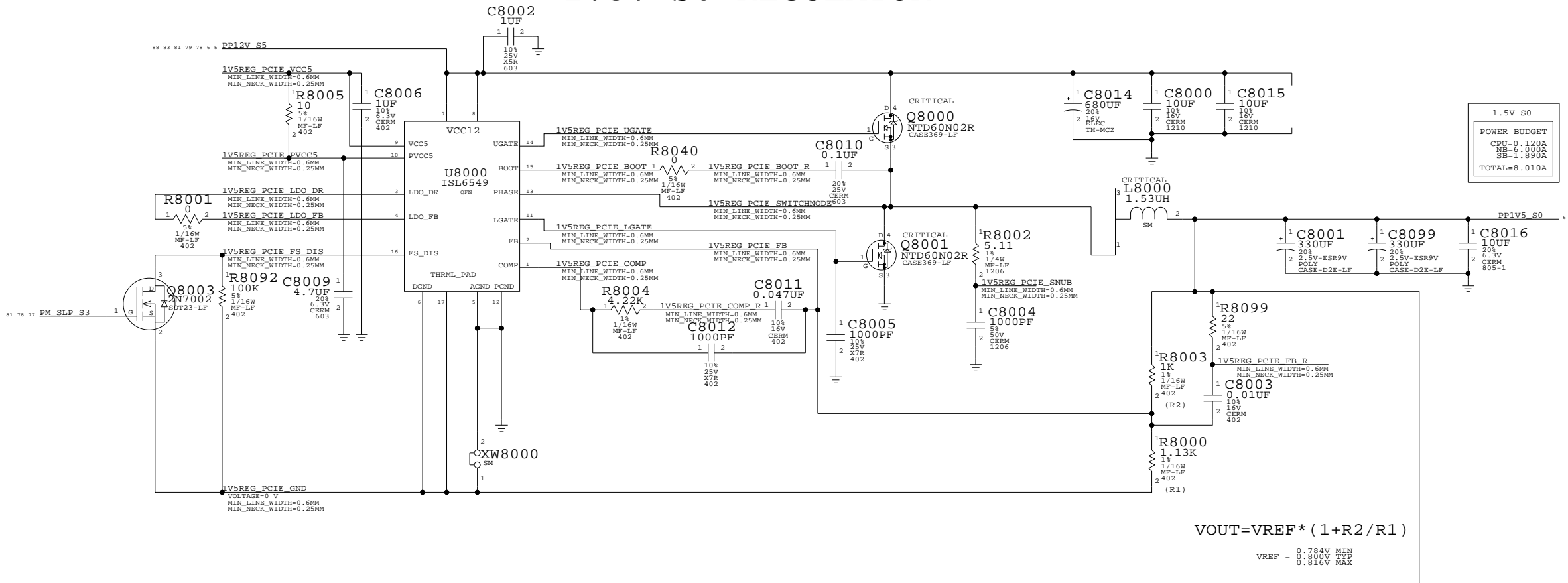
D

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1.5V S0 REGULATOR



1.5V Vreg

SYNC_MASTER=FINO-PC SYNC_DATE=05/18/2005

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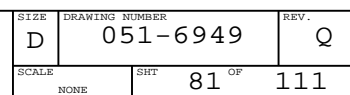


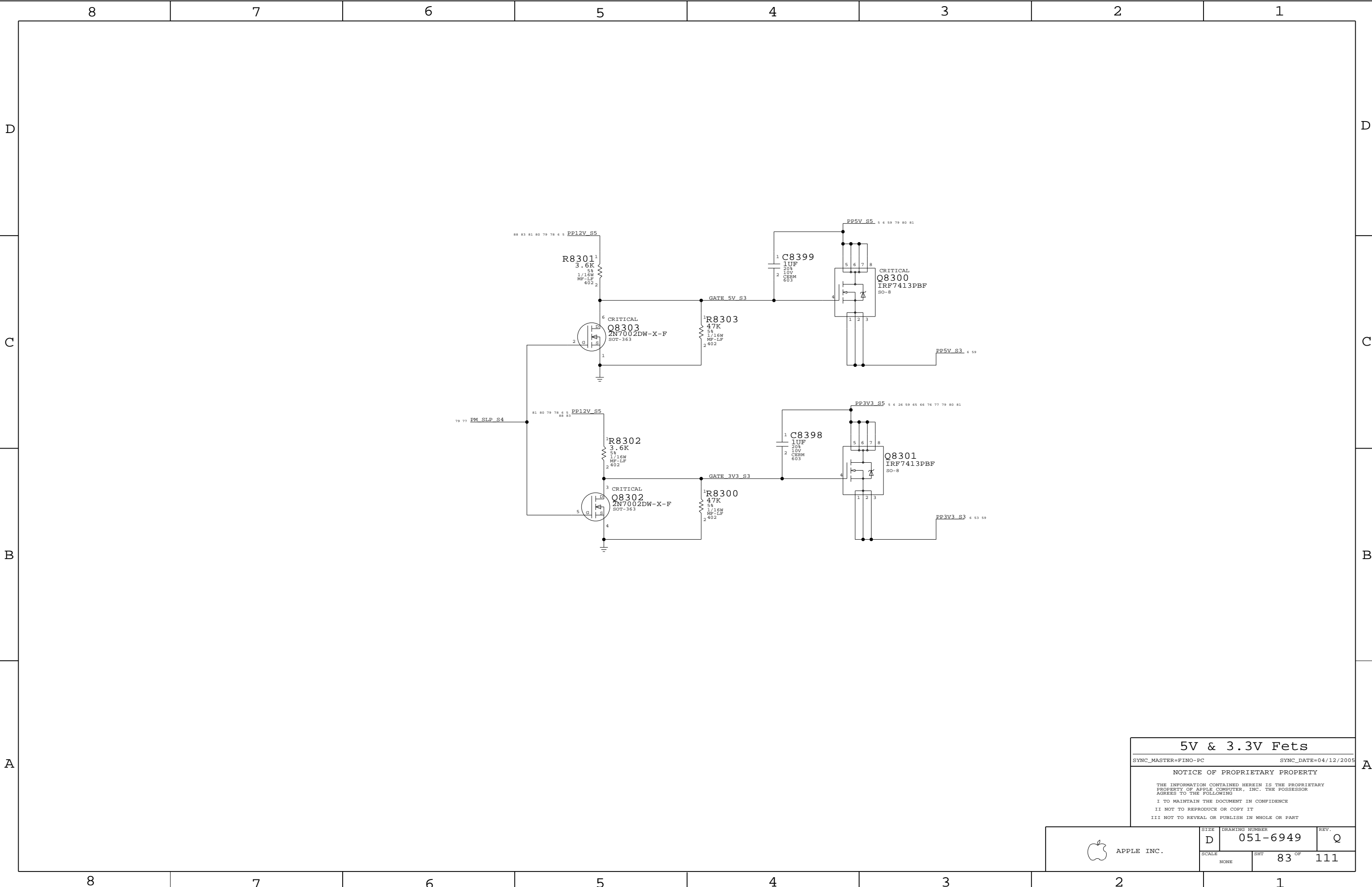
APPLE INC.

SIZE D DRAWING NUMBER 051-6949 REV. Q

SCALE NONE SHT 80 OF 111

D

A



5V & 3.3V Fets

SYNC_MASTER=FINO-PC

SYNC_DATE=04/12/2005


NOTICE OF PROPRIETARY PROPERTY

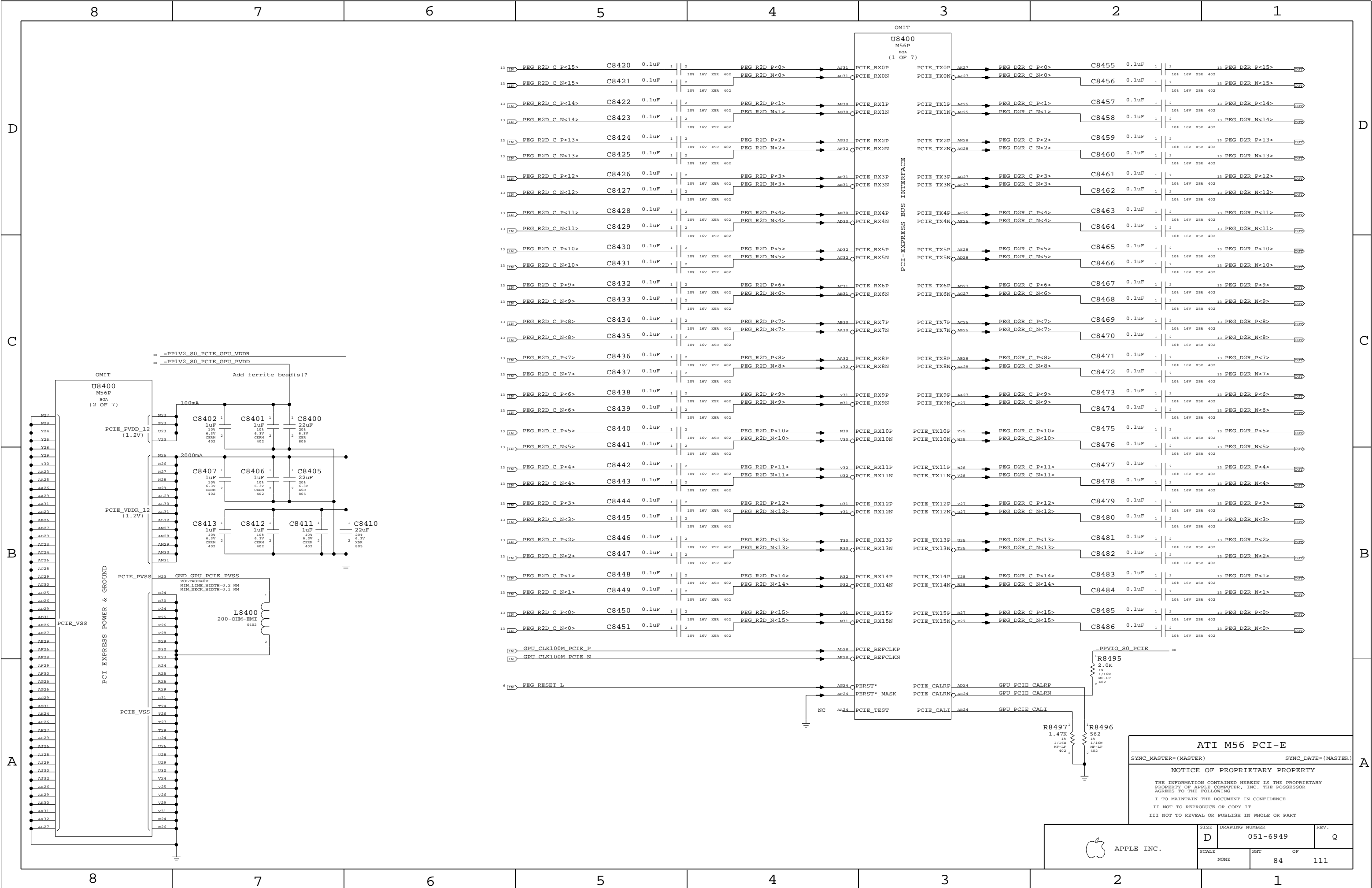
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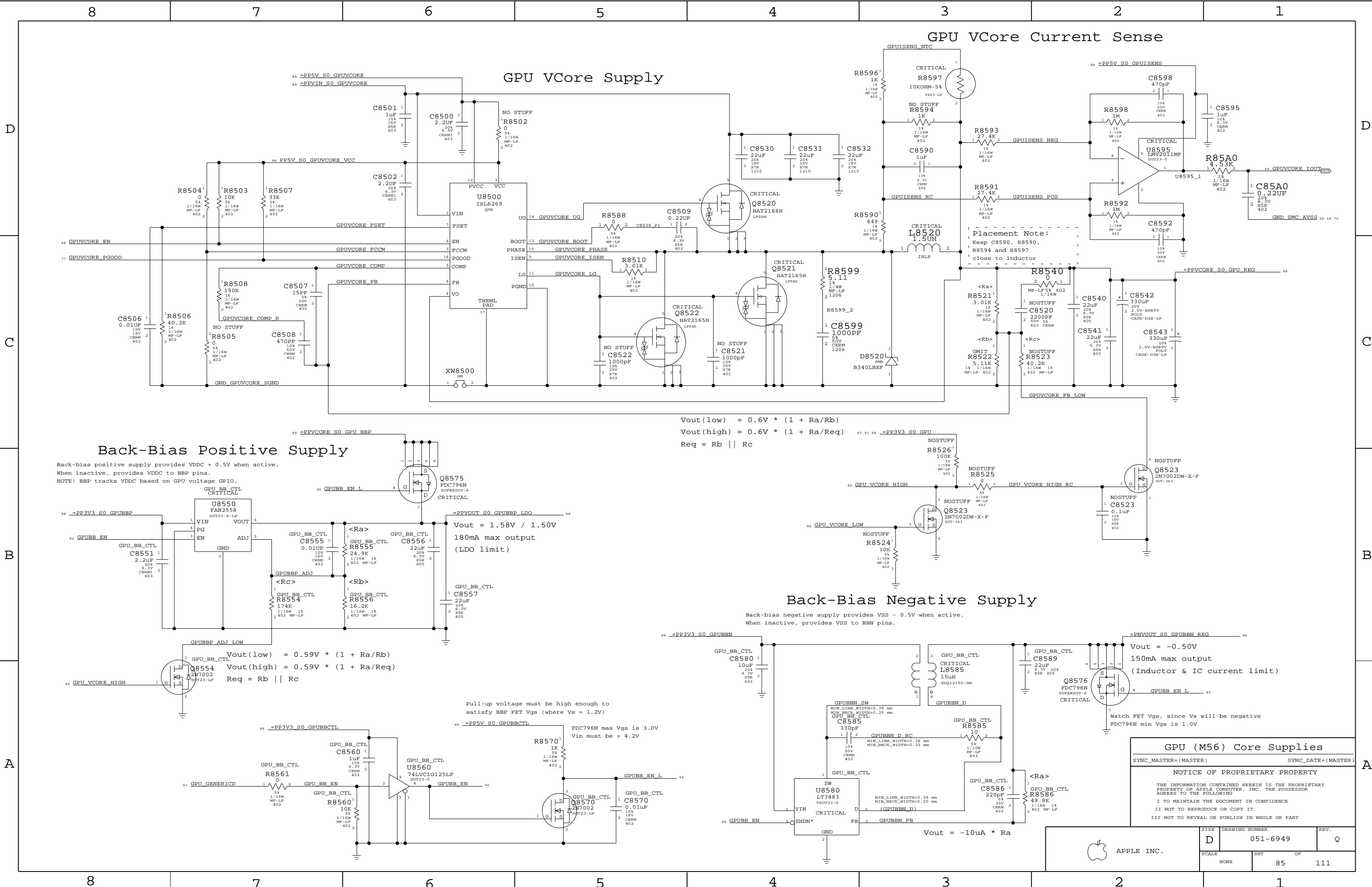
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 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	Q
SCALE	SHT	83	OF 111
NONE			





GPU VCore Supply

GPU VCore Current Sense

Back-Bias Positive Supply

Back-Bias Negative Supply

GPU (M56) Core Supplies

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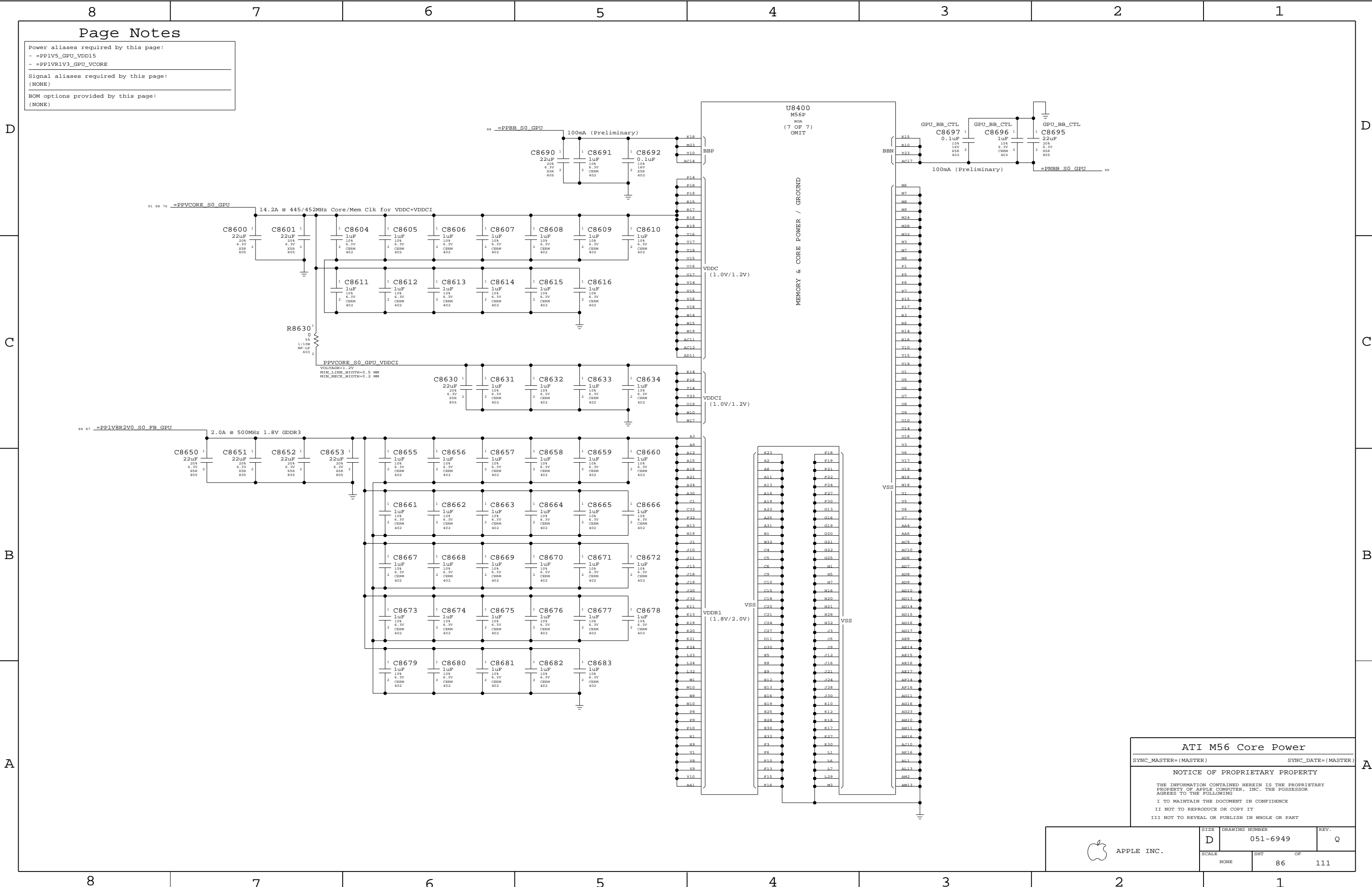
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SIZE	DRAWING NUMBER	REV.
D	051-6949	Q
SCALE	SHT	OF
NONE	85	111



Power aliases required by this page:
- =PP1V5_GPU_VDD15
- =PP1VR1V3_GPU_VCORE

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

ATI M56 Core Power

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

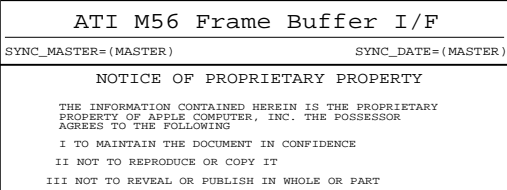
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APPLE INC.	SIZE D	DRAWING NUMBER 051-6949	REV. Q
	SCALE NONE	SHT 86	OF 111



Power aliases required by this page:

- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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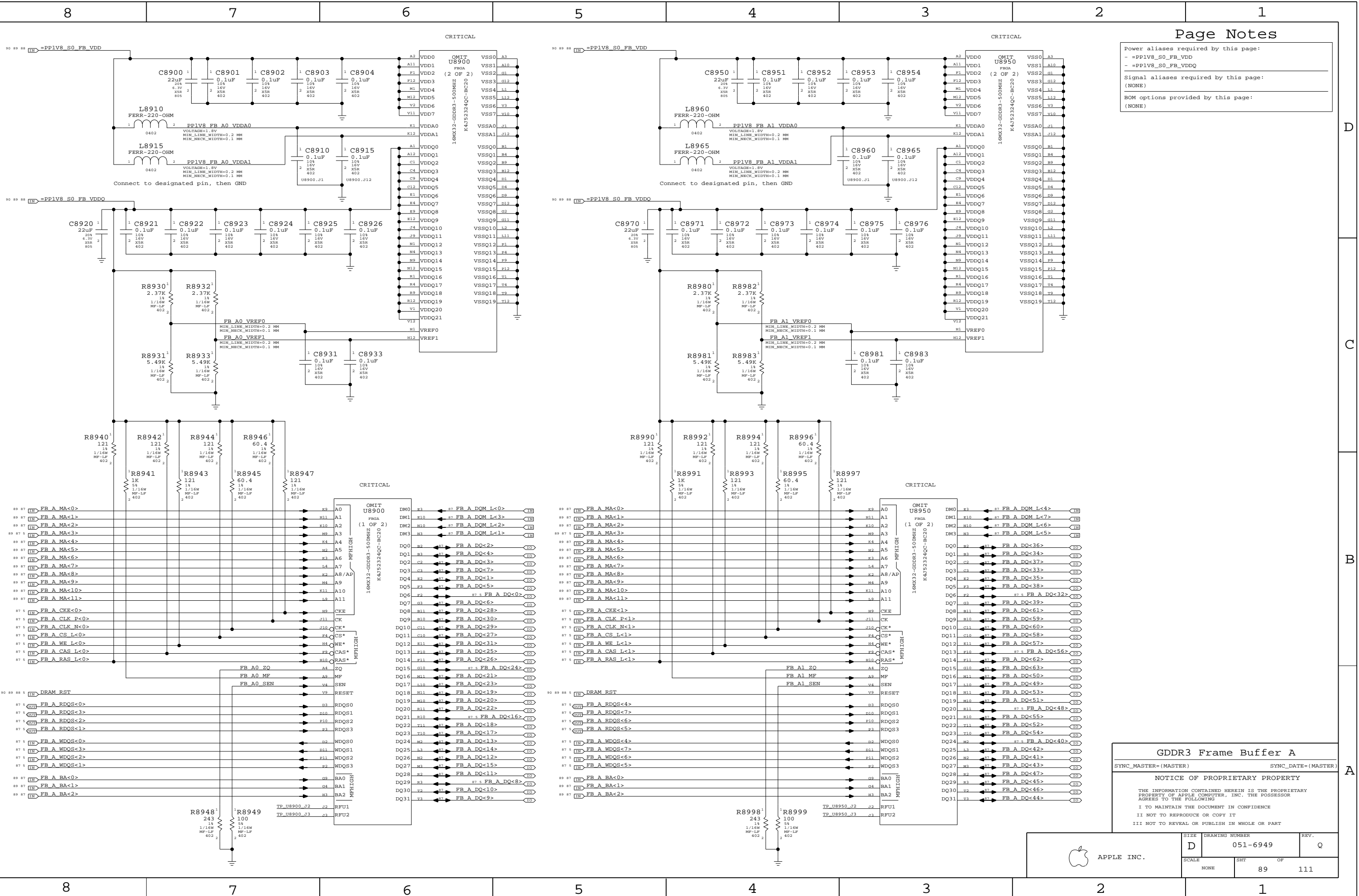
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE INC.

SIZE D DRAWING NUMBER 051-6949 REV. Q

SCALE NONE SHT 89 OF 111



Power aliases required by this page:
 - =PP1V8_S0_FB_VDD
 - =PP1V8_S0_FB_VDDQ

Signal aliases required by this page:
 (NONE)

BOM options provided by this page:
 (NONE)

GDDR3 Frame Buffer B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

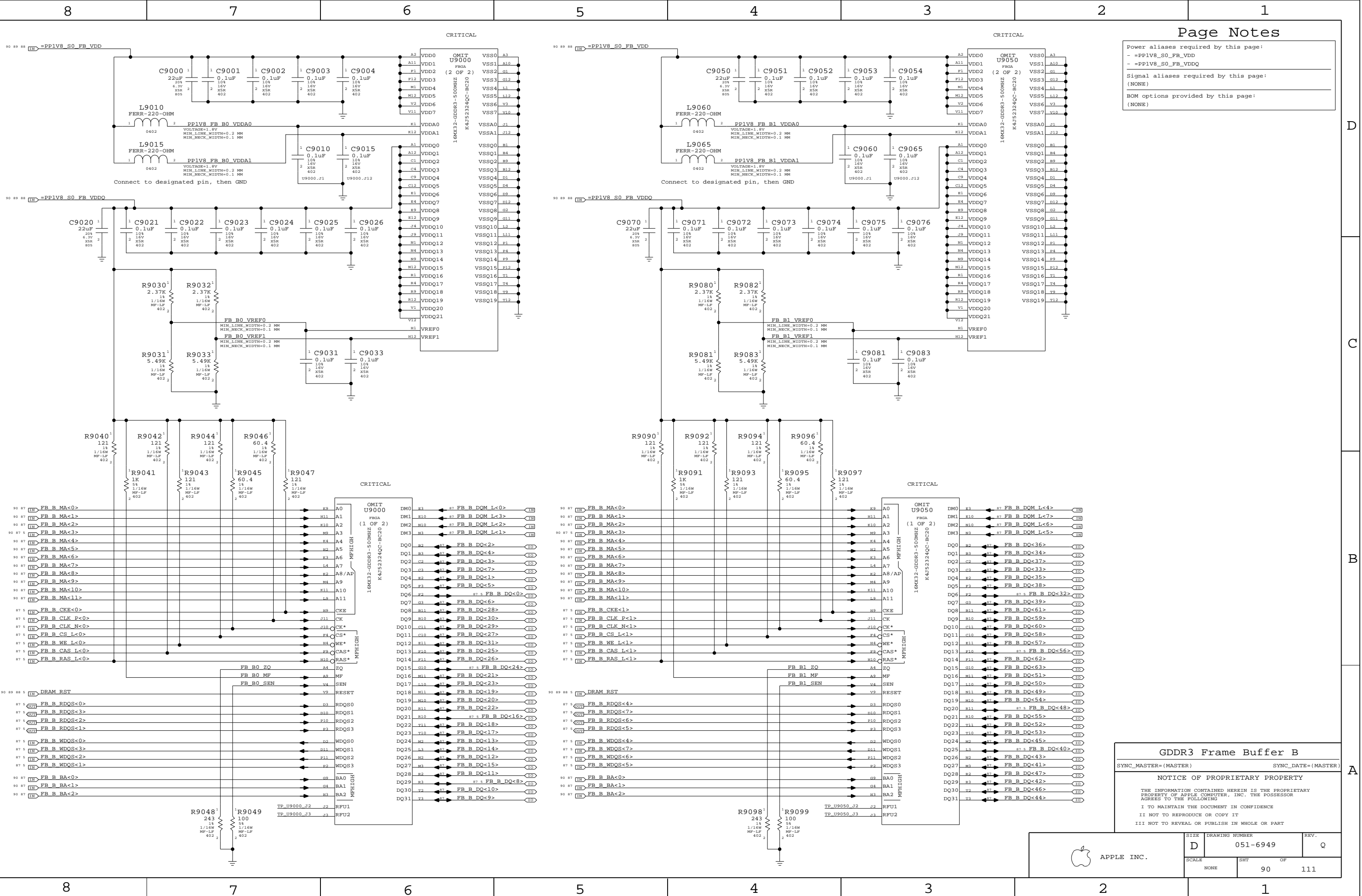
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APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	Q
SCALE	SHT	OF
NONE	90	111

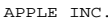
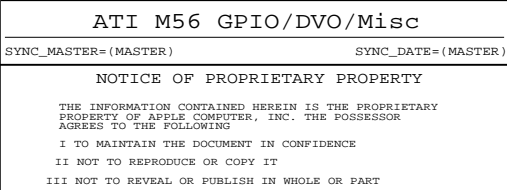


```
- =PP3V3_GPU_GPIOS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLI
```

Signal aliases required by this page:

- =I2C_GPU_TMDS_SDA - I2C data line for external TMDS transmitters
- =I2C_GPU_TMDS_SCL - I2C clock line for external TMDS transmitters

BOM options provided by this page:
(NONE)



SIZE	DRAWING NUMBER	REV.
D	051-6949	
SCALE	SHT	OF
NONE	91	111

Page Notes

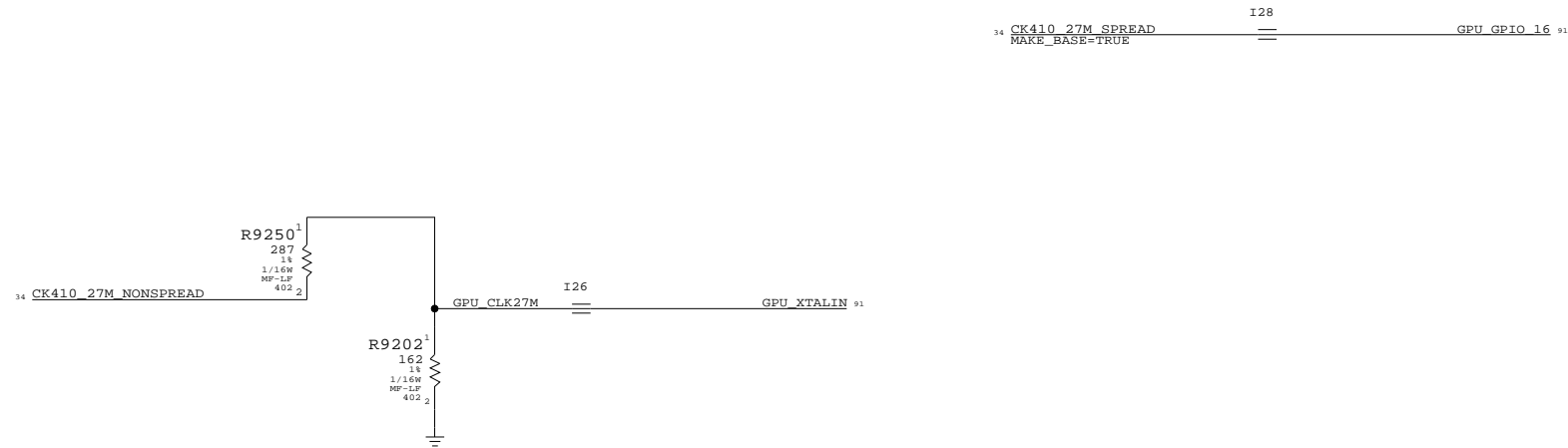
Power aliases required by this page:

```
- =PP3V3_GPU_CLOCKS      - =PP3V3_GPU_PWRSEQ
- =PPVIN_GPU_LVDDR_LDO    - =PP2V5_GPU_PWRSEQ
- =PP2V5_GPU_LVDDR_LDO    - =PP1V8_GPU_PWRSEQ
                           - =PP1V5_GPU_PWRSEQ
```

Signal aliases required by this page:
(NONE)

BOM options provided by this page:

- GPU_SS	- GPU_LVDDR_2V8
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GPU CLOCKS

SYNC_MASTER=BOZEMAN

SYNC_DATE=05/21/2005	7
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APPLE INC.

SIZE
D

SIZE	DRAWING NUMBER
D	051-6949

Q

SCALE	NONE
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SHT	92
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OF

111

Page Notes

Power aliases required by this page:

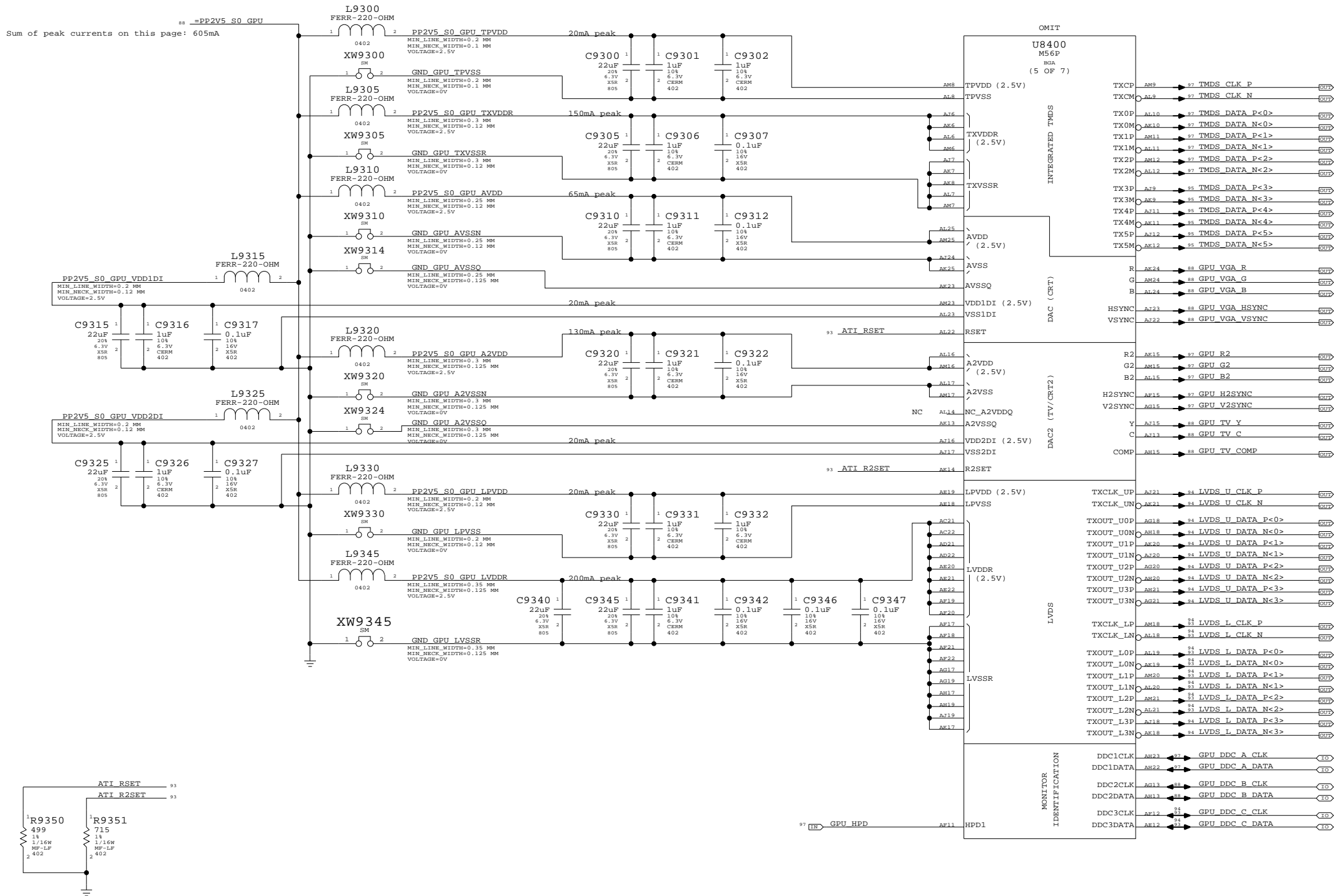
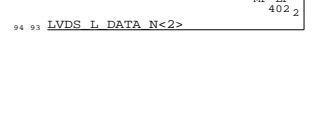
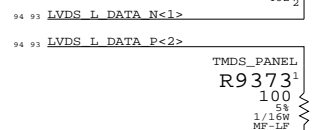
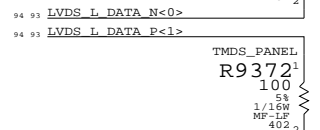
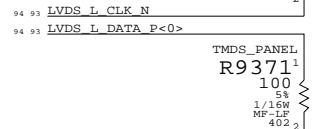
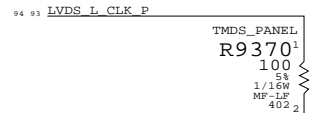
- =PP2V5_S0_GPU
- =PP1V8R2V5_S0_GPU_LVDDR

Signal aliases required by this page:

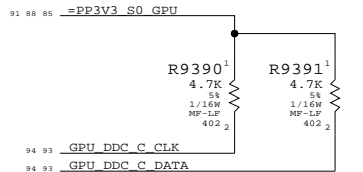
(NONE)

BOM options provided by this page:

(NONE)

TERMINATION FOR TMDS USAGE OF LVDS PINS
PLACE CLOSE TO GPU (U8400)

Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb



ATI M56 Video Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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D	051-6949	Q
SCALE	SHT	OF
NONE	93	111

[illegible]

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D	AUD_GPIO_1_A	AUD_GPIO_1_A - @m38_1ib.M38	68A8 73C2	CK410_SRC6_P	CK410_SRC6_P - @m38_1ib.M38	3384 34B6	DMI_S2N_P<0>	DMI_S2N_P<0> - @m38_1ib.M38	5C7 14B4 22D2	FB_A_DQ<30>	FB_A_DQ<30> - @m38_1ib.M38	87C7 89B6
	AUD_GPIO_2	AUD_GPIO_2 - @m38_1ib.M38	68C7 74A6	CK410_SRC7_N	CK410_SRC7_N - @m38_1ib.M38	3384 34D3	DMI_S2N_P<1>	DMI_S2N_P<1> - @m38_1ib.M38	1484 22D2	FB_A_DQ<31>	FB_A_DQ<31> - @m38_1ib.M38	87C7 89A6
	AUD_LI_DET_EMI	AUD_LI_DET_EMI - @m38_1ib.M38	73D7	CK410_SRC8_P	CK410_SRC8_P - @m38_1ib.M38	3384 34A6	DMI_S2N_P<2>	DMI_S2N_P<2> - @m38_1ib.M38	1484 22D2	FB_A_DQ<32>	FB_A_DQ<32> - @m38_1ib.M38	5A6 5D6 87C7 89B2
	AUD_LI_DET_H	AUD_LI_DET_H - @m38_1ib.M38	73D4 74C5	CK410_SRC8_N	CK410_SRC8_N - @m38_1ib.M38	3384 34A6	DMI_S2N_P<3>	DMI_S2N_P<3> - @m38_1ib.M38	1484 22D2	FB_A_DQ<33>	FB_A_DQ<33> - @m38_1ib.M38	87C7 89B3
	AUD_LI_DET_JACK	AUD_LI_DET_JACK - @m38_1ib.M38	73D8	CK410_SRC8_P	CK410_SRC8_P - @m38_1ib.M38	3384 34A6	DVI_DDC_CLK	DVI_DDC_CLK - @m38_1ib.M38	97D2	FB_A_DQ<34>	FB_A_DQ<34> - @m38_1ib.M38	87C7 89B3
	AUD_LI_GND_EMI	AUD_LI_GND_EMI - @m38_1ib.M38	73C7	CK410_SRC_CLKREQ1_L	CK410_SRC_CLKREQ1_L - @m38_1ib.M38	3384 34D8	DVI_DDC_CLK_UF	DVI_DDC_CLK_UF - @m38_1ib.M38	97D3 97D5	FB_A_DQ<35>	FB_A_DQ<35> - @m38_1ib.M38	87C7 89B3
	AUD_LI_GND_JACK	AUD_LI_GND_JACK - @m38_1ib.M38	73D8	CK410_SRC_CLKREQ3_L	CK410_SRC_CLKREQ3_L - @m38_1ib.M38	3384 34D8	DVI_DDC_DATA	DVI_DDC_DATA - @m38_1ib.M38	97C2	FB_A_DQ<36>	FB_A_DQ<36> - @m38_1ib.M38	87C7 89B3
	AUD_LI_L_EMI	AUD_LI_L_EMI - @m38_1ib.M38	73D7	CK410_SRC_CLKREQ6_L	CK410_SRC_CLKREQ6_L - @m38_1ib.M38	3384 53C6	DVI_DDC_DATA_UF	DVI_DDC_DATA_UF - @m38_1ib.M38	97C3 97D5	FB_A_DQ<37>	FB_A_DQ<37> - @m38_1ib.M38	87C7 89B3
	AUD_LI_L_JACK	AUD_LI_L_JACK - @m38_1ib.M38	73D8	CK410_SRC_CLKREQ8_L	CK410_SRC_CLKREQ8_L - @m38_1ib.M38	3384 34D8	DVI_HPD_UF	DVI_HPD_UF - @m38_1ib.M38	97C3 97D5	FB_A_DQ<38>	FB_A_DQ<38> - @m38_1ib.M38	87C7 89B3
	AUD_LI_R_EMI	AUD_LI_R_EMI - @m38_1ib.M38	73D7	CK410_USB48_FSA	CK410_USB48_FSA - @m38_1ib.M38	3384 34C6	ENET_C4106_2	ENET_C4106_2 - @m38_1ib.M38	41D2	FB_A_DQ<39>	FB_A_DQ<39> - @m38_1ib.M38	87C7 89B3
C	AUD_LI_R_JACK	AUD_LI_R_JACK - @m38_1ib.M38	73D8	CK410_XTAL_IN	CK410_XTAL_IN - @m38_1ib.M38	33C6	ENET_C4107_2	ENET_C4107_2 - @m38_1ib.M38	41D2	FB_A_DQ<40>	FB_A_DQ<40> - @m38_1ib.M38	5A6 5D6 87C7 89A2
	AUD_LO_DET1	AUD_LO_DET1 - @m38_1ib.M38	73B8 74A5	CK410_XTAL_OUT	CK410_XTAL_OUT - @m38_1ib.M38	33C6	ENET_C4117_1	ENET_C4117_1 - @m38_1ib.M38	41B2	FB_A_DQ<41>	FB_A_DQ<41> - @m38_1ib.M38	87C7 89A3
	AUD_LO_DET1_1	AUD_LO_DET1_1 - @m38_1ib.M38	74A4 74B2	CLK_NB_OE_L	CLK_NB_OE_L - @m38_1ib.M38	14B6 33B4	ENET_C4118_1	ENET_C4118_1 - @m38_1ib.M38	41B2	FB_A_DQ<42>	FB_A_DQ<42> - @m38_1ib.M38	87C7 89A3
	AUD_LO_DET1_EMI	AUD_LO_DET1_EMI - @m38_1ib.M38	73B7	CPUVCORE_ISENSE_CAL	CPUVCORE_ISENSE_CAL - @m38_1ib.M38	76A5	ENET_CLK100M_PCIE_N	ENET_CLK100M_PCIE_N - @m38_1ib.M38	5D5 34A4 34B2 41C5	FB_A_DQ<43>	FB_A_DQ<43> - @m38_1ib.M38	87C7 89A3
	AUD_LO_DET1_INV	AUD_LO_DET1_INV - @m38_1ib.M38	74B3	CPU_A20M_L	CPU_A20M_L - @m38_1ib.M38	5C8 7C7 21C4	ENET_CLK100M_PCIE_P	ENET_CLK100M_PCIE_P - @m38_1ib.M38	5D5 34A4 34B2 41C5	FB_A_DQ<44>	FB_A_DQ<44> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET1_JACK	AUD_LO_DET1_JACK - @m38_1ib.M38	73B4	CPU_BSEL<0>	CPU_BSEL<0> - @m38_1ib.M38	7B4 34B8	ENET_CTRL12	ENET_CTRL12 - @m38_1ib.M38	41C7 42B8	FB_A_DQ<45>	FB_A_DQ<45> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET2	AUD_LO_DET2 - @m38_1ib.M38	73B8 74B5	CPU_BSEL<1>	CPU_BSEL<1> - @m38_1ib.M38	7B4 34A8	TP_ENET_CTRL12	TP_ENET_CTRL12 - @m38_1ib.M38	42B7	FB_A_DQ<46>	FB_A_DQ<46> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET2_1	AUD_LO_DET2_1 - @m38_1ib.M38	74B4	CPU_BSEL<2>	CPU_BSEL<2> - @m38_1ib.M38	7B2	ENET_CTRL25	ENET_CTRL25 - @m38_1ib.M38	41C7 42C6	FB_A_DQ<47>	FB_A_DQ<47> - @m38_1ib.M38	87B7 89A3
	AUD_LO_DET2_EMI	AUD_LO_DET2_EMI - @m38_1ib.M38	73B7	CPU_COMP<0>	CPU_COMP<0> - @m38_1ib.M38	7B2	ENET_GATED_RST_L	ENET_GATED_RST_L - @m38_1ib.M38	41C5 42D2	FB_A_DQ<48>	FB_A_DQ<48> - @m38_1ib.M38	5A6 5D6 87B7 89A2
	AUD_LO_DET2_JACK	AUD_LO_DET2_JACK - @m38_1ib.M38	73B4	CPU_COMP<1>	CPU_COMP<1> - @m38_1ib.M38	7B2	ENET_LED_ACT_L	ENET_LED_ACT_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<49>	FB_A_DQ<49> - @m38_1ib.M38	87B7 89A3
B	AUD_LO_GND_EMI	AUD_LO_GND_EMI - @m38_1ib.M38	73A7	CPU_COMP<2>	CPU_COMP<2> - @m38_1ib.M38	7B2	ENET_LED_LINK10_100_L	ENET_LED_LINK10_100_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<50>	FB_A_DQ<50> - @m38_1ib.M38	87B7 89A3
	AUD_LO_GND_JACK	AUD_LO_GND_JACK - @m38_1ib.M38	73B4	CPU_COMP<3>	CPU_COMP<3> - @m38_1ib.M38	7B2	ENET_LED_LINK1000_L	ENET_LED_LINK1000_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<51>	FB_A_DQ<51> - @m38_1ib.M38	87B7 89A3
	AUD_LO_L_EMI	AUD_LO_L_EMI - @m38_1ib.M38	73B7	CPU_DCIN_SENSE	CPU_DCIN_SENSE - @m38_1ib.M38	76D7	ENET_LED_LINK_L	ENET_LED_LINK_L - @m38_1ib.M38	41C8 43C3	FB_A_DQ<52>	FB_A_DQ<52> - @m38_1ib.M38	87B7 89A3
	AUD_LO_L_JACK	AUD_LO_L_JACK - @m38_1ib.M38	73B4	CPU_DPRSTP_L	CPU_DPRSTP_L - @m38_1ib.M38	7B3 21C4 75C6	ENET_LOM_DIS_L	ENET_LOM_DIS_L - @m38_1ib.M38	41C7	FB_A_DQ<53>	FB_A_DQ<53> - @m38_1ib.M38	87B7 89A3
	AUD_LO_R_EMI	AUD_LO_R_EMI - @m38_1ib.M38	73A7	CPU_DPSLP_L	CPU_DPSLP_L - @m38_1ib.M38	7B3 21C4	ENET_MD1_N<0>	ENET_MD1_N<0> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<54>	FB_A_DQ<54> - @m38_1ib.M38	87B7 89A3
	AUD_LO_R_JACK	AUD_LO_R_JACK - @m38_1ib.M38	73B4	CPU_FERR_L	CPU_FERR_L - @m38_1ib.M38	7C7 21C2	ENET_MD1_N<1>	ENET_MD1_N<1> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<55>	FB_A_DQ<55> - @m38_1ib.M38	87B7 89A3
	AUD_MAX9714_CHOLD	AUD_MAX9714_CHOLD - @m38_1ib.M38	72C4	CPU_GTLREF	CPU_GTLREF - @m38_1ib.M38	5D4 7B4	ENET_MD1_N<2>	ENET_MD1_N<2> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<56>	FB_A_DQ<56> - @m38_1ib.M38	5A6 5D6 87B7 89A2
	AUD_MAX9714_VREF0	AUD_MAX9714_VREF0 - @m38_1ib.M38	72C5	CPU_HS_ZH607	CPU_HS_ZH607 - @m38_1ib.M38	9D4	ENET_MD1_N<3>	ENET_MD1_N<3> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<57>	FB_A_DQ<57> - @m38_1ib.M38	87B7 89A3
	AUD_MIC_IN_N	AUD_MIC_IN_N - @m38_1ib.M38	73C6 74A6	CPU_HS_ZH608	CPU_HS_ZH608 - @m38_1ib.M38	9D3 66A4 66A6 66B4 66B6	ENET_MD1_N<3>	ENET_MD1_N<3> - @m38_1ib.M38	41C2 43B7	FB_A_DQ<58>	FB_A_DQ<58> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_N_CONN	AUD_MIC_IN_N_CONN - @m38_1ib.M38	47C2 73C4	CPU_HS_ZH609	CPU_HS_ZH609 - @m38_1ib.M38	9D3	ENET_MD1_P<0>	ENET_MD1_P<0> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<59>	FB_A_DQ<59> - @m38_1ib.M38	87B7 89B3
A	AUD_MIC_IN_N_EMI	AUD_MIC_IN_N_EMI - @m38_1ib.M38	73C5	CPU_HS_ZH610	CPU_HS_ZH610 - @m38_1ib.M38	9D2	ENET_MD1_P<1>	ENET_MD1_P<1> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<60>	FB_A_DQ<60> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_P	AUD_MIC_IN_P - @m38_1ib.M38	73C6 74A6	CPU_IGNNE_L	CPU_IGNNE_L - @m38_1ib.M38	5C8 7C7 21C4	ENET_MD1_P<2>	ENET_MD1_P<2> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<61>	FB_A_DQ<61> - @m38_1ib.M38	87B7 89B3
	AUD_MIC_IN_P_CONN	AUD_MIC_IN_P_CONN - @m38_1ib.M38	47C2 73C4	CPU_INIT_L	CPU_INIT_L - @m38_1ib.M38	5C8 7D6 21C4	ENET_MD1_P<3>	ENET_MD1_P<3> - @m38_1ib.M38	41C2 43C7	FB_A_DQ<62>	FB_A_DQ<62> - @m38_1ib.M38	87B7 89A3
	AUD_MIC_IN_P_EMI	AUD_MIC_IN_P_EMI - @m38_1ib.M38	73C5	CPU_INTR	CPU_INTR - @m38_1ib.M38	5C8 7C7 21C4	ENET_MD1_R<0>	ENET_MD1_R<0> - @m38_1ib.M38	43C6	FB_A_DQ<63>	FB_A_DQ<63> - @m38_1ib.M38	87B7 89A3
	AUD_MIC_P1	AUD_MIC_P1 - @m38_1ib.M38	74A4	CPU_ISENSE_OUT_R	CPU_ISENSE_OUT_R - @m38_1ib.M38	76D3	ENET_MD1_R<1>	ENET_MD1_R<1> - @m38_1ib.M38	43C6	FB_A_DQ<64>	FB_A_DQ<64> - @m38_1ib.M38	87D5 89B6
	AUD_PORT_A_DET_L	AUD_PORT_A										

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D	SMC_XDP_TRST_L_R	SMC_XDP_TRST_L_R - @m38_lib.M38	59A3	TP_PCI_GNT1_L	TP_PCI_GNT1_L - @m38_lib.M38	22B6	TP_PCI_GNT2_L	TP_PCI_GNT2_L - @m38_lib.M38	22B6	TP_PCI_GNT4_L	TP_PCI_GNT4_L - @m38_lib.M38	22B6	TP_PCI_PME_L	TP_PCI_PME_L - @m38_lib.M38	22A6	TP_SB_ACZ_SDIN1	TP_SB_ACZ_SDIN1 - @m38_lib.M38	21C6	TP_SB_ACZ_SDIN2	TP_SB_ACZ_SDIN2 - @m38_lib.M38	21C6	TP_SB_DRQ0_L	TP_SB_DRQ0_L - @m38_lib.M38	21D4	TP_SB_DRQ6	TP_SB_DRQ6 - @m38_lib.M38	23C5	TP_SB_GPIO23	TP_SB_GPIO23 - @m38_lib.M38	21D5	TP_SB_GPIO25_DO_NOT_USE	TP_SB_GPIO25_DO_NOT_USE - @m38_lib.M38	23C3	TP_SB_GPIO38	TP_SB_GPIO38 - @m38_lib.M38	23C3	TP_SB_RSVD9	TP_SB_RSVD9 - @m38_lib.M38	22A6	TP_SB_SATALED_L	TP_SB_SATALED_L - @m38_lib.M38	21C6	TP_SB_XOR_AD5	TP_SB_XOR_AD5 - @m38_lib.M38	22A7	TP_SB_XOR_AD9	TP_SB_XOR_AD9 - @m38_lib.M38	22A7	TP_SB_XOR_AE5	TP_SB_XOR_AE5 - @m38_lib.M38	22A7	TP_SB_XOR_AE9	TP_SB_XOR_AE9 - @m38_lib.M38	22A6	TP_SB_XOR_AG4	TP_SB_XOR_AG4 - @m38_lib.M38	22A7	TP_SB_XOR_AG8	TP_SB_XOR_AG8 - @m38_lib.M38	22A6	TP_SB_XOR_AH4	TP_SB_XOR_AH4 - @m38_lib.M38	22A7	TP_SB_XOR_AH8	TP_SB_XOR_AH8 - @m38_lib.M38	22A6	TP_SB_XOR_T5	TP_SB_XOR_T5 - @m38_lib.M38	21C6	TP_SB_XOR_U3	TP_SB_XOR_U3 - @m38_lib.M38	21C6	TP_SB_XOR_U5	TP_SB_XOR_U5 - @m38_lib.M38	21C6	TP_SB_XOR_U7	TP_SB_XOR_U7 - @m38_lib.M38	21C6	TP_SB_XOR_V3	TP_SB_XOR_V3 - @m38_lib.M38	21C6	TP_SB_XOR_V4	TP_SB_XOR_V4 - @m38_lib.M38	21C6	TP_SB_XOR_V6	TP_SB_XOR_V6 - @m38_lib.M38	21C6	TP_SB_XOR_V7	TP_SB_XOR_V7 - @m38_lib.M38	21C6	TP_SB_XOR_W1	TP_SB_XOR_W1 - @m38_lib.M38	21C6	TP_SB_XOR_W3	TP_SB_XOR_W3 - @m38_lib.M38	21C6	TP_SB_XOR_Y1	TP_SB_XOR_Y1 - @m38_lib.M38	21C6	TP_SB_XOR_Y2	TP_SB_XOR_Y2 - @m38_lib.M38	21C6	TP_U5999_P1	TP_U5999_P1 - @m38_lib.M38	59A7	TP_U5999_P13	TP_U5999_P13 - @m38_lib.M38	59A5	TP_U5999_P14	TP_U5999_P14 - @m38_lib.M38	59A5	TP_U8400_AG14	TP_U8400_AG14 - @m38_lib.M38	91A5	TP_U8900_J2	TP_U8900_J2 - @m38_lib.M38	89A7	TP_U8900_J3	TP_U8900_J3 - @m38_lib.M38	89A7	TP_U8950_J2	TP_U8950_J2 - @m38_lib.M38	89A4	TP_U8950_J3	TP_U8950_J3 - @m38_lib.M38	89A4	TP_U9000_J2	TP_U9000_J2 - @m38_lib.M38	90A7	TP_U9000_J3	TP_U9000_J3 - @m38_lib.M38	90A7	TP_U9050_J2	TP_U9050_J2 - @m38_lib.M38	90A4	TP_U9050_J3	TP_U9050_J3 - @m38_lib.M38	90A4	TSSENSE_CPU_DXP	TSSENSE_CPU_DXP - @m38_lib.M38	61C5	TSSENSE_NB_DXP	TSSENSE_NB_DXP - @m38_lib.M38	61B5	TSSENSE_NB_GPU_DNX	TSSENSE_NB_GPU_DNX - @m38_lib.M38	61B5	TV_DACA_OUT	TV_DACA_OUT - @m38_lib.M38	13C5 19B1	TV_DACB_OUT	TV_DACB_OUT - @m38_lib.M38	13C5 19B1	TV_DACC_OUT	TV_DACC_OUT - @m38_lib.M38	13C5 19B1	TV_IRTNA	TV_IRTNA - @m38_lib.M38	13C5 19B1	TV_IRTNB	TV_IRTNB - @m38_lib.M38	13C5 19B1	TV_IRTNC	TV_IRTNC - @m38_lib.M38	13C5 19B1	TV_IREF	TV_IREF - @m38_lib.M38	13C5 19B1	PP3V3_S0_NB_VCCA_TVBG	PP3V3_S0_NB_VCCA_TVBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDBG	PP3V3_S0_NB_VCCA_TVDBG - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACC	PP3V3_S0_NB_VCCA_TVDACC - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACB	PP3V3_S0_NB_VCCA_TVDACB - @m38_lib.M38	17C6 19B1	PP3V3_S0_NB_VCCA_TVDACA	PP3V3_S0_NB_VCCA_TVDACA - @m38_lib.M38	17C

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D	Title: Cref Part Report Design: m38 Date: Mar 8 13:19:15 2006			C2514 CAP_402 m38[25C6] C2515 CAP_402 m38[25B6] C2516 CAP_P_CASE-C2 m38[25D3] C2517 CAP_402 m38[25D6] C2518 CAP_402 m38[25D4] C2519 CAP_402 m38[25D3] C2520 CAP_402 m38[25B6] C2521 CAP_402 m38[25C3] C2522 CAP_402 m38[25B3] C2523 CAP_402 m38[25B4] C2524 CAP_603 m38[25B3] C2525 CAP_402 m38[25B3] C2526 CAP_402 m38[25A4] C2527 CAP_402 m38[25A3] C2528 CAP_402 m38[25A3] C2529 CAP_402 m38[25A3] C2530 CAP_402 m38[25A3] C2531 CAP_402 m38[25D1] C2532 CAP_402 m38[25C1] C2533 CAP_402 m38[25C1] C2534 CAP_402 m38[25D1] C2605 CAP_402 m38[26C7] C2607 CAP_402 m38[26D5] C2608 CAP_402 m38[26D8] C2609 CAP_402 m38[26D8] C2610 CAP_402 m38[26C7] C2611 CAP_402 m38[26B7] C2698 CAP_402 m38[26C4] C2699 CAP_402 m38[26C5] C2800 CAP_402 m38[28D6] C2801 CAP_603 m38[28B2] C2802 CAP_603 m38[28B2] C2803 CAP_603 m38[28B1] C2804 CAP_603 m38[28B1] C2810 CAP_402 m38[28B2] C2811 CAP_402 m38[28B2] C2812 CAP_402 m38[28B1] C2813 CAP_402 m38[28B1] C2814 CAP_402 m38[28B2] C2815 CAP_402 m38[28B2] C2816 CAP_402 m38[28B1] C2817 CAP_402 m38[28B1] C2818 CAP_402 m38[28B2] C2819 CAP_402 m38[28B2] C2820 CAP_402 m38[28B1] C2821 CAP_402 m38[28B1] C2850 CAP_603 m38[28D6] C2851 CAP_603 m38[28A6] C2852 CAP_402 m38[28A6] C2900 CAP_402 m38[29D6] C2908 CAP_402 m38[29B2] C2909 CAP_402 m38[29B2] C2910 CAP_402 m38[29B1] C2911 CAP_402 m38[29B1] C2912 CAP_402 m38[29B2] C2913 CAP_402 m38[29B2] C2914 CAP_402 m38[29B1] C2915 CAP_402 m38[29B1] C2916 CAP_402 m38[29B2] C2917 CAP_402 m38[29B2] C2918 CAP_402 m38[29B1] C2919 CAP_402 m38[29B1] C2920 CAP_402 m38[29B2] C2921 CAP_402 m38[29B2] C2922 CAP_402 m38[29B1] C2923 CAP_402 m38[29B1] C2950 CAP_603 m38[29D6] C2951 CAP_603 m38[29A7] C2952 CAP_402 m38[29A6] C3004 CAP_402 m38[30B4] C3005 CAP_402 m38[30D4] C3006 CAP_402 m38[30B3] C3007 CAP_402 m38[30D3] C3008 CAP_402 m38[30A3] C3009 CAP_402 m38[30A4] C3010 CAP_402 m38[30D4] C3011 CAP_402 m38[30D3] C3013 CAP_402 m38[30A4] C3014 CAP_402 m38[30A4] C3015 CAP_402 m38[30A3] C3030 CAP_402 m38[30C4] C3033 CAP_402 m38[30C3] C3035 CAP_402 m38[30C3] C3100 CAP_402 m38[31C4] C3101 CAP_603 m38[31B6] C3102 CAP_603 m38[31B4] C3105 CAP_P_SMC-LF m38[31B4] C3109 CAP_603 m38[31C5] C3110 CAP_402 m38[31B6] C3301 CAP_402 m38[33D6] C3302 CAP_402 m38[33D6] C3303 CAP_402 m38[33D6] C3304 CAP_402 m38[33D6] C3305 CAP_402 m38[33D4] C3306 CAP_402 m38[33D4] C3307 CAP_402 m38[33C4] C3308 CAP_402 m38[33D4] C3309 CAP_603 m38[33D4] C3310 CAP_402 m38[33D3] C3311 CAP_402 m38[33C6] C3312 CAP_603 m38[33C6] C3314 CAP_402 m38[33D8] C3315 CAP_402 m38[33D7] C3316 CAP_603 m38[33D7] C3317 CAP_603 m38[33D4] C3389 CAP_402 m38[33C7] C3390 CAP_402 m38[33C7] C3800 CAP_402 m38[38B7] C3801 CAP_402 m38[38B7] C3802 CAP_402 m38[38B7] C3803 CAP_402 m38[38B7] C3804 CAP_402 m38[38C3] C3805 CAP_402 m38[38C2] C3806 CAP_805-2 m38[38C1] C4101 CAP_402 m38[41D7] C4102 CAP_402 m38[41D6] C4103 CAP_402 m38[41D6] C4104 CAP_402 m38[41D6] C4105 CAP_402 m38[41D5] C4106 CAP_402 m38[41D2] C4107 CAP_402 m38[41D2] C4110 CAP_402 m38[41D4] C4111 CAP_402 m38[41D4] C4112 CAP_402 m38[41C4]			C	C4113 CAP_402 m38[41C4] C4115 CAP_402 m38[41B5] C4116 CAP_402 m38[41B5] C4117 CAP_402 m38[41B2] C4118 CAP_402 m38[41B2] C4126 CAP_402 m38[41A8] C4127 CAP_402 m38[41A8] C4128 CAP_402 m38[41A8] C4129 CAP_402 m38[41A8] C4130 CAP_402 m38[41A7] C4131 CAP_402 m38[41A7] C4132 CAP_402 m38[41A7] C4133 CAP_402 m38[41A6] C4134 CAP_402 m38[41A6] C4135 CAP_402 m38[41A5] C4136 CAP_402 m38[41A5] C4137 CAP_402 m38[41A5] C4138 CAP_402 m38[41A4] C4139 CAP_402 m38[41A4] C4140 CAP_402 m38[41B3] C4150 CAP_402 m38[41D5] C4200 CAP_1210 m38[42D8] C4201 CAP_402 m38[42D7] C4202 CAP_1210 m38[42D7] C4203 CAP_1206-1 m38[42D6] C4204 CAP_402 m38[42D6] C4205 CAP_1210 m38[42C5] C4206 CAP_402 m38[42C5] C4209 CAP_603 m38[42B7] C4210 CAP_402 m38[42B6] C4300 CAP_402 m38[43D7] C4301 CAP_402 m38[43D6] C4304 CAP_402 m38[43C6] C4305 CAP_402 m38[43B6] C4401 CAP_402 m38[44D1] C4402 CAP_402 m38[44C1] C4410 CAP_402 m38[44D6] C4412 CAP_402 m38[44D1] C4500 CAP_402 m38[45D4] C4501 CAP_402 m38[45D3] C4502 CAP_402 m38[45D3] C4503 CAP_603 m38[45C6] C4504 CAP_402 m38[45C4] C4505 CAP_402 m38[45C5] C4506 CAP_402 m38[45C5] C4507 CAP_402 m38[45C5] C4508 CAP_402 m38[45D5] C4509 CAP_402 m38[45D5] C4510 CAP_402 m38[45D5] C4515 CAP_603 m38[45D6] C4520 CAP_402 m38[45D5] C4521 CAP_402 m38[45D4] C4522 CAP_402 m38[45D3] C4523 CAP_402 m38[45D3] C4609 CAP_603-1 m38[46D5] C4610 CAP_402 m38[46D4] C4611 CAP_402 m38[46D4] C4612 CAP_402 m38[46C4] C4613 CAP_402 m38[46C4] C4615 CAP_603-1 m38[46C2] C4616 CAP_402 m38[46B2] C4620 CAP_402 m38[46B4] C4621 CAP_402 m38[46B4] C4622 CAP_402 m38[46A4] C4623 CAP_402 m38[46A4] C4625 CAP_603-1 m38[46A2] C4626 CAP_402 m38[46A2] C4650 CAP_402 m38[46C7] C4654 CAP_402 m38[46B8] C4660 CAP_402 m38[46C7] C4664 CAP_402 m38[46B7] C4710 CAP_P_SMD2 m38[47D6] C4712 CAP_402 m38[47D5] C4713 CAP_402 m38[47D5] C4720 CAP_P_SMD2 m38[47C6] C4722 CAP_402 m38[47C5] C4723 CAP_402 m38[47C5] C4732 CAP_402 m38[47A5] C4733 CAP_402 m38[47A5] C4742 CAP_402 m38[47D2] C4743 CAP_402 m38[47D1] C4797 CAP_805-2 m38[47D3] C4798 CAP_402 m38[47A2] C4799 CAP_805-2 m38[47A2] C4950 CAP_805-2 m38[49C4] C4951 CAP_402 m38[49C4] C5300 CAP_402 m38[53B7] C5301 CAP_402 m38[53B7] C5304 CAP_402 m38[53D5] C5305 CAP_402 m38[53D5] C5306 CAP_402 m38[53D4] C5307 CAP_402 m38[53C4] C5308 CAP_402 m38[53C5] C5309 CAP_402 m38[53C4] C5310 CAP_402 m38[53C4] C5311 CAP_603 m38[53C3] C5312 CAP_603 m38[53D4] C5313 CAP_402 m38[53C5] C5314 CAP_603 m38[53C4] C5800 CAP_402 m38[58B8] C5801 CAP_402 m38[58B8] C5802 CAP_805 m38[58D3] C5803 CAP_402 m38[58D2] C5804 CAP_402 m38[58D2] C5805 CAP_402 m38[58D2] C5806 CAP_402 m38[58D1] C5807 CAP_402 m38[58D2] C5820 CAP_402 m38[58C3] C5900 CAP_402 m38[59D8] C5901 CAP_402 m38[59D8] C5902 CAP_402 m38[59B7] C5903 CAP_402 m38[59A8] C5919 CAP_402 m38[59B4] C5940 CAP_402 m38[59A4] C5941 CAP_402 m38[59A3] C5942 CAP_603 m38[59A3] C5943 CAP_402 m38[59A5] C6000 CAP_402 m38[60D6] C6001 CAP_402 m38[60D6] C6002 CAP_402 m38[60C6] C6003 CAP_402 m38[60C6] C6100 CAP_402 m38[61B5] C6101 CAP_402 m38[61B5] C6301 CAP_402 m38[63C2]			B	C6308 CAP_402 m38[63C5] C6309 CAP_402 m38[63C6] C6311 CAP_402 m38[63C2] C6312 CAP_402 m38[63D3] C6500 CAP_603 m38[65D5] C6501 CAP_805 m38[65D5] C6502 CAP_603 m38[65B4] C6503 CAP_805 m38[65B5] C6504 CAP_P_6_3X11-TH-LF m38[65C4] C6505 CAP_P_6_3X11-TH-LF m38[65B3] C6600 CAP_603 m38[66D4] C6601 CAP_805 m38[66C5] C6602 CAP_P_SM-LF m38[66C3] C6650 CAP_402 m38[66B5] C6651 CAP_402 m38[66A5] C6652 CAP_402 m38[66B3] C6653 CAP_402 m38[66A3] C6654 CAP_402 m38[66B4] C6655 CAP_402 m38[66B2] C6700 CAP_402 m38[67C4] C6701 CAP_402 m38[67C4] C6702 CAP_402 m38[67C3] C6703 CAP_402 m38[67C3] C6704 CAP_402 m38[59B7] C6705 CAP_402 m38[59B7] C6800 CAP_603 m38[68D6] C6801 CAP_402 m38[68D6] C6802 CAP_P_B2 m38[68D4] C6803 CAP_P_B2 m38[68D3] C6804 CAP_P_SMA-LF m38[68B4] C6805 CAP_805 m38[68B3] C6806 CAP_805 m38[68B3] C6807 CAP_P_SMA-LF m38[68B3] C6808 CAP_402 m38[68B3] C6809 CAP_402 m38[68B2] C6810 CAP_P_SMA-LF m38[68B2] C6811 CAP_402 m38[68B2] C6812 CAP_402 m38[68B4] C6813 CAP_402 m38[68B3] C6821 CAP_402 m38[68C6] C6822 CAP_603 m38[68A5] C6823 CAP_402 m38[68A4] C6825 CAP_402 m38[68A3] C6826 CAP_603 m38[68A3] C6829 CAP_402 m38[68B3] C6830 CAP_402 m38[68D4] C6832 CAP_402 m38[68B2] C6833 CAP_402 m38[68B2] C6834 CAP_402 m38[68B2] C6835 CAP_402 m38[68D6] C6836 CAP_402 m38[68D3] C7200 CAP_P_6_3X8-SM m38[72D5] C7201 CAP_1210 m38[72D5] C7202 CAP_805 m38[72D4] C7203 CAP_1210 m38[72D3] C7204 CAP_805 m38[72D6] C7205 CAP_805 m38[72C6] C7206 CAP_805 m38[72C6] C7207 CAP_805 m38[72C6] C7208 CAP_603-1 m38[72C4] C7209 CAP_805 m38[72B4] C7210 CAP_402 m38[72B3] C7211 CAP_402 m38[72B2] C7212 CAP_402 m38[72B2] C7213 CAP_402 m38[72B2] C7214 CAP_603 m38[72B5] C7215 CAP_402 m38[72C3] C7216 CAP_402 m38[72C6] C7217 CAP_P_6_3X8-SM m38[72D6] C7218 CAP_603 m38[72D5] C7219 CAP_603 m38[72D4] C7220 CAP_402 m38[72B7] C7221 CAP_402 m38[72B7] C7223 CAP_1210 m38[72D3] C7300 CAP_402 m38[73D7] C7301 CAP_402 m38[73D6] C7302 CAP_402 m38[73D6] C7303 CAP_402 m38[73D6] C7311 CAP_402 m38[73A7] C7312 CAP_402 m38[73A6] C7313 CAP_402 m38[73A6] C7314 CAP_402 m38[73A7] C7315 CAP_402 m38[73A6] C7317 CAP_402 m38[73B4] C7318 CAP_805 m38[73B4] C7321 CAP_402 m38[73C5] C7322 CAP_402 m38[73C5] C7323 CAP_402 m38[73A6] C7324 CAP_402 m38[73A5] C7400 CAP_402 m38[74B4] C7401 CAP_402 m38[74C4] C7402 CAP_402 m38[74A4] C7403 CAP_P_CASE-B-SM m38[74B7] C7404 CAP_P_CASE-B-SM m38[74B6] C7405 CAP_P_CASE-B-SM m38[74C7] C7406 CAP_P_CASE-B-SM m38[74C6] C7407 CAP_402 m38[74D8] C7408 CAP_402 m38[74D7] C7409 CAP_402 m38[74B8] C7410 CAP_402 m38[74A8] C7411 CAP_402 m38[74B8] C7412 CAP_402 m38[74A8] C7413 CAP_402 m38[74A8] C7414 CAP_402 m38[74A7] C7415 CAP_402 m38[74A8] C7416 CAP_402 m38[74A8] C7417 CAP_402 m38[74A8] C7418 CAP_805 m38[74A5] C7419 CAP_603-1 m38[74A4] C7420 CAP_402 m38[74A7] C7421 CAP_402 m38[74A7] C7422 CAP_402 m38[74A7] C7423 CAP_402 m38[74A7] C7500 CAP_402 m38[75C4] C7501 CAP_P_TH-MCZ m38[75C2] C7502 CAP_402 m38[75B4] C7503 CAP_402 m38[75C1] C7504 CAP_402 m38[75B1] C7505 CAP_402 m38[75C8] C7506 CAP_402 m38[75B7] C7507 CAP_402 m38[75B7] C7508 CAP_1210 m38[75C2] C7509 CAP_1210 m38[75D1] C7510 CAP_402 m38[75C8]			A	C85A0 CAP_402 m38[85D1] C600 CAP_402 m38[6C7] C601 CAP_402 m38[6A3] C602 CAP_402 m38[6A3] C603 CAP_402 m38[6A3] C604 CAP_402 m38[6A4] C610 CAP_402 m38[6C7] C699 CAP_P_CASE-C1 m38[6D7] C0800 CAP_402 m38[8B5] C0801 CAP_603 m38[8B5] C900 CAP_805 m38[9B6] C901 CAP_805 m38[9B6] C902 CAP_805 m38[9A6] C903 CAP_805 m38[9A6] C904 CAP_805 m38[9A6] C905 CAP_805 m38[9A6] C906 CAP_805 m38[9							

8				7				6				5				4				3				2				1																							
D	C7511	CAP_603	m38[75B2]	C8413	CAP_402	m38[84B7]	C8657	CAP_402	m38[86B6]	C9135	CAP_805	m38[91A6]	C	C7512	CAP_603	m38[75C2]	C8420	CAP_402	m38[84D5]	C8658	CAP_402	m38[86B5]	C9136	CAP_402	m38[91A6]	B	C7513	CAP_402	m38[75B7]	C8421	CAP_402	m38[84D5]	C8659	CAP_402	m38[86B5]	C9137	CAP_402	m38[91A5]	A	C7514	CAP_402	m38[75B8]	C8422	CAP_402	m38[84D5]	C8660	CAP_402	m38[86B5]	C9140	CAP_805	m38[91A6]
	C7515	CAP_603	m38[75C4]	C8423	CAP_402	m38[84D5]	C8661	CAP_402	m38[86B6]	C9141	CAP_402	m38[91A6]		C7516	CAP_402	m38[75B4]	C8424	CAP_402	m38[84D5]	C8662	CAP_402	m38[86B6]	C9142	CAP_402	m38[91A5]		C7517	CAP_P_SM-3	m38[75D2]	C8425	CAP_402	m38[84D5]	C8663	CAP_402	m38[86B6]	C9191	CAP_402	m38[91D2]													
	C7518	CAP_P_SM-3	m38[75D2]	C8426	CAP_402	m38[84D5]	C8664	CAP_402	m38[86B5]	C9300	CAP_805	m38[93C6]		C7521	CAP_402	m38[75A6]	C8427	CAP_402	m38[84D5]	C8665	CAP_402	m38[86B5]	C9301	CAP_402	m38[93C6]		C7526	CAP_603	m38[75D6]	C8428	CAP_402	m38[84D5]	C8666	CAP_402	m38[86B5]	C9302	CAP_402	m38[93C5]													
	C7527	CAP_603	m38[75C5]	C8429	CAP_402	m38[84C5]	C8667	CAP_402	m38[86B6]	C9305	CAP_805	m38[93C6]		C7528	CAP_402	m38[75B5]	C8430	CAP_402	m38[84C5]	C8668	CAP_402	m38[86B6]	C9306	CAP_402	m38[93C6]		C7529	CAP_402	m38[75B5]	C8431	CAP_402	m38[84C5]	C8669	CAP_402	m38[86B6]	C9307	CAP_402	m38[93C5]													
	C7530	CAP_402	m38[75D6]	C8432	CAP_402	m38[84C5]	C8670	CAP_402	m38[86B5]	C9310	CAP_805	m38[93C6]		C7531	CAP_402	m38[75B5]	C8433	CAP_402	m38[84C5]	C8671	CAP_402	m38[86B5]	C9311	CAP_402	m38[93C6]		C7532	CAP_402	m38[75B6]	C8434	CAP_402	m38[84C5]	C8672	CAP_402	m38[86B5]	C9312	CAP_402	m38[93C5]													
	C7533	CAP_402	m38[75B6]	C8435	CAP_402	m38[84C5]	C8673	CAP_402	m38[86B6]	C9315	CAP_805	m38[93B8]		C7534	CAP_402	m38[75B5]	C8436	CAP_402	m38[84C5]	C8674	CAP_402	m38[86B6]	C9316	CAP_402	m38[93B8]		C7535	CAP_603	m38[75D5]	C8437	CAP_402	m38[84C5]	C8675	CAP_402	m38[86B6]	C9317	CAP_402	m38[93B7]													
	C7550	CAP_603	m38[75D1]	C8438	CAP_402	m38[84C5]	C8676	CAP_402	m38[86B5]	C9320	CAP_805	m38[93B6]		C7551	CAP_603	m38[75D1]	C8439	CAP_402	m38[84C5]	C8677	CAP_402	m38[86B5]	C9321	CAP_402	m38[93B6]		C7590	CAP_402	m38[75C3]	C8440	CAP_402	m38[84B5]	C8678	CAP_402	m38[86B5]	C9322	CAP_402	m38[93B5]													
	C7592	CAP_402	m38[75B3]	C8441	CAP_402	m38[84B5]	C8679	CAP_402	m38[86A6]	C9325	CAP_805	m38[93B8]		C7596	CAP_402	m38[75D6]	C8442	CAP_402	m38[84B5]	C8680	CAP_402	m38[86A6]	C9326	CAP_402	m38[93B8]		C7597	CAP_1210	m38[75D1]	C8443	CAP_402	m38[84B5]	C8681	CAP_402	m38[86A6]	C9327	CAP_402	m38[93B7]													
	C7598	CAP_1210	m38[75D1]	C8444	CAP_402	m38[84B5]	C8682	CAP_402	m38[86A5]	C9330	CAP_805	m38[93B6]		C7599	CAP_402	m38[76D6]	C8445	CAP_402	m38[84B5]	C8683	CAP_402	m38[86A5]	C9331	CAP_402	m38[93B6]		C7600	CAP_402	m38[76D3]	C8446	CAP_402	m38[84B5]	C8690	CAP_805	m38[86D5]	C9332	CAP_402	m38[93B5]													
	C7601	CAP_402	m38[76D3]	C8447	CAP_402	m38[84B5]	C8691	CAP_402	m38[86D5]	C9340	CAP_805	m38[93A6]		C7602	CAP_402	m38[76D2]	C8448	CAP_402	m38[84B5]	C8692	CAP_402	m38[86D5]	C9341	CAP_402	m38[93A6]		C7603	CAP_402	m38[76C4]	C8449	CAP_402	m38[84B5]	C8695	CAP_805	m38[86D2]	C9342	CAP_402	m38[93A5]													
C7612	CAP_402	m38[76B2]	C8450	CAP_402	m38[84B5]	C8696	CAP_402	m38[86D3]	C9345	CAP_805	m38[93A6]	C7633	CAP_402	m38[76C7]	C8451	CAP_402	m38[84B5]	C8697	CAP_402	m38[86D3]	C9346	CAP_402	m38[93A5]	C7659	CAP_402	m38[76D4]	C8455	CAP_402	m38[84D2]	C8711	CAP_402	m38[87B7]	C9347	CAP_402	m38[93A5]																
C	C7669	CAP_402	m38[76D4]	C8456	CAP_402	m38[84D2]	C8713	CAP_402	m38[87B7]	C9400	CAP_603-1	m38[94C7]	C7700	CAP_603	m38[77D4]	C8457	CAP_402	m38[84D2]	C8715	CAP_402	m38[87A7]	C9401	CAP_402	m38[94C6]	C7703	CAP_402	m38[77C4]	C8458	CAP_402	m38[84D2]	C8716	CAP_402	m38[87A6]	C9410	CAP_402	m38[94C6]															
	C7704	CAP_402	m38[77C4]	C8459	CAP_402	m38[84D2]	C8721	CAP_402	m38[87B4]	C9420	CAP_1210	m38[94C5]	C7706	CAP_402	m38[77C4]	C8460	CAP_402	m38[84D2]	C8723	CAP_402	m38[87B4]	C9450	CAP_805	m38[94C2]	C7709	CAP_805	m38[77C3]	C8461	CAP_402	m38[84D2]	C8725	CAP_402	m38[87A4]	C9470	CAP_402	m38[94B2]															
	C7710	CAP_402	m38[77C7]	C8462	CAP_402	m38[84D2]	C8726	CAP_402	m38[87A3]	C9700	CAP_402	m38[97C8]	C7711	CAP_402	m38[77B7]	C8463	CAP_402	m38[84D2]	C8900	CAP_805	m38[89D7]	C9710	CAP_603	m38[97C3]	C7712	CAP_402	m38[77C7]	C8464	CAP_402	m38[84C2]	C8901	CAP_402	m38[89D7]	C9711	CAP_402	m38[97D3]															
	C7750	CAP_402	m38[77A5]	C8465	CAP_402	m38[84C2]	C8902	CAP_402	m38[89D7]	C9713	CAP_402	m38[97C2]	C7751	CAP_805	m38[77B4]	C8466	CAP_402	m38[84C2]	C8903	CAP_402	m38[89D7]	C9714	CAP_402	m38[97C2]	C7752	CAP_805	m38[77B4]	C8467	CAP_402	m38[84C2]	C8904	CAP_402	m38[89D6]	C9740	CAP_402	m38[97A7]															
	C7753	CAP_402	m38[77A7]	C8468	CAP_402	m38[84C2]	C8910	CAP_402	m38[89D7]	C9741	CAP_402	m38[97A6]	C7754	CAP_402	m38[77B6]	C8469	CAP_402	m38[84C2]	C8915	CAP_402	m38[89D6]	C9742	CAP_402	m38[97A6]	C7755	CAP_805	m38[77B3]	C8470	CAP_402	m38[84C2]	C8920	CAP_805	m38[89C8]	D2500	DIODE_SCHOT_SOT23	m38[25C8]															
	C7756	CAP_805	m38[77B3]	C8471	CAP_402	m38[84C2]	C8921	CAP_402	m38[89C8]	D2501	DIODE_SCHOT_SOT23	m38[25D8]	C7757	CAP_402	m38[77A6]	C8472	CAP_402	m38[84C2]	C8922	CAP_402	m38[89C7]	D2600	DIODE_SCHOT_SOT23	m38[26D8]	C7799	CAP_402	m38[77A3]	C8473	CAP_402	m38[84C2]	C8923	CAP_402	m38[89C7]	D2601	DIODE_SCHOT_SOT23	m38[26C8]															
	C7800	CAP_1210	m38[78C3]	C8474	CAP_402	m38[84C2]	C8924	CAP_402	m38[89C7]	D4600	DIODE_SMC	m38[46D5]	C7801	CAP_P_SM-LF	m38[78C4]	C8475	CAP_402	m38[84B2]	C8925	CAP_402	m38[89C7]	D4650	ZENER_SOT23	m38[46A6]	C7802	CAP_603	m38[78C6]	C8476	CAP_402	m38[84B2]	C8926	CAP_402	m38[89C6]	D4700	DIODE_SCHOT_3P_A_SC-	m38[47C5]															
	C7803	CAP_603	m38[78B6]	C8477	CAP_402	m38[84B2]	C8931	CAP_402	m38[89C7]	D4701	DIODE_SCHOT_3P_A_SC-	m38[47B5]	C7804	CAP_402	m38[78C6]	C8478	CAP_402	m38[84B2]	C8932	CAP_402	m38[89C6]	75	C7805	CAP_603	m38[78C4]	C8479	CAP_402	m38[78B2]	C8933	CAP_402	m38[89C6]	D4702	DIODE_SCHOT_3P_A_SC-	m38[47A5]																	
	C7806	CAP_805-1	m38[78B2]	C8480	CAP_402	m38[84B2]	C8951	CAP_402	m38[89D4]	D4705	DIODE_SCHOT_3P_A_SC-	m38[47C6]	C7807	CAP_P_CASE-D2E-LF	m38[78B3]	C8481	CAP_402	m38[84B2]	C8952	CAP_402	m38[89D4]	75	C7808	CAP_402	m38[78B3]	C8482	CAP_402	m38[78B3]	C8953	CAP_402	m38[89D3]	D4706	DIODE_SCHOT_3P_A_SC-	m38[47B6]																	
	C7809	CAP_402	m38[78B3]	C8483	CAP_402	m38[84B2]	C8954	CAP_402	m38[89D3]	D4707	DIODE_SCHOT_3P_A_SC-	m38[47A6]	C7810	CAP_402	m38[78B4]	C8484	CAP_402	m38[84B2]	C8955	CAP_402	m38[89D3]	75	C7811	CAP_402	m38[78B5]	C8485	CAP_402	m38[78B4]	C8956	CAP_402	m38[89D3]	D4900	DIODE_SCHOT_3P_A_SC-	m38[49B4]																	
C7812	CAP_402	m38[78B5]	C8486	CAP_402	m38[84B2]	C8970	CAP_805	m38[89C5]	D4901	DIODE_SCHOT_3P_A_SC-	m38[49B4]	C7813	CAP_1206	m38[78B4]	C8487	CAP_402	m38[84B2]	C8971	CAP_402	m38[89C4]	75	C7814	CAP_402	m38[78B5]	C8488	CAP_402	m38[78B5]	C8972	CAP_402	m38[89C4]	D6500	DIODE_SOT23	m38[65C4]																		
B	C7817	CAP_P_CASE-D2E-LF	m38[78B2]	C8500	CAP_603	m38[85D6]	C8973	CAP_402	m38[89C4]	D6501	DIODE_SOT23	m38[65B4]	C7900	CAP_402	m38[79D6]	C8501	CAP_603	m38[85D6]	C8974	CAP_402	m38[89C4]	D6502	DIODE_SCHOT_SMB	m38[65C4]	C7901	CAP_603	m38[79D5]	C8502	CAP_603	m38[85D6]	C8975	CAP_402	m38[89C3]	D6503	DIODE_SCHOT_SMB	m38[65B4]															
	C7902	CAP_402	m38[79C5]	C8506	CAP_402	m38[85C8]	C8976	CAP_402	m38[89C3]	D6600	DIODE_SOT23	m38[66C4]	C7903	CAP_603	m38[79C7]	C8507	CAP_402	m38[85C7]	C8977	CAP_402	m38[89C3]	D6603	DIODE_SCHOT_SMB	m38[66C3]	C7906	CAP_402	m38[79C5]	C8508	CAP_402	m38[85C7]	C8981	CAP_402	m38[89C3]	D7500	DIODE_SCHOT_SMB	m38[75C3]															
	C7907	CAP_402	m38[79C3]	C8509	CAP_402	m38[85C5]	C8983	CAP_402	m38[89C3]	D7501	DIODE_SCHOT_SMB	m38[75B2]	C7908	CAP_402	m38[79C5]	C8520	CAP_603	m38[85C3]	C8988	CAP_402	m38[89C3]	D7599	DIODE_SOT23	m38[76D6]	C7909	CAP_1206	m38[79C4]	C8521	CAP_603	m38[85C4]	C9000	CAP_805	m38[90D7]	D8520	DIODE_SCHOT_SMB	m38[85C3]															
	C7910	CAP_P_SM-3	m38[79D4]	C8522	CAP_402	m38[85C5]	C9001	CAP_402	m38[90D7]	D9700	ZENER_CASE425	m38[97C1]	C7911	CAP_1210	m38[79D3]	C8523	CAP_402	m38[85B2]	C9002	CAP_402	m38[90D7]	DP4610	DIODE_DUAL_6P_SOT-36	m38[46D4 46D3]	C7912	CAP_P_CASE-D2E-LF	m38[79C3]	C8530	CAP_1210	m38[85D4]	C9003	CAP_402	m38[90D7]	3	C7913	CAP_1206	m38[79C2]	C8531	CAP_1210	m38[85D4]	C9004	CAP_402	m38[90D6]	DP4611	DIODE_DUAL_6P_SOT-36	m38[46C4 46C3]					
	C7914	CAP_402	m38[78B5]	C8532	CAP_1210	m38[85D4]	C9010	CAP_402	m38[90D7]	DP4620	DIODE_DUAL_6P_SOT-36	m38[46B4 46B3]	C7915	CAP_603	m38[79D6]	C8540	CAP_805	m38[85C2]	C9015	CAP_402	m38[90D6]	3	C7916	CAP_603	m38[79D6]	C8541	CAP_805	m38[85C2]	C9020	CAP_805	m38[90C8]	DP4621	DIODE_DUAL_6P_SOT-36	m38[46A4 46A3]																	
	C8000	CAP_1210	m38[80D3]	C8542	CAP_P_CASE-D2E-LF	m38[85C2]	C9021	CAP_402	m38[90C8]	DZ7300	SUPPR_TRANSIENT_4P1-	m38[73C6]	C8001	CAP_P_CASE-D2E-LF	m38[80D3]	C8543	CAP_P_CASE-D2E-LF	m38[85C2]	C9022	CAP_402	m38[90C7]	0405	C8002	CAP_603	m38[80D6]	C8544	CAP_603	m38[80D6]	C9023	CAP_402	m38[90C7]	DZ7301	SUPPR_TRANSIENT_4P1-	m38[73A5]																	
	C8003	CAP_402	m38[80C3]	C8551	CAP_603	m38[85B8]	C9024	CAP_402	m38[90C7]	DZ7302	SUPPR_TRANSIENT_4P1-	m38[73C5]	C8004	CAP_1206	m38[80C4]	C8552	CAP_805	m38[85B7]	C9025	CAP_402	m38[90C7]	0405	C8005	CAP_402	m38[80C4]	C8553	CAP_805	m38[85B7]	C9026	CAP_402	m38[90C6]	DZ7303	SUPPR_TRANSIENT_4P1-	m38[73A4]																	
	C8006	CAP_402	m38[80D6]	C8556	CAP_805	m38[85B6]	C9027	CAP_402	m38[90C7]	DZ7304	SUPPR_TRANSIENT_4P1-	m38[73C4]	C8007	CAP_402	m38[80D6]	C8557	CAP_805	m38[85B6]	C9028	CAP_402	m38[90C6]	0405	C8008	CAP_603	m38[80D6]	C8558	CAP_603	m38[80D6]	C9029	CAP_402	m38[90C6]	F4600	FUSE_SM-LF	m38[46D2]																	
	C8009	CAP_603	m38[80C6]	C8560	CAP_402	m38[85A6]	C9031	CAP_402	m38[90C7]	F4601	FUSE_SM-LF	m38[46D2]	C8010	CAP_603	m38[80C4]	C8561	CAP_603	m38[85A4]	C9032	CAP_402	m38[90C6]	F4602	FUSE_MINISMD-LF	m38[46D3]	C8011	CAP_402	m38[80C5]	C8562	CAP_402	m38[85A4]	C9033	CAP_402	m38[90C6]	F4701	FUSE_MINISMD-LF	m38[47D3]															
	C8012	CAP_402	m38[81C5]	C8563	CAP_402	m38[85A5]	C9050	CAP_805	m38[90D4]	F4710	FUSE_SM-LF	m38[47D5]	C8013	CAP_603	m38[81C7]	C8564	CAP_402	m38[85A3]	C9051	CAP_402	m38[90D4]	FL4610	FILTER_4P_2012	m38[46C3]	C8014	CAP_P_TH-MCZ	m38[81D4]	C8565	CAP_805	m38[85A3]	C9052	CAP_402	m38[90D3]	FL4620	FILTER_4P_2012	m38															

8			7			6			5			4			3			2			1																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								
D	J600	CON_M12RT_D_THA_M-RT m38[6D7] -TH	J7000	CPU_YONAH_SKT_BGA m38[7C3 7D7]	J7070	CPU_YONAH_SKT_BGA m38[8D4 8D8]	J1000	CON_2RTSM_125_SM-2MT m38[10B7] -BLK-LF	J1101	CON_F28RT_S2MT_SM_F- m38[11C2] RT-SM	J2600	BATTERY_2P_TH m38[26C8]	J2800	CON_F200RT_DDR2DIMM_ m38[28D5] 5MT_SM_F-RT-SM1	J2900	CON_F200RT_DDR2DIMM_ m38[29D5] 5MT_SM_F-RT-SM1	J2901	CON_F4ST_S2MT_SM_F-S m38[59C7] T-SM	J2903	CON_M2ST_S2MT_SM_M-S m38[59C8] T-SM	J4700	CON_F10ST_D_SMA_F-ST m38[47A2] -SM	J4950	CON_M4RT_S2MT_SM_M-R m38[49C4] T-SM	J5300	CON_F52RT_D2MT_SM_F- m38[53C5] RT-SM	J6000	CON_F30STSM_5047_SM1 m38[60B5]	J6500	CON_M4RT_S2MT_SM_M-R m38[65D3] T-SM	J6501	CON_M5RT_S2MT_SMA_M- m38[65B2] RT-SM	J6600	CON_F4ST_S2MT_SM_F-S m38[66C2] T-SM	J6601	CON_M4RT_S2MT_SM_M-R m38[66B5] T-SM	J6602	CON_M4RT_S2MT_SM_M-R m38[66B3] T-SM	J7300	CON_F4ST_S4MT_TH1_F- m38[73D8] ST-TH	J7301	CON_M7RT_S2MT_SM_M-R m38[73D1] T-SM	J7303	CON_F9ANG_S4MT_TH1_F m38[73B3] -ANG-TH	J9401	CON_M4ST_S_SM_M-ST-S m38[94C2] M	J9402	CON_F30ST_D_SM_F-ST- m38[94B6] SM	J9410	CON_M4ST_S_SM_M-ST-S m38[94A3] M	J9710	CON_DV1_F32ST_Q2MT_S m38[97D5] M_F-ST-SM	JC900	CON_M7ST_SATA_SM_M-S m38[38B8] T-SM	JC901	CON_F50ST_D2MT_SM_F- m38[38D2] ST-SM	JD600	CON_RJ45_10ANG_S3MT_ m38[43C6] TH1_F-ANG-TH	JE000	CON_F6ST_S4MT_TH1_F- m38[46C2] ST-TH	JE001	CON_F6ST_S4MT_TH1_F- m38[46B2] ST-TH	JE310	CON_F4ST_USB_S3MT_TH m38[47D4] _F-ST-TH	JE320	CON_F4ST_USB_S3MT_TH m38[47B4] _F-ST-TH	JE330	CON_F4ST_USB_S3MT_TH m38[47A4] _F-ST-TH	JE350	CON_M14RT_S2MT_SM_M- m38[47C1] RT-SM	L1934	IND_0603 m38[19C7]	L1936	IND_0603 m38[19C7]	L1970	IND_1210 m38[19A5]	L1975	IND_0805 m38[19A5]	L2500	IND_SM-3 m38[25B8]	L2507	IND_1206 m38[25A7]	L3301	IND_0402 m38[33D7]	L3302	IND_0402 m38[33D3]	L4200	IND_SM m38[42D7]	L4201	IND_SM m38[42B7]	L4300	IND_SM m38[43D7]	L4409	IND_0402 m38[44D6]	L4610	IND_1206-LF m38[46D2]	L4620	IND_1206-LF m38[46B2]	L4690	IND_SM-1 m38[46A6]	L4710	IND_SM m38[47D6]	L4712	FILTER_4P_2012 m38[47C6]	L4720	IND_SM m38[47C6]	L4722	FILTER_4P_2012 m38[47B6]	L4730	IND_SM m38[47B6]	L4732	FILTER_4P_2012 m38[47A6]	L4740	IND_SM m38[47D2]	L4742	FILTER_4P_2012 m38[47C2]	L4752	FILTER_4P_2012 m38[47B2]	L5300	FILTER_4P_2012 m38[53B3]	L6800	IND_0402 m38[68A5]	L6801	IND_0402 m38[68D6]	L7200	IND_SM-1 m38[72D6]	L7201	IND_0603 m38[72C2]	L7202	IND_0603 m38[72C2]	L7203	IND_0603 m38[72C3]	L7204	IND_0603 m38[72C3]	L7205	IND_0603 m38[72D6]	L7206	IND_0603 m38[72C6]	L7207	IND_0603 m38[72C6]	L7208	IND_0603 m38[72C6]	L7300	IND_SM m38[73D7]	L7301	IND_SM m38[73D7]	L7302	IND_SM m38[73D7]	L7303	IND_SM m38[73D7]	L7304	IND_SM m38[73D5]	L7305	IND_SM m38[73D5]	L7306	IND_SM m38[73D5]	L7307	IND_SM m38[73D5]	L7309	IND_SM m38[73C6]	L7310	IND_SM m38[73C6]	L7312	IND_SM m38[73C4]	L7313	IND_SM m38[73C4]	L7314	IND_SM m38[73B7]	L7315	IND_SM m38[73B7]	L7316	IND_SM m38[73B7]	L7317	IND_SM m38[73A7]	L7318	IND_SM m38[73B7]	L7319	IND_SM m38[73B7]	L7320	IND_SM m38[73A7]	L7322	IND_SM m38[73B5]	L7323	IND_SM m38[73B5]	L7324	IND_SM m38[73B5]	L7325	IND_SM m38[73A5]	L7326	IND_SM m38[73B5]	L7327	IND_SM m38[73B5]	L7328	IND_SM m38[73A5]	L7500	IND_SM m38[75D1]	L7501	IND_SM m38[75B2]	L7502	IND_TH-VERT-LF m38[76D8]	L7700	IND_SM1-LF m38[77C4]	L7750	IND_SM-LF m38[77A4]	L7800	IND_3P_SM m38[78B3]	L7900	IND_3P_SM m38[79C3]	L8000	IND_3P_SM m38[80C3]	L8100	IND_IHLP m38[81C3]	L8400	IND_0402 m38[84B7]	L8520	IND_IHLP m38[85C3]	L8585	IND_4P_2COIL_SDQ1215 m38[85A3] 0-SM	L8715	IND_0402 m38[87A7]	L8725	IND_0402 m38[87A4]	L8910	IND_0402 m38[89D7]	L8915	IND_0402 m38[89D7]	L8960	IND_0402 m38[89D4]	L8965	IND_0402 m38[89D4]	L9010	IND_0402 m38[90D7]	L9015	IND_0402 m38[90D7]	L9060	IND_0402 m38[90D4]	L9065	IND_0402 m38[90D4]	L9120	IND_0402 m38[91B6]	L9125	IND_0402 m38[91B6]	L9130	IND_0402 m38[91B7]	L9135	IND_0402 m38[91A7]	L9140	IND_0402 m38[91A7]	L9300	IND_0402 m38[93C7]	L9305	IND_0402 m38[93C7]	L9310	IND_0402 m38[93C7]	L9315	IND_0402 m38[93C7]	L9320	IND_0402 m38[93B7]	L9325	IND_0402 m38[93B7]	L9330	IND_0402 m38[93B7]	L9345	IND_0402 m38[93B7]	L9400	IND_SM m38[94C6]	L9700	FILTER_4P_2012H m38[97D7]	L9701	FILTER_4P_2012H m38[97D7]	L9702	FILTER_4P_2012H m38[97C7]	L9703	FILTER_4P_SM m38[97C7]	L9710	IND_SM-1 m38[97D5]	L9D600	LED_2_0X1.25MM-SM m38[6A7]	L9D601	LED_2_0X1.25MM-SM m38[6A8]	L9D602	LED_2_0X1.25MM-SM m38[6A7]	L9D2901	LED_3X2MM-SM m38[59D6]	L9D3800	LED_2_0X1.25MM-SM m38[38B3]	L9D4300	LED_2_0X1.25MM-SM m38[43D2]	L9D4301	LED_2_0X1.25MM-SM m38[43D2]	L9D4302	LED_2_0X1.25MM-SM m38[43D1]	L9D4303	LED_2_0X1.25MM-SM m38[43D1]	L9D7900	LED_2_0X1.25MM-SM m38[79A4]	L9D8000	LED_2_0X1.25MM-SM m38[80A4]	L9D8100	LED_2_0X1.25MM-SM m38[81A4]	PP5E1	PROBEPOINT_SM m38[5B8]	PP5E2	PROBEPOINT_SM m38[5B8]	PP6A0	PROBEPOINT_SM m38[5A6]	PP6A1	PROBEPOINT_SM m38[5A6]	PP6A2	PROBEPOINT_SM m38[5A6]	PP6A3	PROBEPOINT_SM m38[5A6]	PP6A4	PROBEPOINT_SM m38[5A6]	PP6A5	PROBEPOINT_SM m38[5A6]	PP6A6	PROBEPOINT_SM m38[5A6]	PP6A7	PROBEPOINT_SM m38[5A6]	PP6A8	PROBEPOINT_SM m38[5A6]	PP6A9	PROBEPOINT_SM m38[5A6]	PP6B0	PROBEPOINT_SM m38[5A6]	PP6B1	PROBEPOINT_SM m38[5A6]	PP6B2	PROBEPOINT_SM m38[5A6]	PP6B3	PROBEPOINT_SM m38[5A6]	PP6B4	PROBEPOINT_SM m38[5A6]	PP6B5	PROBEPOINT_SM m38[5A6]	PP6B6	PROBEPOINT_SM m38[5A6]	PP6B7	PROBEPOINT_SM m38[5A6]	PP6B8	PROBEPOINT_SM m38[5A6]	PP6B9	PROBEPOINT_SM m38[5A6]	PP6C0	PROBEPOINT_SM m38[5A6]	PP6C1	PROBEPOINT_SM m38[5A6]	PP6C2	PROBEPOINT_SM m38[5A6]	PP6C3	PROBEPOINT_SM m38[5A6]	PP6C4	PROBEPOINT_SM m38[5C8]	PP6C5	PROBEPOINT_SM m38[5C8]	PP6C6	PROBEPOINT_SM m38[5C8]	PP6C7	PROBEPOINT_SM m38[5C8]	PP6C8	PROBEPOINT_SM m38[5C8]	PP6D0	PROBEPOINT_SM m38[5C8]	PP6D1	PROBEPOINT_SM m38[5C8]	PP6D2	PROBEPOINT_SM m38[5B8]	PP6D3	PROBEPOINT_SM m38[5B8]	PP6D4	PROBEPOINT_SM m38[5B8]	PP6D5	PROBEPOINT_SM m38[5B8]	PP6D6	PROBEPOINT_SM m38[5B8]	PP6D7	PROBEPOINT_SM m38[5B8]	PP6D8	PROBEPOINT_SM m38[5B8]	PP6D9	PROBEPOINT_SM m38[5B8]	PP6E0	PROBEPOINT_SM m38[5B8]	PP6E1	PROBEPOINT_SM m38[5B6]	PP600	PROBEPOINT_SM m38[5D8]	PP601	PROBEPOINT_SM m38[5D8]	PP602	PROBEPOINT_SM m38[5D8]	PP603	PROBEPOINT_SM m38[5D8]	PP604	PROBEPOINT_SM m38[5D8]	PP605	PROBEPOINT_SM m38[5D8]	PP606	PROBEPOINT_SM m38[5D8]	PP607	PROBEPOINT_SM m38[5D8]	PP608	PROBEPOINT_SM m38[5D8]	PP609	PROBEPOINT_SM m38[5D8]	PP610	PROBEPOINT_SM m38[5D8]	PP611	PROBEPOINT_SM m38[5D8]	PP612	PROBEPOINT_SM m38[5D8]	PP613	PROBEPOINT_SM m38[5D8]	PP614	PROBEPOINT_SM m38[5D8]	PP615	PROBEPOINT_SM m38[5D8]	PP616	PROBEPOINT_SM m38[5D8]	PP617	PROBEPOINT_SM m38[5D8]	PP618	PROBEPOINT_SM m38[5D8]	PP619	PROBEPOINT_SM m38[5D8]	PP620	PROBEPOINT_SM m38[5D8]	PP621	PROBEPOINT_SM m38[5C8]	PP622	PROBEPOINT_SM m38[5C8]	PP623	PROBEPOINT_SM m38[5C8]	PP624	PROBEPOINT_SM m38[5C8]	PP625	PROBEPOINT_SM m38[5C8]	PP626	PROBEPOINT_SM m38[5C8]	PP627	PROBEPOINT_SM m38[5C8]	PP628	PROBEPOINT_SM m38[5C8]	PP629	PROBEPOINT_SM m38[5C8]	PP630	PROBEPOINT_SM m38[5C8]	PP631	PROBEPOINT_SM m38[5D6]	PP632	PROBEPOINT_SM m38[5D6]	PP633	PROBEPOINT_SM m38[5D6]	PP634	PROBEPOINT_SM m38[5D6]	PP635	PROBEPOINT_SM m38[5D6]	PP636	PROBEPOINT_SM m38[5D6]	PP637	PROBEPOINT_SM m38[5D6]	PP638	PROBEPOINT_SM m38[5D6]	PP639	PROBEPOINT_SM m38[5D6]	PP640	PROBEPOINT_SM m38[5D6]	PP641	PROBEPOINT_SM m38[5D6]	PP642	PROBEPOINT_SM m38[5D6]	PP643	PROBEPOINT_SM m38[5D6]	PP644	PROBEPOINT_SM m38[5D6]	PP645	PROBEPOINT_SM m38[5D6]	PP646	PROBEPOINT_SM m38[5D6]	PP647	PROBEPOINT_SM m38[5D6]	PP648	PROBEPOINT_SM m38[5D6]	PP649	PROBEPOINT_SM m38[5D6]	PP650	PROBEPOINT_SM m38[5D6]	PP651	PROBEPOINT_SM m38[5D6]	PP652	PROBEPOINT_SM m38[5C6]	PP653	PROBEPOINT_SM m38[5C6]	PP654	PROBEPOINT_SM m38[5C6]	PP655	PROBEPOINT_SM m38[5C6]	PP656	PROBEPOINT_SM m38[5C6]	PP657	PROBEPOINT_SM m38[5C6]	PP658	PROBEPOINT_SM m38[5C6]	PP659	PROBEPOINT_SM m38[5C6]	PP660	PROBEPOINT_SM m38[5C6]	PP661	PROBEPOINT_SM m38[5C6]	PP662	PROBEPOINT_SM m38[5C6]	PP663	PROBEPOINT_SM m38[5C6]	PP664	PROBEPOINT_SM m38[5C6]	PP665	PROBEPOINT_SM m38[5C6]	PP666	PROBEPOINT_SM m38[5C6]	PP667	PROBEPOINT_SM m38[5C6]	PP668	PROBEPOINT_SM m38[5C6]	PP673	PROBEPOINT_SM m38[5C6]	PP674	PROBEPOINT_SM m38[5C6]	PP675	PROBEPOINT_SM m38[5B6]	PP676	PROBEPOINT_SM m38[5B6]	PP677	PROBEPOINT_SM m38[5B6]	PP678	PROBEPOINT_SM m38[5B6]	PP679	PROBEPOINT_SM m38[5B6]	PP680	PROBEPOINT_SM m38[5B6]	PP681	PROBEPOINT_SM m38[5B6]	PP682	PROBEPOINT_SM m38[5B6]	PP683	PROBEPOINT_SM m38[5B6]	PP684	PROBEPOINT_SM m38[5B6]	PP685	PROBEPOINT_SM m38[5B6]	PP686	PROBEPOINT_SM m38[5B6]	PP687	PROBEPOINT_SM m38[5B6]	PP688	PROBEPOINT_SM m38[5B6]	PP689	PROBEPOINT_SM m38[5B6]	PP690	PROBEPOINT_SM m38[5B6]	PP691	PROBEPOINT_SM m38[5B6]	PP692	PROBEPOINT_SM m38[5B6]	PP693	PROBEPOINT_SM m38[5B6]	PP694	PROBEPOINT_SM m38[5B6]	PP695	PROBEPOINT_SM m38[5B6]	PP696	PROBEPOINT_SM m38[5B6]	PP697	PROBEPOINT_SM m38[5B6]	PP698	PROBEPOINT_SM m38[5B6]	PP699	PROBEPOINT_SM m38[5B6]	PP700	TP_SM-TP50-TOP m38[5D3]	PP701	TP_SM-TP50-TOP m38[5D3]	PP702	TP_SM-TP50-TOP m38[5D3]	PP1200	TP_SM-TP50-TOP m38[5D3]	PP1201	TP_SM-TP50-TOP m38[5D3]	PP1202	TP_SM-TP50-TOP m38[5D3]	PP2800	TP_SM-TP50-TOP m38[5C3]	PP2801	TP_SM-TP50-TOP m38[5C3]	PP2802	TP_SM-TP50-TOP m38[5C3]	PP4100	PROBEPOINT_SM m38[5D4]	PP4101	PROBEPOINT_SM m38[5D4]	PP8400	PROBEPOINT_SM m38[5C5]	PP8401	PROBEPOINT_SM m38[5C5]	PP8700	PROBEPOINT_SM m38[5D5]	PP8701	PROBEPOINT_SM m38[5D5]	PP8702	PROBEPOINT_SM m38[5D5]	PP8703	PROBEPOINT_SM m38[5D5]	PP8704	PROBEPOINT_SM m38[5D5]	PP8705	PROBEPOINT_SM m38[5D5]	PP8706	PROBEPOINT_SM m38[5D5]	PP8707	PROBEPOINT_SM m38[5D5]	PP8708	PROBEPOINT_SM m38[5D5]	PP8709	PROBEPOINT_SM m38[5D5]	PP8710	PROBEPOINT_SM m38[5D5]	PP8711	PROBEPOINT_SM m38[5D5]	PP8712	PROBEPOINT_SM m38[5D5]	PP8713	PROBEPOINT_SM m38[5D5]	PP8714	PROBEPOINT_SM m38[5D5]	PP8715	PROBEPOINT_SM m38[5D5]	PP8716	PROBEPOINT_SM m38[5D5]	PP8720	PROBEPOINT_SM m38[5D5]	PP8721	PROBEPOINT_SM m38[5D5]	PP8722	PROBEPOINT_SM m38[5D5]	PP8723	PROBEPOINT_SM m38[5C5]	PP8724	PROBEPOINT_SM m38[5C5]	PP8725	PROBEPOINT_SM m38[5C5]	PP8726	PROBEPOINT_SM m38[5C5]	PP8727	PROBEPOINT_SM m38[5C5]	PP8728	PROBEPOINT_SM m38[5C5]	PP8729	PROBEPOINT_SM m38[5C5]	PP8730	PROBEPOINT_SM m38[5C5]	PP8731	PROBEPOINT_SM m38[5C5]	PP8732	PROBEPOINT_SM m38[5C5]	PP8733	PROBEPOINT_SM m38[5C5]	PP8734	PROBEPOINT_SM m38[5C5]	PP8735	PROBEPOINT_SM m38[5C5]	PP8736	PROBEPOINT_SM m38[5C5]	PP8900	PROBEPOINT_SM m38[5B5]	PP8901	PROBEPOINT_SM m38[5B5]	PP8902	PROBEPOINT_SM m38[5B5]	PP8903	PROBEPOINT_SM m38[5B5]	PP8904	PROBEPOINT_SM m38[5B5]	PP8905	PROBEPOINT_SM m38[5B5]	PP8906	PROBEPOINT_SM m38[5B5]	PP8907	PROBEPOINT_SM m38[5B5]	PP8908	PROBEPOINT_SM m38[5B5]	PP8909	PROBEPOINT_SM m38[5B5]	PP8910	PROBEPOINT_SM m38[5B5]	PP8911	PROBEPOINT_SM m38[5B5]	PP8912	PROBEPOINT_SM m38[5B5]	PP8913	PROBEPOINT_SM m38[5B5]	PP8914	PROBEPOINT_SM m38[5A5]	PP8915	PROBEPOINT_SM m38[5A5]	PP8916	PROBEPOINT_SM m38[5A5]	PP8920	PROBEPOINT_SM m38[5A5]	PP8921	PROBEPOINT_SM m38[5A5]	PP8922	PROBEPOINT_SM m38[5A5]	PP8923	PROBEPOINT_SM m38[5A5]	PP8924	PROBEPOINT_SM m38[5A5]	PP8925	PROBEPOINT_SM m38[5A5]	PP8926	PROBEPOINT_SM m38[5A5]	PP8927	PROBEPOINT_SM m38[5A5]	PP8928	PROBEPOINT_SM m38[5A5]	PP8929	PROBEPOINT_SM m38[5A5]	PP8930	PROBEPOINT_SM m38[5A5]	PP8931	PROBEPOINT_SM m38[5A5]	PP8932	PROBEPOINT_SM m38[5A5]	PP8933	PROBEPOINT_SM m38[5A5]	PP8934	PROBEPOINT_SM m38[5A5]	PP8935	PROBEPOINT_SM m38[5A5]	PP8936	PROBEPOINT_SM m38[5A5]	PP9000	PROBEPOINT_SM m38[5B4]	PP9001	PROBEPOINT_SM m38[5B4]	PP9002	PROBEPOINT_SM m38[5B4]	PP9003	PROBEPOINT_SM m38[5B4]	PP9004	PROBEPOINT_SM m38[5B4]	PP9005	PROBEPOINT_SM m38[5B4]	PP9006	PROBEPOINT_SM m38[5B4]	PP9007	PROBEPOINT_SM m38[5B4]	PP9008	PROBEPOINT_SM m38[5B4]	PP9009	PROBEPOINT_SM m38[5B4]	PP9010	PROBEPOINT_SM m38[5B4]	PP9011	PROBEPOINT_SM m38[5B4]	PP9012	PROBEPOINT_SM m38[5B4]	PP9013	PROBEPOINT_SM m38[5B4]	PP9014	PROBEPOINT_SM m38[5A4]	PP9015	PROBEPOINT_SM m38[5A4]	PP9016	PROBEPOINT_SM m38[5A4]	PP9020	PROBEPOINT_SM m38[5A4]	PP9021	PROBEPOINT_SM m38[5A4]	PP9022	PROBEPOINT_SM m38[5A4]	PP9023	PROBEPOINT_SM m38[5A4]	PP9024	PROBEPOINT_SM m38[5A4]	PP9025	PROBEPOINT_SM m38[5A4]	PP9026	PROBEPOINT_SM m38[5A4]	PP9027	PROBEPOINT_SM m38[5A4]	PP9028	PROBEPOINT_SM m38[5A4]	PP9029	PROBEPOINT_SM m38[5A4]	PP9030	PROBEPOINT_SM m38[5A4]	PP9031	PROBEPOINT_SM m38[5A4]	PP9032	PROBEPOINT_SM m38[5A4]	PP9033	PROBEPOINT_SM m38[5A4]	PP9034	PROBEPOINT_SM m38[5A4]	PP9035	PROBEPOINT_SM m38[5A4]	PP9036	PROBEPOINT_SM m38[5A4]	Q4200	TRA_2N7002_SOT23-LF m38[42D2]	Q4201	TRA_F8B55402_SOT23 m38[42C6]	Q4400	TRA_SINGLE_MOSFET_NC m38[44A7] HN_SOT23	Q5900	TRA_FDV301N_SOT23-LF m38[59D7]	Q5901	TRA_2N7002DW_SOT-363 m38[59C7 59C7]	Q5902	TRA_2N7002DW_SOT-363 m38[59B1 59B2]	Q5903	TRA_DUAL_MMDT3904_SO m38[59A2 59A1] T-363-LF	Q5910	TRA_SINGLE_MOSFET_PC m38[59A4] HN_SOT-23	Q5911	TRA_2N7002_SOT23-LF m38[59A4]	Q6500	TRA_NTHS5443T1_1206A m38[65D4] -03-LF	Q6502	TRA_2N7002DW_SOT-363 m38[65D8 65D6]	Q6503	TRA_NTHS5443T1_1206A m38[65B4] -03-LF	Q6505	TRA_2N7002DW_SOT-363 m38[65C8 65B6]	Q6600	TRA_NTHS5443T1_1206A m38[66D4] -03-LF	Q6602	TRA_2N7002DW_SOT-363 m38[66D8 66C5]	Q7200	TRA_2N7002DW_SOT-363 m38[72B8 72B7]	Q7400	TRA_2N7002DW_SOT-363 m38[74B3 74B3]	Q7401	TRA_2N7002_SOT23-LF m38[74C3]	Q7402	TRA_2N7002DW_SOT-363 m38[74B2 74B2]	Q7500	TRA_HAT2165H_LFPAK m38[75D3]	Q7501	TRA_HAT2165H_LFPAK m38[75D3]	Q7502	TRA_HAT2165H_LFPAK m38[75C3]	Q7503	TRA_HAT2165H_LFPAK m38[75B3]	Q7504	TRA_HAT2165H_LFPAK m38[75D3]	Q7505	TRA_HAT2165H_LFPAK m38[75B3]	Q7570	TRA_HAT2165H_LFPAK m38[75D2]	Q

8				7				6				5				4				3				2				1			
D	Q8103	TRA_NTD60N02R_CASE36	m38[81C4] 9-LF					R2299	RES_402	m38[22B5]						R3446	RES_402	m38[34B1]						R5808	RES_402	m38[59C3]					
	Q8104	TRA_2N7002_SOT23-LF	m38[81C7]					R2302	RES_402	m38[23D3]						R3451	RES_402	m38[34C4]						R5809	RES_402	m38[58C2]					
	Q8300	TRA_IRF7413_SO-8	m38[83C4]					R2303	RES_402	m38[23D3]						R3452	RES_402	m38[34B7]						R5815	RES_402	m38[59B3]					
	Q8301	TRA_IRF7413_SO-8	m38[83B4]					R2306	RES_402	m38[23B7]						R3453	RES_402	m38[34B8]						R5817	RES_402	m38[59B3]					
	Q8302	TRA_2N7002DW_SOT-363	m38[83B5]					R2307	RES_402	m38[23A7]						R3454	RES_402	m38[34B7]						R5818	RES_402	m38[59B3]					
	Q8303	TRA_2N7002DW_SOT-363	m38[83C5]					R2308	RES_402	m38[23B7]						R3455	RES_402	m38[34B8]						R5819	RES_402	m38[59B3]					
	Q8520	TRA_HAT2168H_LFPAK	m38[85D4]					R2309	RES_402	m38[23A7]						R3456	RES_402	m38[34B7]						R5821	RES_402	m38[59B3]					
	Q8521	TRA_HAT2165H_LFPAK	m38[85C4]					R2310	RES_402	m38[23A7]						R3457	RES_402	m38[34B7]						R5822	RES_402	m38[59B3]					
	Q8522	TRA_HAT2165H_LFPAK	m38[85C5]					R2311	RES_402	m38[23A7]						R3458	RES_402	m38[34B8]						R5823	RES_402	m38[59B3]					
	Q8523	TRA_2N7002DW_SOT-363	m38[85B3 85B2]					R2313	RES_402	m38[23A7]						R3459	RES_402	m38[34A7]						R5824	RES_402	m38[59B3]					
C	Q8554	TRA_2N7002_SOT23-LF	m38[85A8]					R2314	RES_402	m38[23A7]						R3460	RES_402	m38[34A7]						R5825	RES_402	m38[59B3]					
	Q8570	TRA_2N7002_SOT23-LF	m38[85A5]					R2316	RES_402	m38[23D7]						R3461	RES_402	m38[34A7]						R5826	RES_402	m38[59B3]					
	Q8575	TRA_FDC796N_SUPERSOT	m38[85B6]					R2317	RES_402	m38[23D7]						R3462	RES_402	m38[34A8]						R5827	RES_402	m38[59C5]					
		-6						R2318	RES_402	m38[23D7]						R3463	RES_402	m38[34A7]						R5828	RES_402	m38[59B3]					
	Q8576	TRA_FDC796N_SUPERSOT	m38[85A2] -6					R2319	RES_402	m38[23D2]						R3470	RES_402	m38[34A5]						R5829	RES_402	m38[59C3]					
	Q9400	TRA_S13443DV_TSOP-LF	m38[94C7]					R2320	RES_402	m38[23D7]						R3471	RES_402	m38[34A5]						R5830	RES_402	m38[59C3]					
	Q9401	TRA_2N7002_SOT23-LF	m38[94C7]					R2323	RES_402	m38[23D5]						R3485	RES_402	m38[34D1]						R5831	RES_402	m38[59C5]					
	Q9711	TRA_2N7002DW_SOT-363	m38[97D2 97C2]					R2326	RES_402	m38[23D6]						R3486	RES_402	m38[34D1]						R5832	RES_402	m38[59C3]					
	R75A0	RES_402	m38[75C7]					R2327	RES_402	m38[23D6]						R3487	RES_402	m38[34D1]						R5833	RES_402	m38[59B3]					
	R85A0	RES_402	m38[85D1]					R2343	RES_402	m38[23D1]						R3488	RES_402	m38[34D1]						R5898	RES_402	m38[58C2]					
B	R600	RES_603	m38[6A7]					R2344	RES_402	m38[23A3]						R3489	RES_402	m38[34D2]						R5899	RES_402	m38[58D3]					
	R601	RES_402	m38[6D8]					R2388	RES_402	m38[23A3]						R3490	RES_402	m38[34D2]						R5900	RES_402	m38[59D7]					
	R602	RES_603	m38[6A8]					R2389	RES_402	m38[38D5]						R3491	RES_402	m38[34D2]						R5901	RES_402	m38[59D6]					
	R603	RES_402	m38[6B1]					R2390	RES_402	m38[23B3]						R3492	RES_402	m38[34D2]						R5902	RES_402	m38[59D7]					
	R605	RES_603	m38[6A7]					R2395	RES_402	m38[23D7]						R3493	RES_402	m38[34D7]						R5903	RES_402	m38[59D2]					
	R611	RES_402	m38[6B7]					R2396	RES_402	m38[23D6]						R3494	RES_402	m38[34D7]						R5904	RES_402	m38[59D2]					
	R612	RES_402	m38[6B7]					R2397	RES_402	m38[23D6]						R3495	RES_402	m38[34D7]						R5905	RES_402	m38[59D2]					
	R614	RES_402	m38[6B7]					R2398	RES_402	m38[23D8]						R3496	RES_402	m38[34C5]						R5906	RES_402	m38[59B7]					
	R615	RES_402	m38[6B7]					R2399	RES_402	m38[23C1]						R3497	RES_402	m38[34D4]						R5907	RES_402	m38[59B7]					
	R616	RES_402	m38[6A7]					R2500	RES_603	m38[25A8]						R3498	RES_402	m38[34D5]						R5910	RES_402	m38[59D2]					
	R617	RES_402	m38[6A7]					R2501	RES_402	m38[25C8]						R3499	RES_402	m38[34D5]						R5911	RES_402	m38[59D2]					
A	R618	RES_402	m38[6C7]					R2502	RES_402	m38[25D8]						R3824	RES_402	m38[38D2]						R5912	RES_402	m38[59D2]					
	R619	RES_402	m38[6B7]					R2600	RES_402	m38[26C7]						R3851	RES_402	m38[38D3]						R5913	RES_402	m38[59D2]					
	R0702	RES_402	m38[7D6]					R2606	RES_402	m38[26C7]						R3852	RES_402	m38[38D2]						R5914	RES_402	m38[59C2]					
	R0703	RES_402	m38[7C5]					R2607	RES_402	m38[26C8]						R3853	RES_402	m38[38D2]						R5915	RES_402	m38[59C2]					
	R0704	RES_402	m38[7B4]					R2609	RES_402	m38[26D7]						R3857	RES_402	m38[38B3]						R5916	RES_402	m38[59C2]					
	R0705	RES_402	m38[7B5]					R2611	RES_402	m38[26D5]						R3858	RES_402	m38[38B3]						R5917	RES_402	m38[59C2]					
	R0706	RES_402	m38[7A4]					R2612	RES_402	m38[26D5]						R3859	RES_402	m38[38B2]						R5919	RES_402	m38[59B4]					
	R0712	RES_402	m38[7A3]					R2622	RES_402	m38[26D4]						R3897	RES_402	m38[38B7]						R5920	RES_402	m38[59B5]					
	R0716	RES_402	m38[7B1]					R2623	RES_402	m38[26D2]						R3899	RES_402	m38[38B5]						R5921	RES_402	m38[59B5]					
	R0717	RES_402	m38[7B1]					R2624	RES_402	m38[26D2]						R4101	RES_402	m38[41D7]						R5922	RES_402	m38[59B5]					
	R0718	RES_402	m38[7B1]					R2625	RES_402	m38[26D2]						R4102	RES_402	m38[41C7]						R5923	RES_402	m38[59B5]					
A	R0721	RES_402	m38[7B7]					R2626	RES_402	m38[26D2]						R4103	RES_402	m38[41C2]						R5924	RES_402	m38[59B5]					
	R0722	RES_402	m38[7A7]					R2627	RES_402	m38[26D2]						R4104	RES_402	m38[41C2]						R5925	RES_402	m38[59A1]					
	R0730	RES_402	m38[7A4]					R2628	RES_402	m38[26D2]						R4105	RES_402	m38[41C2]						R5927	RES_402	m38[59A3]					
	R0802	RES_402	m38[8B7]					R2629	RES_402	m38[26D2]						R4106	RES_402	m38[41C2]						R5930	RES_402	m38[59B6]					
	R0803	RES_402	m38[8A7]					R2630	RES_402	m38[26D2]						R4117	RES_402	m38[41B2]						R5931	RES_402	m38[59B6]					
	R1000	RES_402	m38[10D3]					R2631	RES_402	m38[26D2]						R4118	RES_402	m38[41B2]						R5932	RES_402	m38[59A7]					
	R1001	RES_402	m38[10D3]					R2632	RES_402	m38[26D2]						R4119	RES_402	m38[41B2]						R5933	RES_402	m38[59A7]					
	R1002	RES_402	m38[10C6]					R2633	RES_402	m38[26D2]						R4120	RES_402	m38[41B2]						R5934	RES_402	m38[59A6]					
	R1005	RES_402	m38[10D3]					R2634	RES_402	m38[26D2]						R4122	RES_402	m38[41A3]						R5935	RES_402	m38[59A6]					
	R1017	RES_402	m38[10C6]					R2636	RES_402	m38[26D2]						R4123	RES_402	m38[41A2]						R5940	RES_402	m38[59A3]					
	R1018	RES_402	m38[10B6]					R2637	RES_402	m38[26D2]						R4130	RES_402	m38[41C4]						R5941	RES_402	m38[59A5]					
A	R1019	RES_402	m38[10B6]					R2638	RES_402	m38[26D2]						R4131	RES_402	m38[41C4]						R5942	RES_402	m38[59A4]					
	R1100	RES_402	m38[11B5]					R2639	RES_402	m38[26D2]						R4150	RES_402	m38[41C8]						R5995	RES_402	m38[59A5]					
	R1101	RES_402	m38[11C5]					R2640	RES_402	m38[26C2]						R4151	RES_402	m38[41D7]						R6100	RES_402	m38[61C4]					</

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D	XW9314	SHORT_SM	m38[93C7]																	D			
	XW9320	SHORT_SM	m38[93B7]																				
	XW9324	SHORT_SM	m38[93B7]																				
	XW9330	SHORT_SM	m38[93B7]																				
	XW9345	SHORT_SM	m38[93A7]																				
	Y2600	CRYSTAL_4PIN_SM-LF	m38[26D8]																				
	Y3301	CRYSTAL_5X3.2-SM	m38[33C7]																				
	Y4101	CRYSTAL_4PIN_SM-3.2X 2.5MM	m38[41B5]																				
	Y4400	CRYSTAL_HC49-USMD	m38[44D2]																				
	Y5800	CRYSTAL_SM-3	m38[59B8]																				
C	Y6700	CRYSTAL_4PIN_SM-LF	m38[59B7]																	C			
	ZH500	HOLE_VIA	m38[5C1]																				
	ZH501	HOLE_VIA	m38[5C1]																				
	ZH502	HOLE_VIA	m38[5C1]																				
	ZH503	HOLE_VIA	m38[5C1]																				
	ZH504	HOLE_VIA	m38[5B1]																				
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	ZH506	HOLE_VIA	m38[5B1]																				
	ZH507	HOLE_VIA	m38[5B1]																				
	ZH508	HOLE_VIA	m38[5B1]																				
B	ZH509	HOLE_VIA	m38[5B1]																	B			
	ZH510	HOLE_VIA	m38[5C1]																				
	ZH511	HOLE_VIA	m38[5C1]																				
	ZH512	HOLE_VIA	m38[5C1]																				
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A	ZH519	HOLE_VIA	m38[5B1]																	A			
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	ZH529	HOLE_VIA	m38[5B1]																				
	ZH601	MTGHOLE	m38[6A3]																				
	ZH602	MTGHOLE	m38[6A3]																				
	ZH603	MTGHOLE	m38[6A3]																				
	ZH604	MTGHOLE	m38[6B3]																				
	ZH606	MTGHOLE	m38[6A1]																				
	ZH607	MTGHOLE	m38[9D4]																				
	ZH608	MTGHOLE	m38[9D3]																				
	ZH609	MTGHOLE	m38[9D2]																				
	ZH610	MTGHOLE	m38[9D2]																				
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