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1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

SCHEMATIC, "Kriolo", MLB

REV

ZONE

ECN

DESCRIPTION OF CHANGE

CK APPD

ENG APPD

DATE

DATE

R

562949

PRODUCTION RELEASED

02/07/08

06/22/04

02/07/08

PAGE

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M1

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JH

JH

77

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JH

97

JH

JH

78

GPU - EXTERNAL DISPLAY CONN'S

DIMENSIONS ARE IN MILLIMETERS


XX : \_\_\_\_\_

X.XX : \_\_\_\_\_

X.XXX : \_\_\_\_\_

ANGLES : \_\_\_\_\_

DO NOT SCALE DRAWING



THIRD ANGLE PROJECTION

METRIC

DRAFTER

ENG APPD

QA APPD

RELEASE

DESIGN CK

MFG APPD

DESIGNER

SCALE

SIZE


NONE

D

MATERIAL/FINISH

NOTED AS

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TITLE

SCHEM, "Kriolo", MLB

DRAWING NUMBER

051-6950

REV.

R

SHT

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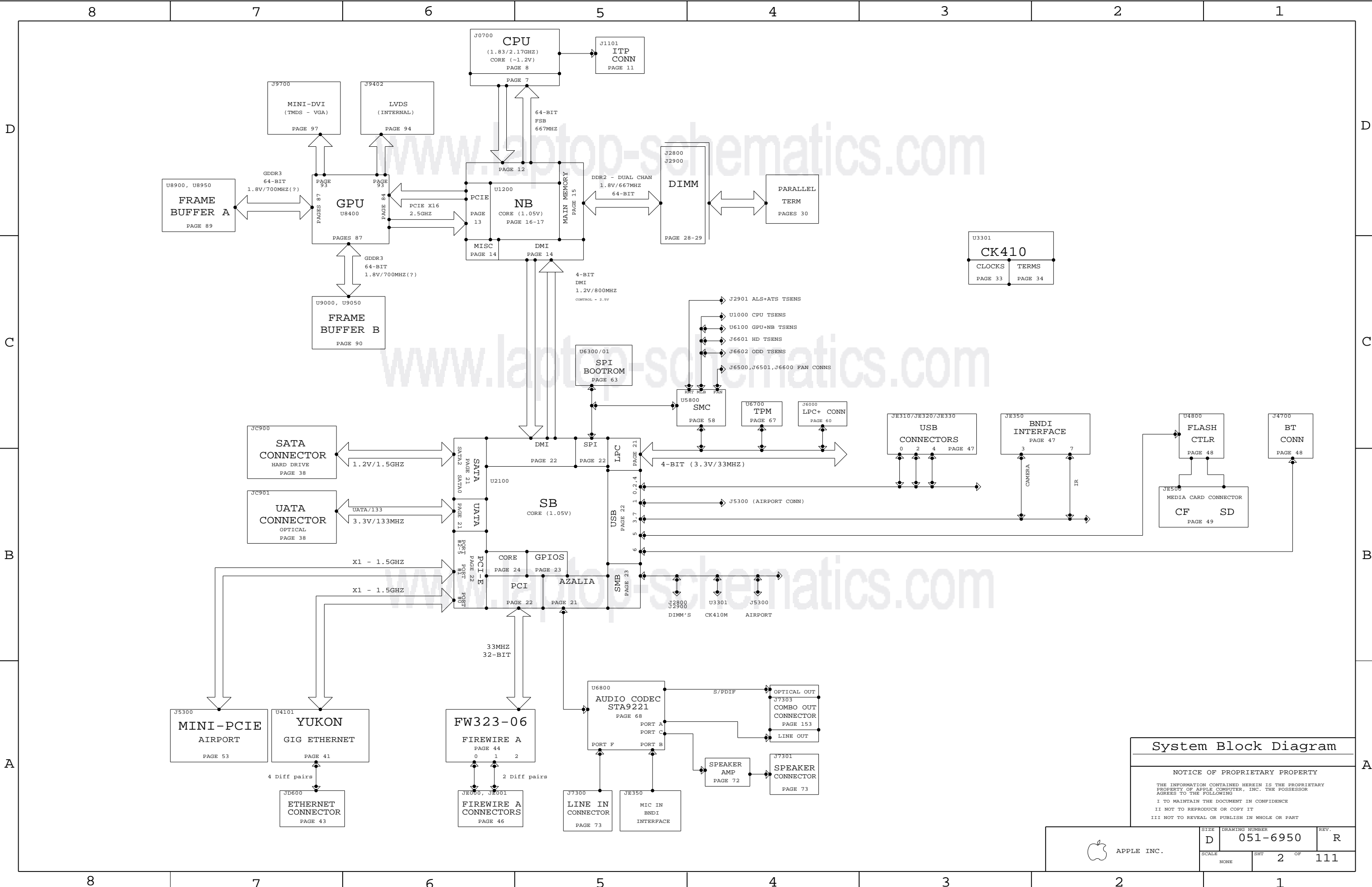
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System Block Diagram

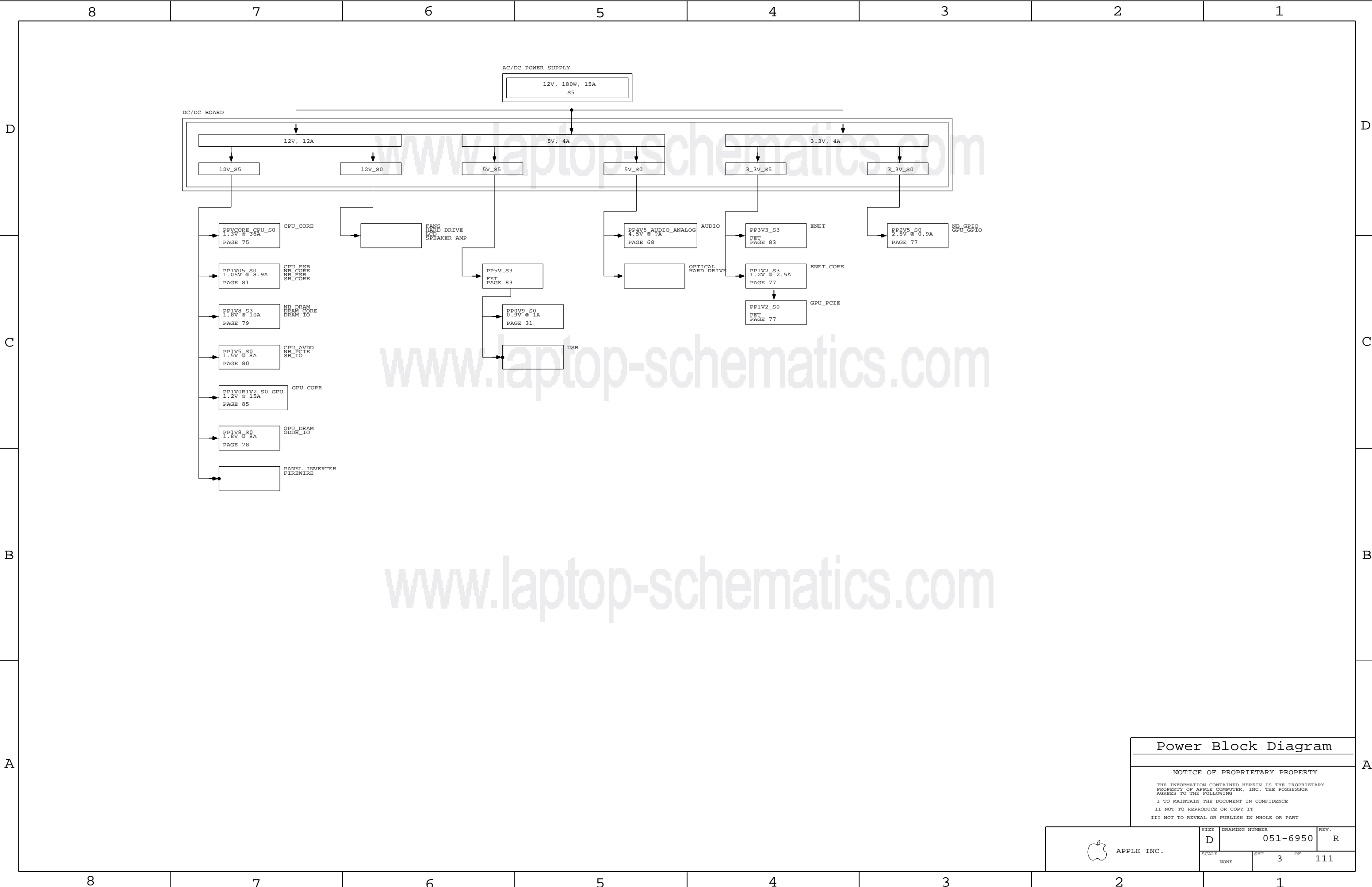
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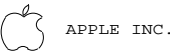
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Power Block Diagram

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COMMON

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL	
338S0269	1	IC,945GM,NORTHBRIDGE	U1200	CRITICAL	
343S0385	1	IC,SB,652BGA	U2100	CRITICAL	
742-0048	1	BAT,COIN,3V,220MAH,CR2032	BT2600	CRITICAL	
359S0101	1	IC,CY28445-5,CLK GEN,68PIN QFP	U3301	CRITICAL	
338S0270	1	IC,88E053,GIGABIT ENET XCVR,64P QFN BMD	U4101	CRITICAL	
341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL	
338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL	

(335S0382)

353S1465	1	IC,CPU VREG,IMVP,TWO PHASE	U7500	CRITICAL	
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UNSCREENED 353S1235

338S0305	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_A24
128S0078	3	CAP,EL,AL,330UF,20%,16V,10X12.7MM,BMK,LF	C7517,C7518,C7910	CRITICAL	
825-6447	1	MLB LABEL,48.0X4.8	X14	CRITICAL	

338S0316	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B26_P
338S0317	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B26_D
338S0315	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B26_LP

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0096	126S0076		C7801	SANYO W16CE680EX 680UF 16V LP
126S0086	126S0078		C699,C940,C1900,C1901,C1968	SANYO W6CE330FS 330UF 6.3V LP
128S0080	128S0078		C7517,C7518,C7910	SANYO 160VP330M 330UF 16V SMD LP
124-0338	124-0333		C7501,C8014	CAP,AL,EL,680UF,16V,RAD,10X12.5MM

138S0580	138S0552			22UF 0805
----------	----------	--	--	-----------

353S1461	353S1465		U7500	INTERSIL CPU SWITCHER
197S0020	197S0177		Y4101	XTAL,25MHZ
353S1321	353S1105		U7910	LM339
353S1275	353S1370		U4700	TPS2024DG4

M38

338S0266	338S0305		U8400	M56 B24 P
338S0312	338S0305		U8400	M56 B24 DP
338S0309	338S0305		U8400	M56 B24 LP
338S0315	338S0316		U8400	M56 B26 LP (ALT FOR P)

M39

338S0266	338S0302		U8400	M56 B24 P
338S0312	338S0302		U8400	M56 B24 DP
338S0316	338S0317		U8400	M56 B26 P (ALT FOR D)

(341S1810 - PROQ)  
(335S0384 - BLNK)

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6950	1	PCB,SCHEM,MLB,M39	SCH1		20_INCH_LCD
820-1888	1	PCB,FAB,MLB,M39	MLB1		20_INCH_LCD
341T0004	1	EFI ROM,M39	U6301	CRITICAL	20_INCH_LCD
114S0276	1	4.02K,1%,1/16W,402,MF-LF	R8522		GPU_VCORE_1P049V
337S3243	1	M39 HI-SPEED CPU (QHJJ)	CPU	CRITICAL	CPU_M39
341T0018	1	IC,SMC,M39	U5800	CRITICAL	20_INCH_LCD
338S0302	1	IC,ATI,M56D,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B24_D
338S0309	1	IC,ATI,M56LP,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B24_LP

(341S1847 - PROQ)  
(338S0274 - BLNK)

341S1847	1	IC,ATI,M56D,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B24_D
338S0274	1	IC,ATI,M56LP,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	GPU_B24_LP

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0354	4	IC,SGRAM,ODDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_SAMSUNG
333S0358	4	IC,SGRAM,ODDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_HYNIX
333S0353	4	IC,SGRAM,ODDR3,8MX32,500MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_500MHZ_SAMSUNG

CTO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0350	4	IC,SGRAM,ODDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_SAMSUNG
333S0351	4	IC,SGRAM,ODDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_HYNIX

LP

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
114S0271	1	3.57K,1%,1/16W,402,MF-LF	R8522		GPU_VCORE_1P106V

Table Items

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SIZE  
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SCALE  
NONE

DRAWING NUMBER  
051-6950

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8	7	6	5	4	3	2	1							
<div>LAYOUT NOTE: PLACE NEAR J0700</div> <div><div><div>12 7 5</div><div>FSB A L&lt;6&gt;</div><div>PP600</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L&lt;0&gt;</div><div>PP601</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB A L&lt;27&gt;</div><div>PP602</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L&lt;1&gt;</div><div>PP603</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L&lt;0&gt;</div><div>PP604</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L&lt;0&gt;</div><div>PP605</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L&lt;0&gt;</div><div>PP606</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L&lt;0&gt;</div><div>PP607</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L&lt;16&gt;</div><div>PP608</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L&lt;1&gt;</div><div>PP609</div><div>OMIT P4MM</div></div><div><div>12 7 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P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L&lt;0&gt;</div><div>PP632</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB A L&lt;27&gt;</div><div>PP633</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB ADSTB L&lt;1&gt;</div><div>PP634</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L&lt;0&gt;</div><div>PP635</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L&lt;0&gt;</div><div>PP636</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L&lt;0&gt;</div><div>PP637</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L&lt;0&gt;</div><div>PP638</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L&lt;16&gt;</div><div>PP639</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L&lt;1&gt;</div><div>PP640</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L&lt;1&gt;</div><div>PP641</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L&lt;1&gt;</div><div>PP642</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L&lt;41&gt;</div><div>PP643</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L&lt;2&gt;</div><div>PP644</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L&lt;2&gt;</div><div>PP645</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L&lt;2&gt;</div><div>PP646</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB D L&lt;59&gt;</div><div>PP647</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBN L&lt;3&gt;</div><div>PP648</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DSTBP L&lt;3&gt;</div><div>PP649</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB DINV L&lt;3&gt;</div><div>PP650</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB LOCK L</div><div>PP651</div><div>OMIT P4MM</div></div><div><div>12 7 5</div><div>FSB HIT L</div><div>PP652</div><div>OMIT P4MM</div></div><div><div>12 7 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C						C								
B						B								
A						A								

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OMIT P4MM

89 87 5

FB\_B\_CKE<1>

PP9020

OMIT P4MM

89 87 5

FB\_B\_CLK\_P<1>

PP9021

OMIT P4MM

89 87 5

FB\_B\_CLK\_N<1>

PP9022

OMIT P4MM

89 87 5

FB\_B\_CS\_L<1>

PP9023

OMIT P4MM

89 87 5

FB\_B\_WE\_L<1>

PP9024

OMIT P4MM

89 87 5

FB\_B\_CAS\_L<1>

PP9025

OMIT P4MM

89 87 5

FB\_B\_RAS\_L<1>

PP9026

OMIT P4MM

89 87 5

FB\_B\_MA<3>

PP9027

OMIT P4MM

90 87 5

DRAM\_RST

PP9028

OMIT P4MM

90 87 5

FB\_B\_WDQS<5>

PP9029

OMIT P4MM

90 87 5

FB\_B\_WDQS<6>

PP9030

OMIT P4MM

90 87 5

FB\_B\_WDQS<4>

PP9031

OMIT P4MM

90 87 5

FB\_B\_WDQS<7>

PP9032

OMIT P4MM

90 87 5

FB\_B\_DQ<32>

PP9033

OMIT P4MM

90 87 5

FB\_B\_DQ<40>

PP9034

OMIT P4MM

90 87 5

FB\_B\_DQ<48>

PP9035

OMIT P4MM

90 87 5

FB\_B\_DQ<56>

PP9036

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U8400

89 87 5

FB\_A\_DQ<0>

PP8700

OMIT P4MM

89 87 5

FB\_A\_DQ<8>

PP8701

OMIT P4MM

89 87 5

FB\_A\_DQ<16>

PP8702

OMIT P4MM

89 87 5

FB\_A\_DQ<24>

PP8703

OMIT P4MM

89 87 5

FB\_A\_DQ<32>

PP8704

OMIT P4MM

89 87 5

FB\_A\_DQ<40>

PP8705

OMIT P4MM

89 87 5

FB\_A\_DQ<48>

PP8706

OMIT P4MM

89 87 5

FB\_A\_DQ<56>

PP8707

OMIT P4MM

89 87 5

FB\_A\_MA<3>

PP8708

OMIT P4MM

89 87 5

FB\_A\_RDQS<0>

PP8709

OMIT P4MM

89 87 5

FB\_A\_RDQS<1>

PP8710

OMIT P4MM

89 87 5

FB\_A\_RDQS<2>

PP8711

OMIT P4MM

89 87 5

FB\_A\_RDQS<3>

PP8712

OMIT P4MM

89 87 5

FB\_A\_RDQS<4>

PP8713

OMIT P4MM

89 87 5

FB\_A\_RDQS<5>

PP8714

OMIT P4MM

89 87 5

FB\_A\_RDQS<6>

PP8715

OMIT P4MM

89 87 5

FB\_A\_RDQS<7>

PP8716

OMIT P4MM

84 34

GPU\_CLK100M\_PCIE\_P

PP8400

OMIT P4MM

84 34

GPU\_CLK100M\_PCIE\_N

PP8401

OMIT P4MM

89 87 5

FB\_B\_CKE<0>

PP8900

OMIT P4MM

89 87 5

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PP8901

OMIT P4MM

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PP8902

OMIT P4MM

89 87 5

FB\_B\_CS\_L<0>

PP8903

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FB\_A\_WE\_L<0>

PP8904

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OMIT P4MM

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PP8907

OMIT P4MM

90 87 5

DRAM\_RST

PP8908

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PP8915

OMIT P4MM

90 87 5

FB\_A\_DQ<24>

PP8916

OMIT P4MM

89 87 5

FB\_B\_CKE<1>

PP8920

OMIT P4MM

89 87 5

FB\_A\_CLK\_P<1>

PP8921

OMIT P4MM

89 87 5

FB\_B\_CLK\_N<1>

PP8922

OMIT P4MM

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PP8923

OMIT P4MM

89 87 5

FB\_B\_WE\_L<1>

PP8924

OMIT P4MM

89 87 5

FB\_A\_CAS\_L<1>

PP8925

OMIT P4MM

89 87 5

FB\_B\_RAS\_L<1>

PP8926

OMIT P4MM

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FB\_B\_MA<3>

PP8927

OMIT P4MM

90 87 5

DRAM\_RST

PP8928

OMIT P4MM

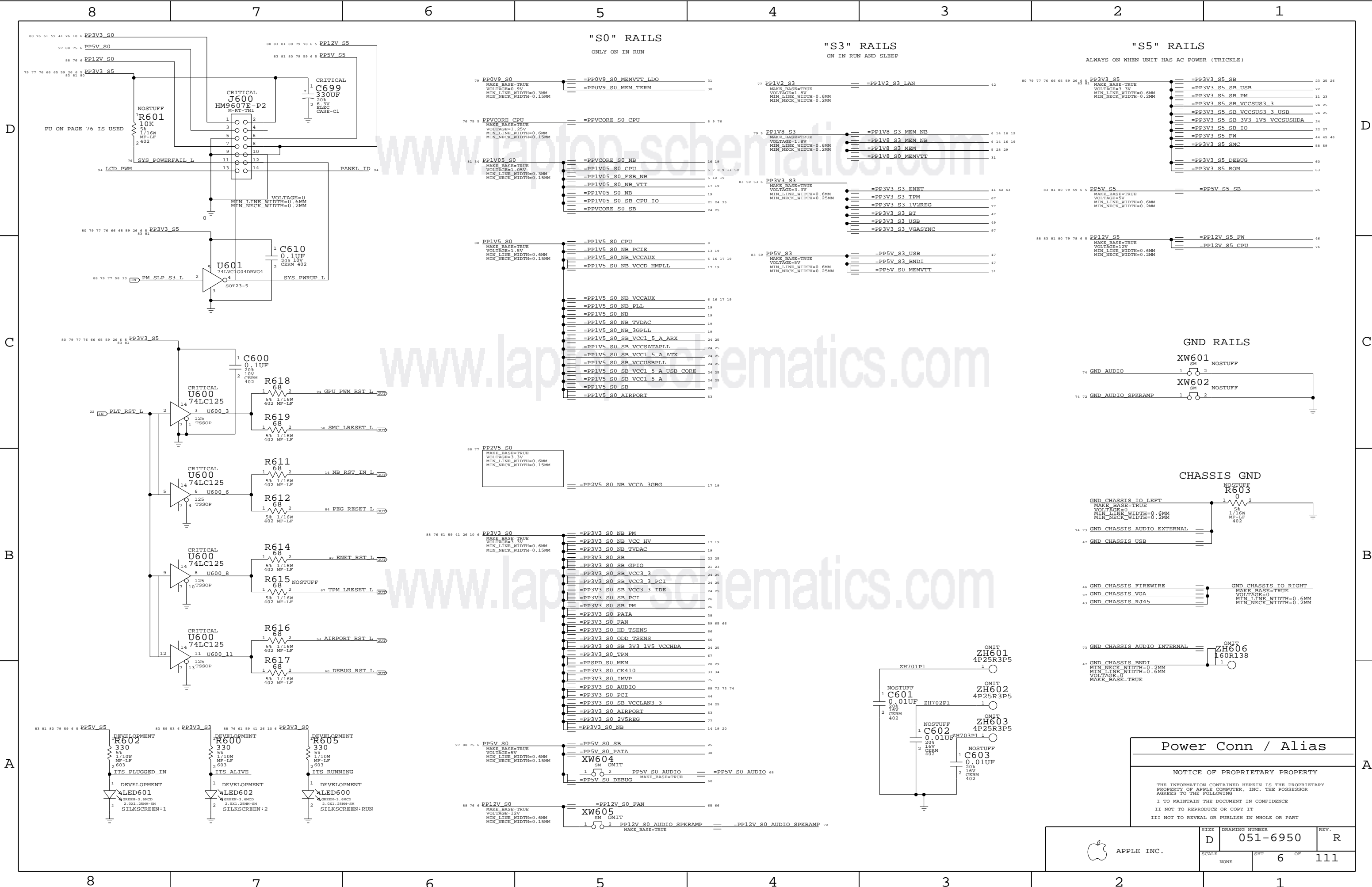
90 87 5

FB\_B\_WDQS<5>

PP8929

OMIT P4MM

9



Power Conn / Alias

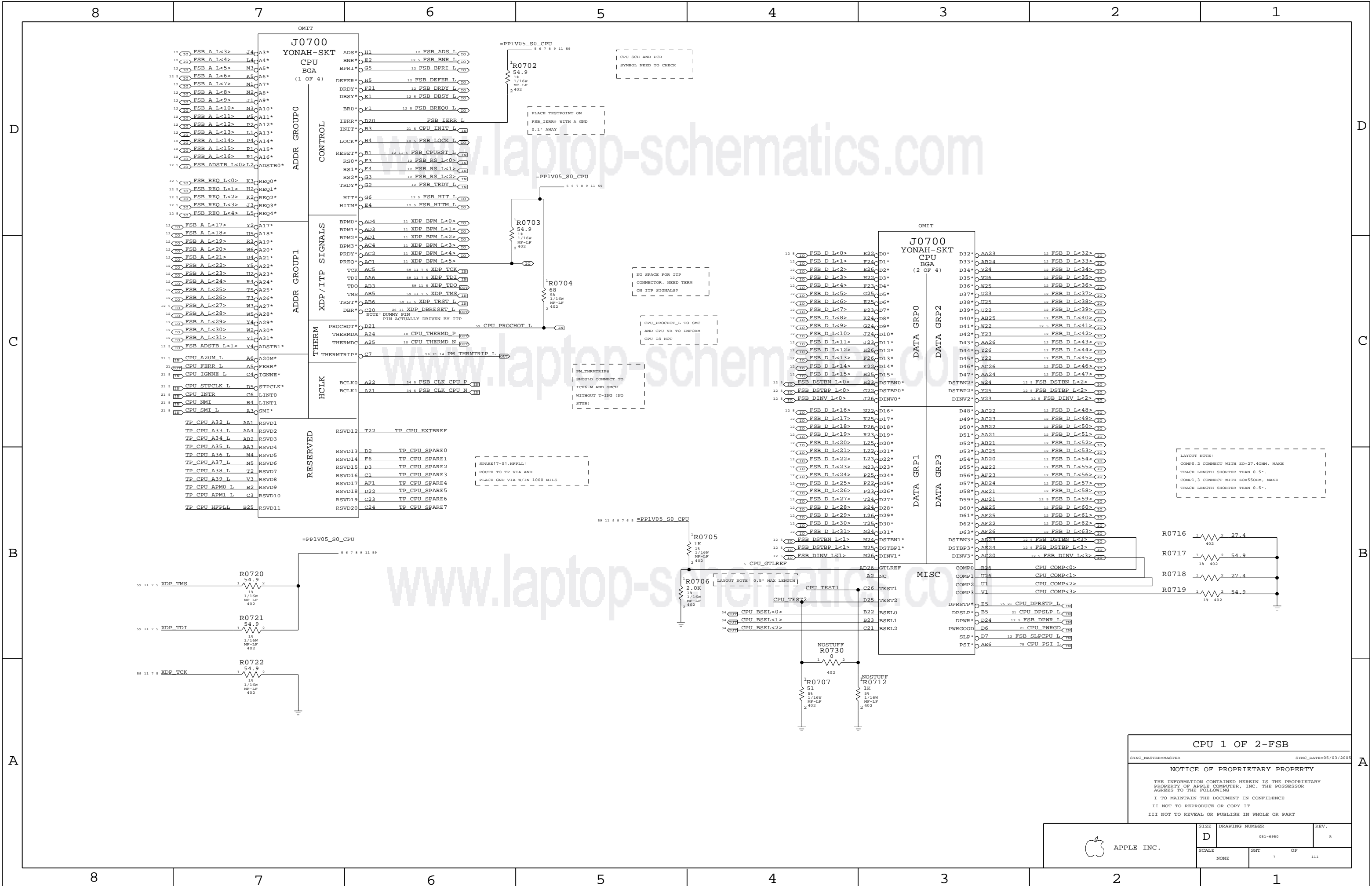
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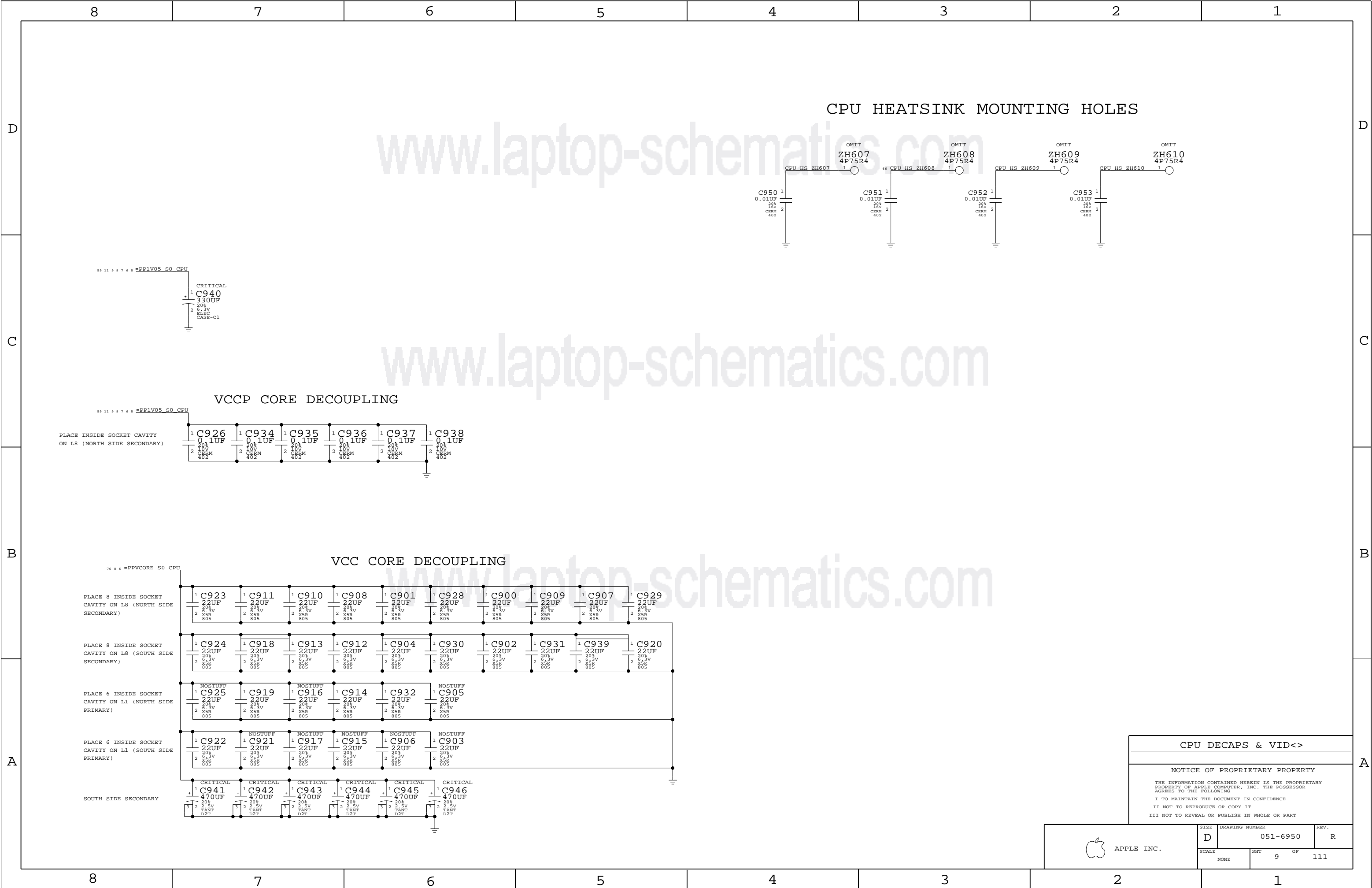


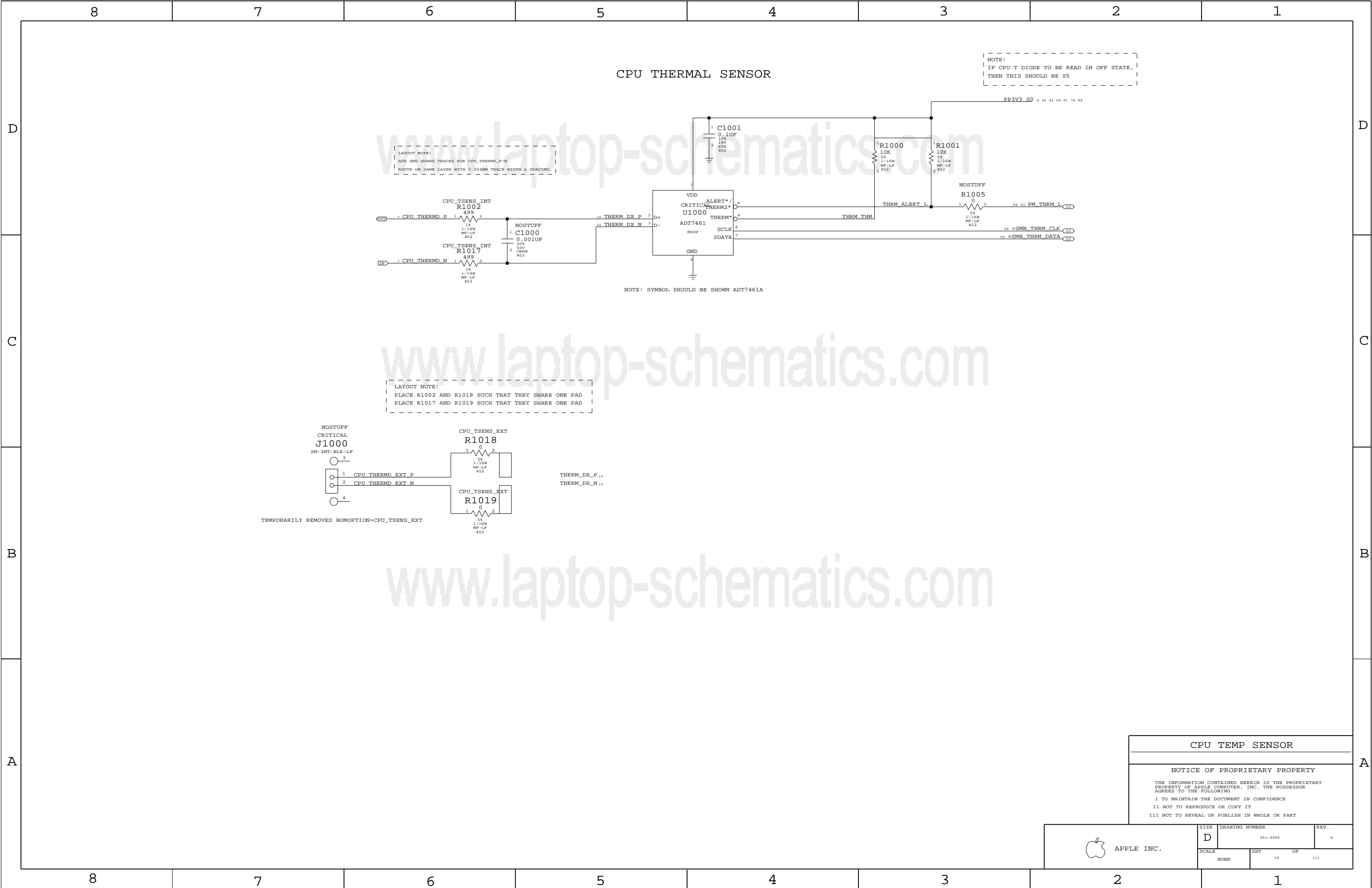
APPLE INC.

SIZE	D	DRAWING NUMBER	051-6950	REV.	R
SCALE	NONE	SHT	6	OF	111









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## CPU ITP700FLEX DEBUG SUPPORT

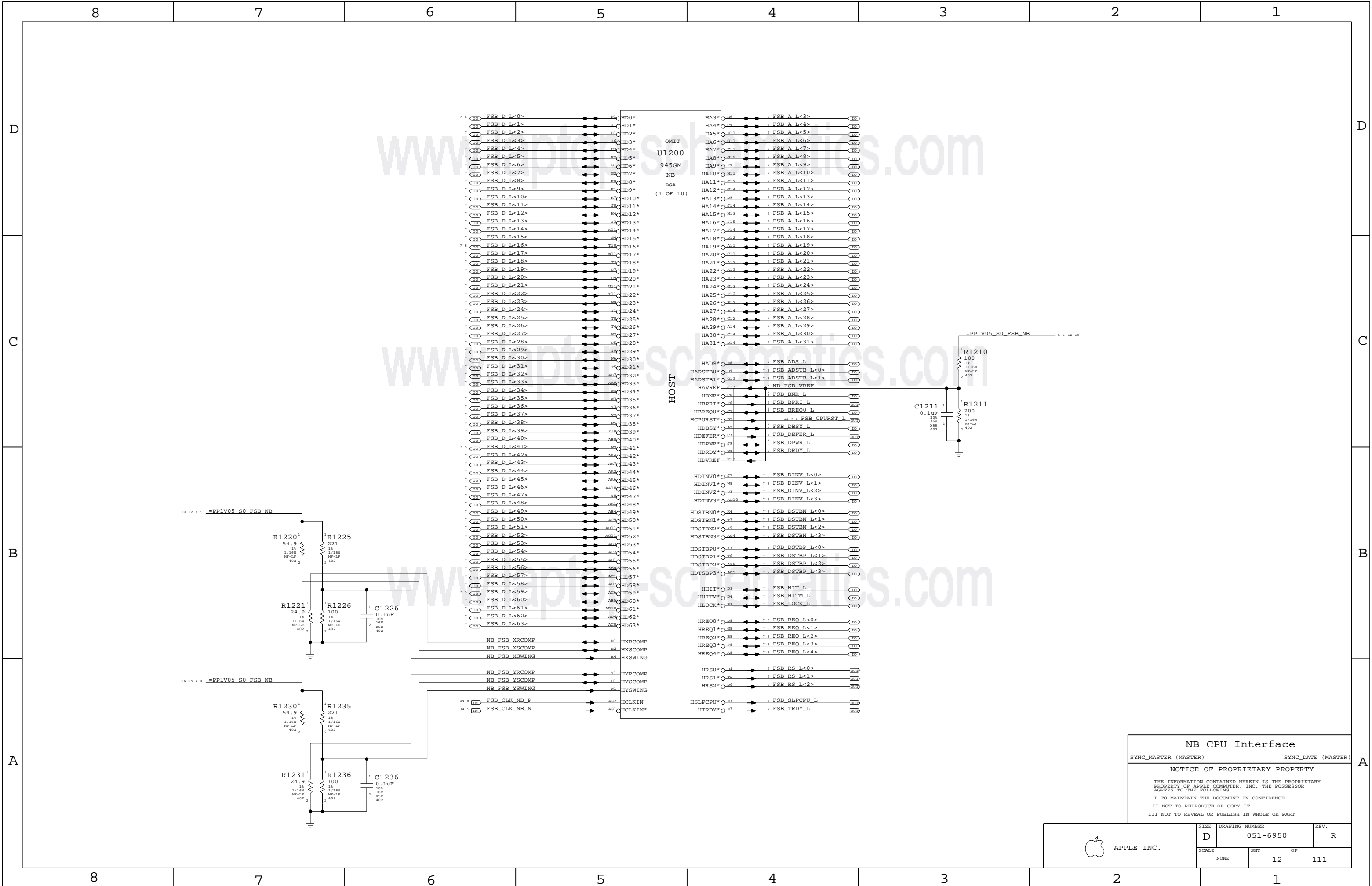
(AND WITH RESET BUTTON)

ITP TCK SIGNAL LAYOUT NOTE:  
ROUTE THE TCK SIGNAL FROM ITP700FLEX CONNECTOR'S TCK PIN TO CPU'S  
TCK PIN AND THEN FORK BACK FROM CPU TCK PIN AND ROUTE BACK TO ITP700FLEX  
CONNECTOR'S FBO PIN.

CPU ITP700FLEX DEBUG		
SYNC_MASTER=MASTER		SYNC_DATE=5/23/05
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SCALE NONE	DRAWING NUMBER 051-6950	
	REV. R	
SHT 11		OF 111



APPLE INC.



NB CPU Interface

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

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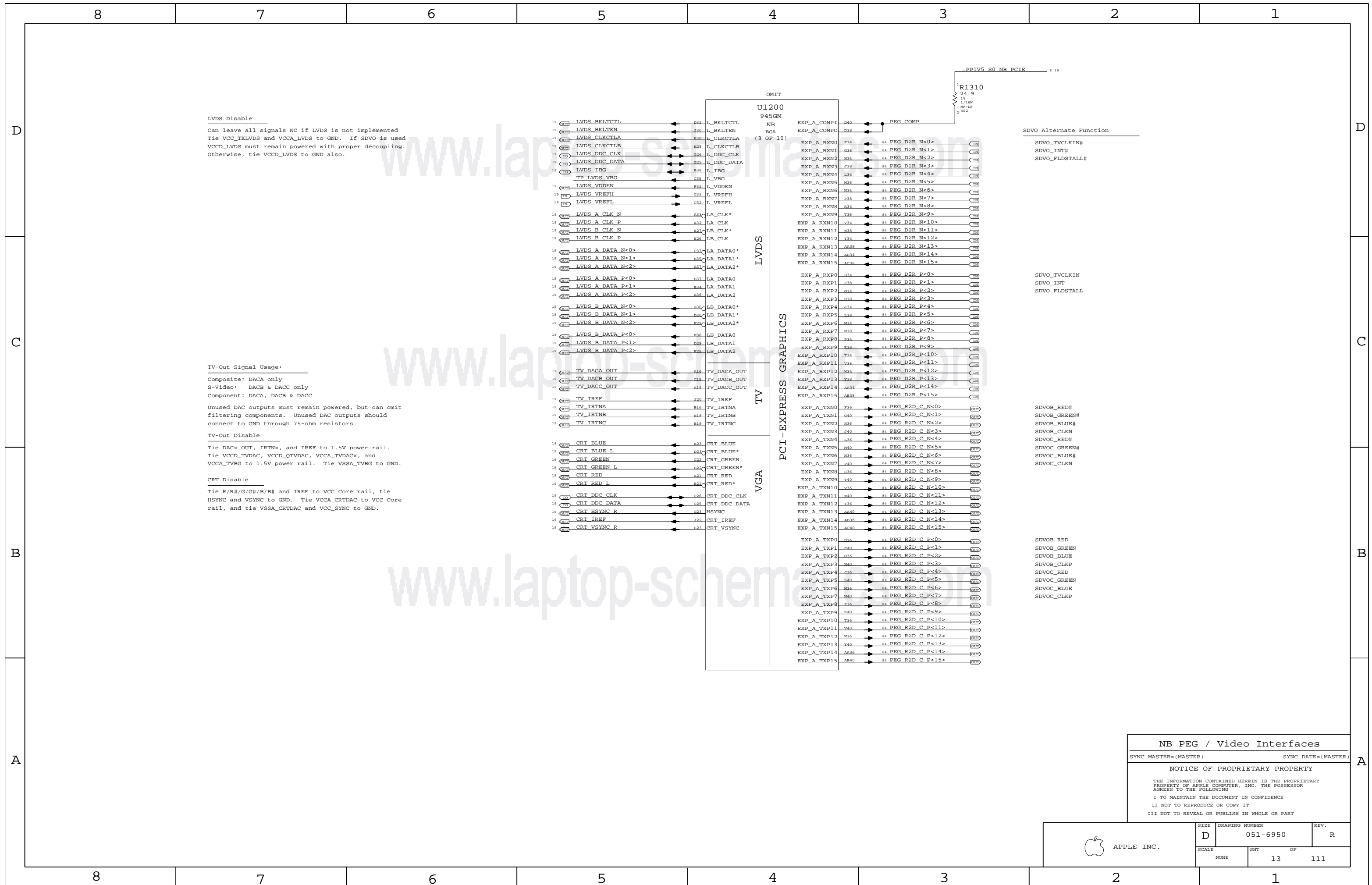
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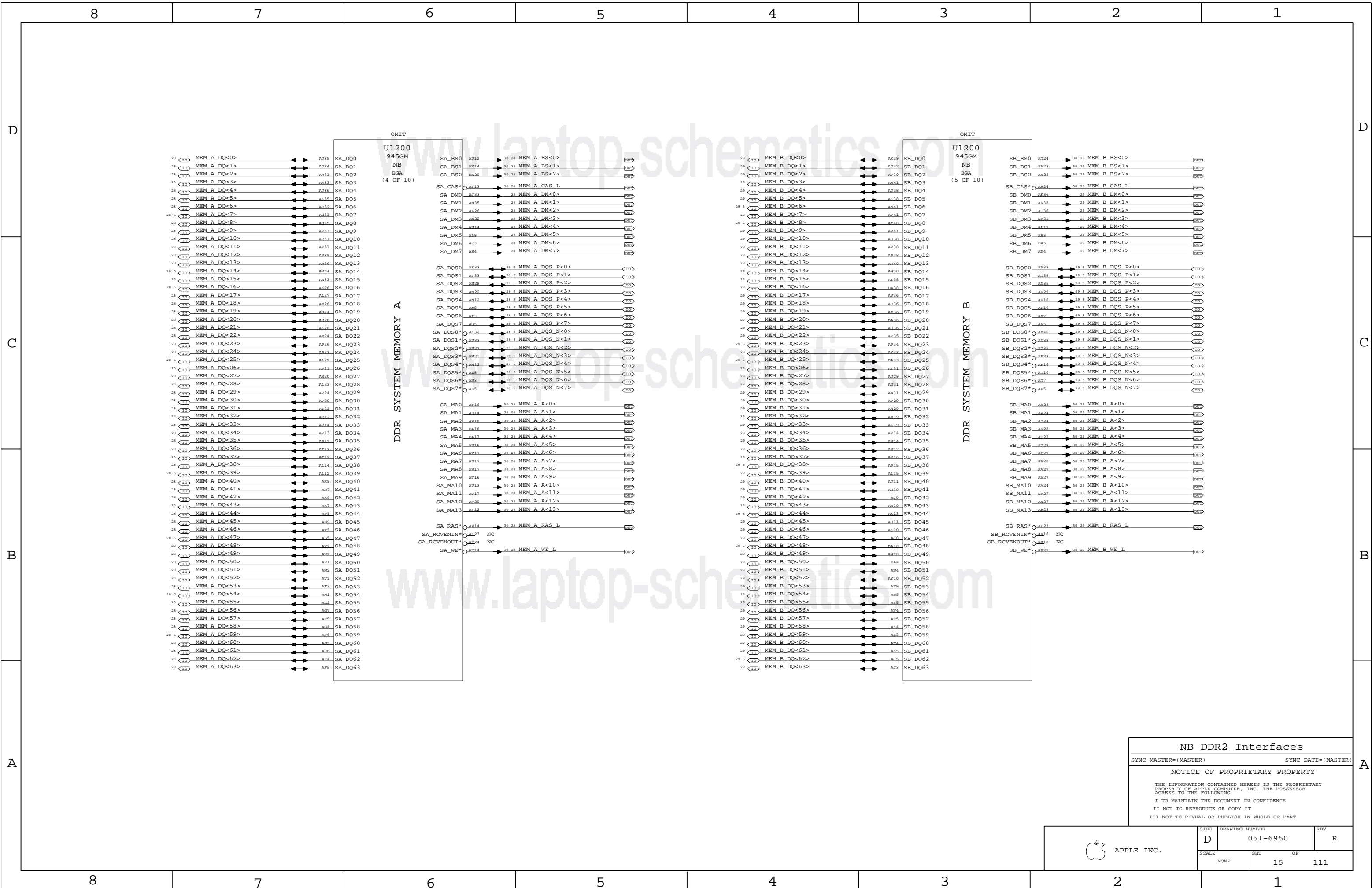
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SCALE		SHT	OF
NONE		12	111







NB DDR2 Interfaces

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)


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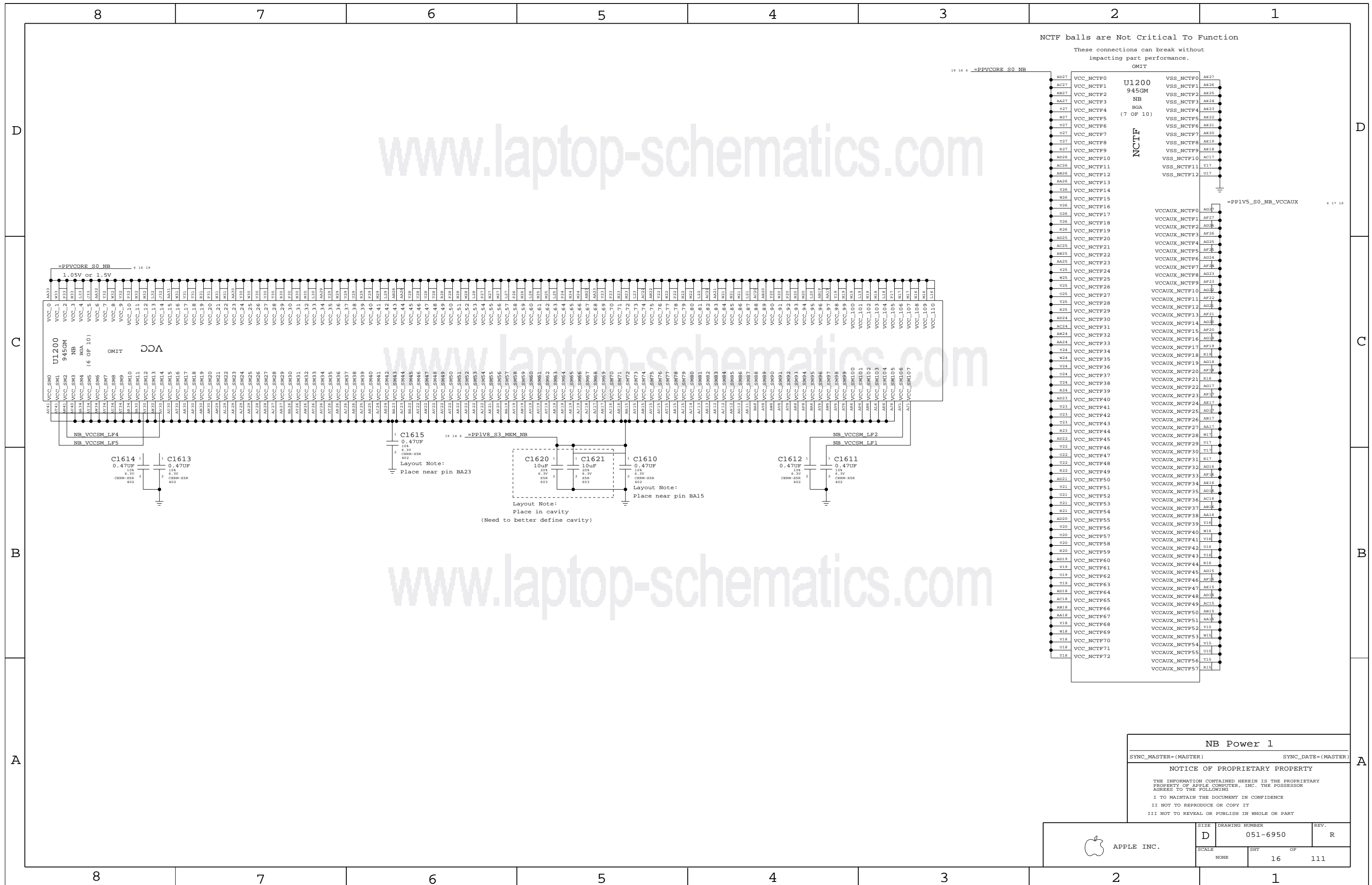
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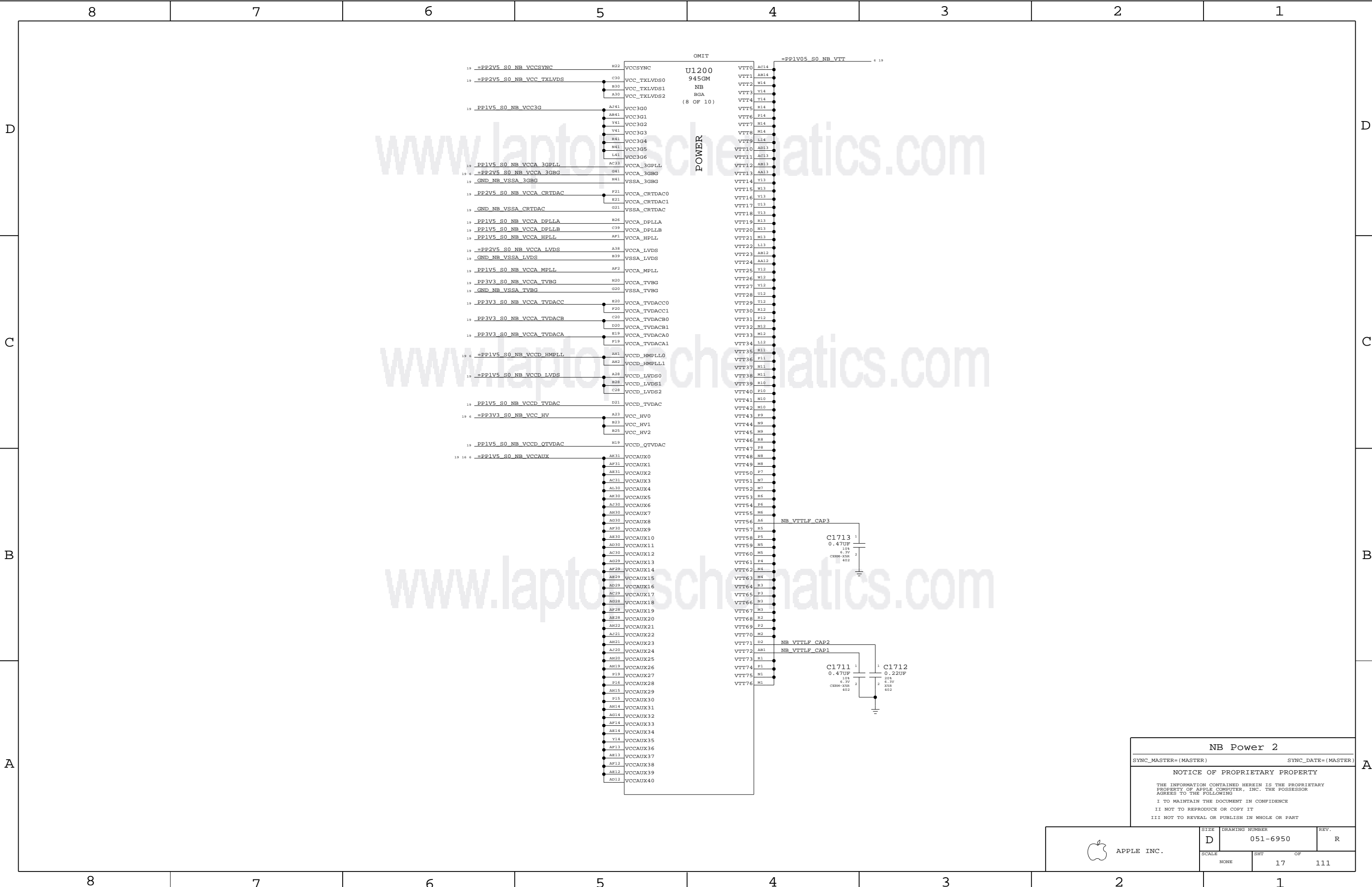
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SCALE		SHT	OF
NONE		15	111





NB Power 2

SYNC\_MASTER=(MASTER)SYNC\_DATE=(MASTER)


NOTICE OF PROPRIETARY PROPERTY

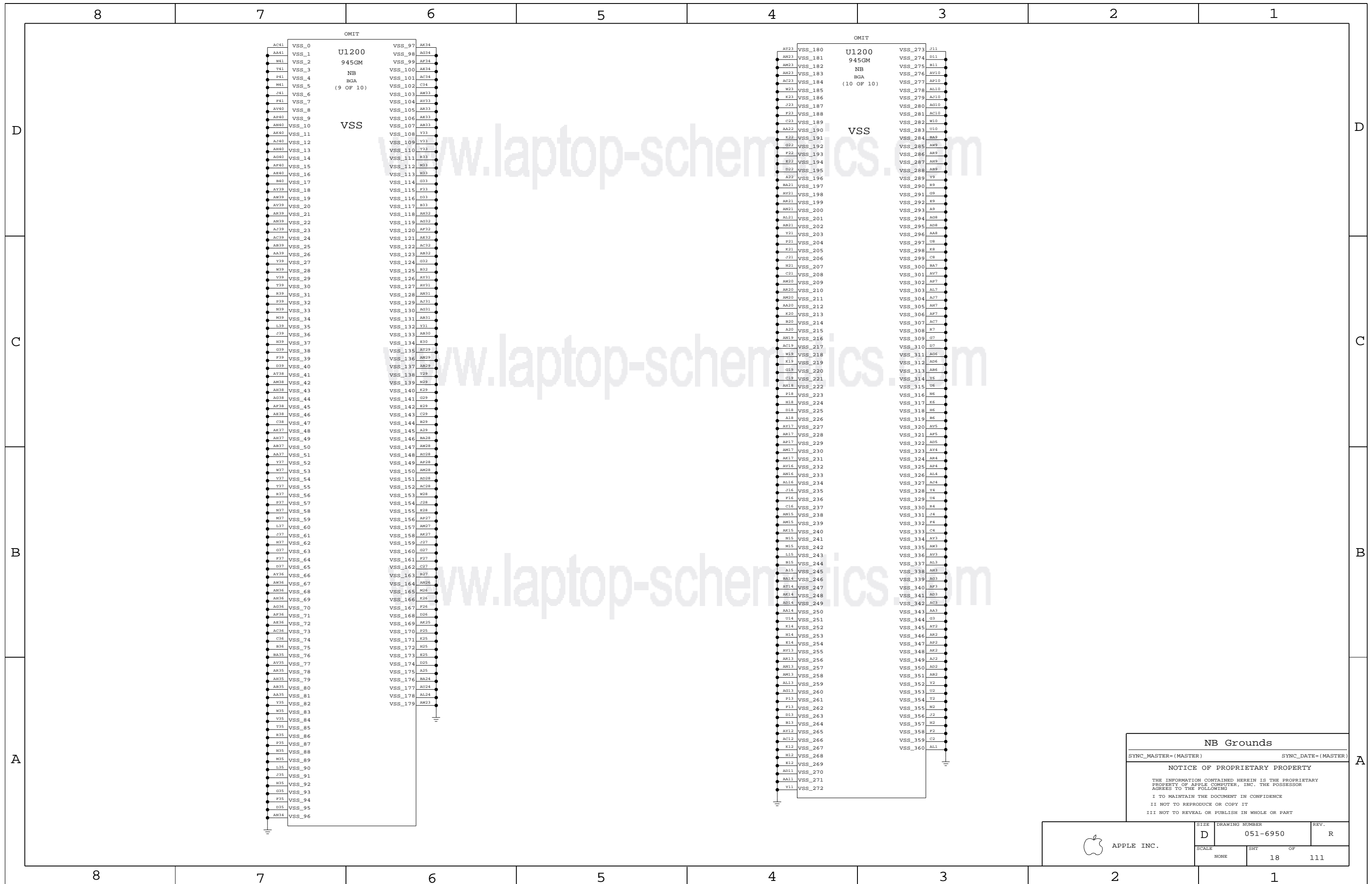
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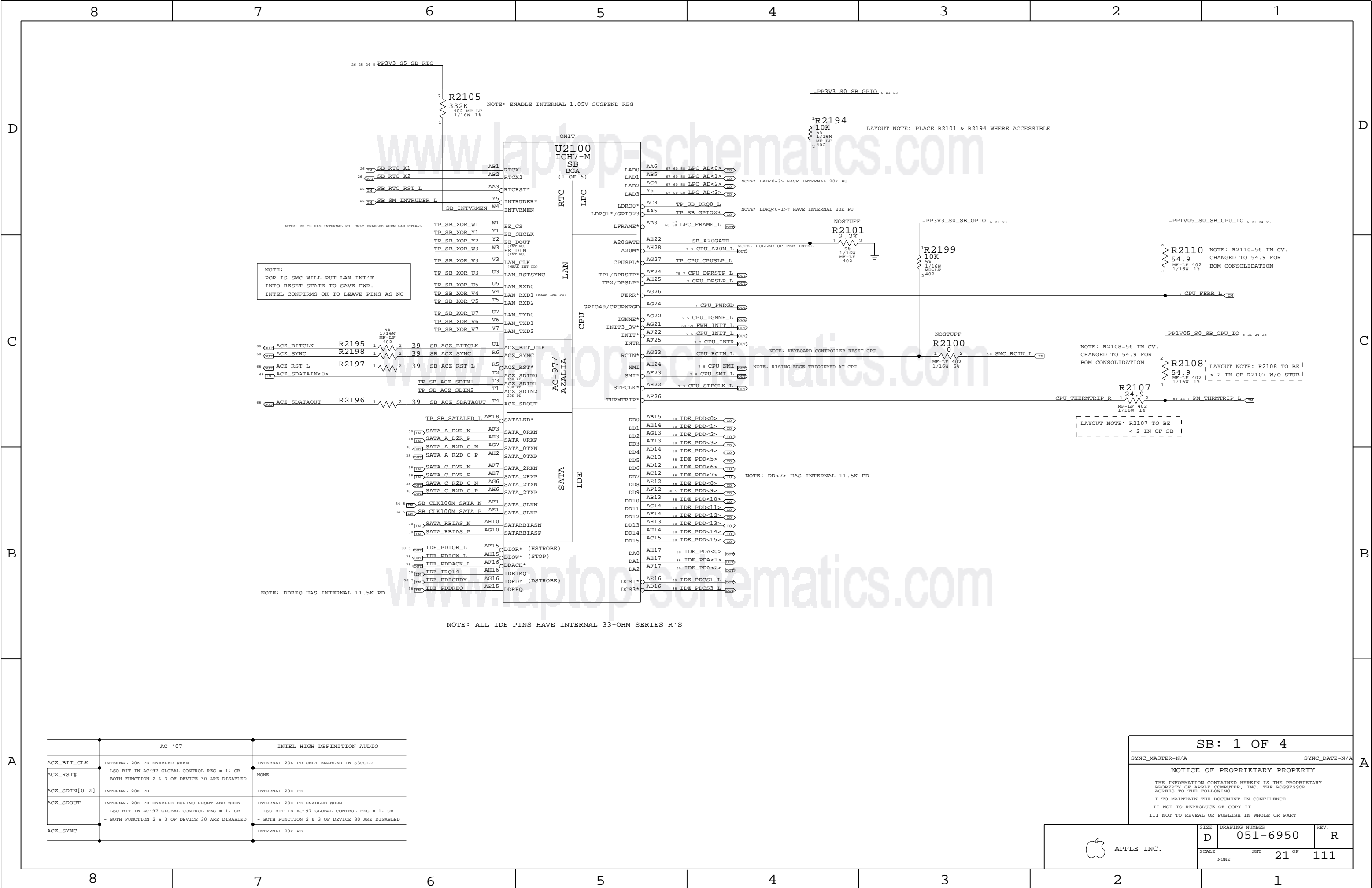
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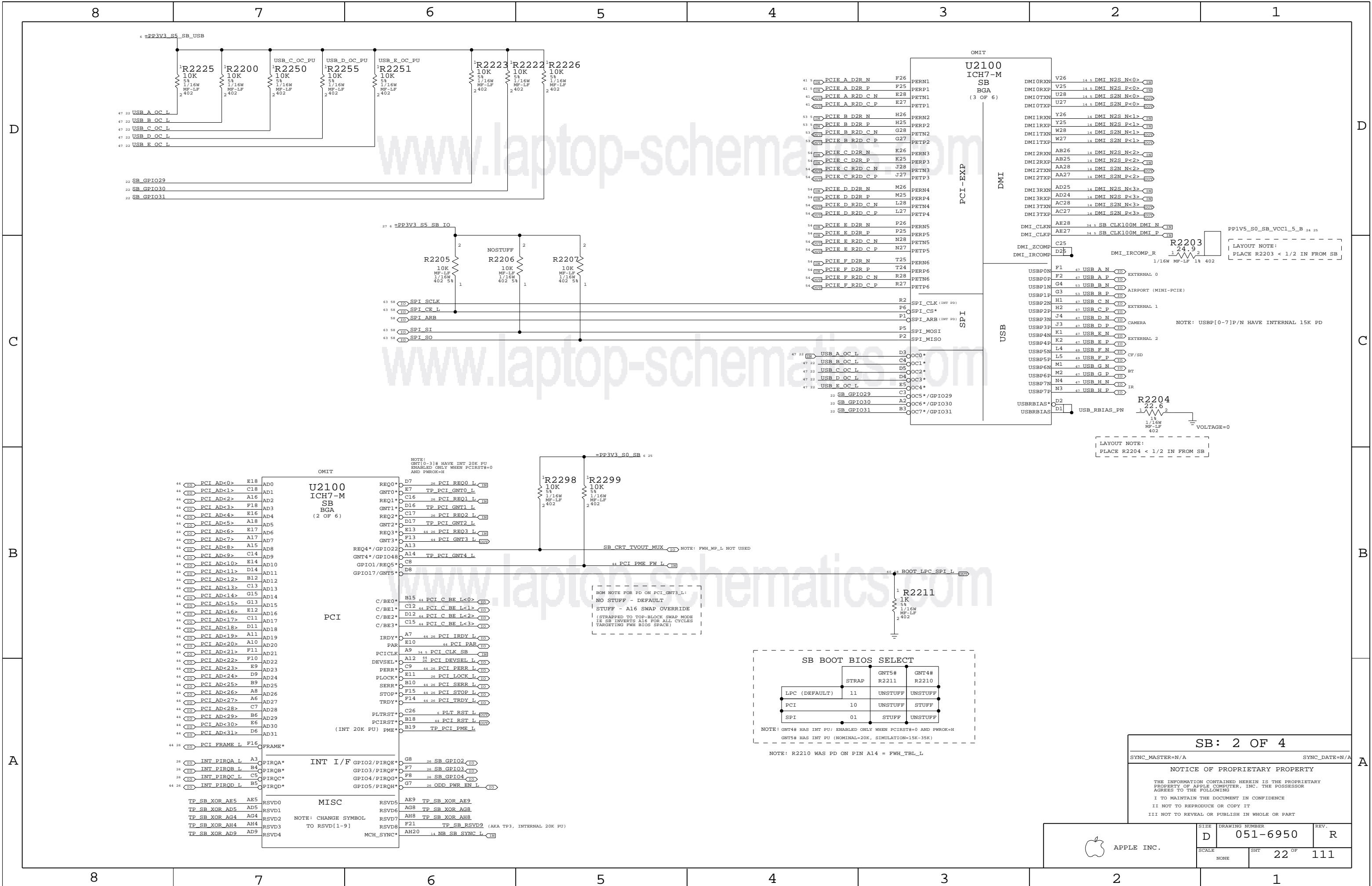
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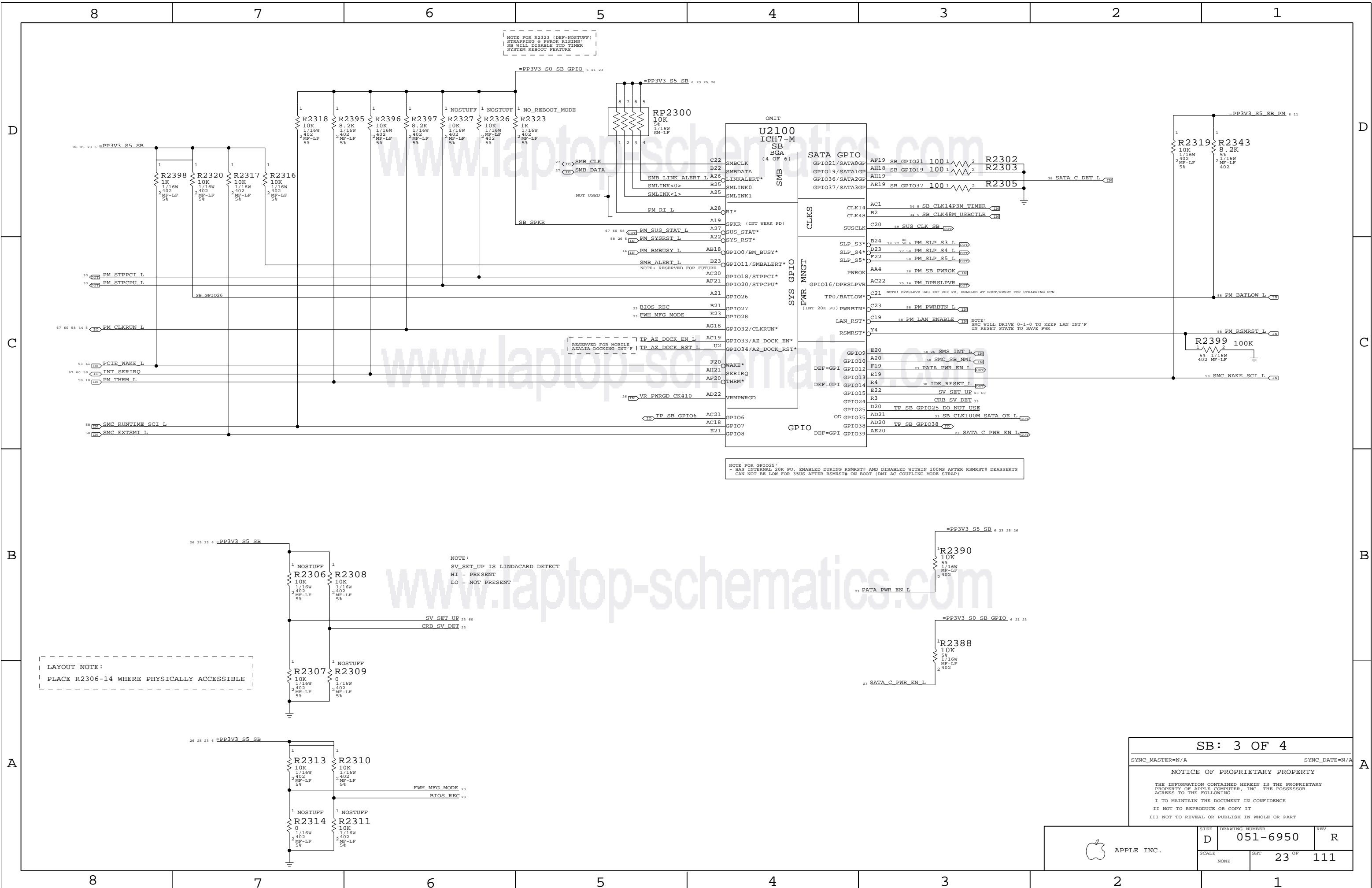






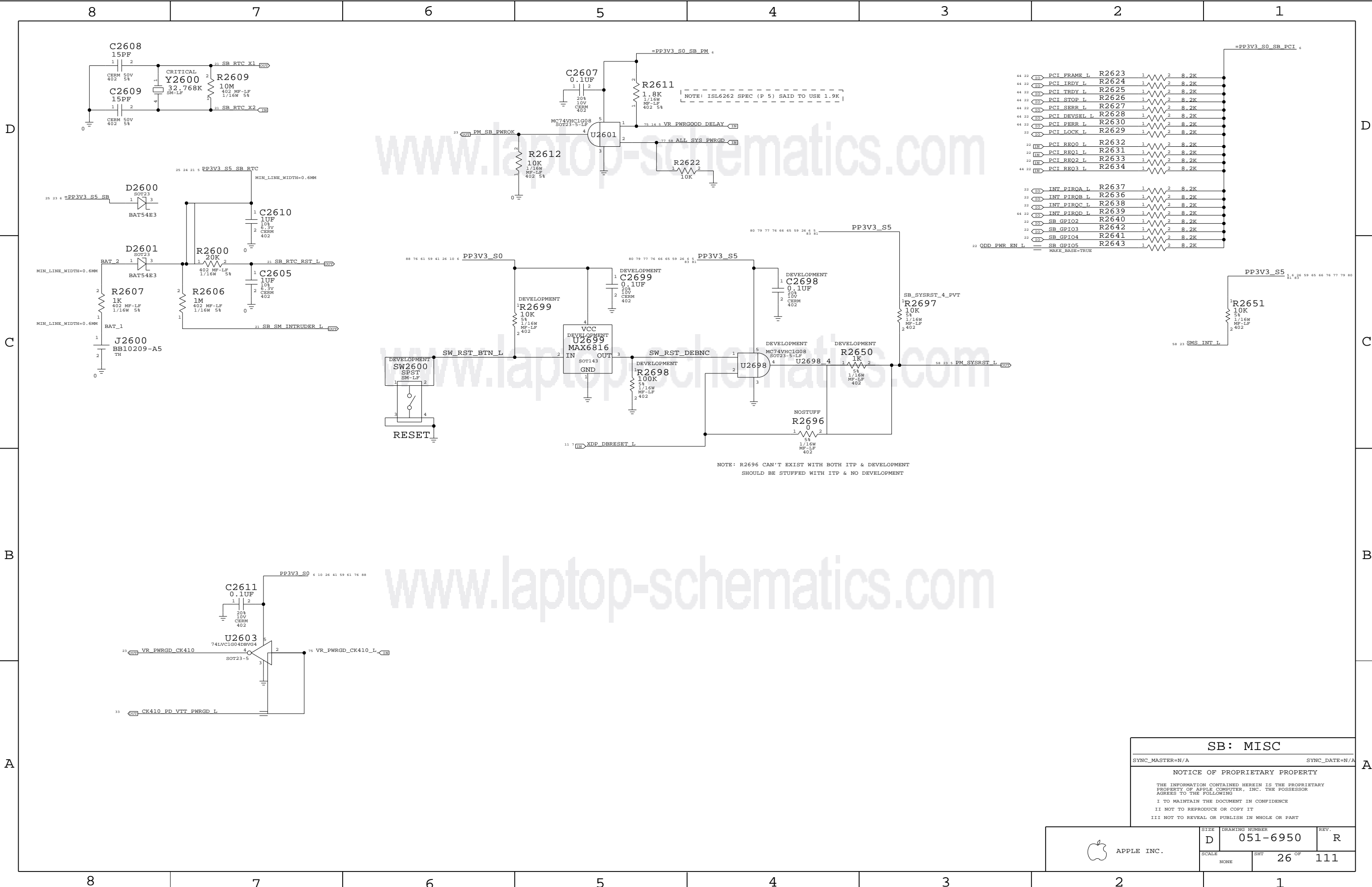












SB: MISC

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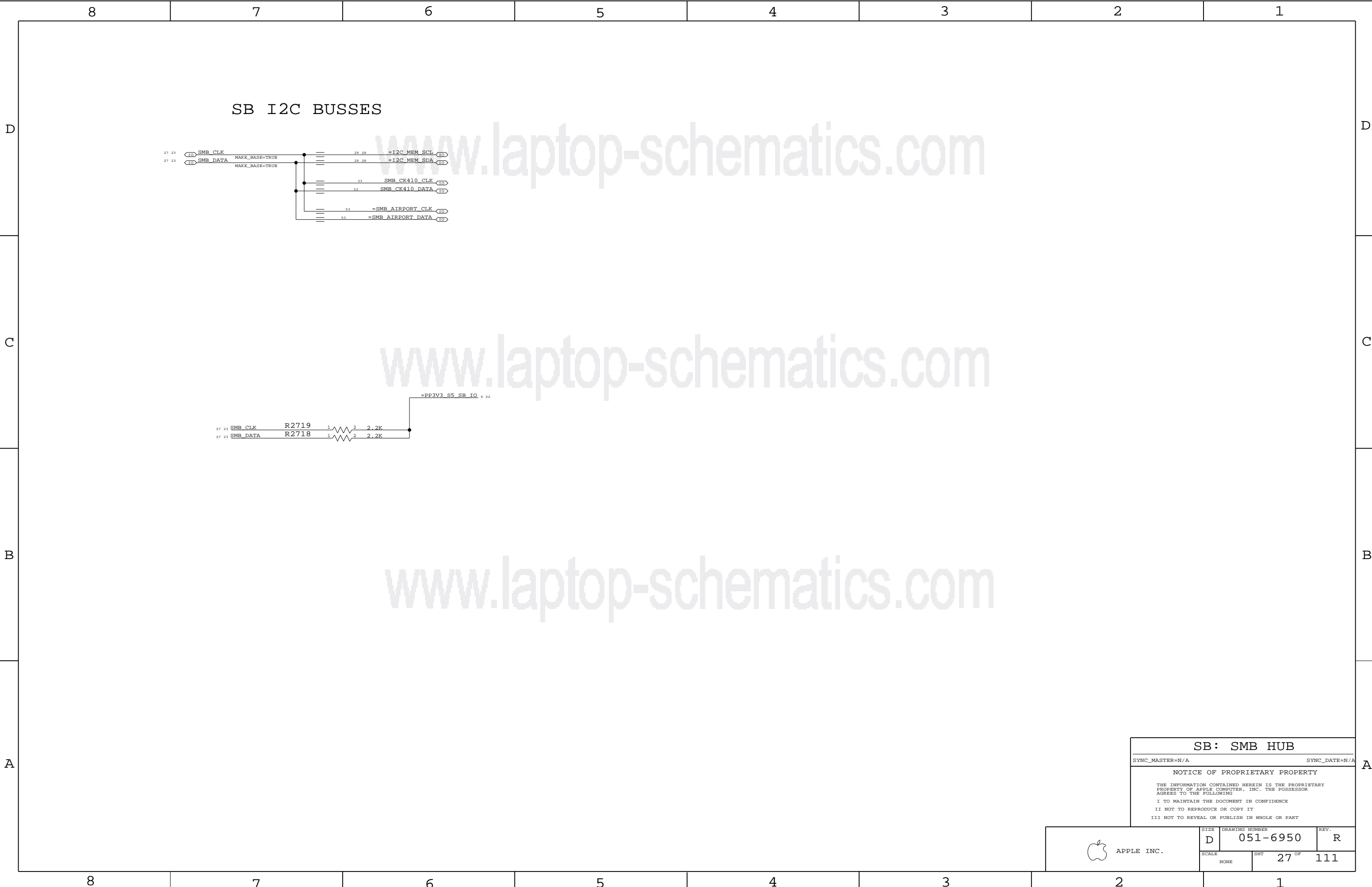
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SB: SMB HUB

SYNC\_MASTER=N/A

SYNC\_DATE=N/A

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APPLE INC.

SIZE

D

DRAWING NUMBER

051-6950

REV.

R

SCALE

NONE

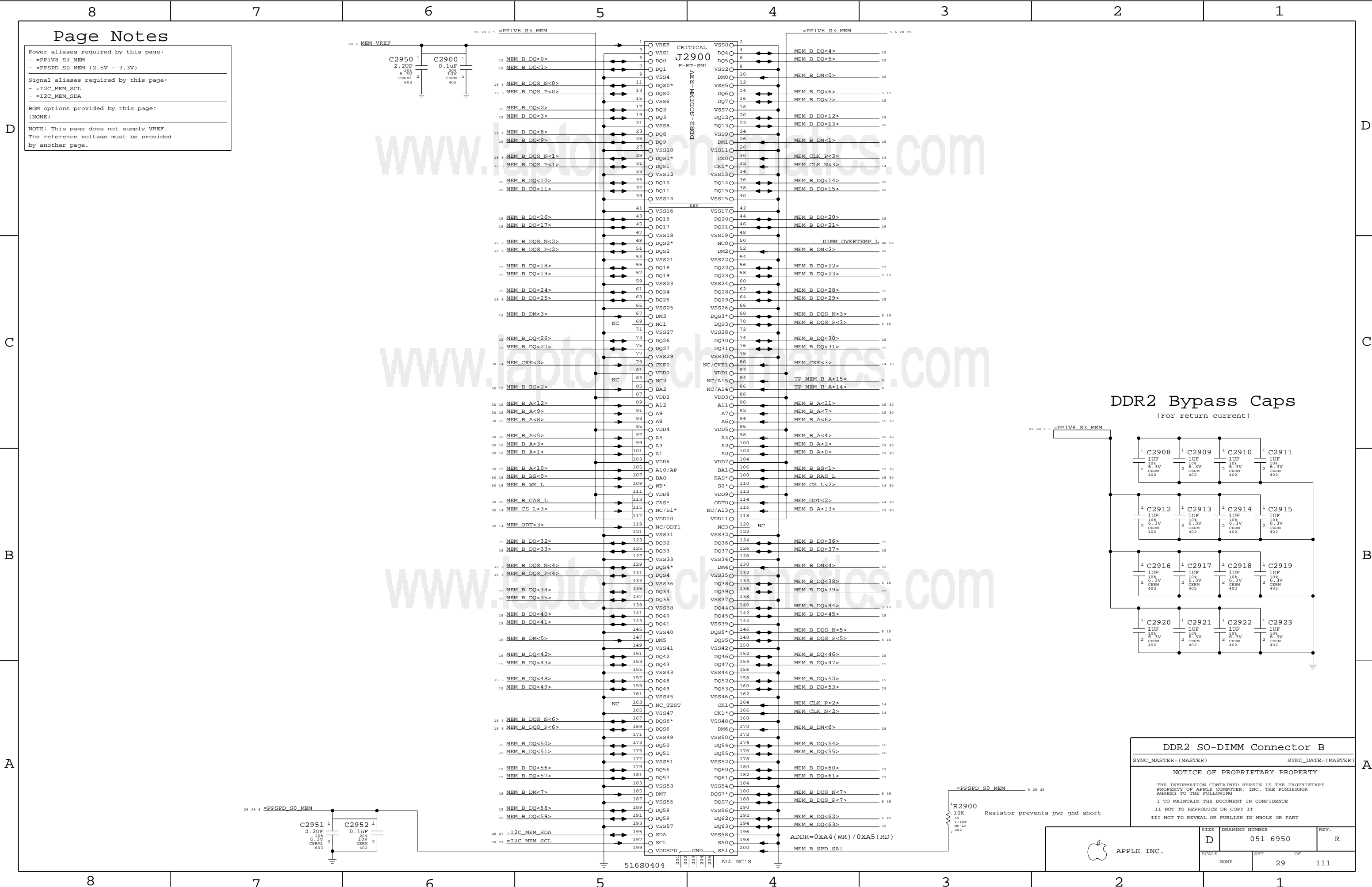
SHT

27

OF

111





**Page Notes**

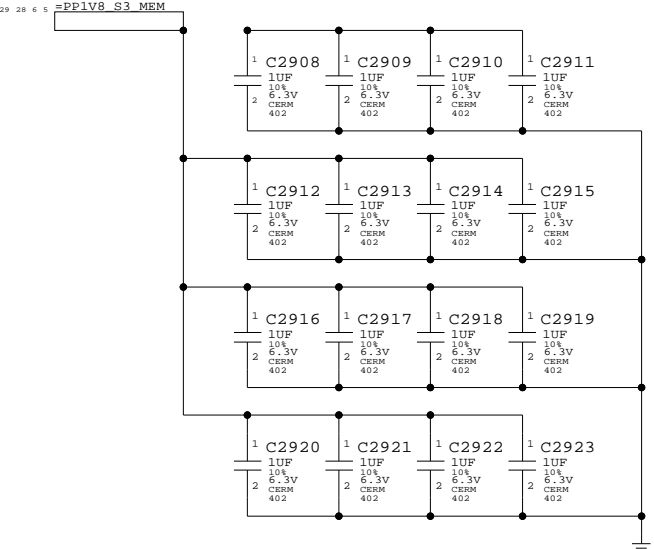
Power aliases required by this page:  
- =PP1V8\_S3\_MEM  
- =PPSPD\_S0\_MEM (2.5V - 3.3V)

Signal aliases required by this page:  
- =I2C\_MEM\_SCL  
- =I2C\_MEM\_SDA

BOM options provided by this page:  
(NONE)

NOTE: This page does not supply VREF.  
The reference voltage must be provided  
by another page.

**DDR2 Bypass Caps**  
(For return current)



**DDR2 SO-DIMM Connector B**

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

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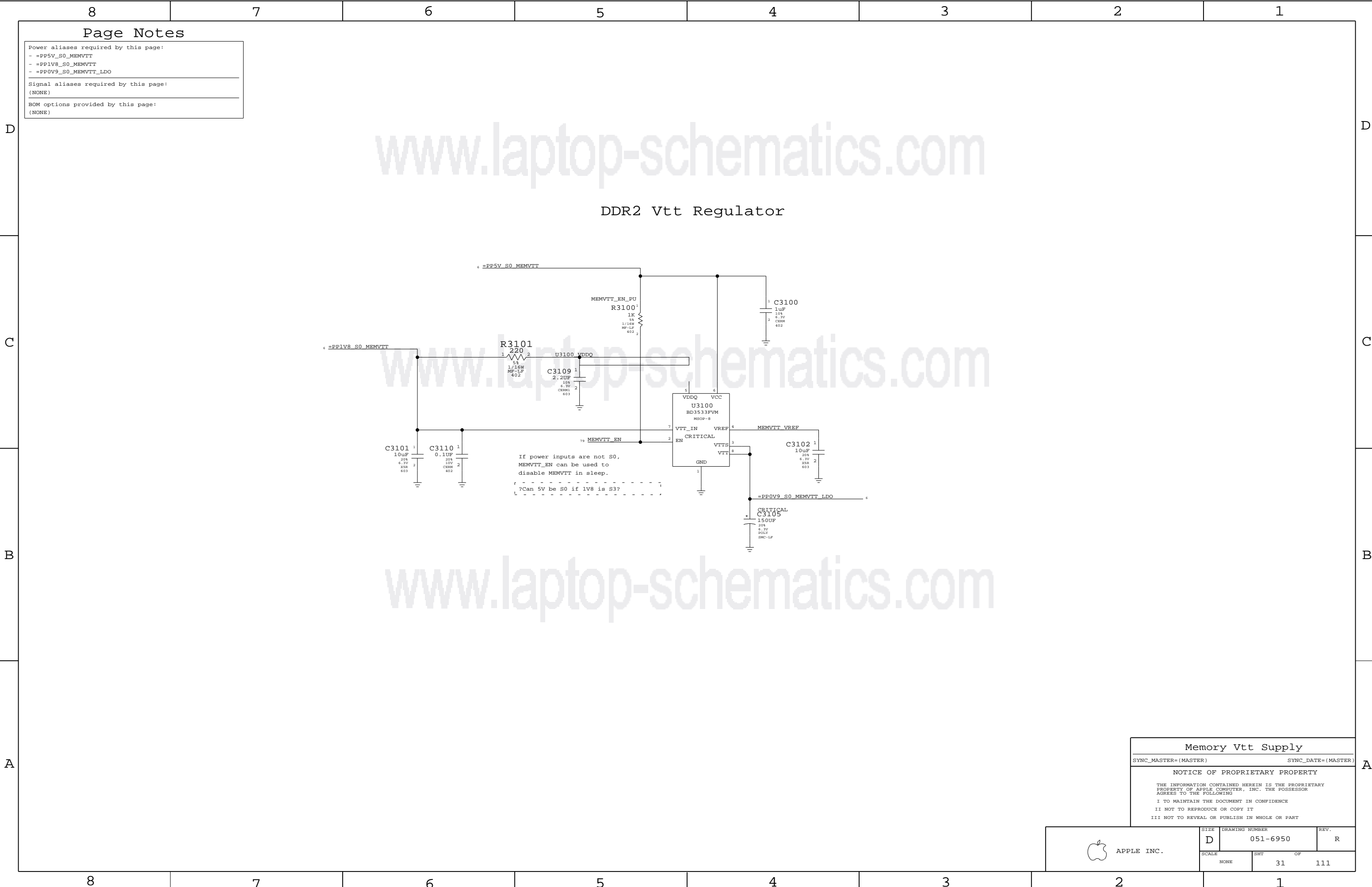
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	D	051-6950	R
SCALE		SHT	OF
NONE		29	111





Memory Vtt Supply

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

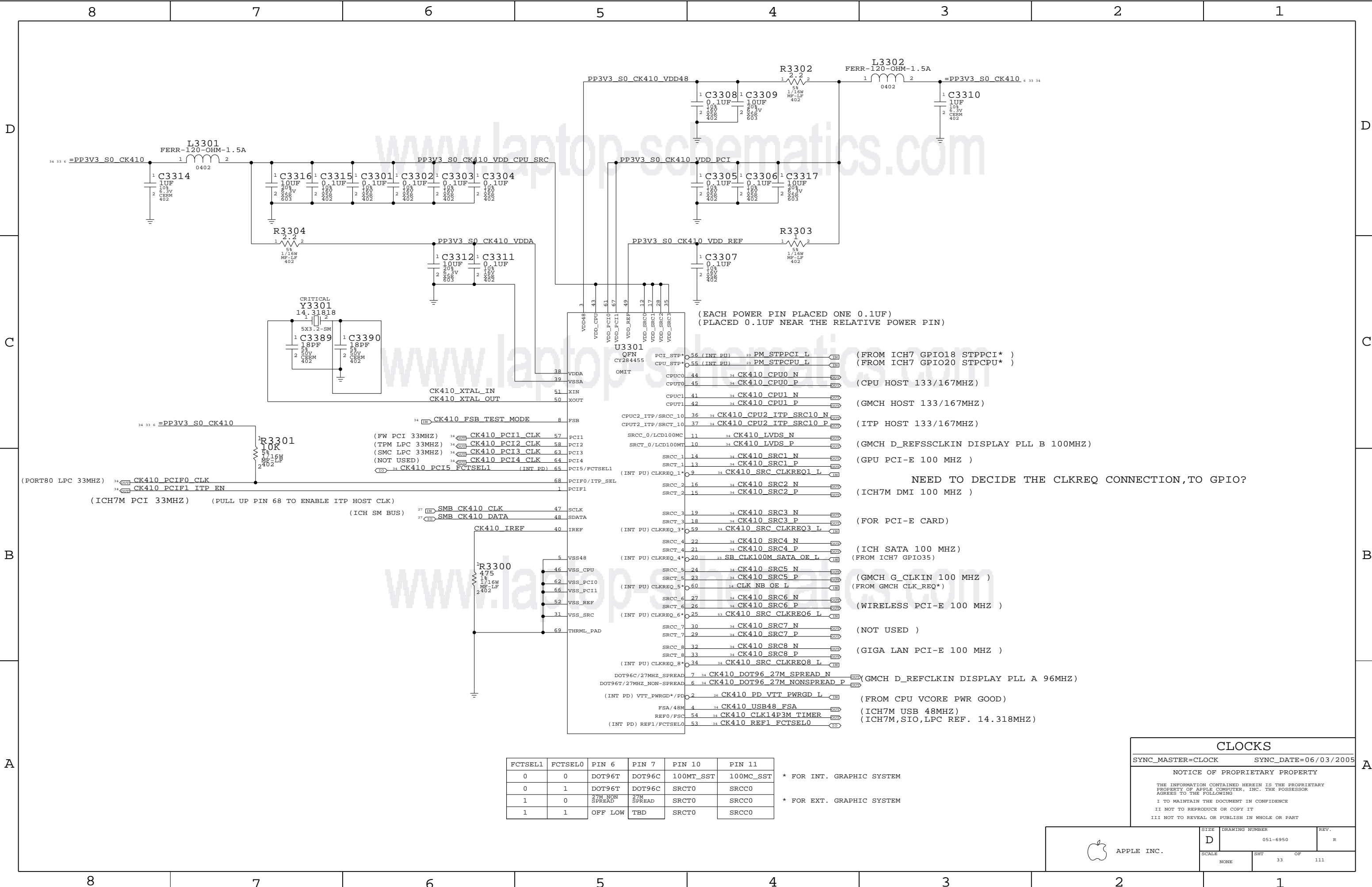
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	SCALE NONE	SHT 31	OF 111



FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M NON SPREAD	27M SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

\* FOR INT. GRAPHIC SYSTEM

\* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC\_MASTER=CLOCK SYNC\_DATE=06/03/2005

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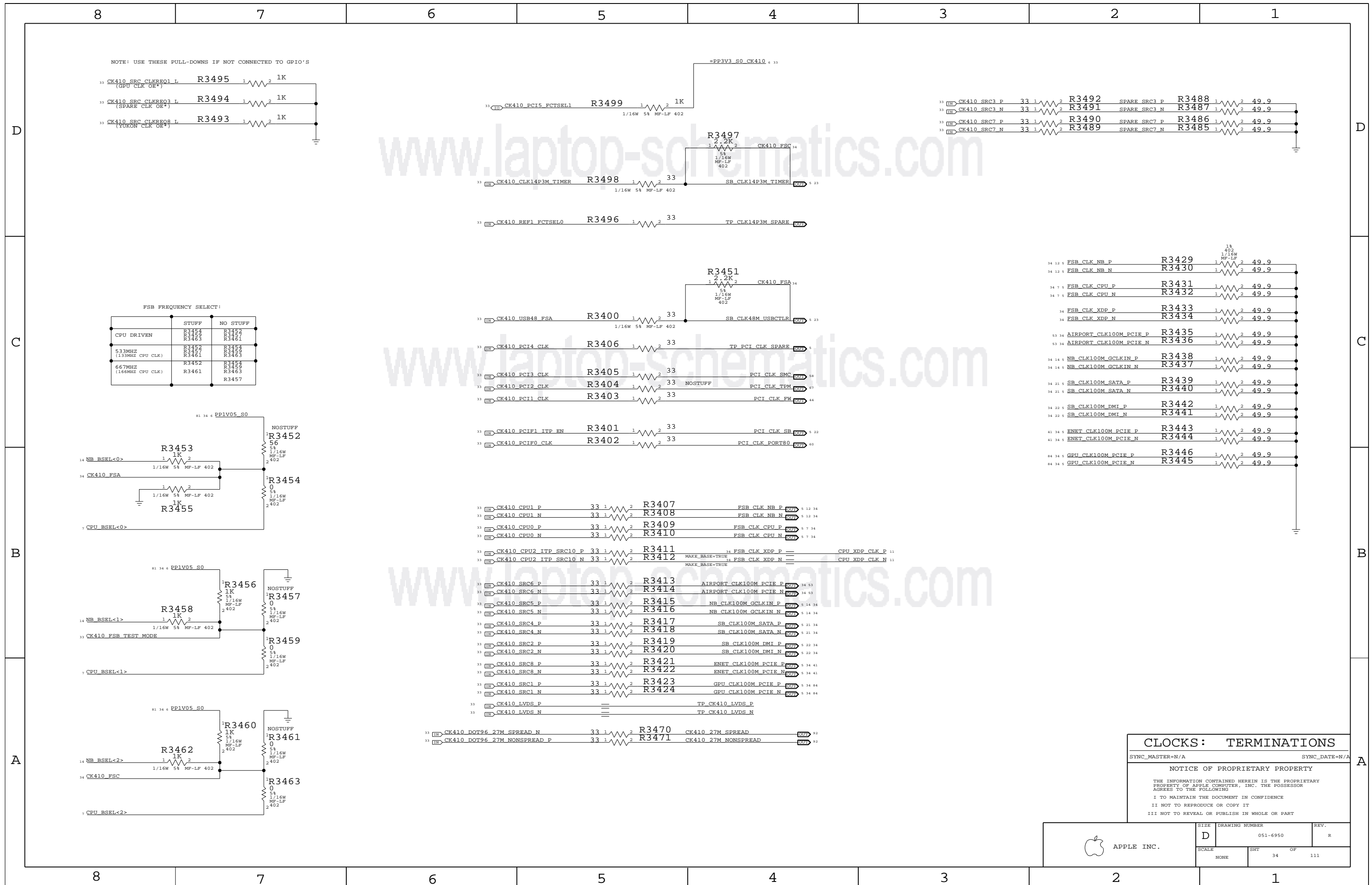
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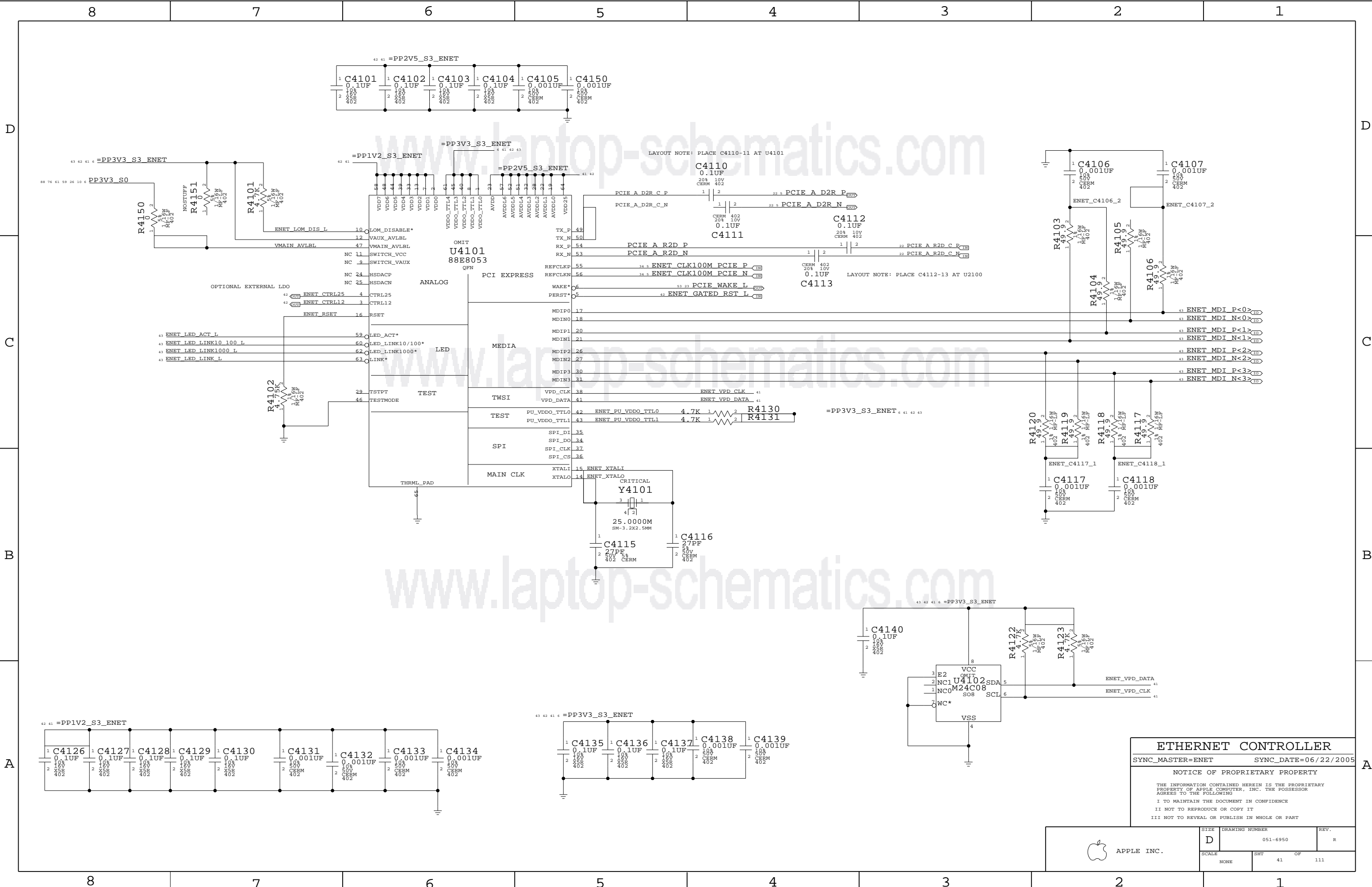
APPLE INC.

SIZE D DRAWING NUMBER 051-6950 REV. R

SCALE NONE SHT 33 OF 111



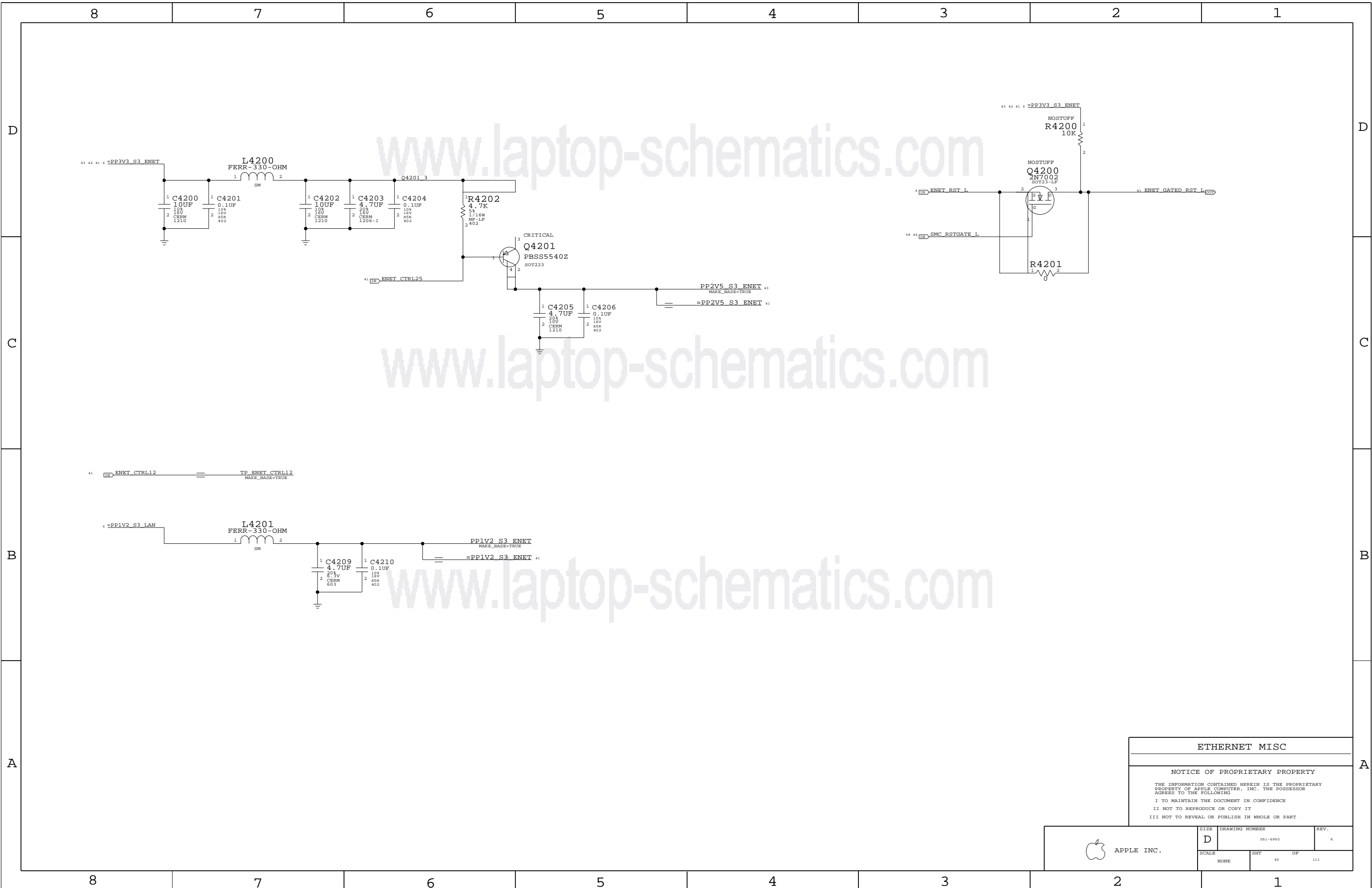


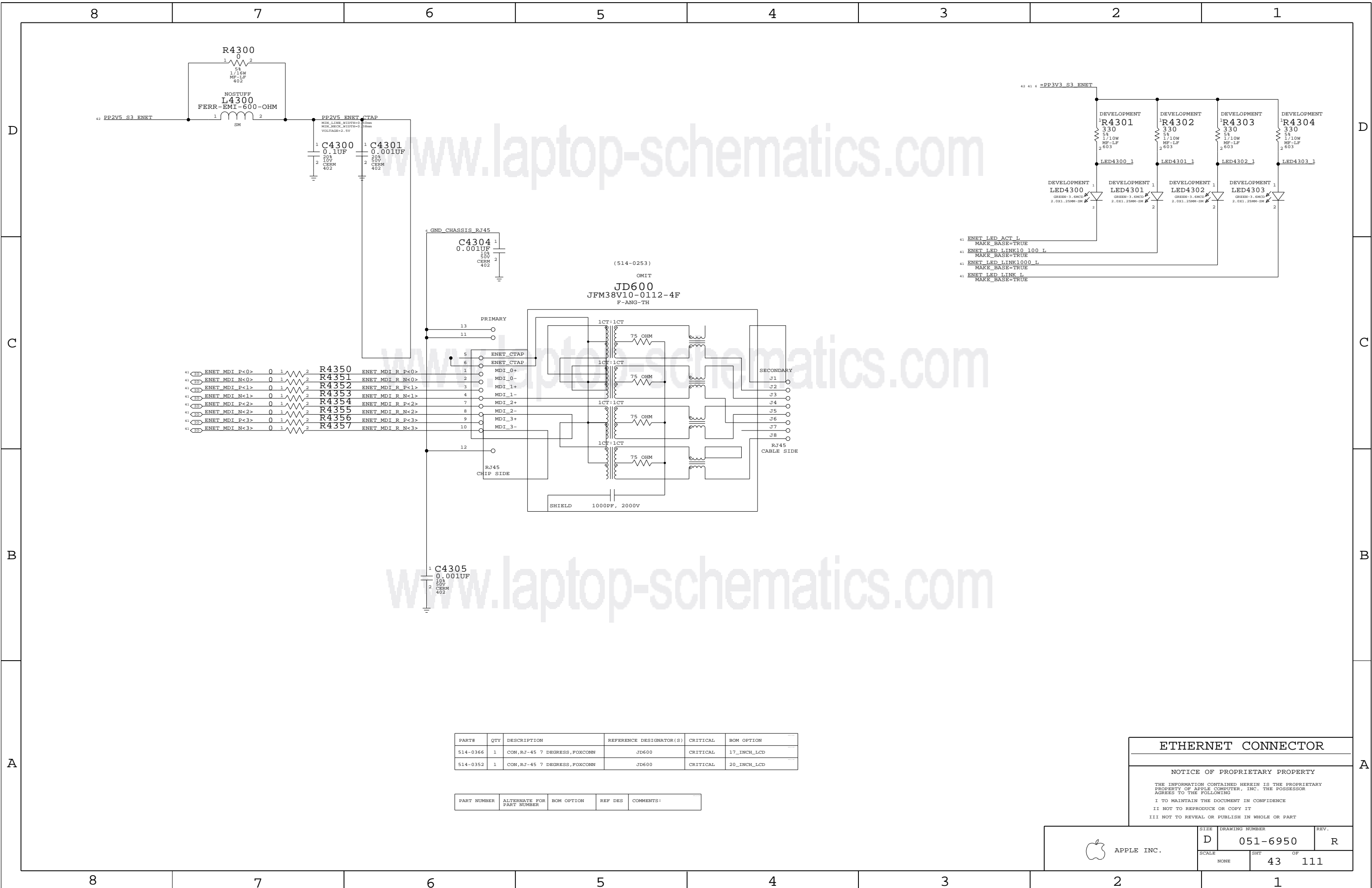


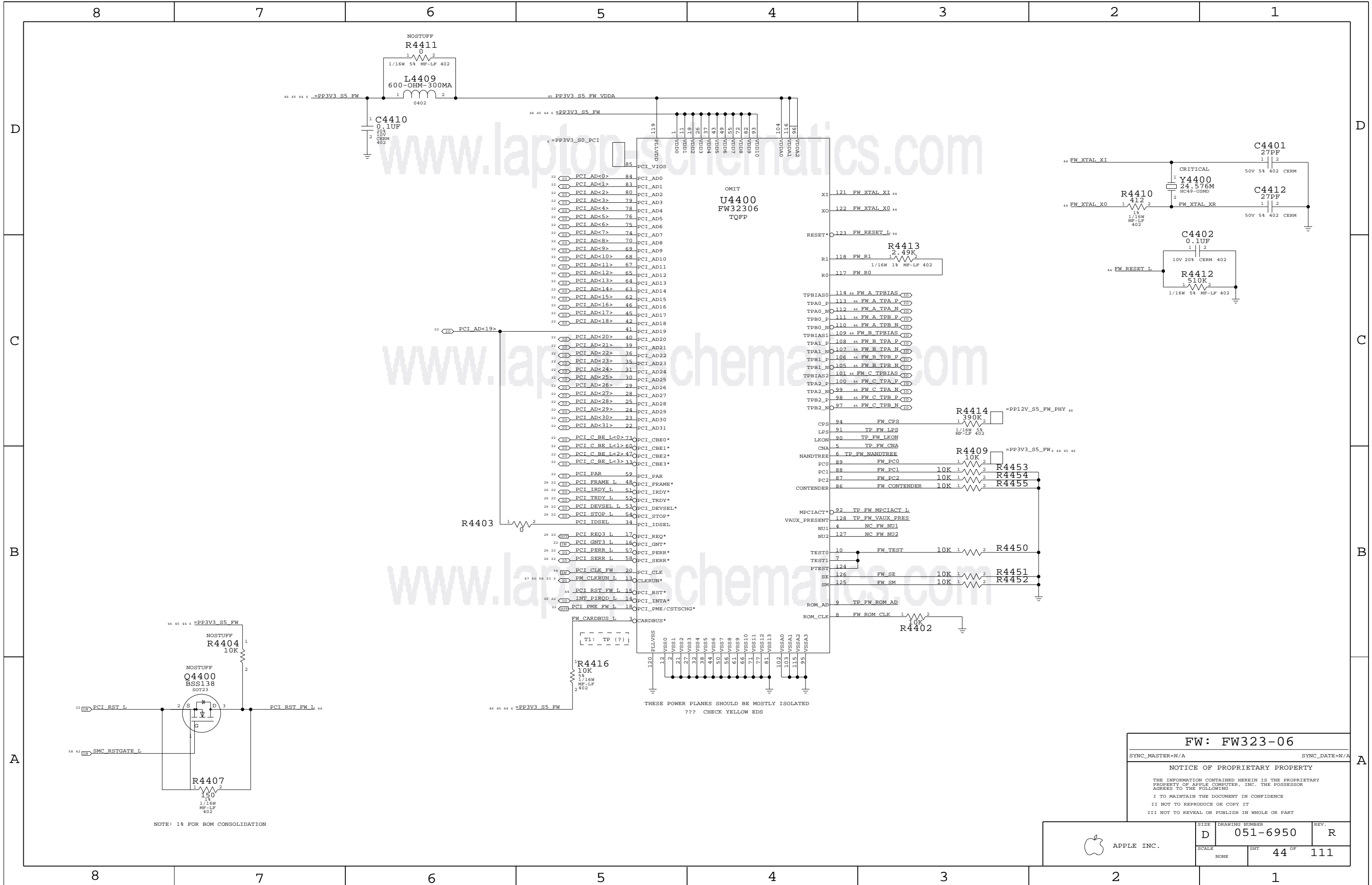
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SCALE		SHT	OF
NONE		41	111







FW: FW323-06

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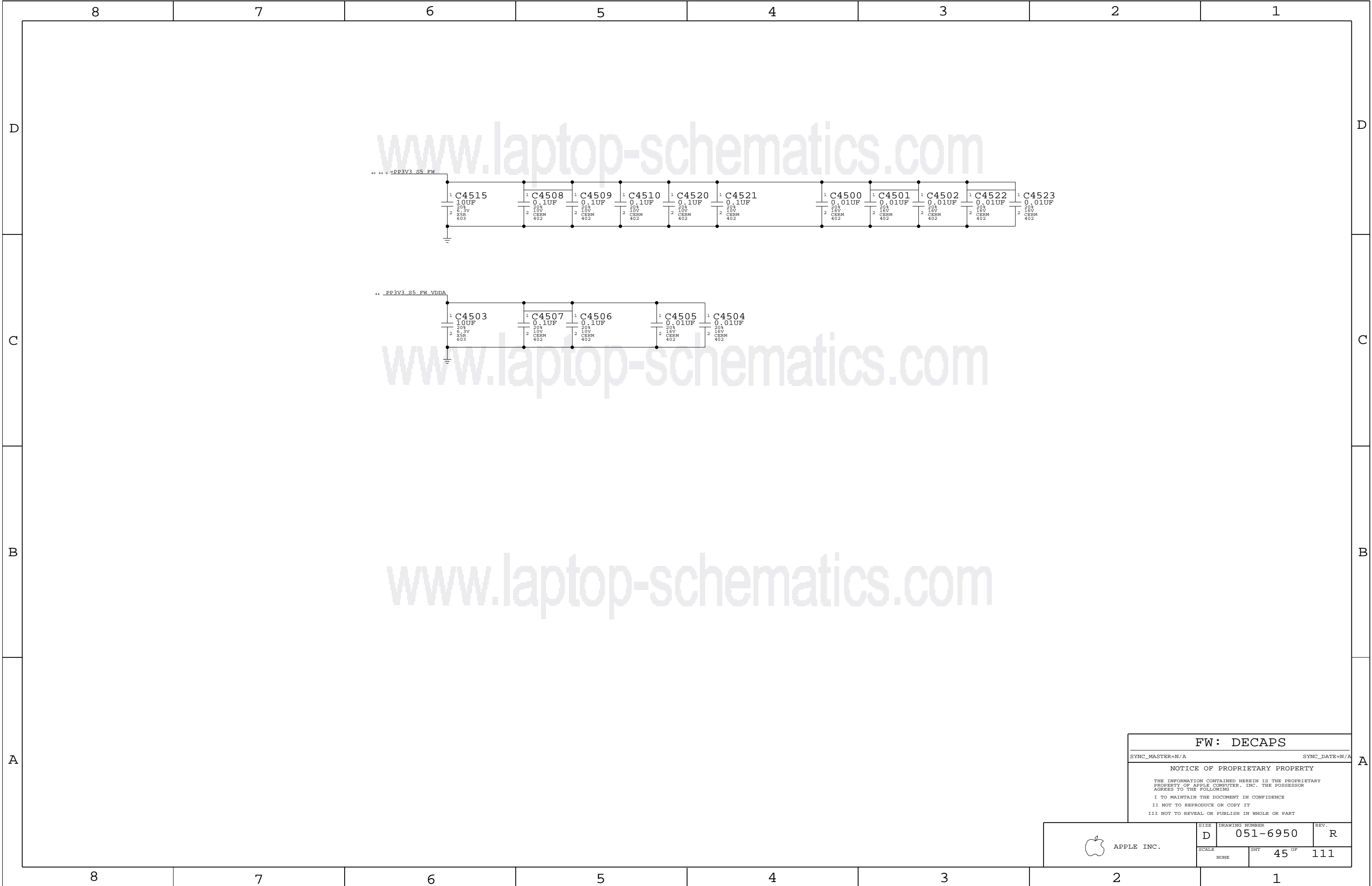
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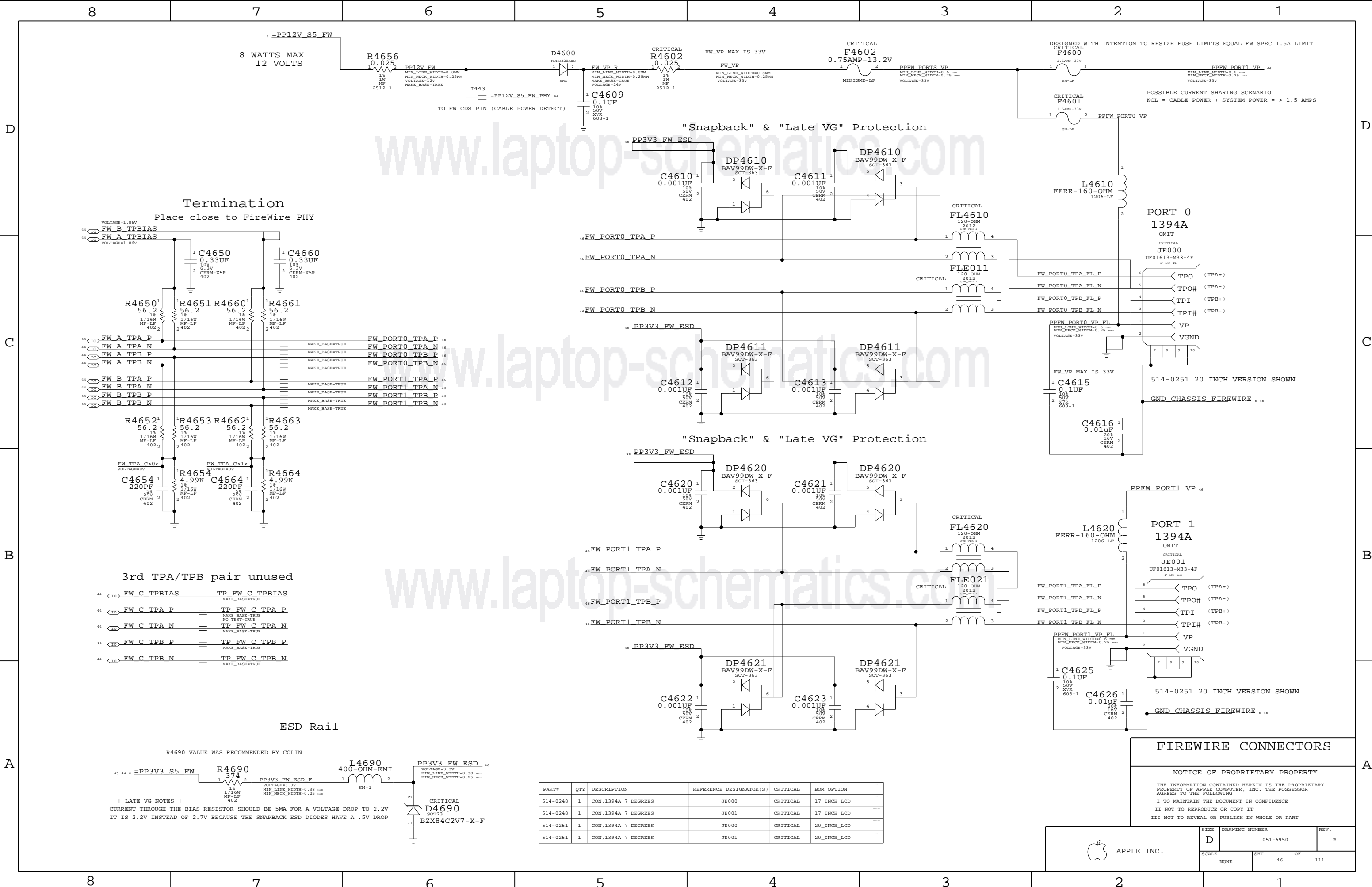
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE	SHT	44 OF	111
NONE			



APPLE INC.

SIZE	D	DRAWING NUMBER	051-6950	REV.	R
SCALE	NONE	SHT	45 OF	111	



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0248	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	17_INCH_LCD
514-0248	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	17_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	20_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	20_INCH_LCD

FIREWIRE CONNECTORS

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SCALE	SHT	OF
NONE	46	111



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D		<div>www.laptop-schematics.com</div>														D			
C		<div>www.laptop-schematics.com</div>														C			
B		<div>www.laptop-schematics.com</div>														B			
A		<div>www.laptop-schematics.com</div>														A			
8		7		6		5		4		3		2		1					
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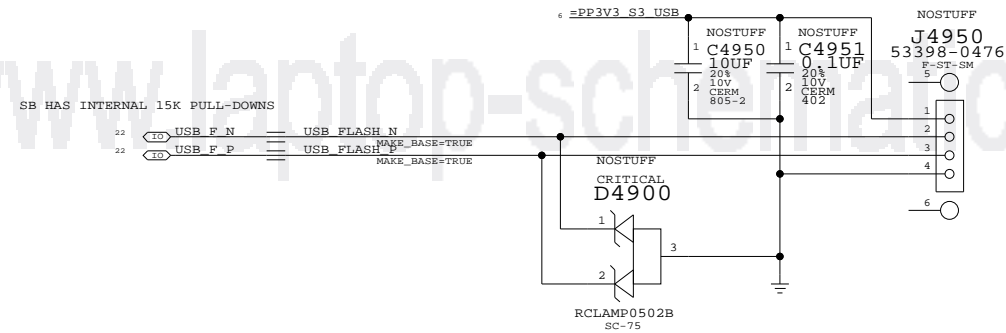
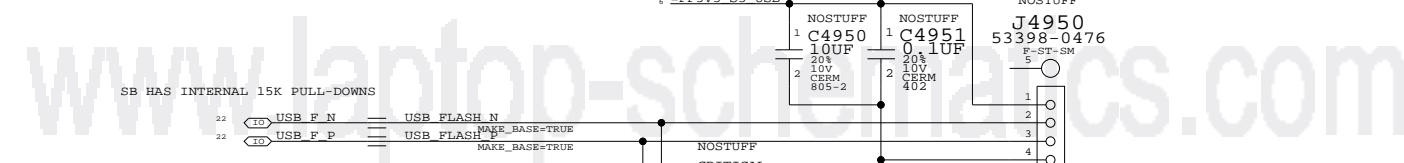
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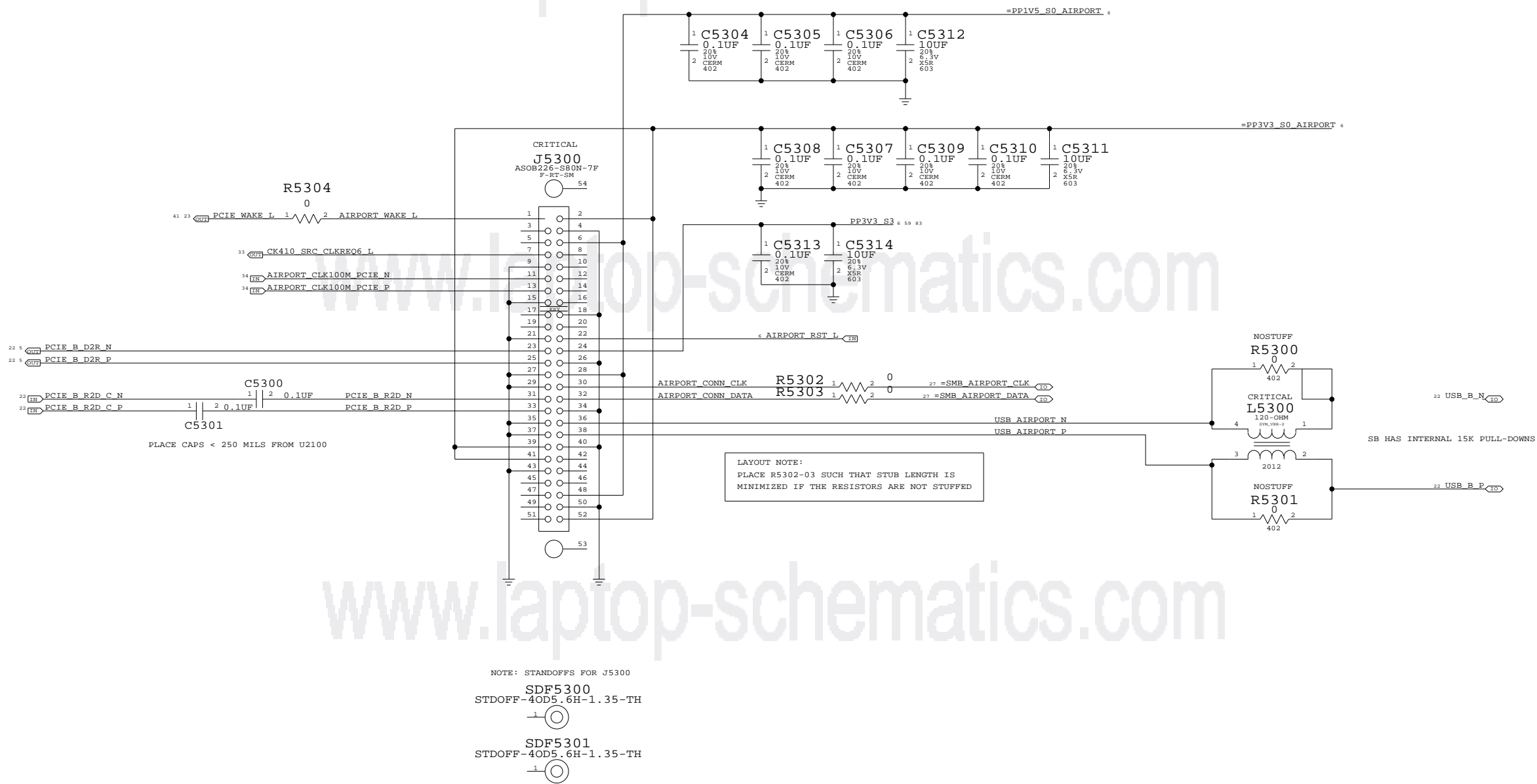
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SCALE NONE	SHT 48 OF	111



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	SCALE	SHT	49 <sup>OF</sup>	111
	NONE			

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# AIRPORT CONN

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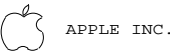
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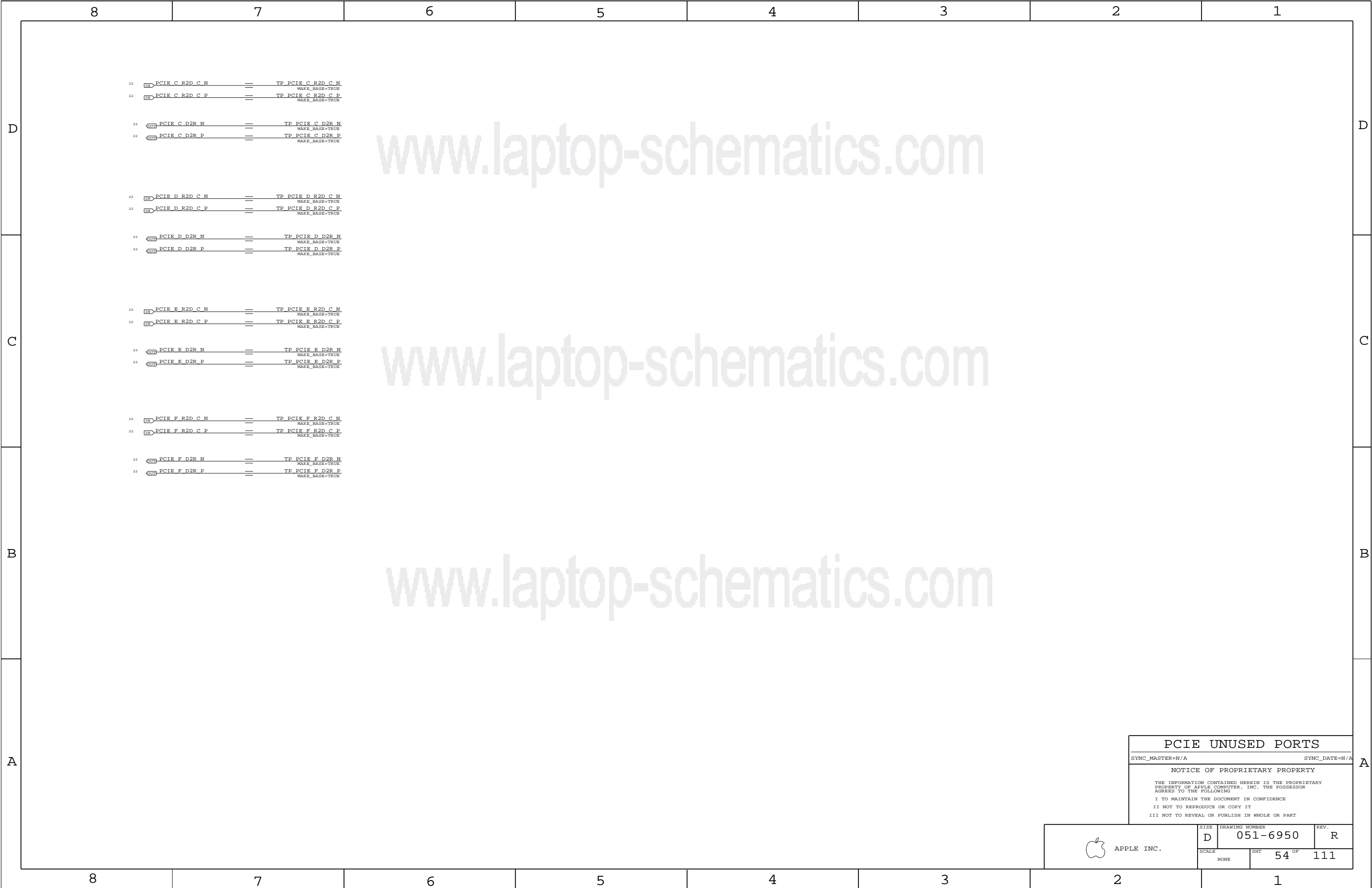
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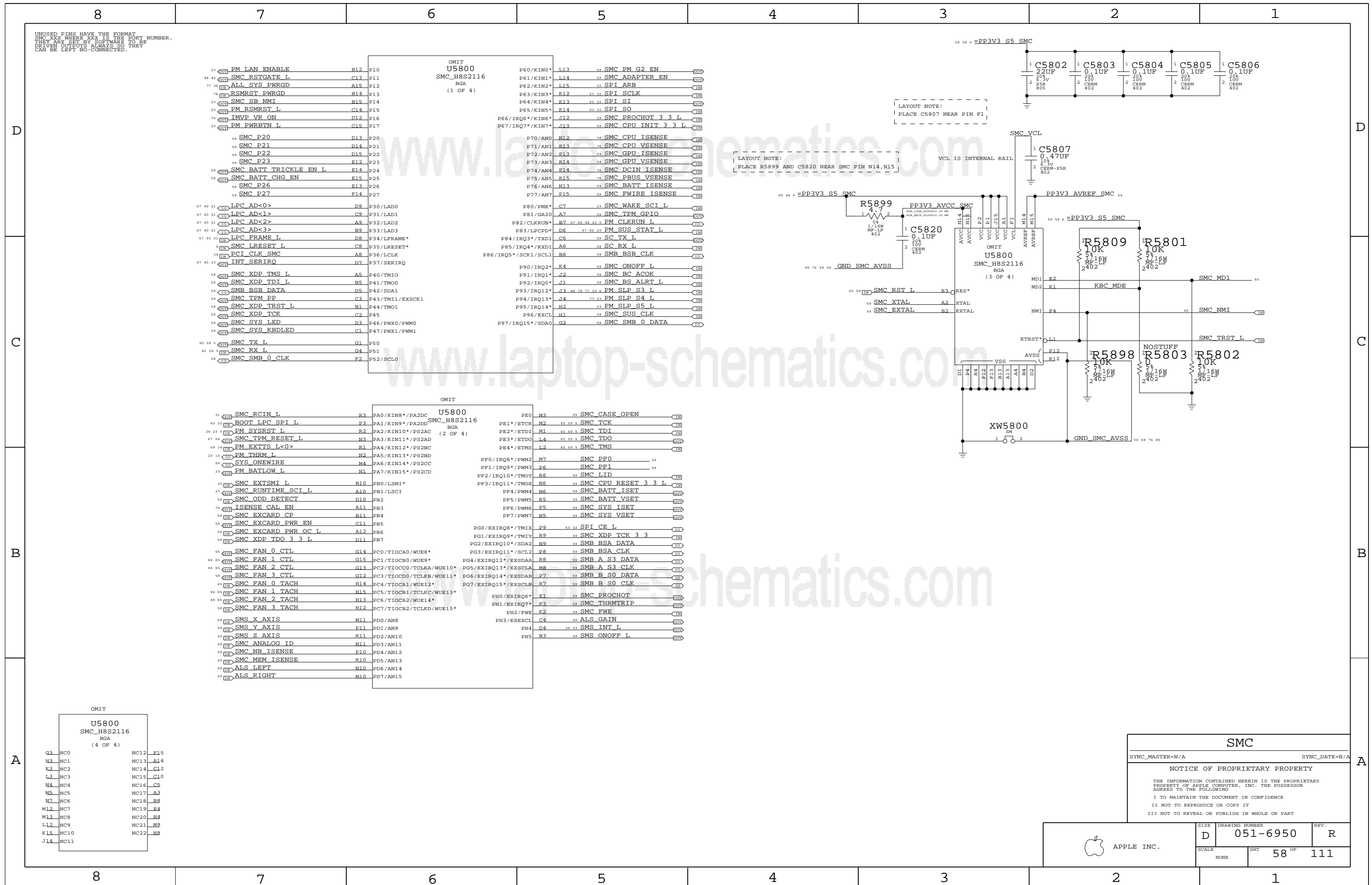


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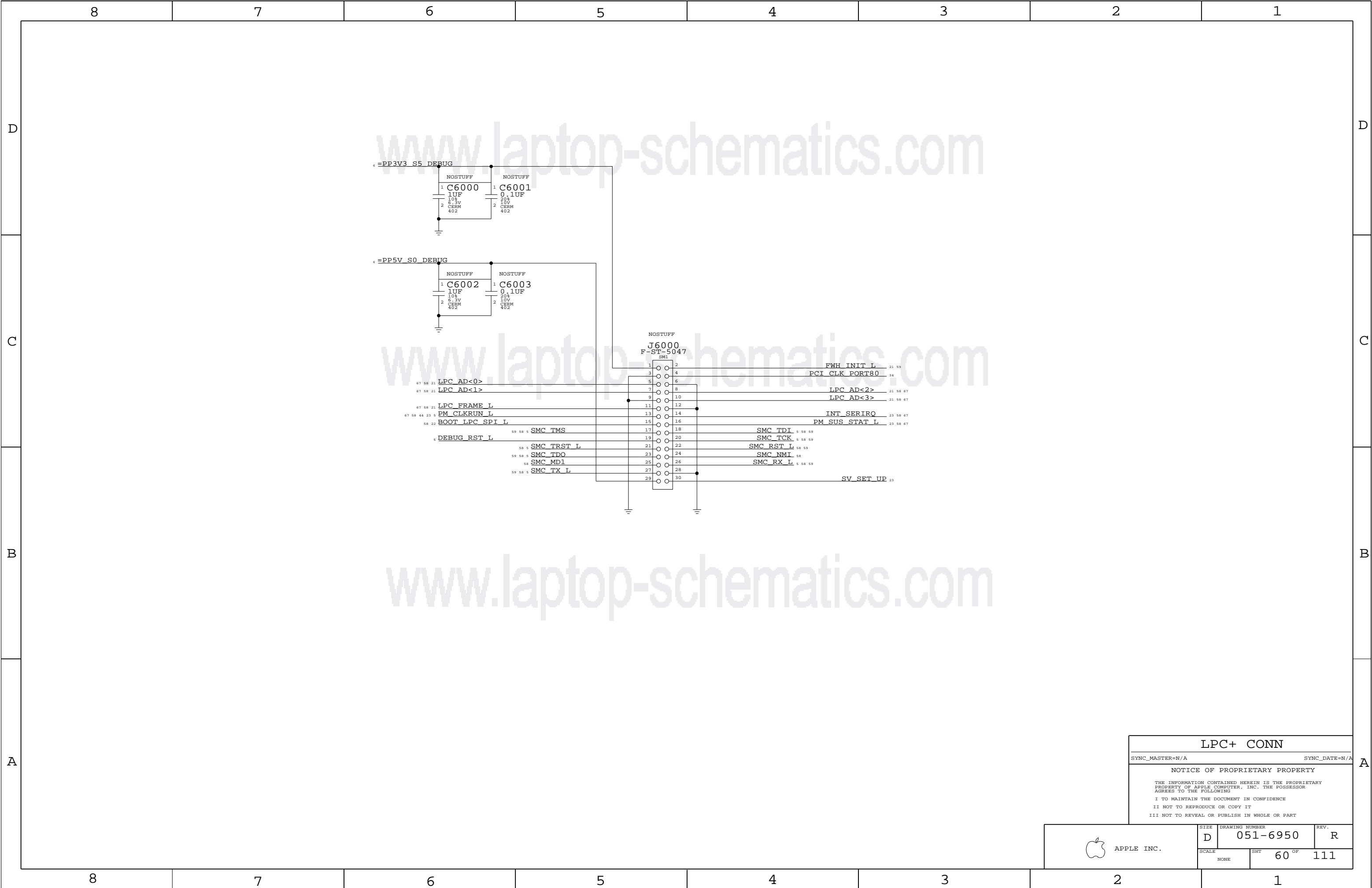
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SCALE NONE SHT 53 OF 111





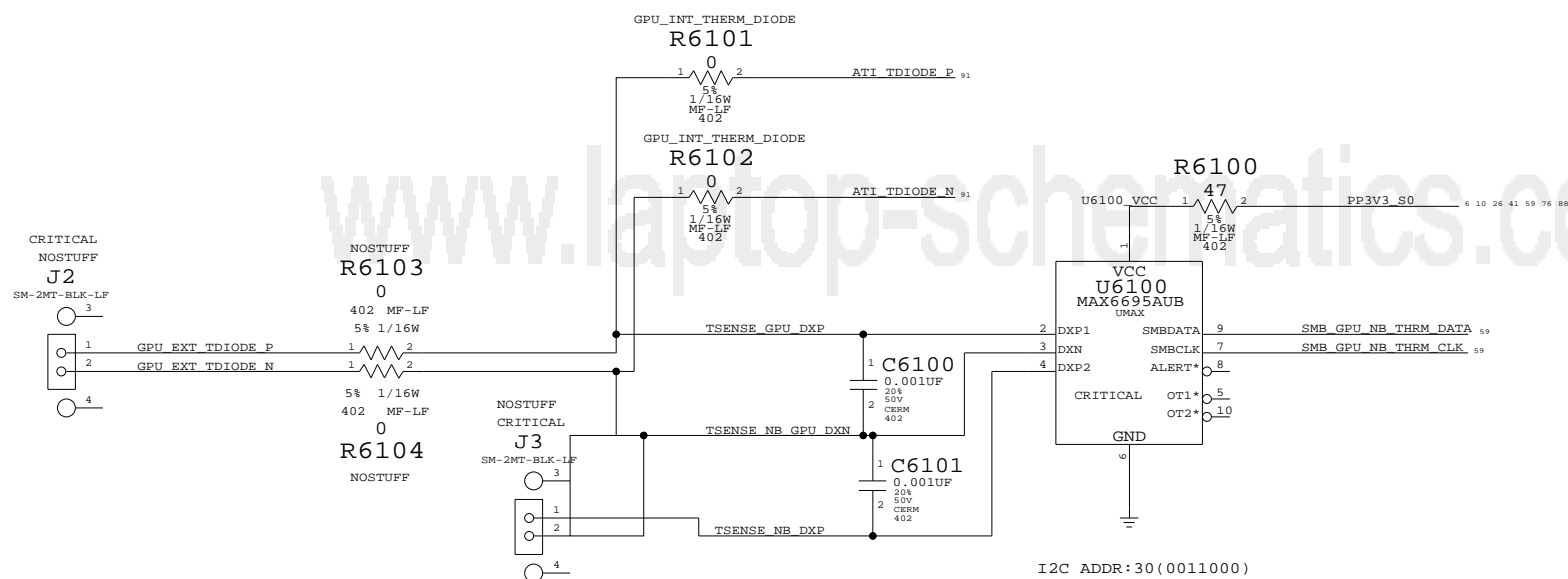




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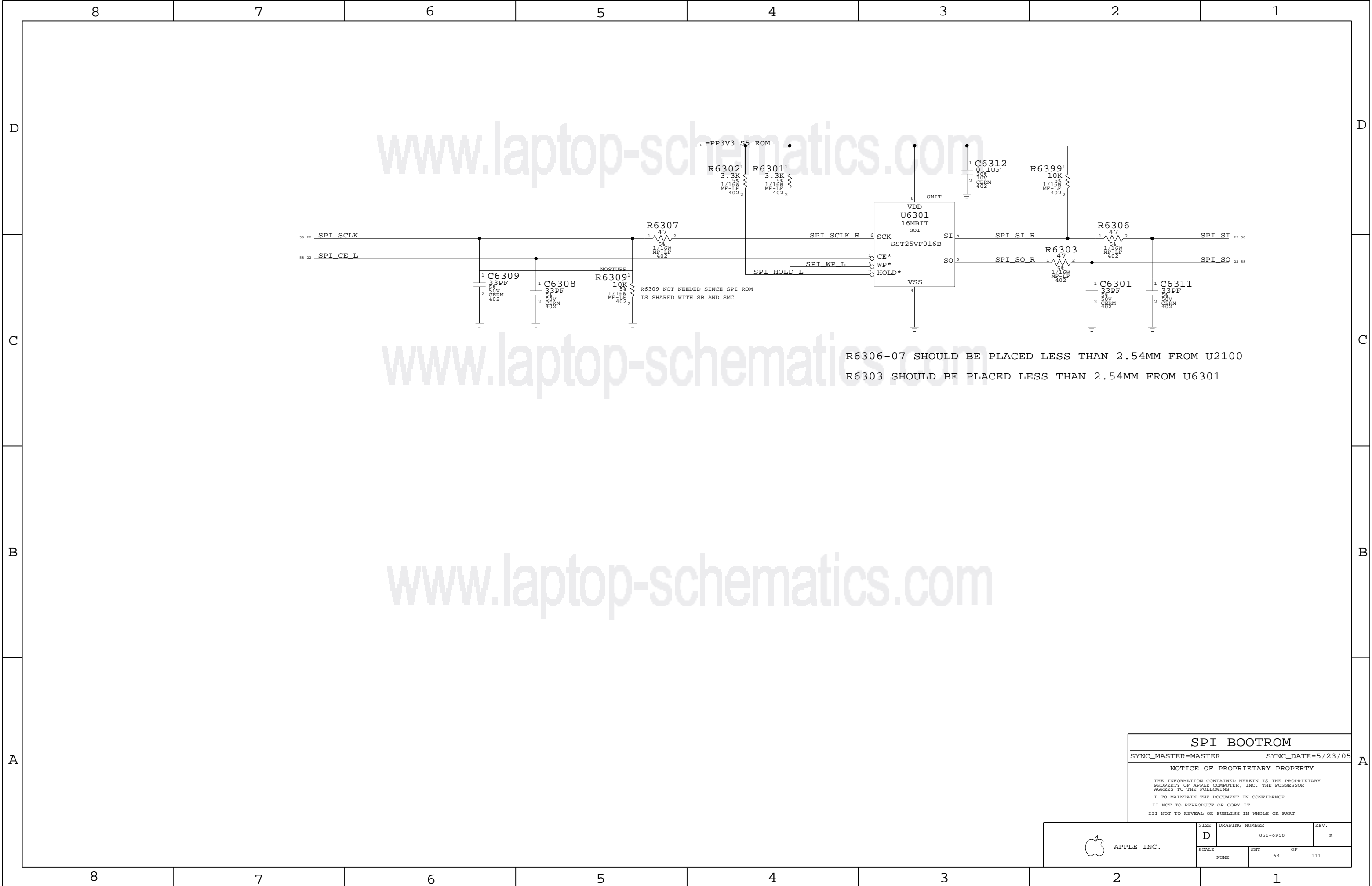
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

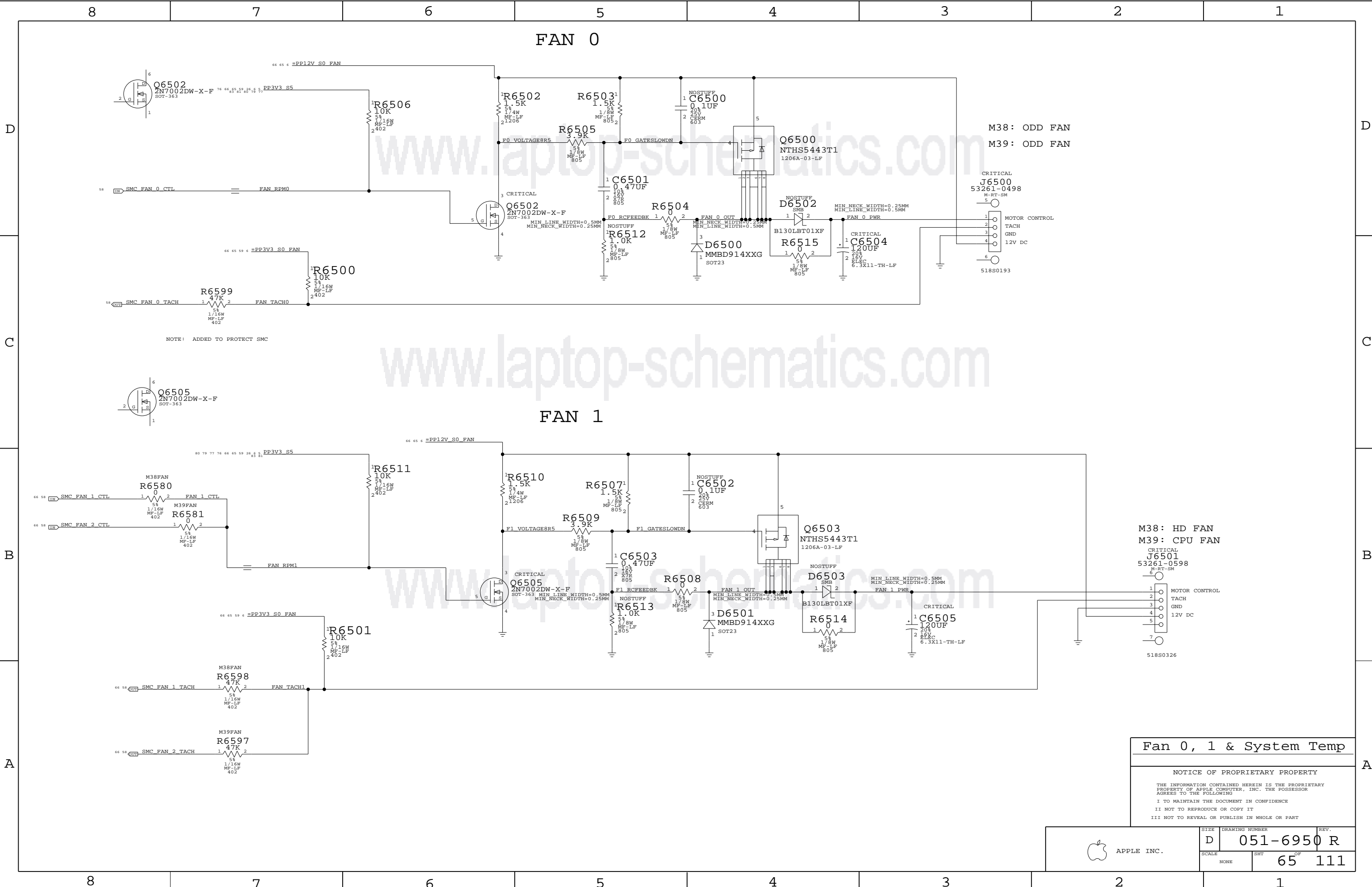


APPLE INC.

SIZE D DRAWING NUMBER 051-6950 REV. R

SCALE NONE SHT 61 OF 111

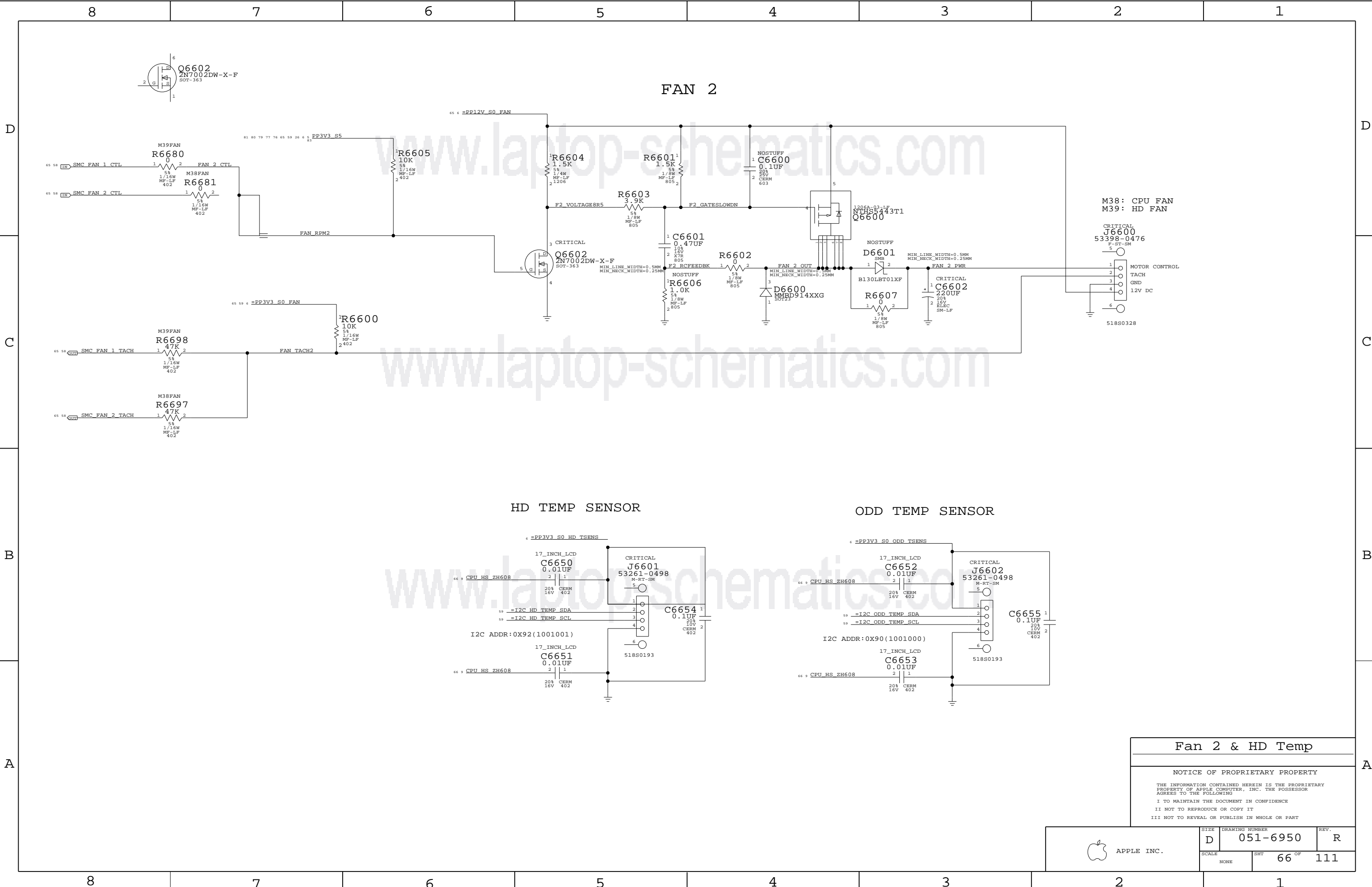




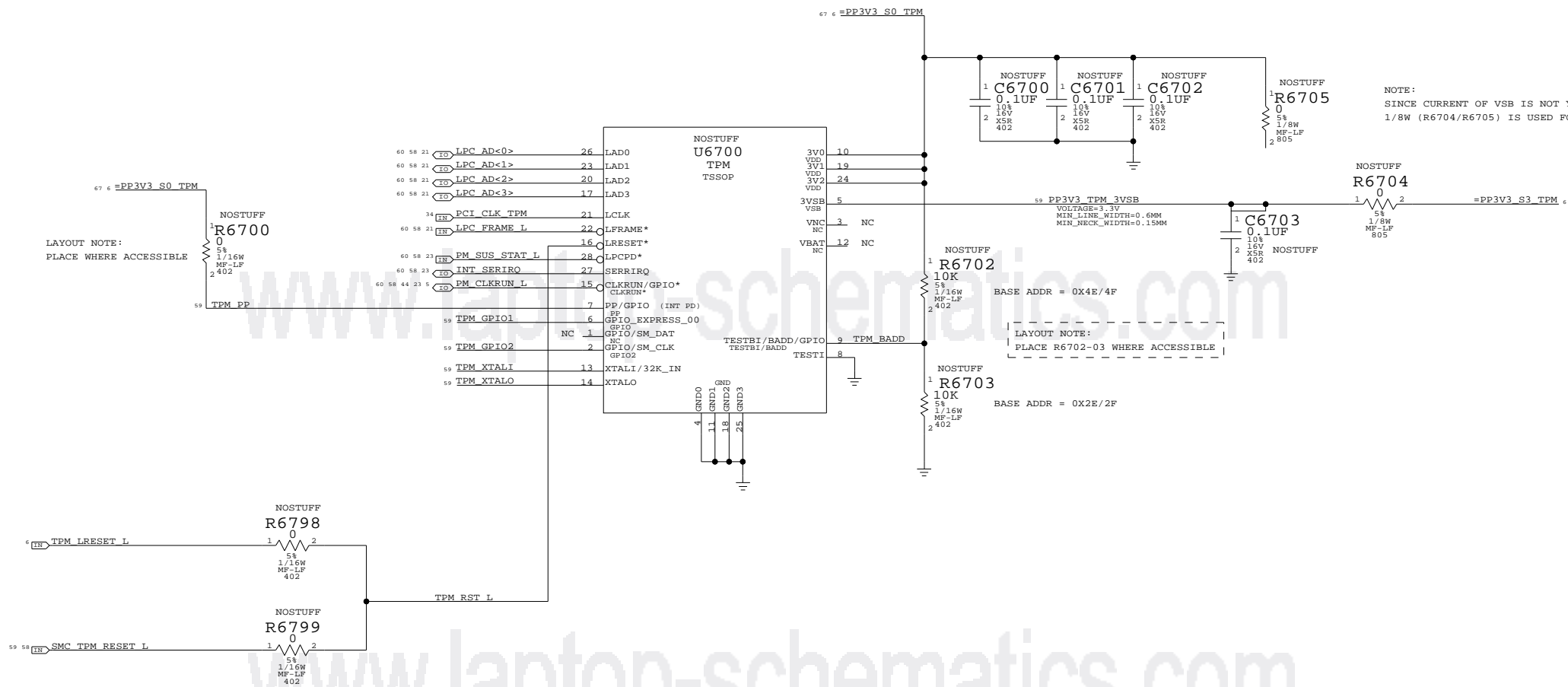
Fan 0, 1 & System Temp

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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE	SHT		REV.
	NONE		65 111



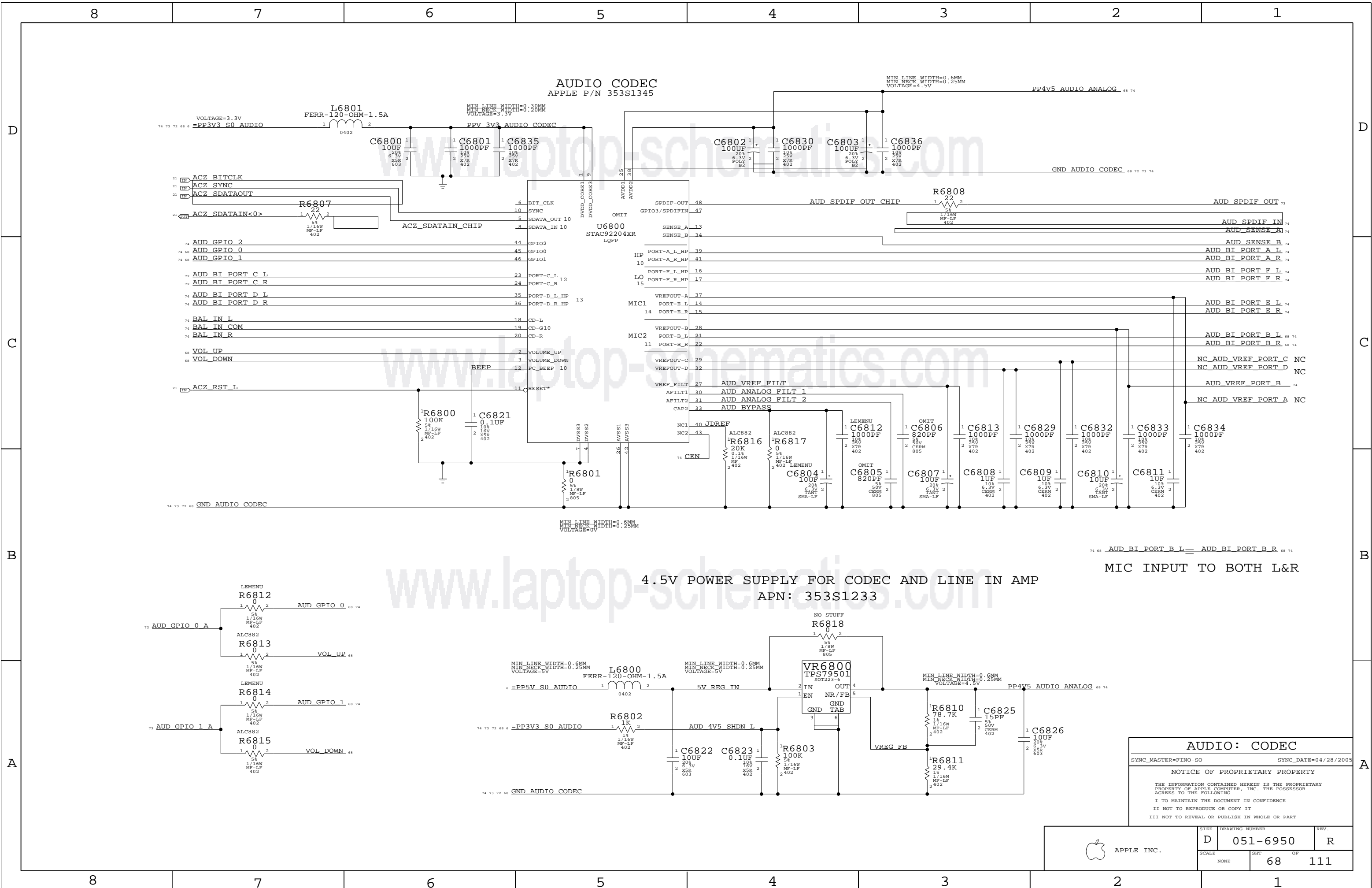
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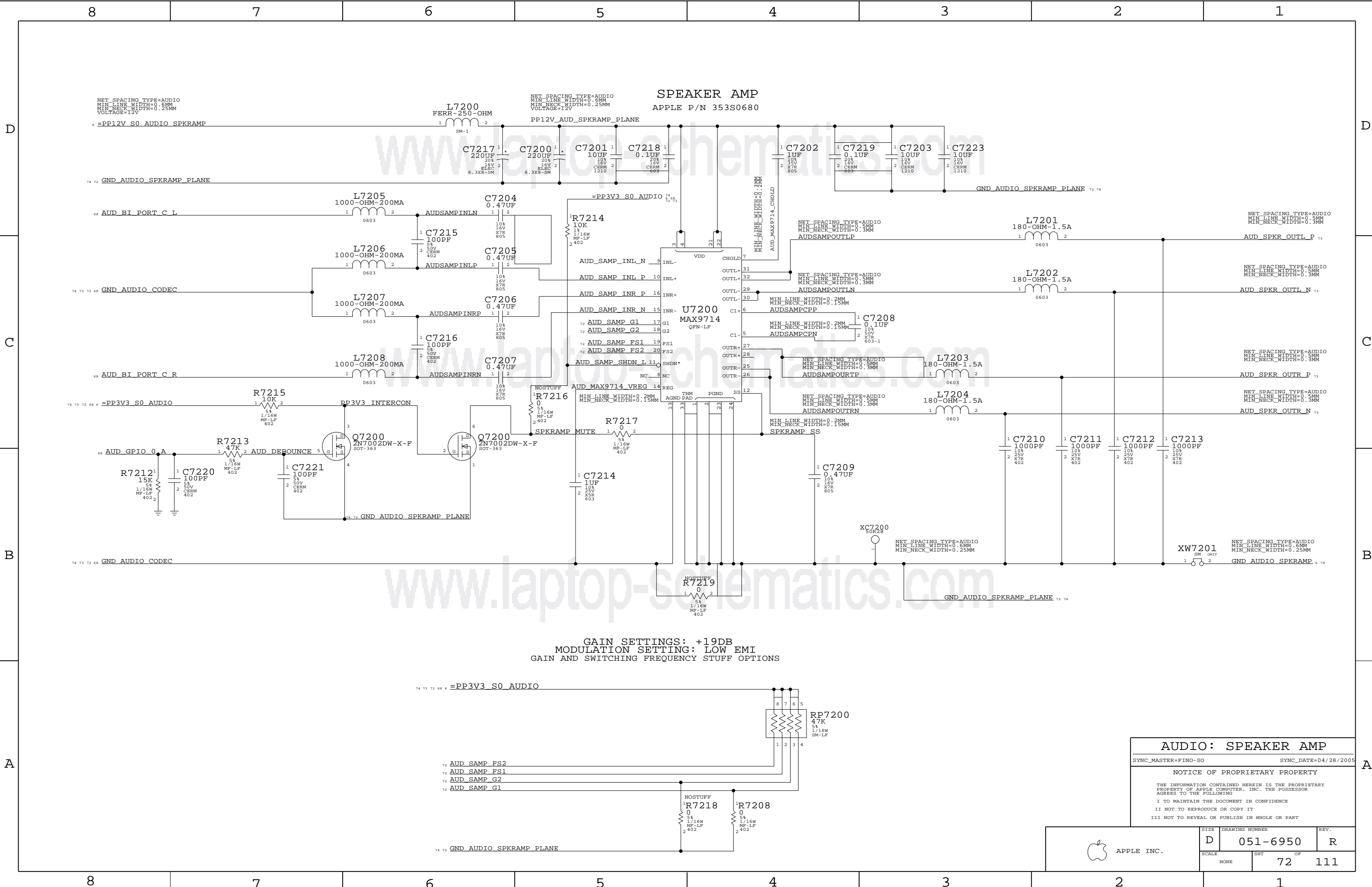


NOTE:  
SINCE CURRENT OF VSB IS NOT YET ON SPEC,  
1/8W (R6704/R6705) IS USED FOR NOW

TPM	
SYNC_MASTER=N/A	SYNC_DATE=N/A
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APPLE INC.	SIZE D	DRAWING NUMBER 051-6950	REV. R
	SCALE NONE	SHT 67 OF 111	

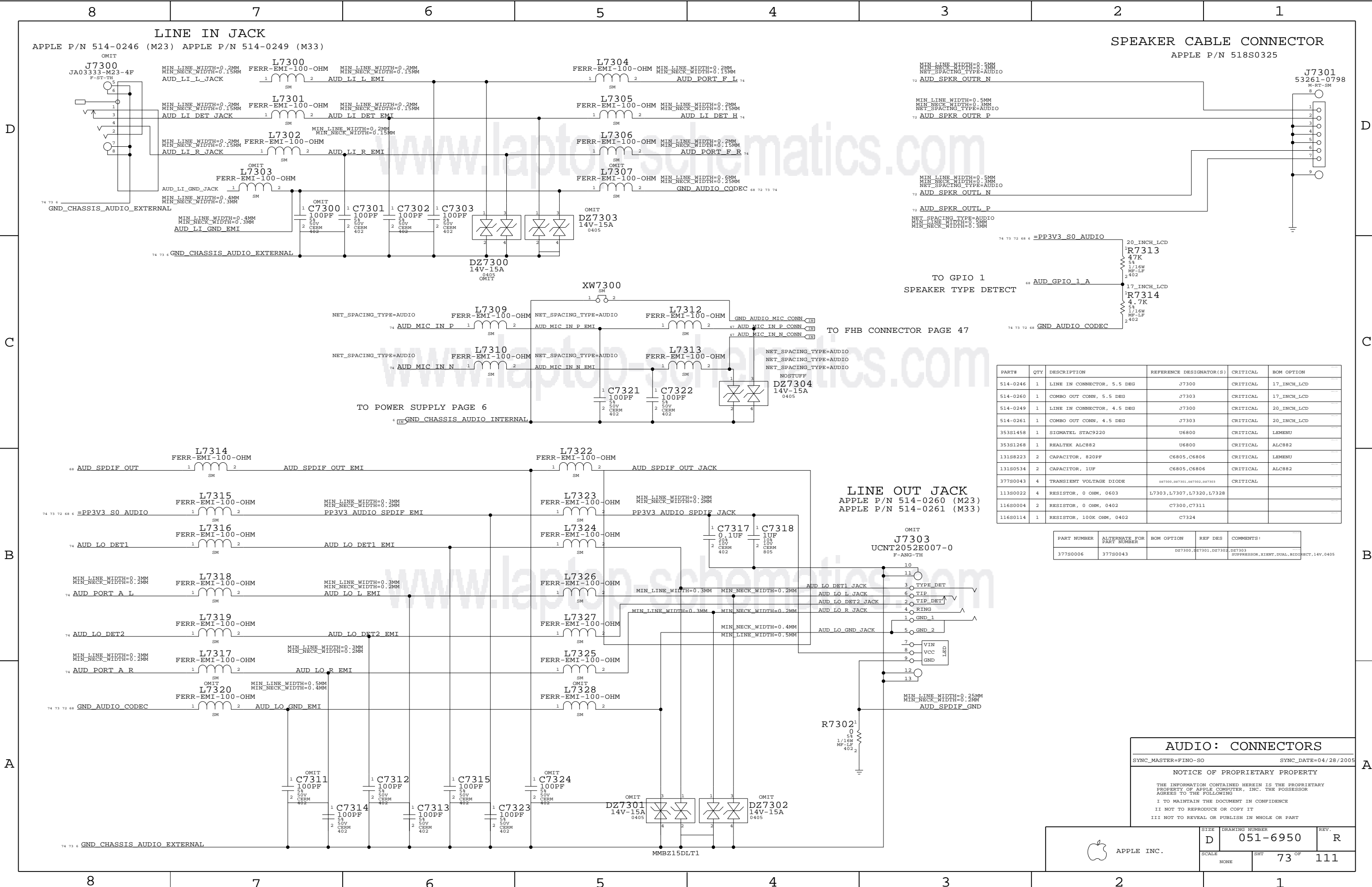




GAIN SETTINGS: +19DB  
MODULATION SETTING: LOW EMI  
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

AUDIO: SPEAKER AMP  
SYNC\_MASTER=FINO-SO SYNC\_DATE=04/28/2005  
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE	SHT OF		
	NONE	72	111



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0246	1	LINE IN CONNECTOR, 5.5 DEG	J7300	CRITICAL	17_INCH_LCD
514-0260	1	COMBO OUT CONN, 5.5 DEG	J7303	CRITICAL	17_INCH_LCD
514-0249	1	LINE IN CONNECTOR, 4.5 DEG	J7300	CRITICAL	20_INCH_LCD
514-0261	1	COMBO OUT CONN, 4.5 DEG	J7303	CRITICAL	20_INCH_LCD
353S1458	1	SIGMATEL STAC9220	U6800	CRITICAL	LEMENU
353S1268	1	REALTEK ALC882	U6800	CRITICAL	ALC882
131S8223	2	CAPACITOR, 820PF	C6805,C6806	CRITICAL	LEMENU
131S0534	2	CAPACITOR, 1UF	C6805,C6806	CRITICAL	ALC882
377S0043	4	TRANSIENT VOLTAGE DIODE	DZ7300,DZ7301,DZ7302,DZ7303	CRITICAL	
113S0022	4	RESISTOR, 0 OHM, 0603	L7303,L7307,L7320,L7328		
116S0004	2	RESISTOR, 0 OHM, 0402	C7300,C7311		
116S0114	1	RESISTOR, 100K OHM, 0402	C7324		

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S0006	377S0043		DZ7300,DZ7301,DZ7302,DZ7303	SUPPRESSOR,XIENT,DUAL,BIDIRECT,14V,0405

**AUDIO: CONNECTORS**

SYNC\_MASTER=FINO-SO SYNC\_DATE=04/28/2005

**NOTICE OF PROPRIETARY PROPERTY**

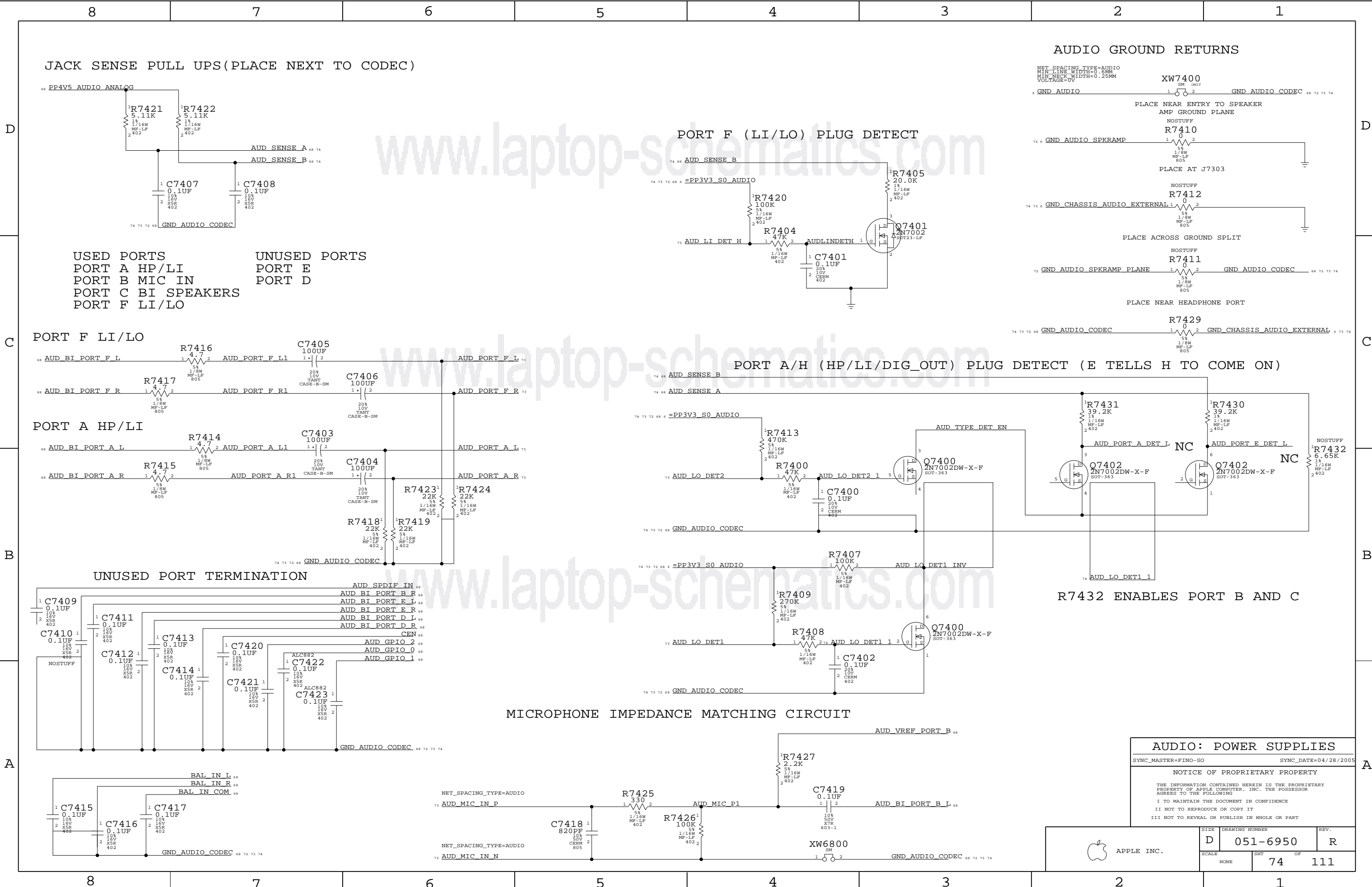
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE		SHT	73 OF 111
NONE			



JACK SENSE PULL UPS(PLACE NEXT TO CODEC)

AUDIO GROUND RETURNS

- USED PORTS
- PORT A HP/LI
- PORT B MIC IN
- PORT C BI SPEAKERS
- PORT F LI/LO
- UNUSED PORTS
- PORT E
- PORT D

PORT F (LI/LO) PLUG DETECT

PORT A/H (HP/LI/DIG\_OUT) PLUG DETECT (E TELLS H TO COME ON)

PORT F LI/LO

PORT A HP/LI

UNUSED PORT TERMINATION

MICROPHONE IMPEDANCE MATCHING CIRCUIT

AUDIO: POWER SUPPLIES

SYNC\_MASTER=FINO-SO SYNC\_DATE=04/28/2005

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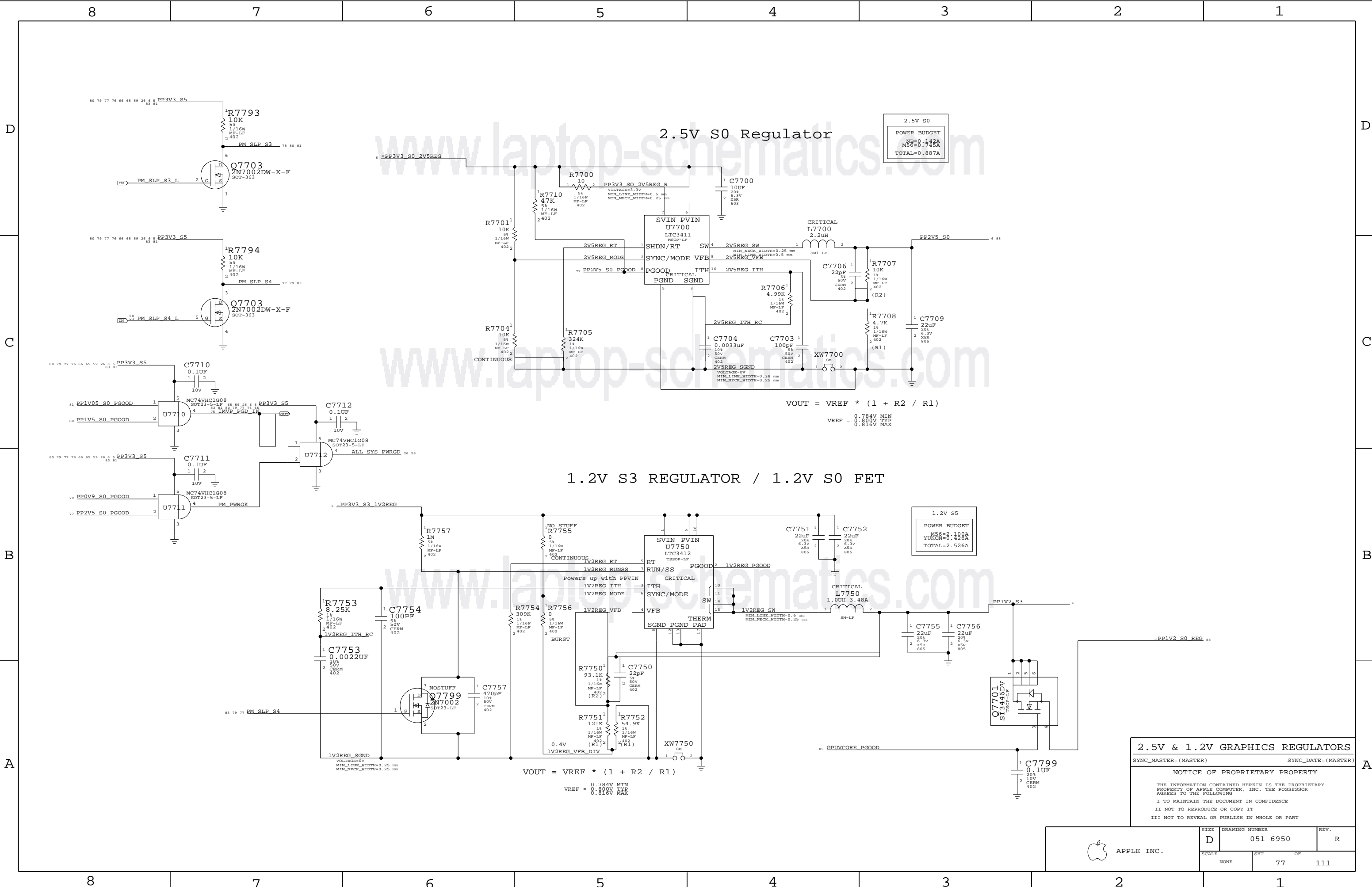
SIZE	DRAWING NUMBER	REV.
D	051-6950	R
SCALE	SHT	OF
NONE	74	111



		MIN_LINE_WIDTH	MIN_NECK_WIDTH
75	IMVP6_OCSET	0.25 MM	0.20 MM
75	IMVP6_VSUM	0.25 MM	0.20 MM
75	GND_IMVP6_SGND	0.50 MM	0.20 MM
75	IMVP6_VO	0.25 MM	0.20 MM
76	IMVP6_DROOP	0.25 MM	0.20 MM
75	IMVP6_DFB	0.25 MM	0.20 MM
75	IMVP6_SOFT	0.25 MM	0.20 MM
75	IMVP6_RBIAIS	0.25 MM	0.20 MM
75	IMVP6_VDIFF	0.25 MM	0.20 MM
75	IMVP6_FB2	0.25 MM	0.20 MM
75	IMVP6_FB	0.25 MM	0.20 MM
75	IMVP6_COMP	0.25 MM	0.20 MM
75	IMVP6_VW	0.25 MM	0.25 MM
75	IMVP6_RTN	0.25 MM	0.25 MM
75	IMVP6_VSEN	0.25 MM	0.25 MM

 APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
	SCALE	SHT	OF
	NONE	75	111





2.5V S0 Regulator

2.5V S0  
POWER BUDGET  
NB=0.142A  
MB=0.745A  
TOTAL=0.887A

1.2V S3 REGULATOR / 1.2V S0 FET

1.2V S5  
POWER BUDGET  
NB=2.108A  
YUKS=0.408A  
TOTAL=2.526A

2.5V & 1.2V GRAPHICS REGULATORS

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

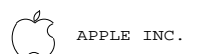
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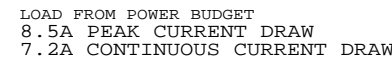


APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6950	R
SCALE	SHT	OF
NONE	77	111

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Schematic diagram of the power supply section of the NTD60N02R MOSFET driver. The circuit includes a 12V input, a 100k pull-up resistor (R7840), a 100nF gate-source capacitor (C7804), a 0.1uF gate-drain capacitor (C7805), a 680uF output capacitor (C7801), and a 100uF output capacitor (C7800). The MOSFET (Q7800) is driven by the gate voltage and switches the load. The output is connected to the load and ground.



$$V_{OUT} = V_{REF} * (1 + R2/R1)$$

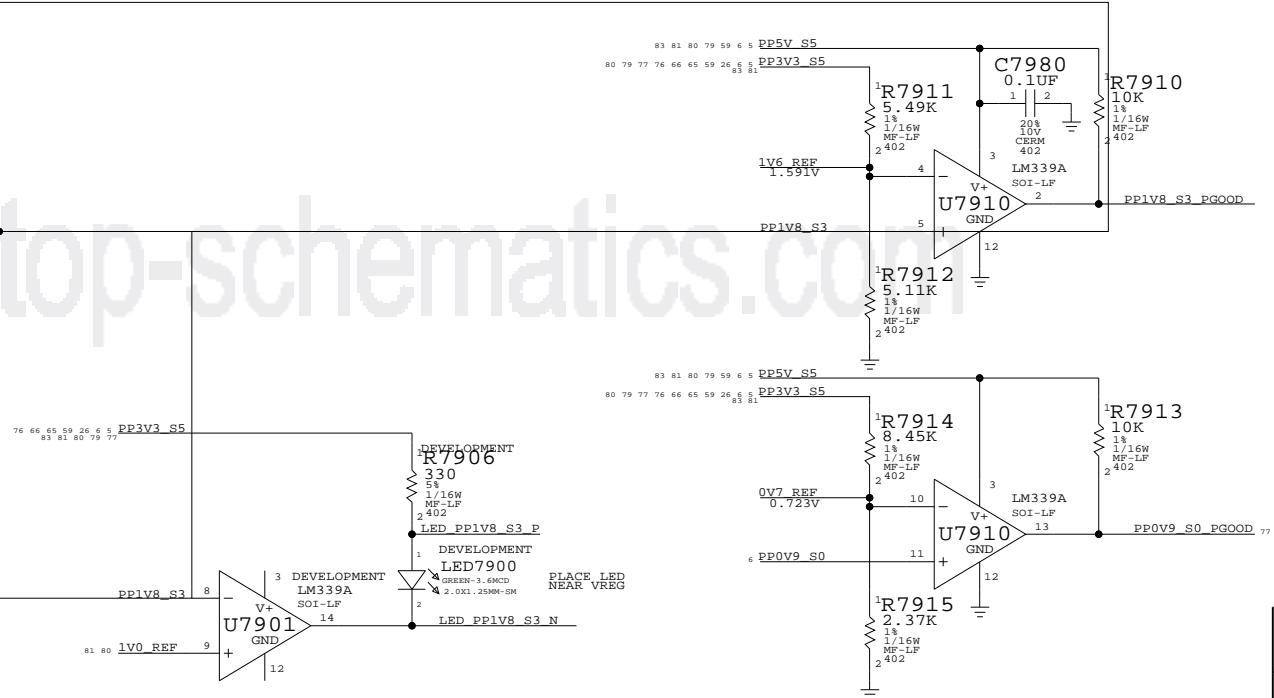
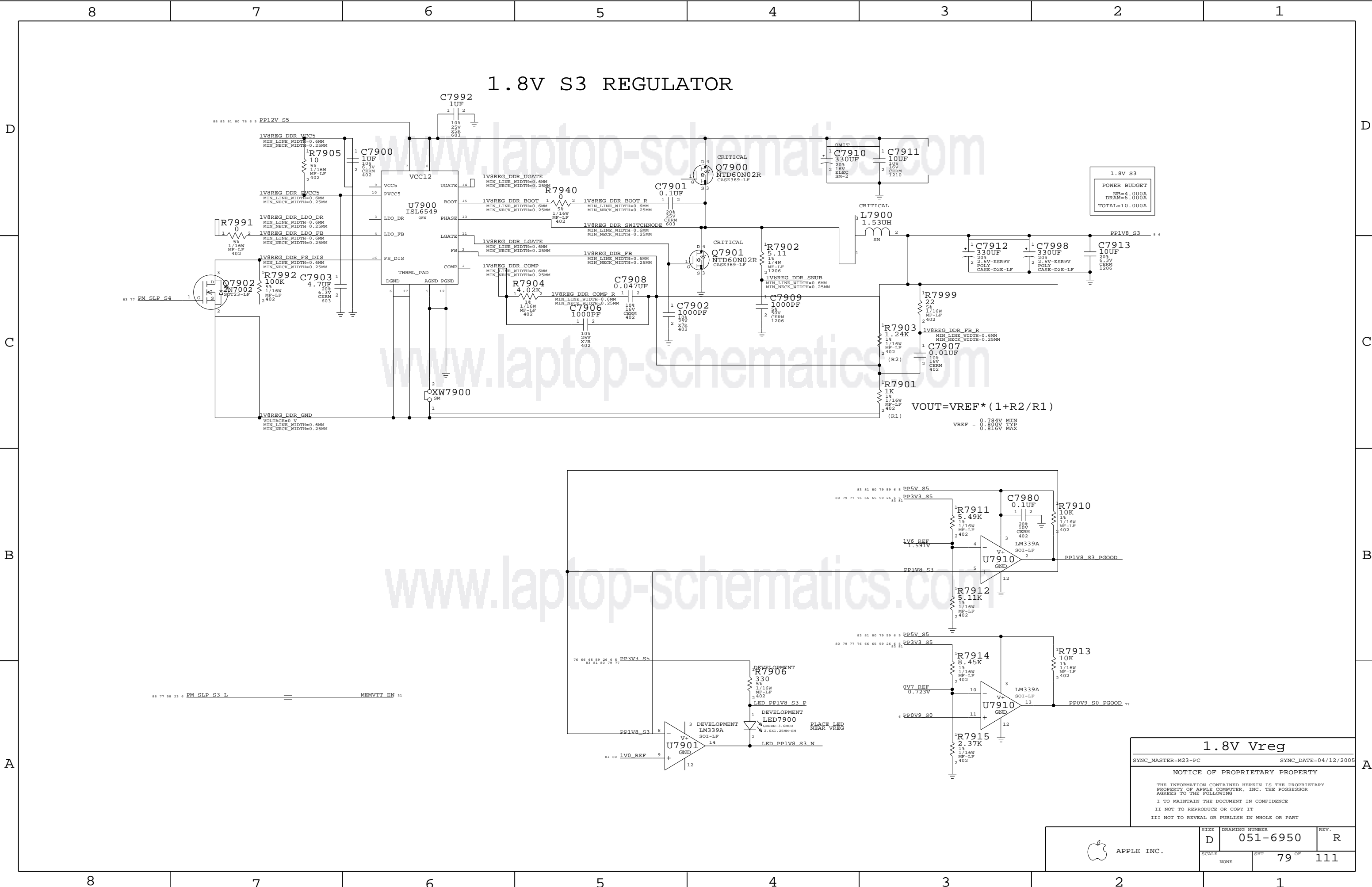
VREF = 0.784V MIN  
0.800V TYP  
0.816V MAX

SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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TRUE	
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1.8V Vreg

SYNC\_MASTER=M23-PC SYNC\_DATE=04/12/2005

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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE		SHT	79 OF 111
NONE			

D

C

B

A

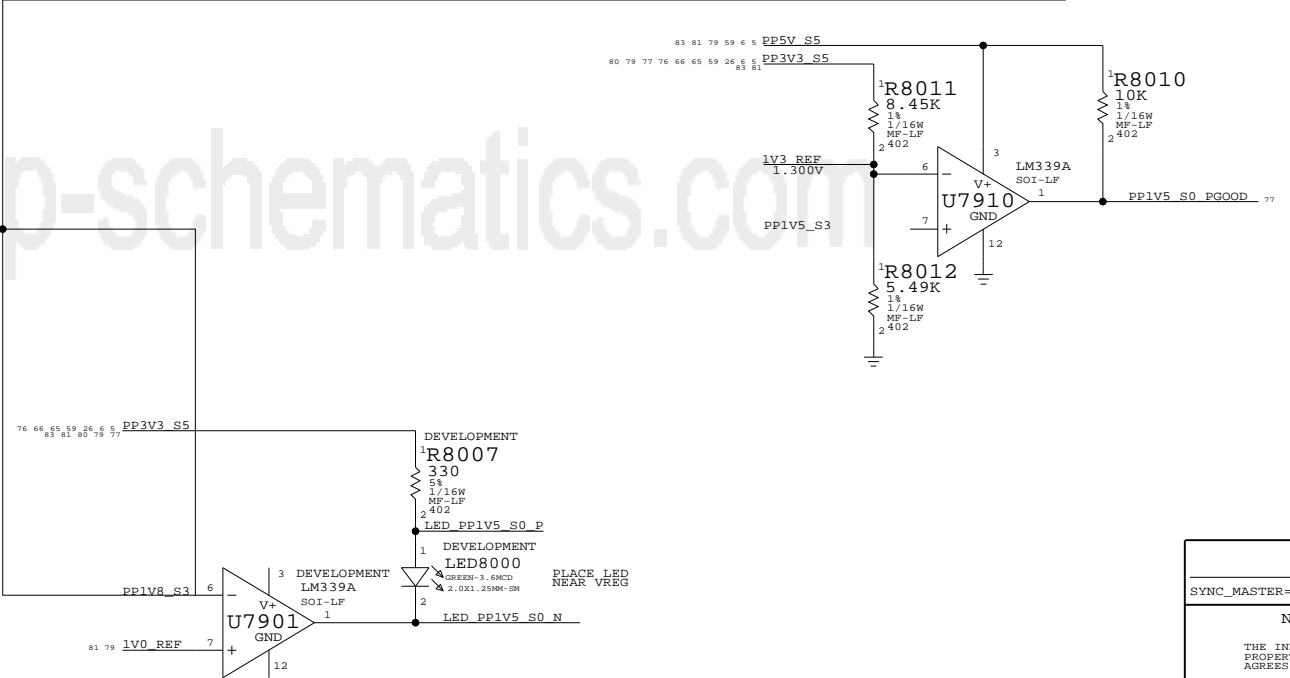
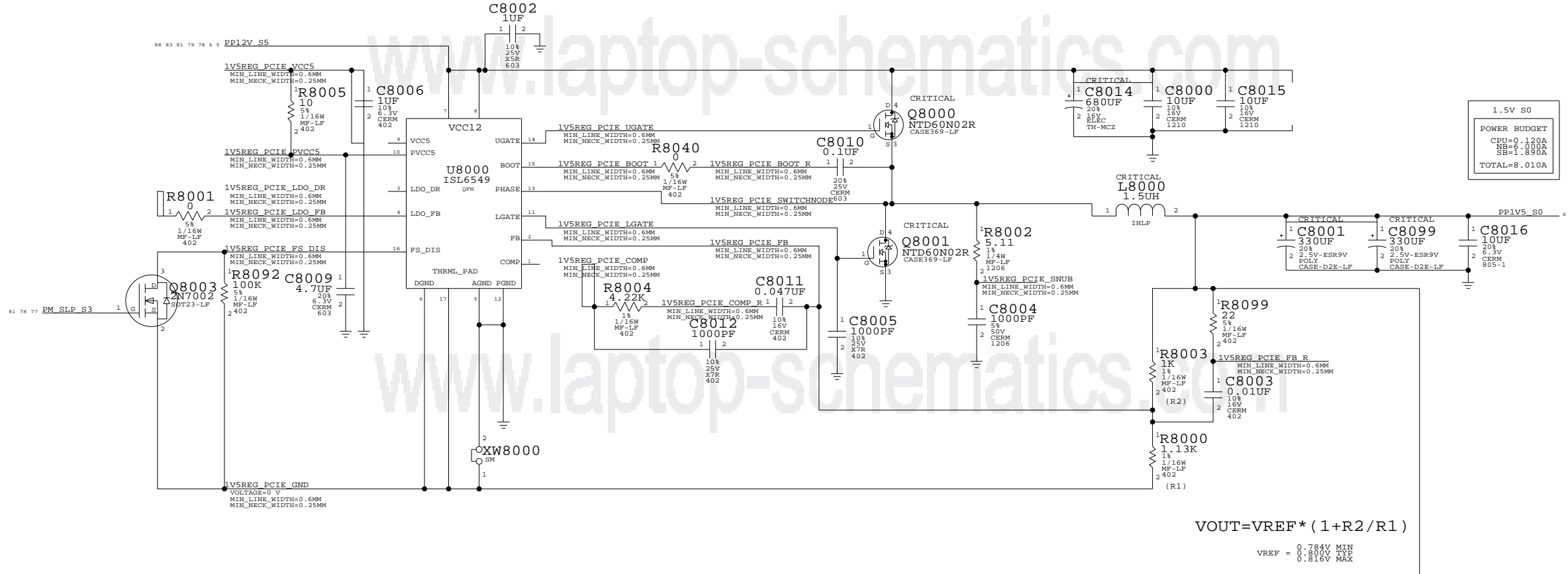
D

C

B

A

1.5V S0 REGULATOR



1.5V Vreg

SYNC\_MASTER=FINO-PC

SYNC\_DATE=05/18/2005

NOTICE OF PROPRIETARY PROPERTY

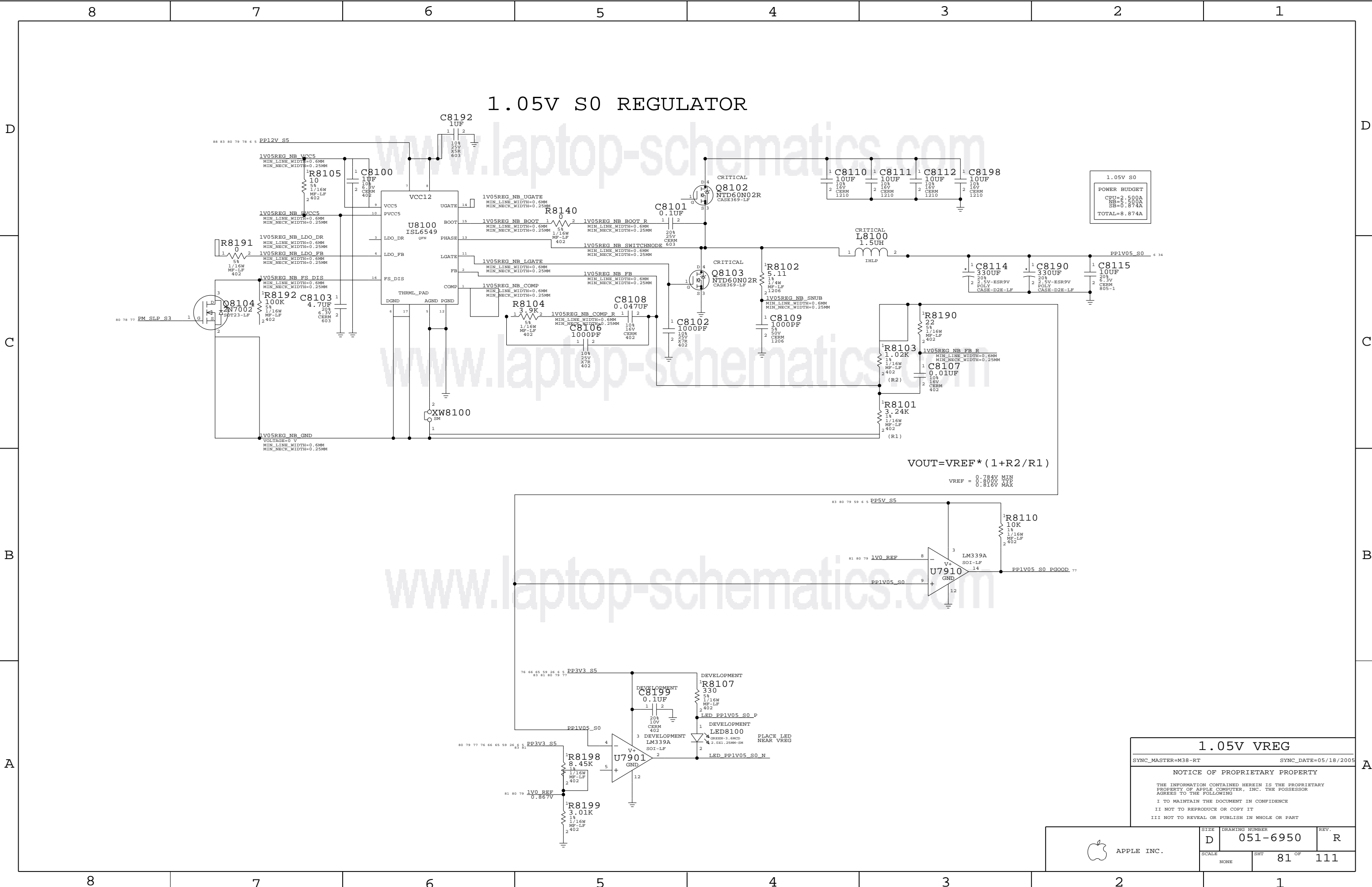
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE		SHT	80 OF 111
NONE			



1.05V S0 REGULATOR

1.05V S0
POWER BUDGET
CPU=2.500A
NB=5.500A
SB=0.874A
TOTAL=8.874A

$V_{OUT} = V_{REF} * (1 + R2/R1)$

$V_{REF} = 0.784V \text{ MIN}$   
 $0.800V \text{ TYP}$   
 $0.816V \text{ MAX}$

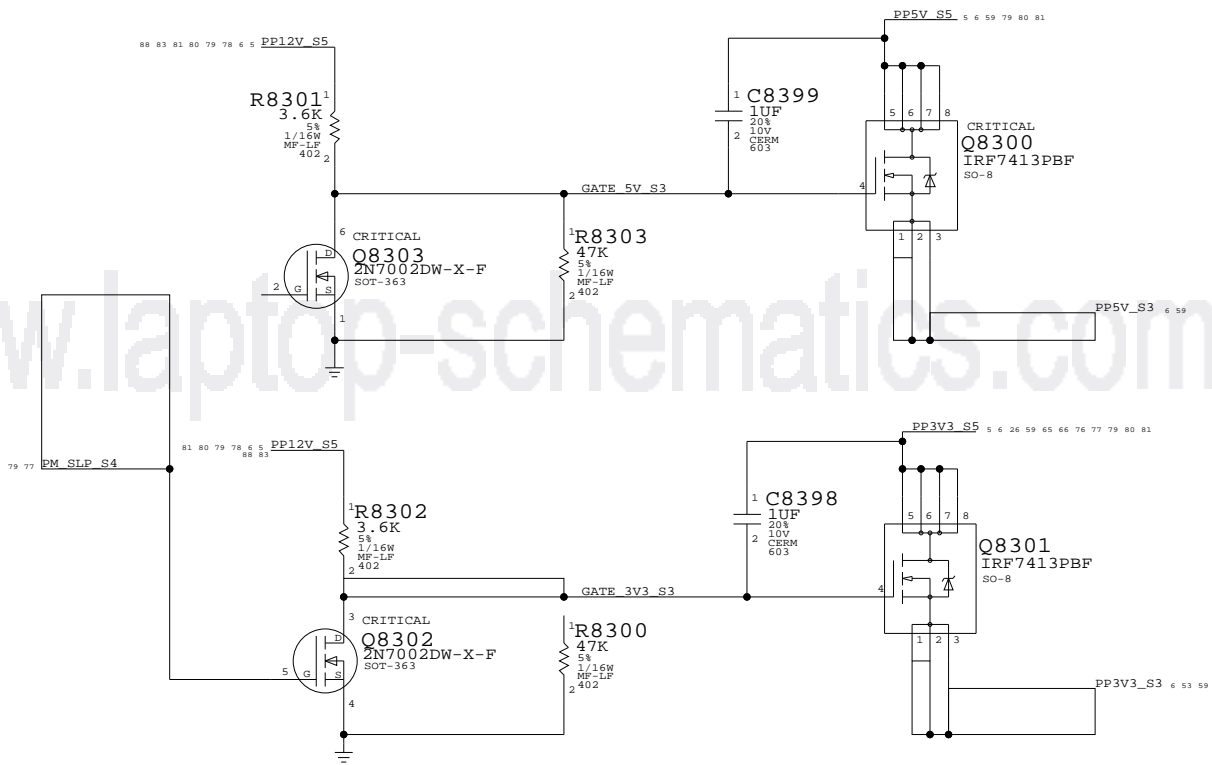
1.05V VREG	
SYNC_MASTER=M38-RT	SYNC_DATE=05/18/2005
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APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
SCALE		SHT	81 OF 111
NONE			

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## 5V & 3.3V Fets

SYNC\_MASTER=FINO-PC SYNC\_DATE=04/12/2005

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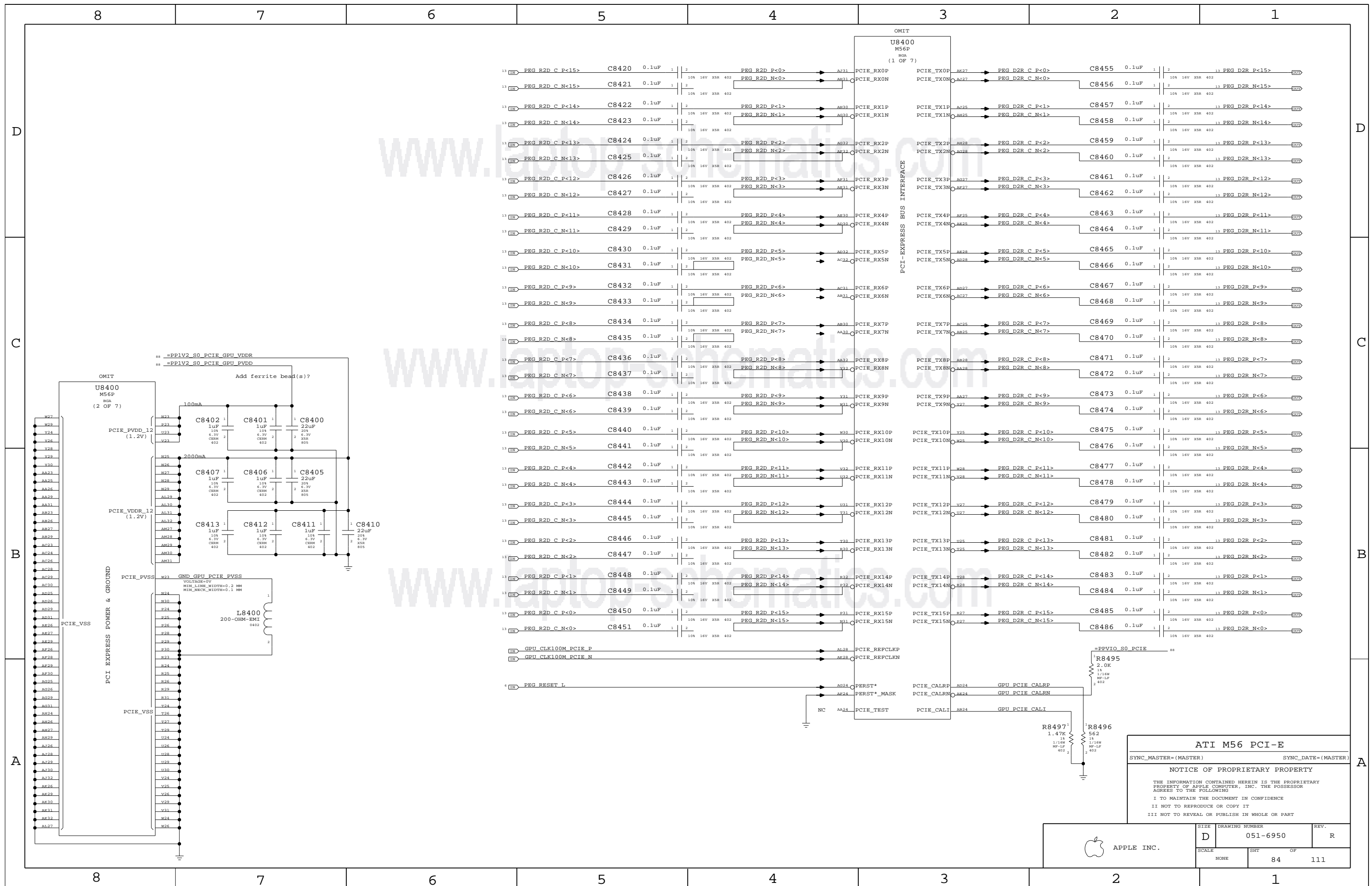
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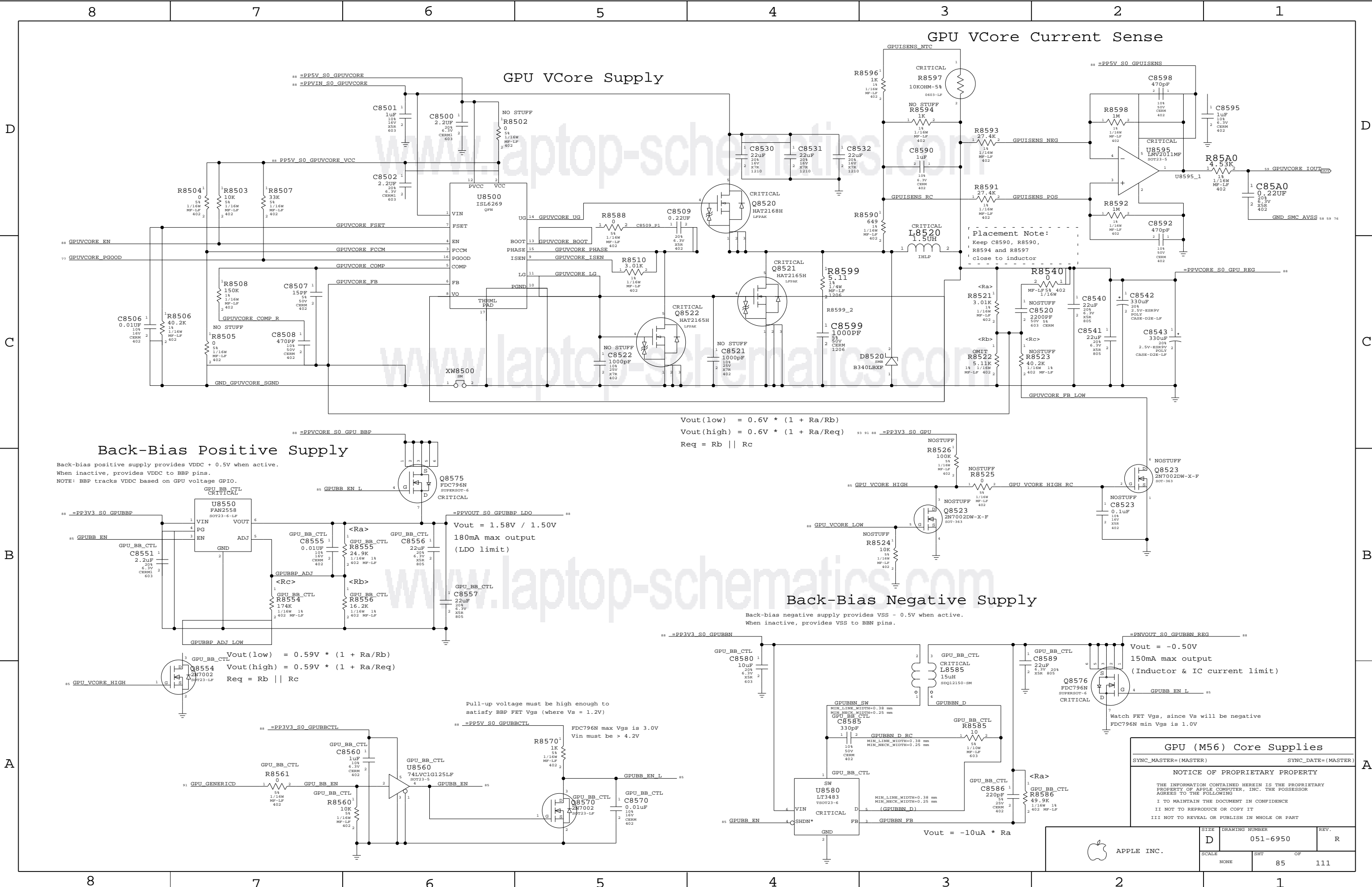
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

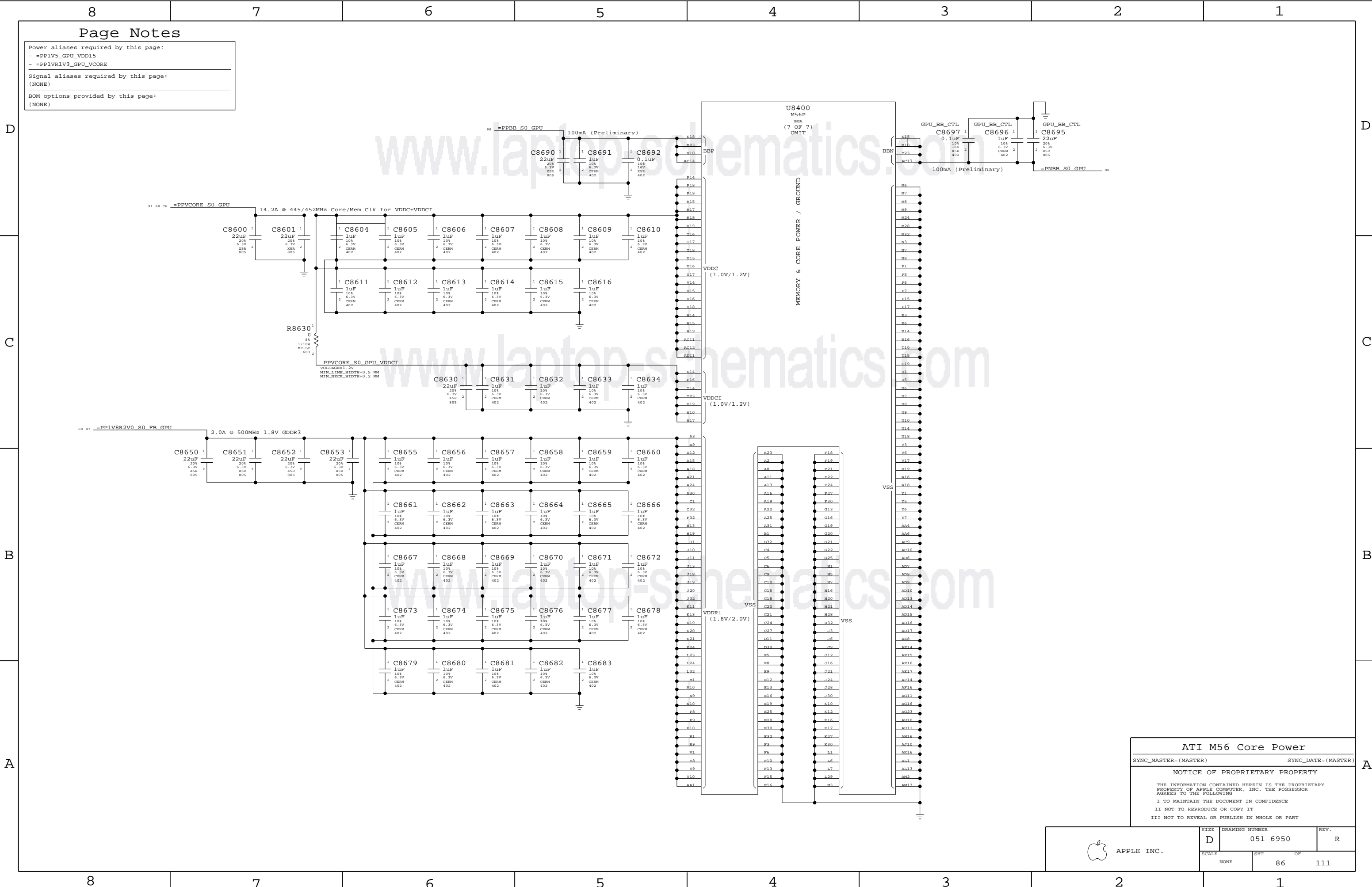


APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6950	R
SCALE	SHT	111
NONE	83 OF	







Page Notes

Power aliases required by this page:

- =PP1V5\_GPU\_VDDI5
- =PP1VR1V3\_GPU\_VCORE

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)

ATI M56 Core Power

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

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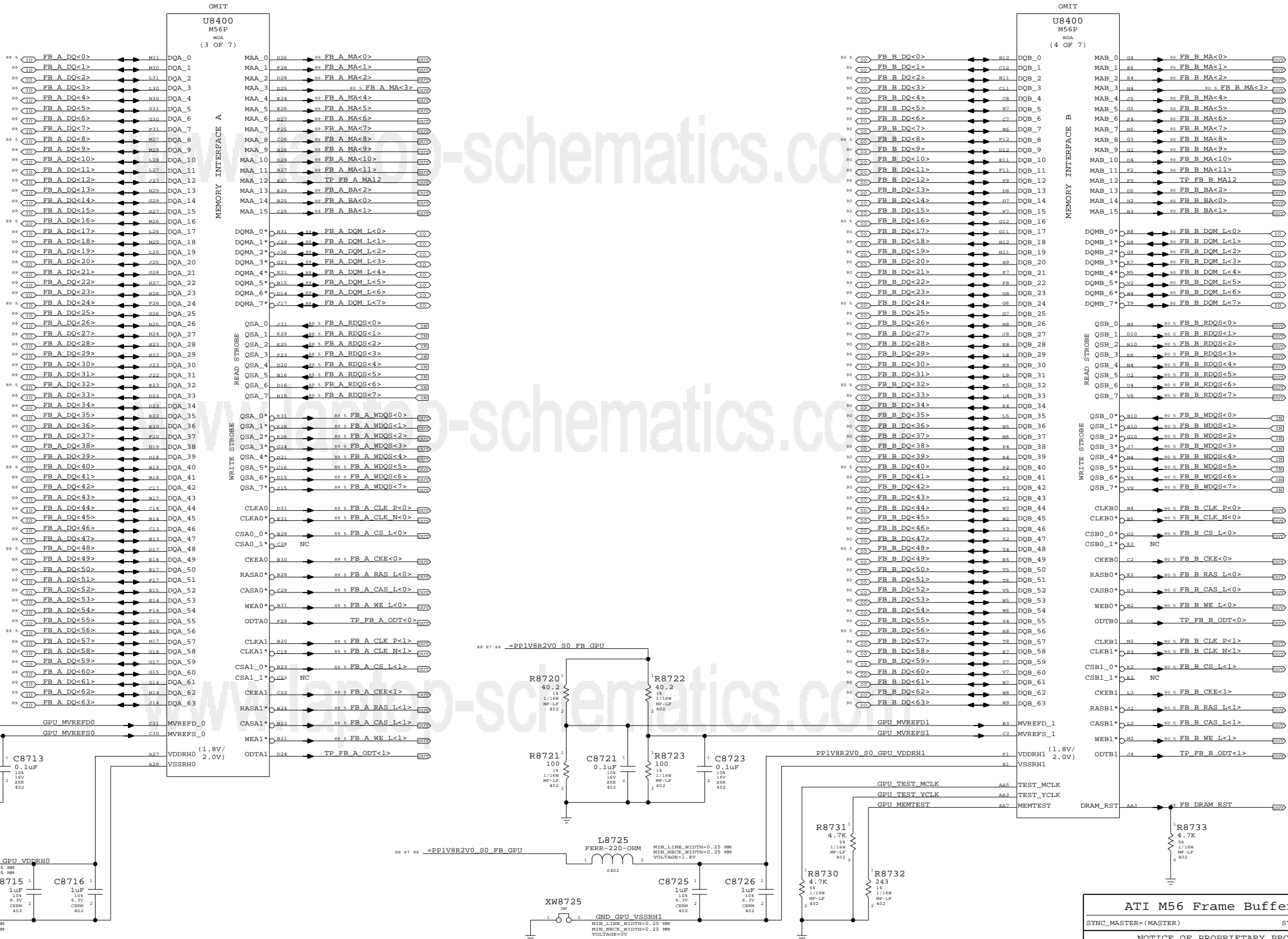
APPLE INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6950	R
	SCALE	SHT	OF
	NONE	86	111

# Page Notes

Power aliases required by this page:  
- =PP1V8R2V0\_S0\_FB\_GPU

Signal aliases required by this page:  
(NONE)

BOM options provided by this page:  
(NONE)



## ATI M56 Frame Buffer I/F

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

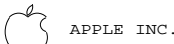
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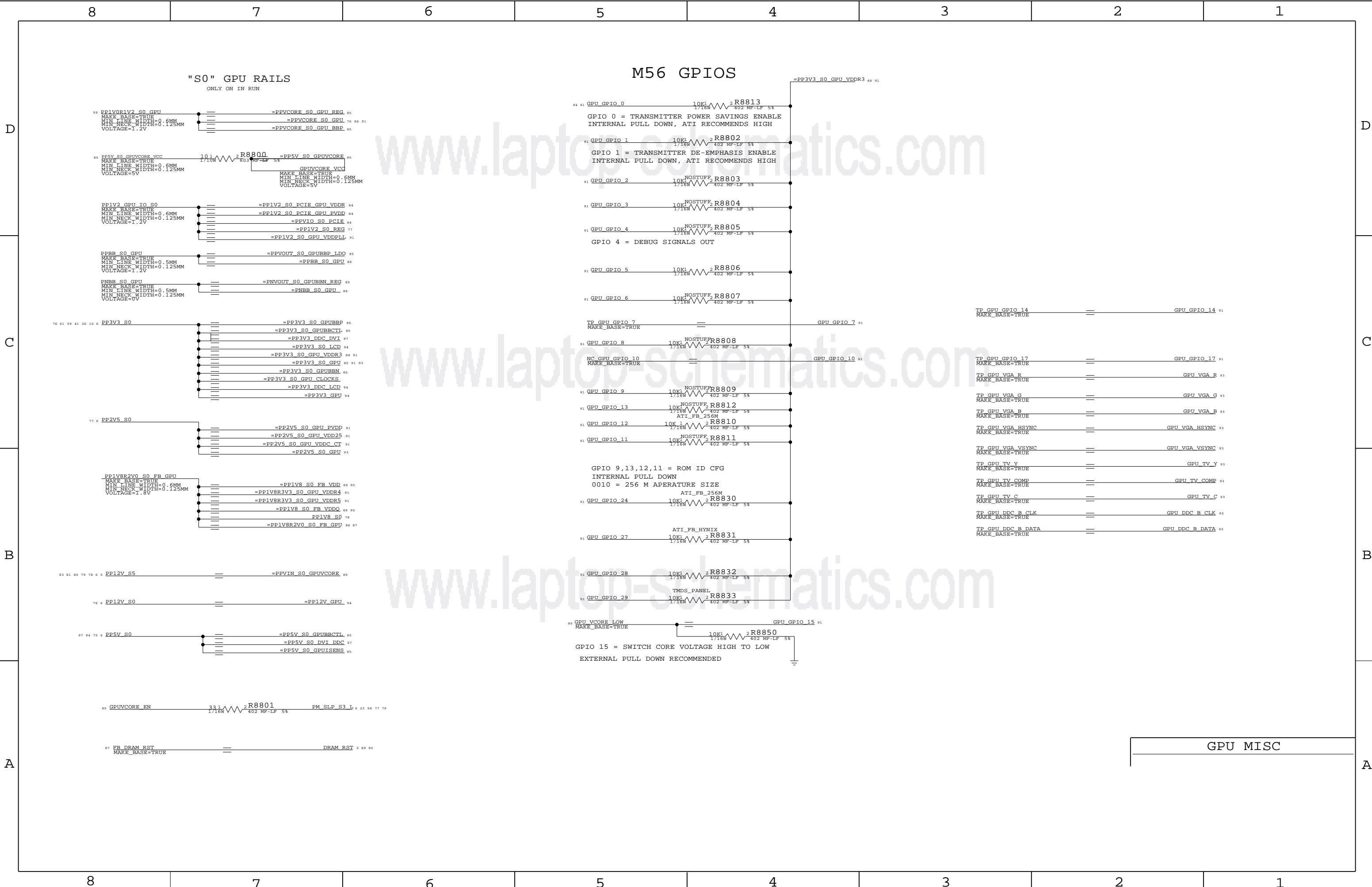
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



APPLE INC.

SIZE D DRAWING NUMBER 051-6950 REV. R

SCALE NONE SHT 87 OF 111





Power aliases required by this page:

- =PP1V8\_S0\_FB\_VDD  
- =PP1V8\_S0\_FB\_VDDQSignal aliases required by this page:  
(NONE)BOM options provided by this page:  
(NONE)

## GDDR3 Frame Buffer B

SYNC\_MASTER=(MASTER)

SYNC\_DATE=(MASTER)

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APPLE INC.

SIZE

D

DRAWING NUMBER

051-6950

REV.

R

SCALE

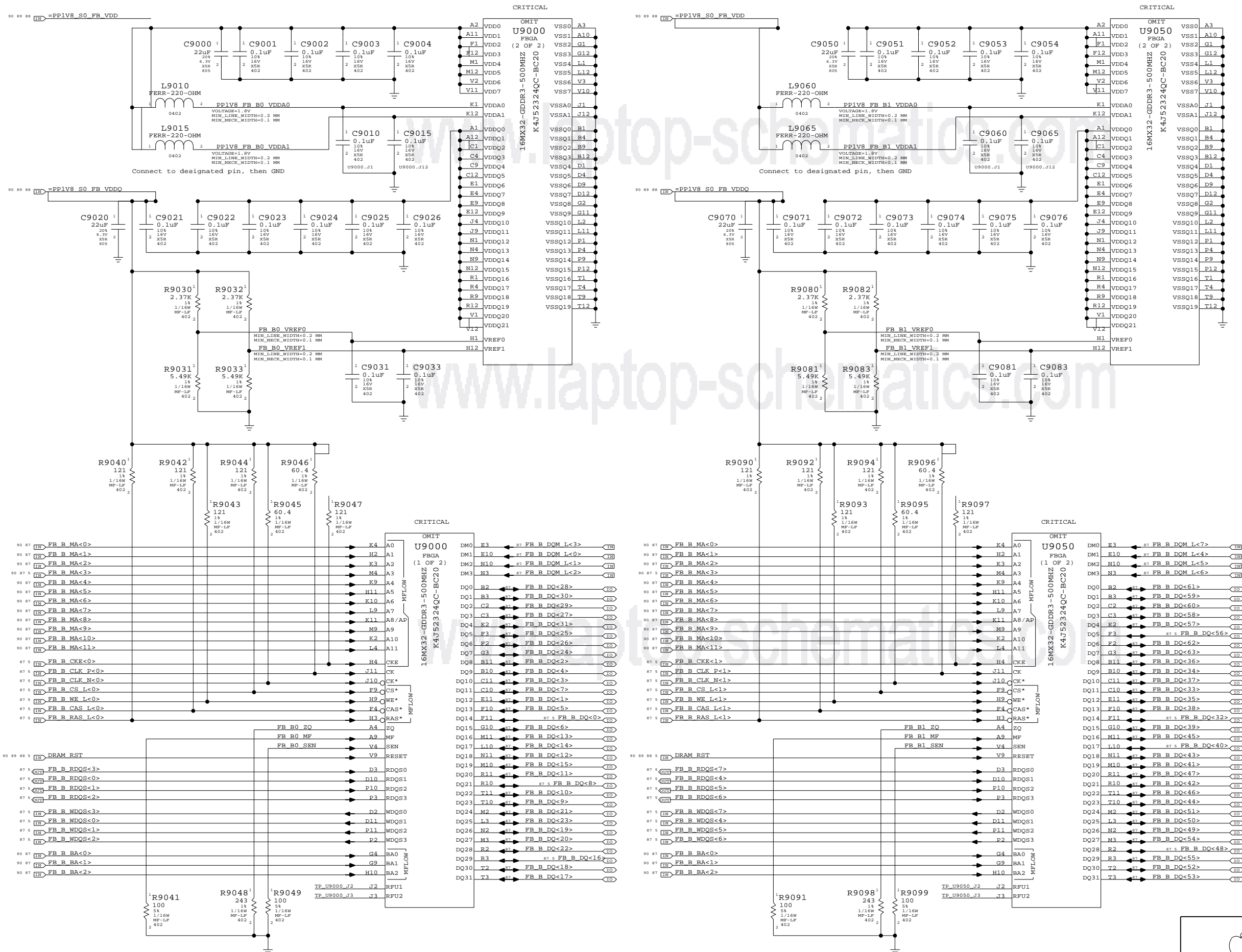
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SHT

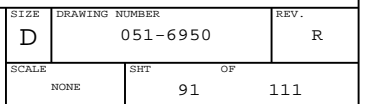
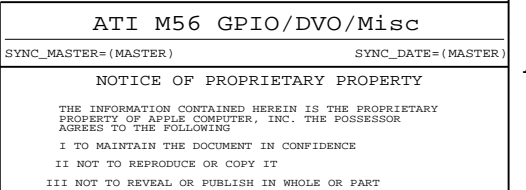
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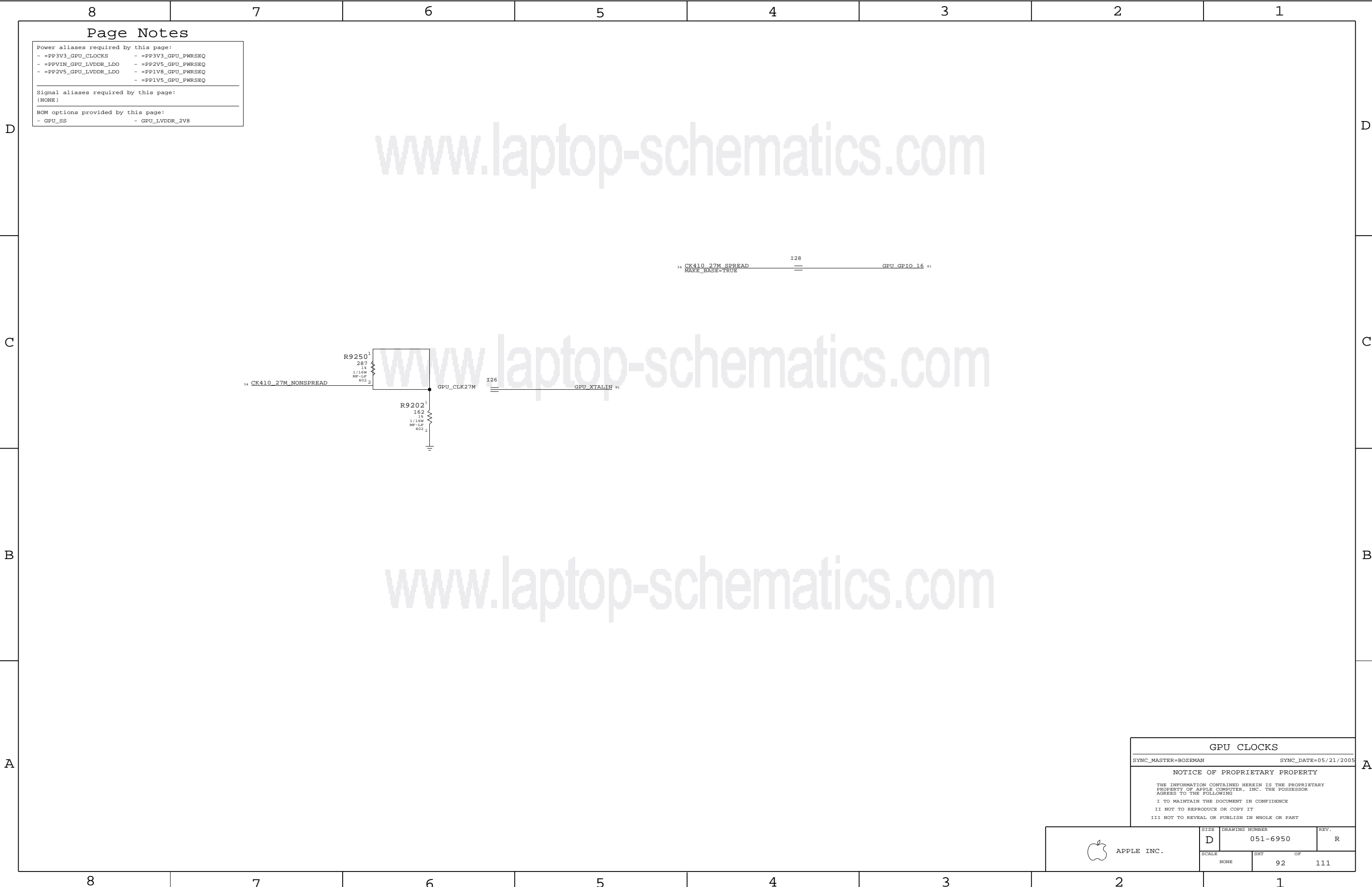
OF

111



BOM options provided by this page:  
(NONE)





Page Notes

- Power aliases required by this page:
- =PP3V3\_GPU\_CLOCKS
  - =PP3V3\_GPU\_PWRSEQ
  - =PPVIN\_GPU\_LVDDR\_LDO
  - =PP2V5\_GPU\_PWRSEQ
  - =PP2V5\_GPU\_LVDDR\_LDO
  - =PP1V8\_GPU\_PWRSEQ
  - =PP1V5\_GPU\_PWRSEQ
- Signal aliases required by this page:
- (NONE)
- BOM options provided by this page:
- GPU\_SS
  - GPU\_LVDDR\_2V8

## Page Notes

Power aliases required by this page:

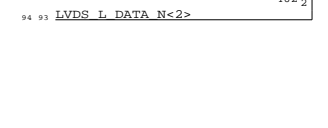
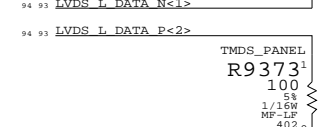
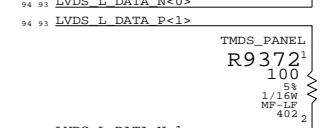
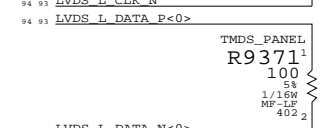
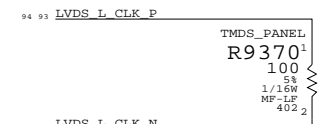
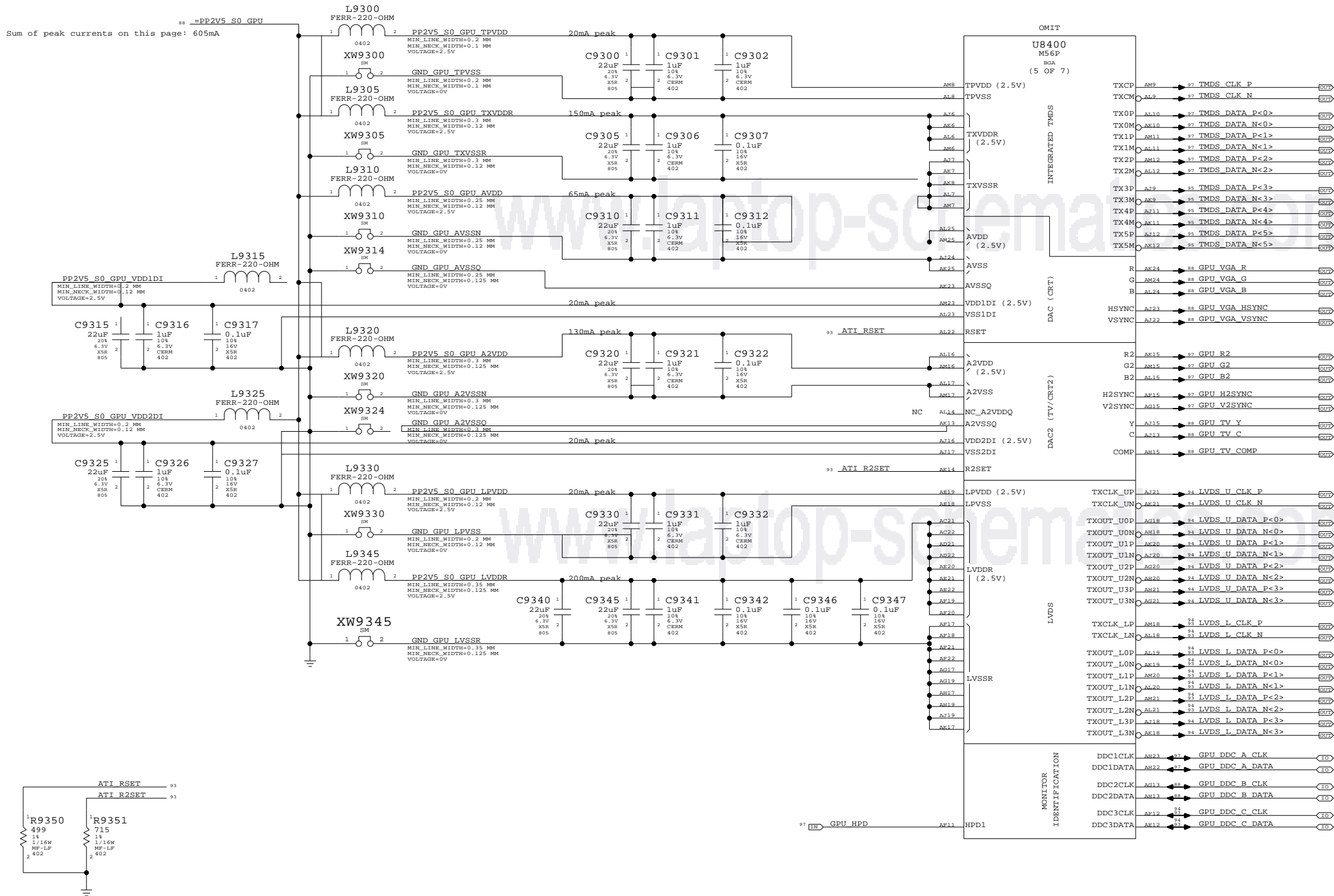
- =PP2V5\_S0\_GPU  
- =PP1V8R2V5\_S0\_GPU\_LVDDR

Signal aliases required by this page:

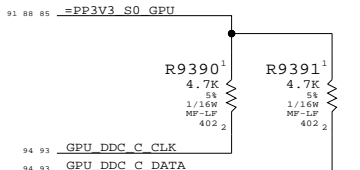
(NONE)

BOM options provided by this page:

(NONE)

TERMINATION FOR TMDS USAGE OF LVDS PINS  
PLACE CLOSE TO GPU (U8400)

Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb



## ATI M56 Video Interfaces

SYNC\_MASTER=(MASTER) SYNC\_DATE=(MASTER)

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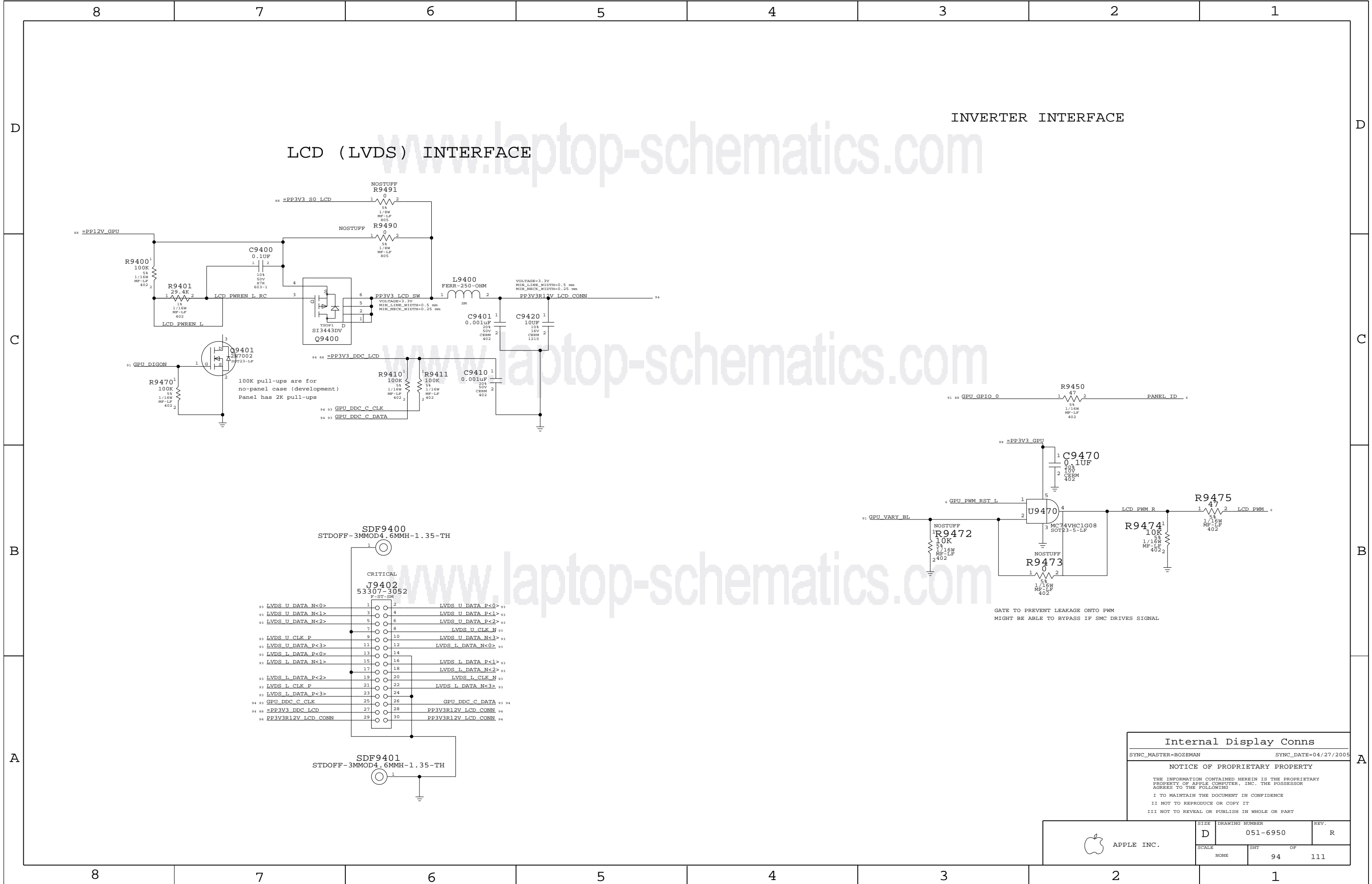
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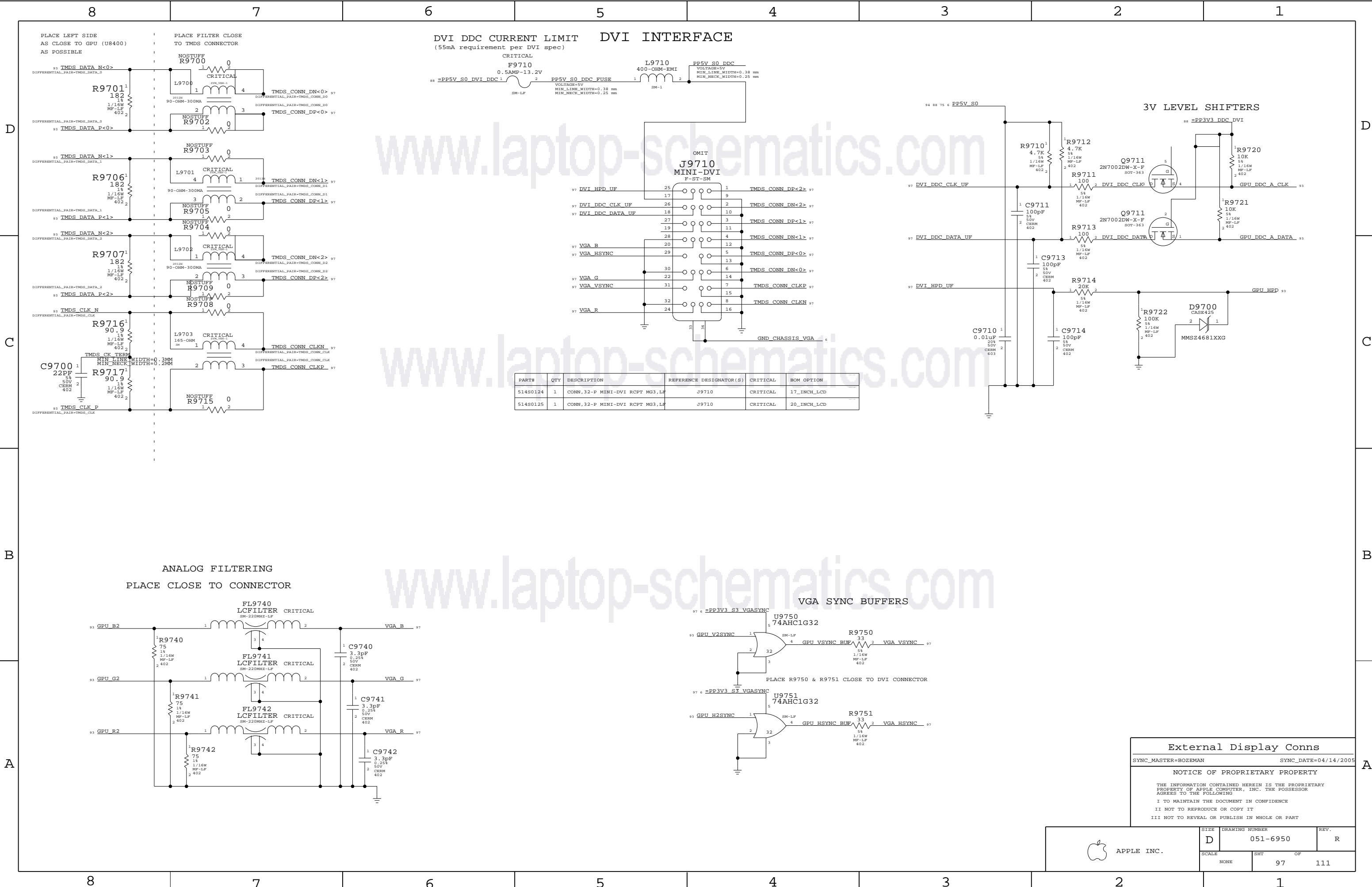


APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6950	R
SCALE	SHT	OF
NONE	93	111

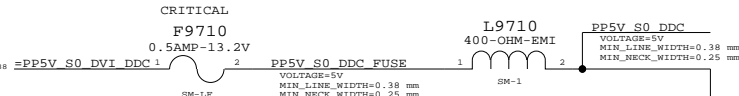


	8	7	6	5	4	3	2	1	
D	<div>TP_TMDS_DATA_P&lt;3&gt;=====TMDS_DATA_P&lt;3&gt; 91 MAKE_BASE=TRUE TP_TMDS_DATA_N&lt;3&gt;=====TMDS_DATA_N&lt;3&gt; 91 MAKE_BASE=TRUE TP_TMDS_DATA_P&lt;4&gt;=====TMDS_DATA_P&lt;4&gt; 91 MAKE_BASE=TRUE TP_TMDS_DATA_N&lt;4&gt;=====TMDS_DATA_N&lt;4&gt; 91 MAKE_BASE=TRUE TP_TMDS_DATA_P&lt;5&gt;=====TMDS_DATA_P&lt;5&gt; 91 MAKE_BASE=TRUE TP_TMDS_DATA_N&lt;5&gt;=====TMDS_DATA_N&lt;5&gt; 91 MAKE_BASE=TRUE</div>								D
C	<div>TP_ATI_DVPPDATA&lt;23&gt;=====ATI_DVPPDATA&lt;23&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;22&gt;=====ATI_DVPPDATA&lt;22&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;21&gt;=====ATI_DVPPDATA&lt;21&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;20&gt;=====ATI_DVPPDATA&lt;20&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;19&gt;=====ATI_DVPPDATA&lt;19&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;18&gt;=====ATI_DVPPDATA&lt;18&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;17&gt;=====ATI_DVPPDATA&lt;17&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;16&gt;=====ATI_DVPPDATA&lt;16&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;15&gt;=====ATI_DVPPDATA&lt;15&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;14&gt;=====ATI_DVPPDATA&lt;14&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;13&gt;=====ATI_DVPPDATA&lt;13&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;12&gt;=====ATI_DVPPDATA&lt;12&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;11&gt;=====ATI_DVPPDATA&lt;11&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;10&gt;=====ATI_DVPPDATA&lt;10&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;9&gt;=====ATI_DVPPDATA&lt;9&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;8&gt;=====ATI_DVPPDATA&lt;8&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;7&gt;=====ATI_DVPPDATA&lt;7&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;6&gt;=====ATI_DVPPDATA&lt;6&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;5&gt;=====ATI_DVPPDATA&lt;5&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;4&gt;=====ATI_DVPPDATA&lt;4&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;3&gt;=====ATI_DVPPDATA&lt;3&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;2&gt;=====ATI_DVPPDATA&lt;2&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;1&gt;=====ATI_DVPPDATA&lt;1&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPDATA&lt;0&gt;=====ATI_DVPPDATA&lt;0&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPCLK=====ATI_DVPPCLK 91 MAKE_BASE=TRUE TP_ATI_DVPPCNTL&lt;0&gt;=====ATI_DVPPCNTL&lt;0&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPCNTL&lt;1&gt;=====ATI_DVPPCNTL&lt;1&gt; 91 MAKE_BASE=TRUE TP_ATI_DVPPCNTL&lt;2&gt;=====ATI_DVPPCNTL&lt;2&gt; 91 MAKE_BASE=TRUE</div>								C
B	<div>TP_GPU_GPIO&lt;34&gt;=====GPU_GPIO_34 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;33&gt;=====GPU_GPIO_33 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;32&gt;=====GPU_GPIO_32 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;31&gt;=====GPU_GPIO_31 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;30&gt;=====GPU_GPIO_30 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;26&gt;=====GPU_GPIO_26 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;25&gt;=====GPU_GPIO_25 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;23&gt;=====GPU_GPIO_23 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;22&gt;=====GPU_GPIO_22 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;21&gt;=====GPU_GPIO_21 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;20&gt;=====GPU_GPIO_20 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;19&gt;=====GPU_GPIO_19 91 MAKE_BASE=TRUE TP_GPU_GPIO&lt;18&gt;=====GPU_GPIO_18 91 MAKE_BASE=TRUE TP_GPU_GENERICA=====GPU_GENERICA 91 MAKE_BASE=TRUE TP_GPU_GENERICB=====GPU_GENERICB 91 MAKE_BASE=TRUE TP_GPU_GENERICC=====GPU_GENERICC 91 MAKE_BASE=TRUE</div>								B
A	<div>M56 TPS</div> <div>NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART</div> <div>APPLE INC.</div> <div><div>SIZE D</div><div>DRAWING NUMBER 051-6950</div><div>REV. R</div></div> <div><div>SCALE NONE</div><div>SHT 95 OF</div><div>111</div></div>								A
	8	7	6	5	4	3	2	1	

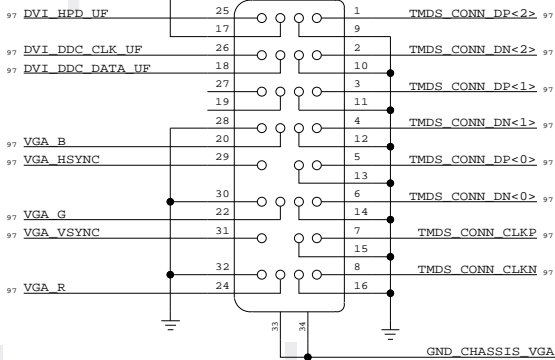


DVI DDC CURRENT LIMIT DVI INTERFACE

(55mA requirement per DVI spec)

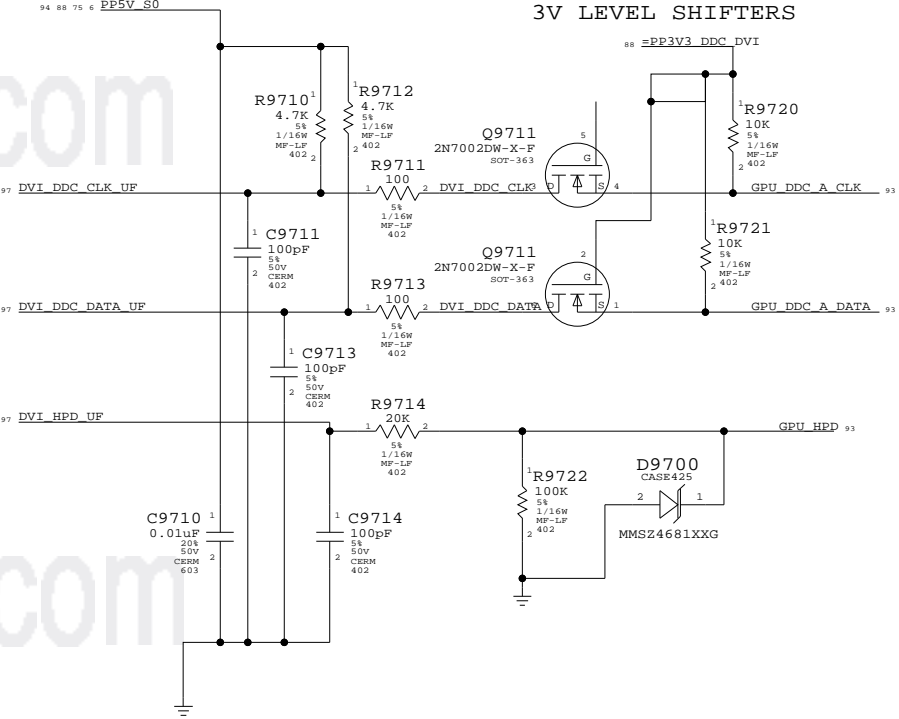


OMIT J9710 MINI-DVI F-ST-SM

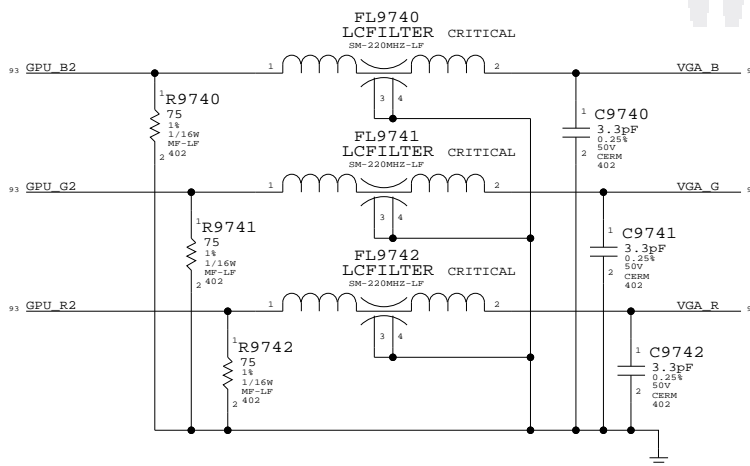


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514S0124	1	CONN, 32-P MINI-DVI RCPT MG3, LF	J9710	CRITICAL	17_INCH_LCD
514S0125	1	CONN, 32-P MINI-DVI RCPT MG3, LF	J9710	CRITICAL	20_INCH_LCD

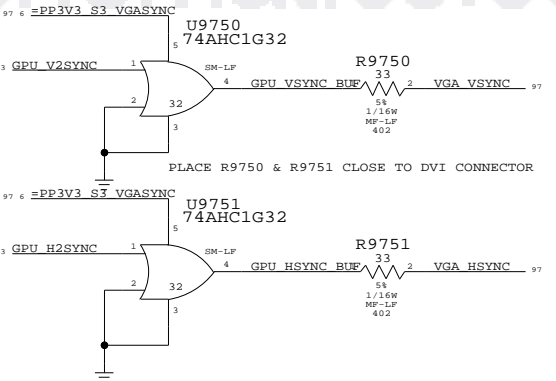
3V LEVEL SHIFTERS



ANALOG FILTERING PLACE CLOSE TO CONNECTOR



VGA SYNC BUFFERS



External Display Conns

SYNC\_MASTER=BOZEMAN SYNC\_DATE=04/14/2005

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APPLE INC.

SIZE	DRAWING NUMBER	REV.
D	051-6950	R
SCALE	SHT	OF
NONE	97	111

[illegible]

	8	7	6	5	4	3	2	1	
D	AUD_LI_DET_EMI AUD_LI_DET_H AUD_LI_DET_JACK AUD_LI_GND_EMI AUD_LI_GND_JACK AUD_LI_L_EMI AUD_LI_L_JACK AUD_LI_R_EMI AUD_LI_R_JACK AUD_LO_DET1 AUD_LO_DET1_1 AUD_LO_DET1_EMI AUD_LO_DET1_INV AUD_LO_DET1_JACK AUD_LO_DET2 AUD_LO_DET2_1 AUD_LO_DET2_EMI AUD_LO_DET2_JACK AUD_LO_GND_EMI AUD_LO_GND_JACK AUD_LO_L_EMI AUD_LO_L_JACK AUD_LO_R_EMI AUD_LO_R_JACK AUD_MAX9714_CHOLD AUD_MAX9714_VREG AUD_MIC_IN_N AUD_MIC_IN_N_CONN AUD_MIC_IN_N_EMI AUD_MIC_IN_P AUD_MIC_IN_P_CONN AUD_MIC_IN_P_EMI AUD_MIC_P1 AUD_PORT_A_DET_L AUD_PORT_A_L AUD_PORT_A_L1 AUD_PORT_A_R AUD_PORT_A_R1 AUD_PORT_E_DET_L AUD_PORT_F_L AUD_PORT_F_L1 AUD_PORT_F_R AUD_PORT_F_R1 AUD_SAMP_FS1 AUD_SAMP_FS2 AUD_SAMP_G1 AUD_SAMP_G2 AUD_SAMP_INL_N AUD_SAMP_INL_P AUD_SAMP_INR_N AUD_SAMP_INR_P AUD_SAMP_SHDN_L AUD_SENSE_A AUD_SENSE_B AUD_SPDIF_GND AUD_SPDIF_IN AUD_SPDIF_OUT AUD_SPDIF_OUT_CHIP AUD_SPDIF_OUT_EMI AUD_SPDIF_OUT_JACK AUD_SPKR_OUTL_N AUD_SPKR_OUTL_P AUD_SPKR_OUTR_N AUD_SPKR_OUTR_P AUD_TVR_DET_EN AUD_VREF_FILT AUD_VREF_PORT_B BAL_IN_COM BAL_IN_L BAL_IN_R BAT_1 BAT_2 BEEP BIOS_REC BOOT_LPC_SPI_L CR509_P1 CEN CK410_27M_NONSPREAD CK410_27M_SPREAD GPU_GPIO_16 CK410_CLK14P3M_TIMER CK410_CPU0_N CK410_CPU0_P CK410_CPU1_N CK410_CPU1_P CK410_CPU2_I2P_SRC10_N CK410_CPU2_I2P_SRC10_P CK410_DOT96_27M_NONSPREAD_P CK410_DOT96_27M_SPREAD_P AD_N CK410_FSA CK410_FSB_TEST_MODE CK410_FSC CK410_IREF CK410_LVDS_N CK410_LVDS_P CK410_PC11_CLK CK410_PC12_CLK CK410_PC13_CLK CK410_PC14_CLK CK410_PC15_FCTSEL1 CK410_PC1F0_CLK CK410_PC1F1_I2P_EN CK410_PD_VTT_PWRGD_L CK410_REF1_FCTSEL0 CK410_SRC1_N CK410_SRC1_P CK410_SRC2_N CK410_SRC2_P CK410_SRC3_N CK410_SRC3_P CK410_SRC4_N CK410_SRC4_P CK410_SRC5_N CK410_SRC5_P CK410_SRC6_N CK410_SRC6_P CK410_SRC7_N	AUD_LI_DET_EMI - @m39_11b.M39 AUD_LI_DET_H - @m39_11b.M39 AUD_LI_DET_JACK - @m39_11b.M39 AUD_LI_GND_EMI - @m39_11b.M39 AUD_LI_GND_JACK - @m39_11b.M39 AUD_LI_L_EMI - @m39_11b.M39 AUD_LI_L_JACK - @m39_11b.M39 AUD_LI_R_EMI - @m39_11b.M39 AUD_LI_R_JACK - @m39_11b.M39 AUD_LO_DET1 - @m39_11b.M39 AUD_LO_DET1_1 - @m39_11b.M39 AUD_LO_DET1_EMI - @m39_11b.M39 AUD_LO_DET1_INV - @m39_11b.M39 AUD_LO_DET1_JACK - @m39_11b.M39 AUD_LO_DET2 - @m39_11b.M39 AUD_LO_DET2_1 - @m39_11b.M39 AUD_LO_DET2_EMI - @m39_11b.M39 AUD_LO_DET2_JACK - @m39_11b.M39 AUD_LO_GND_EMI - @m39_11b.M39 AUD_LO_GND_JACK - @m39_11b.M39 AUD_LO_L_EMI - @m39_11b.M39 AUD_LO_L_JACK - @m39_11b.M39 AUD_LO_R_EMI - @m39_11b.M39 AUD_LO_R_JACK - @m39_11b.M39 AUD_MAX9714_CHOLD - @m39_11b.M39 AUD_MAX9714_VREG - @m39_11b.M39 AUD_MIC_IN_N - @m39_11b.M39 AUD_MIC_IN_N_CONN - @m39_11b.M39 AUD_MIC_IN_N_EMI - @m39_11b.M39 AUD_MIC_IN_P - @m39_11b.M39 AUD_MIC_IN_P_CONN - @m39_11b.M39 AUD_MIC_IN_P_EMI - @m39_11b.M39 AUD_MIC_P1 - @m39_11b.M39 AUD_PORT_A_DET_L - @m39_11b.M39 AUD_PORT_A_L - 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	FB_B_DQ<29>	FB_B_DQ<29> - @m39_11b.M39	87C3 90B6	FSB_DINVL<3>	FSB_DINVL<3> - @m39_11b.M39	5D7 5D8 7B2 12B4	FW_PC1	FW_PC1 - @m39_11b.M39	4483	TP_GPU_GENERICC	TP_GPU_GENERICC - @m39_11b.M39	95B6
	FB_B_DQ<30>	FB_B_DQ<30> - @m39_11b.M39	87C3 90B6	FSB_DPWR_L	FSB_DPWR_L - @m39_11b.M39	5C7 7B3 12B4	FW_PC2	FW_PC2 - @m39_11b.M39	4483	GPU_GENERICICD	GPU_GENERICICD - @m39_11b.M39	85A7 91C3
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	FB_B_DQ<32>	FB_B_DQ<32> - @m39_11b.M39	5A5 5C6 87C3 90A2	FSB_DSTBN_L<0>	FSB_DSTBN_L<0> - @m39_11b.M39	5D7 5D8 7C4 12B4	FW_PORT0_TPA_FL_P	FW_PORT0_TPA_FL_P - @m39_11b.M39	46C2	GPU_GPIO_1	GPU_GPIO_1 - @m39_11b.M39	88D5 91D3
	FB_B_DQ<33>	FB_B_DQ<33> - @m39_11b.M39	87C3 90B3	FSB_DSTBN_L<1>	FSB_DSTBN_L<1> - @m39_11b.M39	5D7 5D8 7B4 12B4	FW_PORT0_TPB_FL_N	FW_PORT0_TPB_FL_N - @m39_11b.M39	46C2	GPU_GPIO_2	GPU_GPIO_2 - @m39_11b.M39	88D5 91D3
	FB_B_DQ<34>	FB_B_DQ<34> - @m39_11b.M39	87C3 90B3	FSB_DSTBN_L<2>	FSB_DSTBN_L<2> - @m39_11b.M39	5D7 5D8 7C2 12B4	FW_PORT0_TPB_FL_P	FW_PORT0_TPB_FL_P - @m39_11b.M39	46C2	GPU_GPIO_3	GPU_GPIO_3 - @m39_11b.M39	88D5 91D3
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	FB_B_DQ<37>	FB_B_DQ<37> - @m39_11b.M39	87C3 90B3	FSB_DSTBP_L<2>	FSB_DSTBP_L<2> - @m39_11b.M39	5D7 5D8 7C2 12B4	FW_PORT1_TPB_FL_P	FW_PORT1_TPB_FL_P - @m39_11b.M39	46C2	GPU_GPIO_6	GPU_GPIO_6 - @m39_11b.M39	88C5 91D3
C	FB_B_DQ<38>	FB_B_DQ<38> - @m39_11b.M39	87C3 90A3	FSB_DL<0>	FSB_DL<0> - @m39_11b.M39	7C4 12D6	FW_R0	FW_R0 - @m39_11b.M39	44C4	GPU_GPIO_7	GPU_GPIO_7 - @m39_11b.M39	88C3 91D3
	FB_B_DQ<39>	FB_B_DQ<39> - @m39_11b.M39	87C3 90A3	FSB_DL<1>	FSB_DL<1> - @m39_11b.M39	7C4 12D6	FW_R1	FW_R1 - @m39_11b.M39	44C4	TP_GPU_GPIO_7	TP_GPU_GPIO_7 - @m39_11b.M39	88C5
	FB_B_DQ<40>	FB_B_DQ<40> - @m39_11b.M39	5A5 5C6 87C3 90A2	FSB_DL<2>	FSB_DL<2> - @m39_11b.M39	7C4 12D6	FW_RESET_L	FW_RESET_L - @m39_11b.M39	44C2 44C3	GPU_GPIO_8	GPU_GPIO_8 - @m39_11b.M39	88C5 91D3
	FB_B_DQ<41>	FB_B_DQ<41> - @m39_11b.M39	87C3 90A3	FSB_DL<3>	FSB_DL<3> - @m39_11b.M39	7C4 12D6	FW_RESET_L	FW_RESET_L - @m39_11b.M39	44C2 44C3	GPU_GPIO_9	GPU_GPIO_9 - @m39_11b.M39	88C5 91C3
	FB_B_DQ<42>	FB_B_DQ<42> - @m39_11b.M39	87C3 90A3	FSB_DL<4>	FSB_DL<4> - @m39_11b.M39	7C4 12D6	FW_ROM_CLK	FW_ROM_CLK - @m39_11b.M39	4	GPU_GPIO_10	GPU_GPIO_10 - @m39_11b.M39	88C3 91C3
	FB_B_DQ<43>	FB_B_DQ<43> - @m39_11b.M39	87C3 90A3	FSB_DL<5>	FSB_DL<5> - @m39_11b.M39	7C4 12D6	FW_SE	FW_SE - @m39_11b.M39	3	NCU_GPIO_10	NCU_GPIO_10 - @m39_11b.M39	88C5
	FB_B_DQ<44>	FB_B_DQ<44> - @m39_11b.M39	87B3 90A3	FSB_DL<6>	FSB_DL<6> - @m39_11b.M39	7C4 12D6	FW_SM	FW_SM - @m39_11b.M39	3	GPU_GPIO_11	GPU_GPIO_11 - @m39_11b.M39	88B5 91C3
	FB_B_DQ<45>	FB_B_DQ<45> - @m39_11b.M39	87B3 90A3	FSB_DL<7>	FSB_DL<7> - @m39_11b.M39	7C4 12D6	FW_TE	FW_TE - @m39_11b.M39	3	GPU_GPIO_12	GPU_GPIO_12 - @m39_11b.M39	88C5 91C3
	FB_B_DQ<46>	FB_B_DQ<46> - @m39_11b.M39	87B3 90A3	FSB_DL<8>	FSB_DL<8> - @m39_11b.M39	7C4 12D6	FW_TP	FW_TP - @m39_11b.M39	7	GPU_GPIO_13	GPU_GPIO_13 - @m39_11b.M39	88C5 91C3
	FB_B_DQ<47>	FB_B_DQ<47> - @m39_11b.M39	87B3 90A3	FSB_DL<9>	FSB_DL<9> - @m39_11b.M39	7C4 12D6	FW_VP	FW_VP - @m39_11b.M39	3	GPU_GPIO_14	GPU_GPIO_14 - @m39_11b.M39	88C1 91C3
B	FB_B_DQ<48>	FB_B_DQ<48> - @m39_11b.M39	5A5 5C6 87B3 90A2	FSB_DL<10>	FSB_DL<10> - @m39_11b.M39	7C4 12D6	FW_XTAL_X0	FW_XTAL_X0 - @m39_11b.M39	44D2 44D3	TP_GPU_GPIO_14	TP_GPU_GPIO_14 - @m39_11b.M39	88C3
	FB_B_DQ<49>	FB_B_DQ<49> - @m39_11b.M39	87B3 90A3	FSB_DL<11>	FSB_DL<11> - @m39_11b.M39	7C4 12D6	FW_XTAL_X1	FW_XTAL_X1 - @m39_11b.M39	44D2 44D3	GPU_GPIO_15	GPU_GPIO_15 - @m39_11b.M39	88B4 91C3
	FB_B_DQ<50>	FB_B_DQ<50> - @m39_11b.M39	87B3 90A3	FSB_DL<12>	FSB_DL<12> - @m39_11b.M39	7C4 12D6	FW_XTAL_XR	FW_XTAL_XR - @m39_11b.M39	44D2	GPU_VCORE_LOW	GPU_VCORE_LOW - @m39_11b.M39	85B4 88B5
	FB_B_DQ<51>	FB_B_DQ<51> - @m39_11b.M39	87B3 90A3	FSB_DL<13>	FSB_DL<13> - @m39_11b.M39	7C4 12D6	GATE_3V3_S3	GATE_3V3_S3 - @m39_11b.M39	83B4	GPU_GPIO_17	GPU_GPIO_17 - @m39_11b.M39	88C1 91C3
	FB_B_DQ<52>	FB_B_DQ<52> - @m39_11b.M39	87B3 90A3	FSB_DL<14>	FSB_DL<14> - @m39_11b.M39	7C4 12D6	GATE_5V_S3	GATE_5V_S3 - @m39_11b.M39	83C4	TP_GPU_GPIO_17	TP_GPU_GPIO_17 - @m39_11b.M39	88C3
	FB_B_DQ<53>	FB_B_DQ<53> - @m39_11b.M39	87B3 90A3	FSB_DL<15>	FSB_DL<15> - @m39_11b.M39	7C4 12D6	GND_AUDIO	GND_AUDIO - @m39_11b.M39	6C7 74D2	GPU_GPIO_18	GPU_GPIO_18 - @m39_11b.M39	91D5 95B4
	FB_B_DQ<54>	FB_B_DQ<54> - @m39_11b.M39	5A5 5C6 87B3 90B2	FSB_DL<16>	FSB_DL<16> - @m39_11b.M39	5D7 5D8 7C4 12C6	GND_AUDIO_CODEC	GND_AUDIO_CODEC - @m39_11b.M39	68A6 68B7 68D2 72B8 72C8	GPU_GPIO_19	GPU_GPIO_19 - @m39_11b.M39	91D5 95B4
	FB_B_DQ<55>	FB_B_DQ<55> - @m39_11b.M39	87B3 90B3	FSB_DL<17>	FSB_DL<17> - @m39_11b.M39	7C4 12C6	GND_AUDIO_MIC_CONN	GND_AUDIO_MIC_CONN - @m39_11b.M39	73A8 73C2 73D4 74A3 74A5	GPU_GPIO_20	GPU_GPIO_20 - @m39_11b.M39	91D5 95B4
	FB_B_DQ<56>	FB_B_DQ<56> - @m39_11b.M39	87B3 90B3	FSB_DL<18>	FSB_DL<18> - @m39_11b.M39	7B4 12C6	GND_AUDIO_SPKRAMP	GND_AUDIO_SPKRAMP - @m39_11b.M39	74A6 74A7 74B5 74B7 74C1	GPU_GPIO_21	GPU_GPIO_21 - @m39_11b.M39	91D5 95B4
	FB_B_DQ<57>	FB_B_DQ<57> - @m39_11b.M39	87B3 90B3	FSB_DL<19>	FSB_DL<19> - @m39_11b.M39	7B4 12C6	GND_AUDIO_SPKRAMP_PL	GND_AUDIO_SPKRAMP_PL - @m39_11b.M39	74C2 74C8 74D1	GPU_GPIO_22	GPU_GPIO_22 - @m39_11b.M39	91D5 95B4
A	FB_B_DQ<58>	FB_B_DQ<58> - @m39_11b.M39	87B3 90B3	FSB_DL<20>	FSB_DL<20> - @m39_11b.M39	7B4 12C6	ANE	ANE - @m39_11b.M39	74C2	GPU_GPIO_23	GPU_GPIO_23 - @m39_11b.M39	91D5 95B4
	FB_B_DQ<59>	FB_B_DQ<59> - @m39_11b.M39	87B3 90B3	FSB_DL<21>	FSB_DL<21> - @m39_11b.M39	7B4 12C6	GND_BNDI	GND_BNDI - @m39_11b.M39	74C2	GPU_GPIO_24	GPU_GPIO_24 - @m39_11b.M39	88B5 91D5
	FB_B_DQ<60>	FB_B_DQ<60> - @m39_11b.M39	87B3 90B3	FSB_DL<22>	FSB_DL<22> - @m39_11b.M39	7B4 12C6	GND_CHASSIS_AUDIO_EX	GND_CHASSIS_AUDIO_EXTERNAL - @m39_11b.M39	6C7 73A8 73C7 73D8 74C1	GPU_GPIO_25	GPU_GPIO_25 - @m39_11b.M39	91D5 95C4
	FB_B_DQ<61>	FB_B_DQ<61> - @m39_11b.M39	87B3 90B3	FSB_DL<23>	FSB_DL<23> - @m39_11b.M39	7C2 12C6	TERNAL	TERNAL - @m39_11b.M39	74D2	GPU_GPIO_26	GPU_GPIO_26 - @m39_11b.M39	91D5 95C4
	FB_B_DQ<62>	FB_B_DQ<62> - @m39_11b.M39	87B3 90B3	FSB_DL<24>	FSB_DL<24> - @m39_11b.M39	7C2 12C6	GND_CHASSIS_AUDIO_IN	GND_CHASSIS_AUDIO_INTERNAL - @m39_11b.M39	6A2 73C6	GPU_GPIO_27	GPU_GPIO_27 - @m39_11b.M39	88B5 91D5
	FB_B_DQ<63>	FB_B_DQ<63> - @m39_11b.M39	87B3 90B3	FSB_DL<25>	FSB_DL<25> - @m39_11b.M39	7C2 12C6	TERNAL	TERNAL - @m39_11b.M39	74D2	GPU_GPIO_28	GPU_GPIO_28 - @m39_11b.M39	88B5 91D5
	FB_B_DQ<64>	FB_B_DQ<64> - @m39_11b.M39	87B3 90B3	FSB_DL<26>	FSB_DL<26> - @m39_11b.M39	7C2 12C6	GND_CHASSIS_FIREWIRE	GND_CHASSIS_FIREWIRE - @m39_11b.M39	68A2 46E1 46C1	GPU_GPIO_29	GPU_GPIO_29 - @m39_11b.M39	88B5 91D5
	FB_B_DQ<65>	FB_B_DQ<65> - @m39_11b.M39	87B3 90B3	FSB_DL<27>	FSB_DL<27> - @m39_11b.M39	7C2 12C6	TERNAL	TERNAL - @m39_11b.M39	74D2</			



<

D	SMLINK<0>	SMLINK<0> - @m39_lib.M39	23D5	TP_PCI_GNT4_L	TP_PCI_GNT4_L - @m39_lib.M39	22B6	U600_3	=PP1V5_S0_AIRPORT - @m39_lib.M39	6C4 53D3
	SMLINK<1>	SMLINK<1> - @m39_lib.M39	23D5	TP_PCI_PME_L	TP_PCI_PME_L - @m39_lib.M39	22A6	U600_6	U600_3 - @m39_lib.M39	6C7
	SMS_INT_L	SMS_INT_L - @m39_lib.M39	23C3 26C2 58B5	TP_SB_ACZ_SDIN1	TP_SB_ACZ_SDIN1 - @m39_lib.M39	21C6	U600_8	U600_6 - @m39_lib.M39	6B7
	SMS_ONOFF_L	SMS_ONOFF_L - @m39_lib.M39	58A5 59B4	TP_SB_ACZ_SDIN2	TP_SB_ACZ_SDIN2 - @m39_lib.M39	21C6	U600_11	U600_8 - @m39_lib.M39	6B7
	SMS_X_AXIS	SMS_X_AXIS - @m39_lib.M39	58B7 59B6	TP_SB_DRQ0_L	TP_SB_DRQ0_L - @m39_lib.M39	21D4	U2698_4	U600_11 - @m39_lib.M39	6A7
		NC_SMS_X_AXIS - @m39_lib.M39	59B5	TP_SB_GPI06	TP_SB_GPI06 - @m39_lib.M39	23C5	U2698_4	U2698_4 - @m39_lib.M39	26C4
	SMS_Y_AXIS	SMS_Y_AXIS - @m39_lib.M39	58B7 59B6	TP_SB_GPI023	TP_SB_GPI023 - @m39_lib.M39	21D5	U3100_VDDQ	U3100_VDDQ - @m39_lib.M39	31C5
		NC_SMS_Y_AXIS - @m39_lib.M39	59B5	TP_SB_GPI025_DO_NOT_USE	TP_SB_GPI025_DO_NOT_USE - @m39_lib.M39	23C3	U6100_VCC	U6100_VCC - @m39_lib.M39	61C4
	SMS_Z_AXIS	SMS_Z_AXIS - @m39_lib.M39	58A7 59B6	USB	@m39_lib.M39		U8595_1	U8595_1 - @m39_lib.M39	85D2
		NC_SMS_Z_AXIS - @m39_lib.M39	59B5	TP_SB_GPI038	TP_SB_GPI038 - @m39_lib.M39	23C3	USB_AIRPORT_N	USB_AIRPORT_N - @m39_lib.M39	53B4
C	SPARE_SRC3_N	SPARE_SRC3_N - @m39_lib.M39	34D2	TP_SB_RSVD9	TP_SB_RSVD9 - @m39_lib.M39	22A6	USB_AIRPORT_P	USB_AIRPORT_P - @m39_lib.M39	53B4
	SPARE_SRC3_P	SPARE_SRC3_P - @m39_lib.M39	34D2	TP_SB_SATALED_L	TP_SB_SATALED_L - @m39_lib.M39	21C6	USB_A_N	USB_A_N - @m39_lib.M39	22C2 47D7
	SPARE_SRC7_N	SPARE_SRC7_N - @m39_lib.M39	34D2	TP_SB_XOR_AD5	TP_SB_XOR_AD5 - @m39_lib.M39	22A7	USB_A_OC_L	USB_A_OC_L - @m39_lib.M39	22C4 22D8 47B8 47B8
	SPARE_SRC7_P	SPARE_SRC7_P - @m39_lib.M39	34D2	TP_SB_XOR_AD9	TP_SB_XOR_AD9 - @m39_lib.M39	22A7	USB_A_P	USB_A_P - @m39_lib.M39	22C2 47C7
	SPI_ARB	SPI_ARB - @m39_lib.M39	22C6 58D5	TP_SB_XOR_AE5	TP_SB_XOR_AE5 - @m39_lib.M39	22A7	USB_B_N	USB_B_N - @m39_lib.M39	22C2 53B2
	SPI_CE_L	SPI_CE_L - @m39_lib.M39	22C6 58B5 63C7	TP_SB_XOR_AE9	TP_SB_XOR_AE9 - @m39_lib.M39	22A6	USB_B_OC_L	USB_B_OC_L - @m39_lib.M39	22C4 22D8 47B8
	SPI_HOLD_L	SPI_HOLD_L - @m39_lib.M39	63C4	TP_SB_XOR_AG4	TP_SB_XOR_AG4 - @m39_lib.M39	22A7	USB_B_P	USB_B_P - @m39_lib.M39	22C2 53B2
	SPI_SCLK	SPI_SCLK - @m39_lib.M39	22C6 58D5 63C7	TP_SB_XOR_AG8	TP_SB_XOR_AG8 - @m39_lib.M39	22A6	USB_CAMERA_N	USB_CAMERA_N - @m39_lib.M39	47B2
	SPI_SCLK_R	SPI_SCLK_R - @m39_lib.M39	63C4	TP_SB_XOR_AH4	TP_SB_XOR_AH4 - @m39_lib.M39	22A7	USB_CAMERA_P	USB_CAMERA_P - @m39_lib.M39	47B2
	SPI_SI	SPI_SI - @m39_lib.M39	22C6 58D5 63C1	TP_SB_XOR_AH8	TP_SB_XOR_AH8 - @m39_lib.M39	22A6	USB_C_N	USB_C_N - @m39_lib.M39	22C2 47B7
B	SPI_SI_R	SPI_SI_R - @m39_lib.M39	63C3	TP_SB_XOR_T5	TP_SB_XOR_T5 - @m39_lib.M39	21C6	USB_C_OC_L	USB_C_OC_L - @m39_lib.M39	22C4 22D8 47B7 47B8
	SPI_SO	SPI_SO - @m39_lib.M39	22C6 58D5 63C1	TP_SB_XOR_U3	TP_SB_XOR_U3 - @m39_lib.M39	21C6	USB_C_P	USB_C_P - @m39_lib.M39	22C2 47B7
	SPI_SO_R	SPI_SO_R - @m39_lib.M39	63C3	TP_SB_XOR_U5	TP_SB_XOR_U5 - @m39_lib.M39	21C6	USB_D_N	USB_D_N - @m39_lib.M39	22C2 47B3
	SPI_WP_L	SPI_WP_L - @m39_lib.M39	63C4	TP_SB_XOR_U7	TP_SB_XOR_U7 - @m39_lib.M39	21C6	USB_D_OC_L	USB_D_OC_L - @m39_lib.M39	22C4 22D8 47B7
	SPKRAMP_MUTE	SPKRAMP_MUTE - @m39_lib.M39	72B5	TP_SB_XOR_V3	TP_SB_XOR_V3 - @m39_lib.M39	21C6	USB_D_P	USB_D_P - @m39_lib.M39	22C2 47B3
	SPKRAMP_SS	SPKRAMP_SS - @m39_lib.M39	72B4	TP_SB_XOR_V4	TP_SB_XOR_V4 - @m39_lib.M39	21C6	USB_E_N	USB_E_N - @m39_lib.M39	22C2 47A7
	SUS_CLK_SB	SUS_CLK_SB - @m39_lib.M39	23C3 59B5	TP_SB_XOR_V6	TP_SB_XOR_V6 - @m39_lib.M39	21C6	USB_E_OC_L	USB_E_OC_L - @m39_lib.M39	22C4 22D8 47B7
	SUS_CLK	SUS_CLK - @m39_lib.M39	58C5 59B6	TP_SB_XOR_V7	TP_SB_XOR_V7 - @m39_lib.M39	21C6	USB_E_P	USB_E_P - @m39_lib.M39	22C2 47A7
	SV_SET_UP	SV_SET_UP - @m39_lib.M39	23B6 23C3 60B3	TP_SB_XOR_W1	TP_SB_XOR_W1 - @m39_lib.M39	21C6	USB_F_N	USB_F_N - @m39_lib.M39	22C2 49C6
	SW_RST_BTN_L	SW_RST_BTN_L - @m39_lib.M39	5D1 26C6	TP_SB_XOR_W3	TP_SB_XOR_W3 - @m39_lib.M39	21C6	USB_F_P	USB_FLASH_N - @m39_lib.M39	49C5
A	SW_RST_DEBNC	SW_RST_DEBNC - @m39_lib.M39	26C4	TP_SB_XOR_Y1	TP_SB_XOR_Y1 - @m39_lib.M39	21C6	USB_G_N	USB_F_P - @m39_lib.M39	22C2 49C6
	SYS_LED_DRV_C	SYS_LED_DRV_C - @m39_lib.M39	59D6	TP_SB_XOR_Y2	TP_SB_XOR_Y2 - @m39_lib.M39	21C6	USB_G_N	USB_FLASH_P - @m39_lib.M39	49C5
	SYS_LED_DRV_K	SYS_LED_DRV_K - @m39_lib.M39	59D6	TP_U5999_P1	TP_U5999_P1 - @m39_lib.M39	59A7	USB_G_P	USB_G_N - @m39_lib.M39	22C2 47A3
	SYS_ONEWIRE	SYS_ONEWIRE - @m39_lib.M39	58B7 59B4	TP_U5999_P13	TP_U5999_P13 - @m39_lib.M39	59A5	USB_H_N	USB_BT_N - @m39_lib.M39	47A3
	SYS_POWERFAIL_L	SYS_POWERFAIL_L - @m39_lib.M39	6D8 76D2	TP_U5999_P14	TP_U5999_P14 - @m39_lib.M39	59A5	USB_H_P	USB_BT_P - @m39_lib.M39	22C2 47A3
	SYS_PWRUP_L	SYS_PWRUP_L - @m39_lib.M39	6C7	TP_U8400_AGL4	TP_U8400_AGL4 - @m39_lib.M39	91A5	USB_IR_N	USB_IR_N - @m39_lib.M39	47A3
	THERM_DX_N	THERM_DX_N - @m39_lib.M39	10B5 10C5	TP_U8900_J2	TP_U8900_J2 - @m39_lib.M39	89A7	USB_IR_P	USB_H_N - @m39_lib.M39	22C2 47C3
	THERM_DX_P	THERM_DX_P - @m39_lib.M39	10B5 10C5	TP_U8900_J3	TP_U8900_J3 - @m39_lib.M39	89A7	USB_IR_P	USB_H_P - @m39_lib.M39	22C2 47C3
	THERM_ALERT_L	THERM_ALERT_L - @m39_lib.M39	10D3	TP_U8950_J2	TP_U8950_J2 - @m39_lib.M39	89A4	USB_PORT0_N	USB_IR_N - @m39_lib.M39	47C2
	THERM_THM	THERM_THM - @m39_lib.M39	10C4	TP_U8950_J3	TP_U8950_J3 - @m39_lib.M39	89A4	USB_PORT0_P	USB_IR_P - @m39_lib.M39	47C2

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D	C7512	CAP_603	m39[75C2]	C8420	CAP_402	m39[84D5]	C8658	CAP_402	m39[86B5]	C9136	CAP_402	m39[91A6]	C	C9137	CAP_402	m39[91A5]	B	A	A				
	C7513	CAP_402	m39[75B7]	C8421	CAP_402	m39[84D5]	C8659	CAP_402	m39[86B5]	C9137	CAP_402	m39[91A5]											
	C7514	CAP_402	m39[75B8]	C8422	CAP_402	m39[84D5]	C8660	CAP_402	m39[86B5]	C9140	CAP_805	m39[91A6]											
	C7515	CAP_603	m39[75C4]	C8423	CAP_402	m39[84D5]	C8661	CAP_402	m39[86B6]	C9141	CAP_402	m39[91A6]											
	C7516	CAP_402	m39[75B4]	C8424	CAP_402	m39[84D5]	C8662	CAP_402	m39[86B6]	C9142	CAP_402	m39[91A5]											
	C7517	CAP_P_SM-3	m39[75D2]	C8425	CAP_402	m39[84D5]	C8663	CAP_402	m39[86B6]	C9191	CAP_402	m39[91D2]											
	C7518	CAP_P_SM-3	m39[75D2]	C8426	CAP_402	m39[84D5]	C8664	CAP_402	m39[86B5]	C9300	CAP_805	m39[93C6]											
	C7521	CAP_402	m39[75A6]	C8427	CAP_402	m39[84D5]	C8665	CAP_402	m39[86B5]	C9301	CAP_402	m39[93C6]											
	C7526	CAP_603	m39[75D6]	C8428	CAP_402	m39[84D5]	C8666	CAP_402	m39[86B5]	C9302	CAP_402	m39[93C5]											
	C7527	CAP_603	m39[75C5]	C8429	CAP_402	m39[84C5]	C8667	CAP_402	m39[86B6]	C9305	CAP_805	m39[93C6]											
C7528	CAP_402	m39[75B5]	C8430	CAP_402	m39[84C5]	C8668	CAP_402	m39[86B6]	C9306	CAP_402	m39[93C6]												
C7529	CAP_402	m39[75B5]	C8431	CAP_402	m39[84C5]	C8669	CAP_402	m39[86B6]	C9307	CAP_402	m39[93C5]												
C7530	CAP_402	m39[75D6]	C8432	CAP_402	m39[84C5]	C8670	CAP_402	m39[86B5]	C9310	CAP_805	m39[93C6]												
C7531	CAP_402	m39[75B5]	C8433	CAP_402	m39[84C5]	C8671	CAP_402	m39[86B5]	C9311	CAP_402	m39[93C6]												
C7532	CAP_402	m39[75B6]	C8434	CAP_402	m39[84C5]	C8672	CAP_402	m39[86B5]	C9312	CAP_402	m39[93C5]												
C7533	CAP_402	m39[75B6]	C8435	CAP_402	m39[84C5]	C8673	CAP_402	m39[86B6]	C9315	CAP_805	m39[93B8]												
C7534	CAP_402	m39[75B5]	C8436	CAP_402	m39[84C5]	C8674	CAP_402	m39[86B5]	C9316	CAP_402	m39[93B8]												
C7535	CAP_603	m39[75D5]	C8437	CAP_402	m39[84C5]	C8675	CAP_402	m39[86B6]	C9317	CAP_402	m39[93B7]												
C7550	CAP_603	m39[75D1]	C8438	CAP_402	m39[84C5]	C8676	CAP_402	m39[86B5]	C9320	CAP_805	m39[93B6]												
C7551	CAP_603	m39[75D1]	C8439	CAP_402	m39[84C5]	C8677	CAP_402	m39[86B5]	C9321	CAP_402	m39[93B6]												
C7590	CAP_402	m39[75C3]	C8440	CAP_402	m39[84B5]	C8678	CAP_402	m39[86B5]	C9322	CAP_402	m39[93B5]												
C7592	CAP_402	m39[75B3]	C8441	CAP_402	m39[84B5]	C8679	CAP_402	m39[86A6]	C9325	CAP_805	m39[93B8]												
C7596	CAP_402	m39[75D6]	C8442	CAP_402	m39[84B5]	C8680	CAP_402	m39[86A6]	C9326	CAP_402	m39[93B8]												
C7597	CAP_1210	m39[75D1]	C8443	CAP_402	m39[84B5]	C8681	CAP_402	m39[86A6]	C9327	CAP_402	m39[93B7]												
C7598	CAP_1210	m39[75D1]	C8444	CAP_402	m39[84B5]	C8682	CAP_402	m39[86A5]	C9330	CAP_805	m39[93B6]												
C7599	CAP_402	m39[76D6]	C8445	CAP_402	m39[84B5]	C8683	CAP_402	m39[86A5]	C9331	CAP_402	m39[93B6]												
C7600	CAP_402	m39[76D3]	C8446	CAP_402	m39[84B5]	C8690	CAP_805	m39[86D5]	C9332	CAP_402	m39[93B5]												
C7601	CAP_402	m39[76D3]	C8447	CAP_402	m39[84B5]	C8691	CAP_402	m39[86D5]	C9340	CAP_805	m39[93A6]												
C7602	CAP_402	m39[76D2]	C8448	CAP_402	m39[84B5]	C8692	CAP_402	m39[86D5]	C9341	CAP_402	m39[93A6]												
C7603	CAP_402	m39[76C4]	C8449	CAP_402	m39[84B5]	C8695	CAP_805	m39[86D2]	C9342	CAP_402	m39[93A5]												
C7612	CAP_402	m39[76B2]	C8450	CAP_402	m39[84B5]	C8696	CAP_402	m39[86D3]	C9345	CAP_805	m39[93A6]												
C7633	CAP_402	m39[76C7]	C8451	CAP_402	m39[84B5]	C8697	CAP_402	m39[86D3]	C9346	CAP_402	m39[93A5]												
C7659	CAP_402	m39[76D4]	C8455	CAP_402	m39[84D2]	C8711	CAP_402	m39[87B7]	C9347	CAP_402	m39[93A5]												
C7669	CAP_402	m39[76D4]	C8456	CAP_402	m39[84D2]	C8713	CAP_402	m39[87B7]	C9400	CAP_603-1	m39[94C7]												
C7700	CAP_603	m39[77D4]	C8457	CAP_402	m39[84D2]	C8715	CAP_402	m39[87A7]	C9401	CAP_402	m39[94C6]												
C7703	CAP_402	m39[77C4]	C8458	CAP_402	m39[84D2]	C8716	CAP_402	m39[87A6]	C9410	CAP_402	m39[94C6]												
C7704	CAP_402	m39[77C4]	C8459	CAP_402	m39[84D2]	C8721	CAP_402	m39[87B4]	C9420	CAP_1210	m39[94C5]												
C7706	CAP_402	m39[77C4]	C8460	CAP_402	m39[84D2]	C8723	CAP_402	m39[87B4]	C9470	CAP_402	m39[94B2]												
C7709	CAP_805	m39[77C3]	C8461	CAP_402	m39[84D2]	C8725	CAP_402	m39[87A4]	C9700	CAP_402	m39[97C8]												
C7710	CAP_402	m39[77C7]	C8462	CAP_402	m39[84D2]	C8726	CAP_402	m39[87A3]	C9710	CAP_603	m39[97C3]												
C7711	CAP_402	m39[77B7]	C8463	CAP_402	m39[84D2]	C8900	CAP_805	m39[89D7]	C9711	CAP_402	m39[97D3]												
C7712	CAP_402	m39[77C7]	C8464	CAP_402	m39[84C2]	C8901	CAP_402	m39[89D7]	C9713	CAP_402	m39[97C2]												
C7750	CAP_402	m39[77A5]	C8465	CAP_402	m39[84C2]	C8902	CAP_402	m39[89D7]	C9714	CAP_402	m39[97C2]												
C7751	CAP_805	m39[77B4]	C8466	CAP_402	m39[84C2]	C8903	CAP_402	m39[89D7]	C9740	CAP_402	m39[97A7]												
C7752	CAP_805	m39[77B4]	C8467	CAP_402	m39[84C2]	C8904	CAP_402	m39[89D6]	C9741	CAP_402	m39[97A6]												
C7753	CAP_402	m39[77A7]	C8468	CAP_402	m39[84C2]	C8910	CAP_402	m39[89D7]	C9742	CAP_402	m39[97A6]												
C7754	CAP_402	m39[77B6]	C8469	CAP_402	m39[84C2]	C8915	CAP_402	m39[89D6]	D2500	DIODE_SCHOT_SOT23	m39[25C8]												
C7755	CAP_805	m39[77B3]	C8470	CAP_402	m39[84C2]	C8920	CAP_805	m39[89C8]	D2501	DIODE_SCHOT_SOT23	m39[25D8]												
C7756	CAP_805	m39[77B3]	C8471	CAP_402	m39[84C2]	C8921	CAP_402	m39[89C8]	D2600	DIODE_SCHOT_SOT23	m39[26D8]												
C7757	CAP_402	m39[77A6]	C8472	CAP_402	m39[84C2]	C8922	CAP_402	m39[89C7]	D2601	DIODE_SCHOT_SOT23	m39[26C8]												
C7799	CAP_402	m39[77A3]	C8473	CAP_402	m39[84C2]	C8923	CAP_402	m39[89C7]	D4600	DIODE_SMC	m39[46D5]												
C7800	CAP_1210	m39[78C3]	C8474	CAP_402	m39[84C2]	C8924	CAP_402	m39[89C7]	D4690	ZENER_SOT23	m39[46A6]												
C7801	CAP_P_SM-LF	m39[78C4]	C8475	CAP_402	m39[84B2]	C8925	CAP_402	m39[89C7]	D4700	DIODE_SCHOT_3P_A_SC-	m39[47C5]												
C7802	CAP_603	m39[78C6]	C8476	CAP_402	m39[84B2]	C8926	CAP_402	m39[89C6]	75														
C7803	CAP_603	m39[78B6]	C8477	CAP_402	m39[84B2]	C8931	CAP_402	m39[89C7]	D4701	DIODE_SCHOT_3P_A_SC-	m39[47B5]												
C7804	CAP_402	m39[78C6]	C8478	CAP_402	m39[84B2]	C8933	CAP_402	m39[89C6]	75														
C7805	CAP_603	m39[78C4]	C8479	CAP_402	m39[84B2]	C8950	CAP_805	m39[89D4]	D4702	DIODE_SCHOT_3P_A_SC-	m39[47A5]												
C7806	CAP_805-1	m39[78B2]	C8480	CAP_402	m39[84B2]	C8951	CAP_402	m39[89D4]	75														
C7807	CAP_P_CASE-D2E-LF	m39[78B3]	C8481	CAP_402	m39[84B2]	C8952	CAP_402	m39[89D4]	D4705	DIODE_SCHOT_3P_A_SC-	m39[47C6]												
C7809	CAP_402	m39[78B3]	C8482	CAP_402	m39[84B2]	C8953	CAP_402	m39[89D3]	75														
C7810	CAP_402	m39[78B4]	C8483	CAP_402	m39[84B2]	C8954	CAP_402	m39[89D3]	D4706	DIODE_SCHOT_3P_A_SC-	m39[47B6]												
C7811	CAP_402	m39[78B5]	C8484	CAP_402	m39[84B2]	C8960	CAP_402	m39[89D3]	75														
C7813	CAP_1206	m39[78B4]	C8485	CAP_402	m39[84B2]	C8965	CAP_402	m39[89D3]	D4707	DIODE_SCHOT_3P_A_SC-	m39[47A6]												
C7814	CAP_402	m39[78B5]	C8486	CAP_402	m39[84B2]	C8970	CAP_805	m39[89C5]	75														
C7817	CAP_P_CASE-D2E-LF	m39[78B2]	C8500	CAP_603	m39[85D6]	C8971	CAP_402	m39[89C4]	D4710	DIODE_SCHOT_3P_A_SC-	m39[47C6]												
C7900	CAP_402	m39[79D6]	C8501	CAP_603	m39[85D6]	C8972	CAP_402	m39[89C4]	75														
C7901	CAP_603	m39[79D5]	C8502	CAP_603	m39[85D6]	C8973	CAP_402	m39[89C4]	D4900	DIODE_SCHOT_3P_A_SC-	m39[49B4]												
C7902	CAP_402	m39[79C5]	C8506	CAP_402	m39[85C8]	C8974	CAP_402	m39[89C4]	75														
C7903	CAP_603	m39[79C7]	C8507	CAP_402	m39[85C7]	C8975	CAP_402	m39[89C3]	D6500	DIODE_SOT23	m39[65C4]												
C7906	CAP_402	m39[79C5]	C8508	CAP_402	m39[85C7]	C8976	CAP_402	m39[89C3]	D6501	DIODE_SOT23	m39[65B4]												
C7907	CAP_402	m39[79C3]	C8509	CAP_402	m39[85C5]	C8981	CAP_402	m39[89C3]	D6502	DIODE_SCHOT_SMB	m39[65C4]												
C7908	CAP_402	m39[79C5]	C8520	CAP_603	m39[85C3]	C8983	CAP_402	m39[89C3]	D6503	DIODE_SCHOT_SMB	m39[65B4]												
C7909	CAP_1206	m39[79C4]	C8521	CAP_402	m39[85C4]	C9000	CAP_805	m39[90D7]	D6600	DIODE_SOT23	m39[66C4]												
C7910	CAP_P_SM-3	m39[79D4]	C8522	CAP_402	m39[85C5]	C9002	CAP_402	m39[90D7]	D6601	DIODE_SCHOT_SMB	m39[66C3]												
C7911	CAP_1210	m39[79D3]	C8523	CAP_402	m39[85B2]	C9002	CAP_402	m39[90D7]	D7500	DIODE_SCHOT_SMB	m39[75C3]												
C7912	CAP_P_CASE-D2E-LF	m39[79C3]	C8530	CAP_1210	m39[85D4]	C9003	CAP_402	m39[90D7]	D7501	DIODE_SCHOT_SMB	m39[75B2]												
C7913	CAP_1206	m39[79C2]	C8531	CAP_1210	m39[85D4]	C9004	CAP_402	m39[90D6]	D7599	DIODE_SOT23	m39[76D6]												
C7980	CAP_402	m39[79B3]	C8532	CAP_1210	m39[85D4]	C9010	CAP_402	m39[90D7]	D8520	DIODE_SCHOT_SMB	m39[85C3]												
C7992	CAP_603	m39[79D6]	C8540	CAP_805	m39[85C2]	C9015	CAP_402	m39[90D6]	D9700	ZENER_CASE425	m39[97C1]												
C7998	CAP_P_CASE-D2E-LF	m39[79C3]	C8541	CAP_805	m39[85C2]	C9020	CAP_805	m39[90C8]	DP4610	DIODE_DUAL_6P_SOT-36	m39[46D4 46D3]												
C8000	CAP_1210	m39[80D3]	C8542	CAP_P_CASE-D2E-LF	m39[85C2]	C9021	CAP_402	m39[90C8]	3														
C8001	CAP_P_CASE-D2E-LF	m39[80C3]	C8543	CAP_P_CASE-D2E-LF	m39[85C2]	C9022	CAP_402	m39[90C7]	DP4611	DIODE_DUAL_6P_SOT-36	m39[46C4 46C3]												
C8002	CAP_603	m39[80D6]	C8551	CAP_603	m39[85B8]	C9023	CAP_402	m39[90C7]	3														
C8003	CAP_402	m39[80C3]	C8555	CAP_402	m39[85B7]	C9024	CAP_402	m39[90C7]	DP4620	DIODE_DUAL_6P_SOT-36	m39[46B4 46B3]												
C8004	CAP_1206	m39[80C4]	C8556	CAP_805	m39[85B6]	C9025	CAP_402	m39[90C7]	3														
C8005	CAP_402	m39[80C4]	C8557	CAP_805	m39[85B6]	C9026	CAP_402	m39[90C6]	DP4621	DIODE_DUAL_6P_SOT-36	m39[46A4 46A3]												
C8006	CAP_402	m39[80D6]	C8560	CAP_402	m39[85A6]	C9031	CAP_402	m39[90C7]	3														
C8009	CAP_603	m39[80C6]	C8570	CAP_402	m39[85A5]	C9033	CAP_402	m39[90C6]	D27300	SUPPR_TRANSIENT_4P1_	m39[73C6]												
C8010	CAP_603	m39[80C4]	C8580	CAP_603	m39[85A4]	C9050	CAP_805	m39[90D4]	0405														
C8011	CAP_402	m39[80C5]	C8585	CAP_402	m39[85A4]	C9051	CAP_402	m39[90D4]	D27301	SUPPR_TRANSIENT_4P1_	m39[73A5]												
C8012	CAP_402	m39[80C5]	C8586	CAP_402	m39[85A3]	C9052	CAP_402	m39[90D4]	0405														
C8014	CAP_P_TH-MCZ	m39[80D4]	C8589	CAP_805	m39[85A3]	C9053	CAP_402	m39[90D3]	D27302	SUPPR_TRANSIENT_4P1_	m39[73A4]												
C8015	CAP_1210	m39[80D3]	C8590	CAP_402	m39[85D3]	C9054	CAP_402	m39[90D3]	0405														
C8016	CAP_805-1	m39[80C2]	C8592	CAP_402	m39[85C2]	C9060	CAP_402	m39[90D3]	D27303	SUPPR_TRANSIENT_4P1_	m39[73C5]												
C8099	CAP_P_CASE-D2E-LF	m39[80C2]	C8595	CAP_402	m39[85D1]	C9065	CAP_402	m39[90D3]	0405														
C8100	CAP_402	m39[81D6]	C8598	CAP_402	m39[85D2]	C9070	CAP_805	m39[90C5]	D27304	SUPPR_TRANSIENT_4P1_	m39[73C4]</												

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D	J600	CON_M14RT_D_THA_M-RT	m39[6D7]	-TH1				L7500	IND_SM	m39[75D1]		PP627	PROBEPOINT_SM	m39[5C8]		PP8908	PROBEPOINT_SM	m39[5B5]		D	C	B	A	D	C	B	A	D	C	B	A		
	J0700	CPU_YONAH_SKT_BGA	m39[7C3 7D7]					L7501	IND_SM	m39[75B2]		PP628	PROBEPOINT_SM	m39[5C8]		PP8909	PROBEPOINT_SM	m39[5B5]															
	J0700	CPU_YONAH_SKT_BGA	m39[8D4 8D8]					L7502	IND_TH-VERT-LF	m39[76D8]		PP629	PROBEPOINT_SM	m39[5C8]		PP8910	PROBEPOINT_SM	m39[5B5]															
	J1000	CON_2RTSM_125_SM-2MT	m39[10B7]	-BLK-LF				L7700	IND_SM1-LF	m39[77C4]		PP630	PROBEPOINT_SM	m39[5C8]		PP8911	PROBEPOINT_SM	m39[5B5]															
	J1101	CON_F28RT_S2MT_SM_F-	m39[11C2]	RT-SM				L7750	IND_SM-LF	m39[77B4]		PP631	PROBEPOINT_SM	m39[5D6]		PP8912	PROBEPOINT_SM	m39[5B5]															
	J2600	BATTERY_2P_TH	m39[26C8]					L7800	IND_3P_SM	m39[78B3]		PP632	PROBEPOINT_SM	m39[5D6]		PP8913	PROBEPOINT_SM	m39[5B5]															
	J2800	CON_F200RT_DDR2DIMM_	m39[28D5]	5MT_SM_F-RT-SM1				L7900	IND_3P_SM	m39[79C3]		PP633	PROBEPOINT_SM	m39[5D6]		PP8914	PROBEPOINT_SM	m39[5A5]															
	J2900	CON_F200RT_DDR2DIMM_	m39[29D5]	5MT_SM_F-RT-SM1				L8000	IND_IHLP	m39[80C3]		PP634	PROBEPOINT_SM	m39[5D6]		PP8915	PROBEPOINT_SM	m39[5A5]															
	J2901	CON_F45T_S2MT_SM_F-S	m39[59C7]	T-SM				L8100	IND_IHLP	m39[81C3]		PP635	PROBEPOINT_SM	m39[5D6]		PP8916	PROBEPOINT_SM	m39[5A5]															
	J2903	CON_M2ST_S2MT_SM-M-S	m39[59C8]	T-SM				L8400	IND_0402	m39[84B7]		PP636	PROBEPOINT_SM	m39[5D6]		PP8920	PROBEPOINT_SM	m39[5A5]															
C	J4700	CON_F10ST_D_SMA_F-ST	m39[47A2]	-SM				L8520	IND_IHLP	m39[85C3]		PP637	PROBEPOINT_SM	m39[5D6]		PP8921	PROBEPOINT_SM	m39[5A5]		C	B	A	D	C	B	A	D	C	B	A			
	J4950	CON_F45T_S2MT_SM_F-S	m39[49C4]	T-SM				L8585	IND_4P_2COIL_SDQ1215	m39[85A3]		PP638	PROBEPOINT_SM	m39[5D6]		PP8922	PROBEPOINT_SM	m39[5A5]															
	J5300	CON_F52RT_D2MT_SM_F-	m39[53C5]	RT-SM				L8715	IND_0402	m39[87A7]		PP639	PROBEPOINT_SM	m39[5D6]		PP8923	PROBEPOINT_SM	m39[5A5]															
	J6000	CON_F30STSM_5047_SM1	m39[60B5]					L8725	IND_0402	m39[87A4]		PP640	PROBEPOINT_SM	m39[5D6]		PP8924	PROBEPOINT_SM	m39[5A5]															
	J6500	CON_M4RT_S2MT_SM-M-R	m39[65D3]	T-SM				L8910	IND_0402	m39[89D7]		PP641	PROBEPOINT_SM	m39[5D6]		PP8925	PROBEPOINT_SM	m39[5A5]															
	J6501	CON_M5RT_S2MT_SMA_M-	m39[65B2]	RT-SM				L8915	IND_0402	m39[89D7]		PP642	PROBEPOINT_SM	m39[5D6]		PP8926	PROBEPOINT_SM	m39[5A5]															
	J6600	CON_F45T_S2MT_SM_F-S	m39[66C2]	T-SM				L8960	IND_0402	m39[89D4]		PP643	PROBEPOINT_SM	m39[5D6]		PP8927	PROBEPOINT_SM	m39[5A5]															
	J6601	CON_M4RT_S2MT_SM-M-R	m39[66B5]	T-SM				L8965	IND_0402	m39[89D4]		PP644	PROBEPOINT_SM	m39[5D6]		PP8928	PROBEPOINT_SM	m39[5A5]															
	J6602	CON_M4RT_S2MT_SM-M-R	m39[66B3]	T-SM				L9010	IND_0402	m39[90D7]		PP645	PROBEPOINT_SM	m39[5D6]		PP8929	PROBEPOINT_SM	m39[5A5]															
	J7300	CON_F45T_S4MT_TH1_F-	m39[73D8]	ST-TH				L9015	IND_0402	m39[90D7]		PP646	PROBEPOINT_SM	m39[5D6]		PP8930	PROBEPOINT_SM	m39[5A5]															
B	J7301	CON_M7RT_S2MT_SM-M-R	m39[73D1]	T-SM				L9060	IND_0402	m39[90D4]		PP647	PROBEPOINT_SM	m39[5D6]		PP8931	PROBEPOINT_SM	m39[5A5]		B	A	D	C	B	A	D	C	B	A	D	C	B	A
	J7303	CON_F9ANG_S4MT_TH1_F	m39[73B3]	-ANG-TH				L9065	IND_0402	m39[90D4]		PP648	PROBEPOINT_SM	m39[5D6]		PP8932	PROBEPOINT_SM	m39[5A5]															
	J9402	CON_F30ST_D_SM_F-ST-	m39[94B6]	SM				L9120	IND_0402	m39[91B6]		PP649	PROBEPOINT_SM	m39[5D6]		PP8933	PROBEPOINT_SM	m39[5A5]															
	J9710	CON_DVI_F32ST_Q2MT_S	m39[97D5]	M_F-ST-SM				L9125	IND_0402	m39[91B6]		PP650	PROBEPOINT_SM	m39[5D6]		PP8934	PROBEPOINT_SM	m39[5A5]															
	JC900	CON_M7ST_SATA_SM-M-S	m39[38B8]	T-SM				L9130	IND_0402	m39[91B7]		PP651	PROBEPOINT_SM	m39[5D6]		PP8935	PROBEPOINT_SM	m39[5A5]															
	JC901	CON_M50SM_5MM_M-ST-S	m39[38C2]	M2-LF				L9135	IND_0402	m39[91A7]		PP652	PROBEPOINT_SM	m39[5C6]		PP8936	PROBEPOINT_SM	m39[5A5]															
	JD600	CON_RJ45_10ANG_S3MT_	m39[43C6]	TH1_F-ANG-TH				L9140	IND_0402	m39[91A7]		PP653	PROBEPOINT_SM	m39[5C6]		PP9000	PROBEPOINT_SM	m39[5B4]															
	JE000	CON_F6ST_S4MT_TH1_F-	m39[46C2]	ST-TH				L9300	IND_0402	m39[93C7]		PP654	PROBEPOINT_SM	m39[5C6]		PP9001	PROBEPOINT_SM	m39[5B4]															
	JE001	CON_F6ST_S4MT_TH1_F-	m39[46B2]	ST-TH				L9305	IND_0402	m39[93C7]		PP655	PROBEPOINT_SM	m39[5C6]		PP9002	PROBEPOINT_SM	m39[5B4]															
	JE310	CON_F45T_USB_S3MT_TH	m39[47D4]	_F-ST-TH				L9310	IND_0402	m39[93C7]		PP656	PROBEPOINT_SM	m39[5C6]		PP9003	PROBEPOINT_SM	m39[5B4]															
A	JE320	CON_F45T_USB_S3MT_TH	m39[47B4]	_F-ST-TH				L9315	IND_0402	m39[93C7]		PP657	PROBEPOINT_SM	m39[5C6]		PP9004	PROBEPOINT_SM	m39[5B4]		A	D	C	B	A	D	C	B	A	D	C	B	A	
	JE330	CON_F45T_USB_S3MT_TH	m39[47A4]	_F-ST-TH				L9320	IND_0402	m39[93B7]		PP658	PROBEPOINT_SM	m39[5C6]		PP9005	PROBEPOINT_SM	m39[5B4]															
	JE350	CON_M14RT_S2MT_SM-M-	m39[47C1]	RT-SM				L9325	IND_0402	m39[93B7]		PP659	PROBEPOINT_SM	m39[5C6]		PP9006	PROBEPOINT_SM	m39[5B4]															
	L1934	IND_0603	m39[19C7]					L9330	IND_0402	m39[93B7]		PP660	PROBEPOINT_SM	m39[5C6]		PP9007	PROBEPOINT_SM	m39[5B4]															
	L1936	IND_0603	m39[19C7]					L9345	IND_0402	m39[93B7]		PP661	PROBEPOINT_SM	m39[5C6]		PP9008	PROBEPOINT_SM	m39[5B4]															
	L1970	IND_1210	m39[19A5]					L9400	IND_SM	m39[94C6]		PP662	PROBEPOINT_SM	m39[5C6]		PP9009	PROBEPOINT_SM	m39[5B4]															
	L1975	IND_0805	m39[19A5]					L9700	FILTER_4P_2012H	m39[97D7]		PP663	PROBEPOINT_SM	m39[5C6]		PP9010	PROBEPOINT_SM	m39[5B4]															
	L2500	IND_SM-3	m39[25B8]					L9701	FILTER_4P_2012H	m39[97D7]		PP664	PROBEPOINT_SM	m39[5C6]		PP9011	PROBEPOINT_SM	m39[5B4]															
	L2507	IND_1206	m39[25A7]					L9702	FILTER_4P_2012H	m39[97C7]		PP665	PROBEPOINT_SM	m39[5C6]		PP9012	PROBEPOINT_SM	m39[5B4]															
	L3301	IND_0402	m39[33D7]					L9703	FILTER_4P_SM	m39[97C7]		PP666	PROBEPOINT_SM	m39[5C6]		PP9013	PROBEPOINT_SM	m39[5B4]															

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D	Q8301	TRA_IRP7413_SO-8	m39[83B4]	Q8302	TRA_2N7002DW_SOT-363	m39[83B5]	Q8303	TRA_2N7002DW_SOT-363	m39[83C5]	Q8520	TRA_HAT2168H_LFFPAK	m39[85D4]	Q8521	TRA_HAT2165H_LFFPAK	m39[85C4]	Q8522	TRA_HAT2165H_LFFPAK	m39[85C5]	Q8523	TRA_2N7002DW_SOT-363	m39[85B3 85B2]	Q8554	TRA_2N7002_SOT23-LF	m39[85A8]	Q8570	TRA_2N7002_SOT23-LF	m39[85A5]	Q8575	TRA_FDC796N_SUPERSOT	m39[85B6]	Q8576	TRA_FDC796N_SUPERSOT	m39[85A2]	Q9400	TRA_S13443DV_TSOP-LF	m39[94C7]	Q9401	TRA_2N7002_SOT23-LF	m39[94C7]	Q9711	TRA_2N7002DW_SOT-363	m39[97D2 97C2]	R75A0	RES_402	m39[75C7]	R85A0	RES_402	m39[85D1]	R600	RES_603	m39[6A7]	R601	RES_402	m39[6D8]	R602	RES_603	m39[6A8]	R603	RES_402	m39[6B1]	R605	RES_603	m39[6A7]	R611	RES_402	m39[6B7]	R612	RES_402	m39[6B7]	R614	RES_402	m39[6B7]	R615	RES_402	m39[6B7]	R616	RES_402	m39[6A7]	R617	RES_402	m39[6A7]	R618	RES_402	m39[6C7]	R619	RES_402	m39[6B7]	R0702	RES_402	m39[7D6]	R0703	RES_402	m39[7C6]	R0704	RES_402	m39[7C5]	R0705	RES_402	m39[7B4]	R0706	RES_402	m39[7B5]	R0707	RES_402	m39[7A4]	R0712	RES_402	m39[7A3]	R0716	RES_402	m39[7B1]	R0717	RES_402	m39[7B1]	R0718	RES_402	m39[7B1]	R0719	RES_402	m39[7B1]	R0720	RES_402	m39[7B7]	R0721	RES_402	m39[7B7]	R0722	RES_402	m39[7A7]	R0730	RES_402	m39[7A4]	R0802	RES_402	m39[6B7]	R0803	RES_402	m39[6B7]	R1000	RES_402	m39[10D3]	R1001	RES_402	m39[10D3]	R1002	RES_402	m39[10C6]	R1005	RES_402	m39[10D3]	R1017	RES_402	m39[10C6]	R1018	RES_402	m39[10B6]	R1019	RES_402	m39[10B6]	R1100	RES_402	m39[11B5]	R1101	RES_402	m39[11C5]	R1102	RES_402	m39[11B4]	R1103	RES_402	m39[11C5]	R1104	RES_402	m39[11B5]	R1106	RES_402	m39[11A3]	R1210	RES_402	m39[12C3]	R1211	RES_402	m39[12C3]	R1220	RES_402	m39[12B7]	R1221	RES_402	m39[12B7]	R1225	RES_402	m39[12B7]	R1226	RES_402	m39[12B7]	R1230	RES_402	m39[12A7]	R1231	RES_402	m39[12A7]	R1235	RES_402	m39[12A7]	R1236	RES_402	m39[12A7]	R1310	RES_402	m39[13D3]	R1410	RES_402	m39[14C3]	R1411	RES_402	m39[14C3]	R1420	RES_402	m39[14B6]	R1430	RES_402	m39[14B6]	R1440	RES_402	m39[14D6]	R1441	RES_402	m39[14D6]	R1975	RES_402	m39[19A4]	R1980	RES_402	m39[19B7]	R1981	RES_402	m39[19B7]	R1982	RES_402	m39[19B8]	R1983	RES_402	m39[19B8]	R2058	RES_402	m39[20B4]	R2059	RES_402	m39[20B4]	R2060	RES_402	m39[20A4]	R2075	RES_402	m39[20C7]	R2077	RES_402	m39[20B7]	R2079	RES_402	m39[20B7]	R2085	RES_402	m39[20C4]	R2100	RES_402	m39[21C3]	R2101	RES_402	m39[21C4]	R2105	RES_402	m39[21D6]	R2107	RES_402	m39[21C2]	R2108	RES_402	m39[21C2]	R2110	RES_402	m39[21C2]	R2194	RES_402	m39[21D4]	R2195	RES_402	m39[21C6]	R2196	RES_402	m39[21C6]	R2197	RES_402	m39[21C6]	R2198	RES_402	m39[21C6]	R2199	RES_402	m39[21C3]	R2200	RES_402	m39[22D7]	R2203	RES_402	m39[22C2]	R2204	RES_402	m39[22C2]	R2205	RES_402	m39[22C6]	R2206	RES_402	m39[22C5]	R2207	RES_402	m39[22C5]	R2211	RES_402	m39[22B3]	R2222	RES_402	m39[22D6]	R2223	RES_402	m39[22D6]	R2225	RES_402	m39[22D7]	R2226	RES_402	m39[22D5]	R2250	RES_402	m39[22D7]	R2251	RES_402	m39[22D6]	R2252	RES_402	m39[47B8]	R2253	RES_402	m39[47B8]	R2254	RES_402	m39[47B8]	R2255	RES_402	m39[22D7]	R2298	RES_402	m39[22B5]	R2299	RES_402	m39[22B5]	R2302	RES_402	m39[23D3]	R2303	RES_402	m39[23D3]	R2305	RES_402	m39[23D3]	R2306	RES_402	m39[23B7]	R2307	RES_402	m39[23A7]	R2308	RES_402	m39[23B7]	R2309	RES_402	m39[23A7]	R2310	RES_402	m39[23A7]	R2311	RES_402	m39[23A7]	R2313	RES_402	m39[23A7]	R2314	RES_402	m39[23A7]	R2316	RES_402	m39[23D7]	R2317	RES_402	m39[23D7]	R2318	RES_402	m39[23D7]	R2319	RES_402	m39[23D2]	R2320	RES_402	m39[23D7]	R2323	RES_402	m39[23D5]	R2326	RES_402	m39[23D6]	R2327	RES_402	m39[23D6]	R2343	RES_402	m39[23D1]	R2388	RES_402	m39[23A3]	R2389	RES_402	m39[38D5]	R2390	RES_402	m39[23B3]	R2395	RES_402	m39[23D7]	R2396	RES_402	m39[23D6]	R2397	RES_402	m39[23D6]	R2398	RES_402	m39[23D8]	R2399	RES_402	m39[23C1]	R2500	RES_603	m39[25A8]	R2501	RES_402	m39[25C8]	R2502	RES_402	m39[25D8]	R2600	RES_402	m39[26C7]	R2606	RES_402	m39[26C7]	R2607	RES_402	m39[26C8]	R2609	RES_402	m39[26D7]	R2611	RES_402	m39[26D5]	R2612	RES_402	m39[26D5]	R2622	RES_402	m39[26D4]	R2623	RES_402	m39[26D2]	R2624	RES_402	m39[26D2]	R2625	RES_402	m39[26D2]	R2626	RES_402	m39[26D2]	R2627	RES_402	m39[26D2]	R2628	RES_402	m39[26D2]	R2629	RES_402	m39[26D2]	R2630	RES_402	m39[26D2]	R2631	RES_402	m39[26D2]	R2632	RES_402	m39[26D2]	R2633	RES_402	m39[26D2]	R2634	RES_402	m39[26D2]	R2636	RES_402	m39[26D2]	R2637	RES_402	m39[26D2]	R2638	RES_402	m39[26D2]	R2639	RES_402	m39[26D2]	R2640	RES_402	m39[26C2]	R2641	RES_402	m39[26C2]	R2642	RES_402	m39[26C2]	R2643	RES_402	m39[26C2]	R2650	RES_402	m39[26C4]	R2651	RES_402	m39[26C1]	R2696	RES_402	m39[26B4]	R2697	RES_402	m39[26C3]	R2698	RES_402	m39[26C5]	R2699	RES_402	m39[26C5]	R2718	RES_402	m39[27B7]	R2719	RES_402	m39[27B7]	R2800	RES_402	m39[28C7]	R2801	RES_402	m39[28C7]	R2900	RES_402	m39[29A3]	R3001	RES_402	m39[30D4]	R3009	RES_402	m39[30D4]	R3011	RES_402	m39[30C4]	R3025	RES_402	m39[30C4]	R3035	RES_402	m39[30B4]	R3100	RES_402	m39[31C5]	R3101	RES_402	m39[31C5]	R3300	RES_402	m39[33B6]	R3301	RES_402	m39[33B7]	R3302	RES_402	m39[33D4]	R3303	RES_402	m39[33C4]	R3304	RES_402	m39[33C7]	R3400	RES_402	m39[34C5]	R3401	RES_402	m39[34B5]	R3402	RES_402	m39[34B5]	R3403	RES_402	m39[34C5]	R3404	RES_402	m39[34C5]	R3405	RES_402	m39[34C5]	R3406	RES_402	m39[34C5]	R3407	RES_402	m39[34B5]	R3408	RES_402	m39[34B5]	R3409	RES_402	m39[34B5]	R3410	RES_402	m39[34B5]	R3411	RES_402	m39[34B5]	R3412	RES_402	m39[34B5]	R3413	RES_402	m39[34B5]	R3414	RES_402	m39[34B5]	R3415	RES_402	m39[34B5]	R3416	RES_402	m39[34B5]	R3417	RES_402	m39[34B5]	R3418	RES_402	m39[34B5]	R3419	RES_402	m39[34A5]	R3420	RES_402	m39[34A5]	R3421	RES_402	m39[34A5]	R3422	RES_402	m39[34A5]	R3423	RES_402	m39[34A5]	R3424	RES_402	m39[34A5]	R3429	RES_402	m39[34C1]	R3430	RES_402	m39[34C1]	R3431	RES_402	m39[34C1]	R3432	RES_402	m39[34C1]	R3433	RES_402	m39[34C1]	R3434	RES_402	m39[34C1]	R3435	RES_402	m39[34C1]	R3436	RES_402	m39[34C1]	R3437	RES_402	m39[34C1]	R3438	RES_402	m39[34C1]	R3439	RES_402	m39[34C1]	R3440	RES_402	m39[34C1]	R3441	RES_402	m39[34C1]	R3442	RES_402	m39[34C1]	R3443	RES_402	m39[34B1]	R3444	RES_402	m39[34B1]	R3445	RES_402	m39[34B1]	R3446	RES_402	m39[34B1]	R3451	RES_402	m39[34C4]	R3452	RES_402	m39[34B7]	R3453	RES_402	m39[34B8]	R3454	RES_402	m39[34B7]	R3455	RES_402	m39[34B8]	R3456	RES_402	m39[34B7]	R3457	RES_402	m39[34B7]	R3458	RES_402	m39[34B8]	R3459	RES_402	m39[34A7]	R3460	RES_402	m39[34A7]	R3461	RES_402	m39[34A7]	R3462	RES_402	m39[34A8]	R3463	RES_402	m39[34A7]	R3470	RES_402	m39[34A5]	R3471	RES_402	m39[34A5]	R3485	RES_402	m39[34D1]	R3486	RES_402	m39[34D1]	R3487	RES_402	m39[34D1]	R3488	RES_402	m39[34D1]	R3489	RES_402	m39[34D2]	R3490	RES_402	m39[34D2]	R3491	RES_402	m39[34D2]	R3492	RES_402	m39[34D2]	R3493	RES_402	m39[34D7]	R3494	RES_402	m39[34D7]	R3495	RES_402	m39[34D7]	R3496	RES_402	m39[34C5]	R3497	RES_402	m39[34D4]	R3498	RES_402	m39[34D5]	R3499	RES_402	m39[34D5]	R3824	RES_402	m39[38D2]	R3851	RES_402	m39[38D3]	R3852	RES_402	m39[38D2]	R3853	RES_402	m39[38D2]	R3857	RES_402	m39[38B3]	R3858	RES_402	m39[38B3]	R3859	RES_402	m39[38B2]	R3897	RES_402	m39[38B7]	R3899	RES_402	m39[38B5]	R4101	RES_402	m39[41D7]	R4102	RES_402	m39[41C7]	R4103	RES_402	m39[41C2]	R4104	RES_402	m39[41C2]	R4105	RES_402	m39[41C2]	R4106	RES_402	m39[41C2]	R4117	RES_402	m39[41B2]	R4118	RES_402	m39[41B2]	R4119	RES_402	m39[41B2]	R4120	RES_402	m39[41B2]	R4122	RES_402	m39[41A3]	R4123	RES_402	m39[41A2]	R4130	RES_402	m39[41C4]	R4131	RES_402	m39[41C4]	R4150	RES_402	m39[41C8]	R4151	RES_402	m39[41D7]	R4200	RES_402	m39[42D2]	R4201	RES_402	m39[42C2]	R4202	RES_402	m39[42D6]	R4300	RES_402	m39[43D7]	R4301	RES_603	m39[43D2]	R4302	RES_603	m39[43D2]	R4303	RES_603	m39[43D1]	R4304	RES_603	m39[43D1]	R4350	RES_402	m39[43C7]	R4351	RES_402	m39[43C7]	R4352	RES_402	m39[43C7]	R4353	RES_402	m39[43C7]	R4354	RES_402	m39[43C7]	R4355	RES_402	m39[43C7]	R4356	RES_402	m39[43C7]	R4357	RES_402	m39[43B7]	R4402	RES_402	m39[44B3]	R4403	RES_402	m39[44B5]	R4404	RES_402	m39[44A7]	R4407	RES_402	m39[44A7]	R4409	RES_402	m39[44B3]	R4410	RES_402	m39[44D2]	R4411	RES_402	m39[44D6]	R4412	RES_402	m39[44C1]	R4413	RES_402	m39[44C3]	R4414	RES_402	m39[44C3]	R4416	RES_402	m39[44A5]	R4450	RES_402	m39[44B3]	R4451	RES_402	m39[44B3]	R4452	RES_402	m39[44B3]	R4453	RES_402	m39[44B3]	R4454	RES_402	m39[44B3]	R4455	RES_402	m39[44B3]	R4602	RES_2512-1	m39[46D5]	R4650	RES_402	m39[46C8]	R4651	RES_402	m39[46C7]	R4652	RES_402	m39[46B8]	R4653	RES_402	m39[46B7]	R4654	RES_402	m39[46B7]	R4656	RES_2512-1	m39[46D6]	R4660	RES_402	m39[46C7]	R4661	RES_402	m39[46C7]	R4662	RES_402	m39[46B7]	R4663	RES_402	m39[46B7]	R4664	RES_402	m39[46B7]	R4690	RES_402	m39[46A7]	R4712	RES_402	m39[47C6]	R4713	RES_402	m39[47C6]	R4722	RES_402	m39[47B6]	R4723	RES_402	m39[47B6]	R4732	RES_402	m39[47A6]	R4733	RES_402	m39[47A6]	R4734	RES_805	m39[47A6]	R4735	RES_805	m39[47C6]	R4736	RES_805	m39[47D6]	R4742	RES_402	m39[47C2]	R4743	RES_402	m39[47C2]	R4746	RES_805	m39[47D2]	R4754	RES_402	m39[47C2]	R4755	RES_402	m39[47B2]	R5300	RES_402	m39[53C3]	R5301	RES_402	m39[53B3]	R5302	RES_402	m39[53B4]	R5303	RES_402	m39[53B4]	R5304	RES_402	m39[53C6]	R5801	RES_402	m39[58C2]	R5802	RES_402	m39[58C2]	R5803	RES_402	m39[58C2]	R5808	RES_402	m39[59C3]	R5809	RES_402	m39[58C2]	R5815	RES_402	m39[59B3]	R5817	RES_402	m39[59B3]	R5818	RES_402	m39[59B3]	R5819	RES_402	m39[59B3]	R5821	RES_402	m39[59B3]	R5822	RES_402	m39[59B3]	R5823	RES_402	m39[59B3]	R5824	RES_402	m39[59B3]	R5825	RES_402	m39[59B3]	R5826	RES_402	m39[59B3]	R5827	RES_402	m39[59C5]	R5828	RES_402	m39[59B3]	R5829	RES_402	m39[59C3]	R5830	RES_402	m39[59C3]	R5831	RES_402	m39[59C3]	R5832	RES_402	m39[59C3]	R5833	RES_4

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D	R7217	RES_402	m39[72B5]	R7218	RES_402	m39[72A5]	R7219	RES_402	m39[72B4]	R7302	RES_402	m39[73A3]	R7313	RES_402	m39[73C2]	R7314	RES_402	m39[73C2]	R7400	RES_402	m39[74B4]	R7404	RES_402	m39[74C4]	R7405	RES_402	m39[74D3]	R7407	RES_402	m39[74B4]	R7408	RES_402	m39[74A4]	R7409	RES_402	m39[74B4]	R7410	RES_805	m39[74D2]	R7411	RES_805	m39[74C2]	R7412	RES_805	m39[74D2]	R7413	RES_402	m39[74B4]	R7414	RES_805	m39[74B7]	R7415	RES_805	m39[74B8]	R7416	RES_805	m39[74C7]	R7417	RES_805	m39[74C8]	R7418	RES_402	m39[74B6]	R7419	RES_402	m39[74B6]	R7420	RES_402	m39[74D4]	R7421	RES_402	m39[74D8]	R7422	RES_402	m39[74D7]	R7423	RES_402	m39[74B6]	R7424	RES_402	m39[74B6]	R7425	RES_402	m39[74A5]	R7426	RES_402	m39[74A4]	R7427	RES_402	m39[74A4]	R7429	RES_805	m39[74C2]	R7430	RES_402	m39[74C1]	R7431	RES_402	m39[74C2]	R7432	RES_402	m39[74B1]	R7500	RES_402	m39[75C2]	R7501	RES_603	m39[75C2]	R7502	RES_1206	m39[75B2]	R7503	RES_1206	m39[75D2]	R7504	RES_402	m39[75C1]	R7505	RES_402	m39[75B2]	R7506	RES_603	m39[75B2]	R7507	RES_402	m39[75B1]	R7508	RES_402	m39[75B8]	R7509	RES_402	m39[75B8]	R7510	RES_402	m39[75B8]	R7511	RES_402	m39[75B7]	R7512	RES_402	m39[75D7]	R7513	RES_402	m39[75B7]	R7514	RES_402	m39[75B8]	R7515	RES_402	m39[75B4]	R7516	RES_402	m39[75B4]	R7517	RES_402	m39[75B5]	R7518	RES_402	m39[75B5]	R7519	RES_402	m39[75C7]	R7520	RES_402	m39[75D7]	R7521	RES_402	m39[75D7]	R7522	RES_402	m39[75A5]	R7523	RES_402	m39[75A5]	R7526	THERMISTE_402	m39[75C8]	R7527	RES_402	m39[75C8]	R7528	RES_402	m39[75A5]	R7529	RES_402	m39[75A5]	R7530	RES_402	m39[75B4]	R7531	THERMISTE_0603-LF	m39[75B4]	R7590	RES_402	m39[75C7]	R7591	RES_402	m39[75C7]	R7592	RES_402	m39[75C7]	R7593	RES_402	m39[75C7]	R7594	RES_402	m39[75C7]	R7595	RES_402	m39[75C7]	R7596	RES_402	m39[75D7]	R7597	RES_402	m39[76D6]	R7598	RES_402	m39[76C7]	R7599	RES_2512-1	m39[76D7]	R7600	RES_402	m39[76D3]	R7602	RES_402	m39[76D3]	R7603	RES_402	m39[76C3]	R7604	RES_402	m39[76C3]	R7605	RES_402	m39[76D4]	R7606	RES_402	m39[76D4]	R7607	RES_402	m39[76C3]	R7612	RES_402	m39[76B2]	R7620	RES_402	m39[76D2]	R7623	RES_402	m39[76D1]	R7630	RES_402	m39[76C8]	R7631	RES_402	m39[76C8]	R7632	RES_402	m39[76C7]	R7639	RES_402	m39[76B7]	R7640	RES_402	m39[76A7]	R7641	RES_402	m39[76A7]	R7642	RES_402	m39[76A6]	R7643	RES_1206	m39[76B5]	R7644	RES_1206	m39[76B4]	R7659	RES_402	m39[76D5]	R7669	RES_402	m39[76D4]	R7700	RES_402	m39[77D5]	R7701	RES_402	m39[77C5]	R7704	RES_402	m39[77C5]	R7705	RES_402	m39[77C5]	R7706	RES_402	m39[77C4]	R7707	RES_402	m39[77C3]	R7708	RES_402	m39[77C3]	R7710	RES_402	m39[77D5]	R7750	RES_402	m39[77A5]	R7751	RES_402	m39[77A5]	R7752	RES_402	m39[77A5]	R7753	RES_402	m39[77B7]	R7754	RES_402	m39[77B6]	R7755	RES_402	m39[77B5]	R7756	RES_402	m39[77B5]	R7757	RES_402	m39[77B6]	R7759	RES_402	m39[77D7]	R7794	RES_402	m39[77C7]	R7800	RES_402	m39[78C7]	R7801	RES_402	m39[78B7]	R7802	RES_402	m39[78B3]	R7803	RES_402	m39[78B3]	R7804	RES_1206	m39[78B4]	R7805	RES_402	m39[78B5]	R7812	RES_402	m39[78B3]	R7840	RES_402	m39[78C5]	R7892	RES_402	m39[78B7]	R7901	RES_402	m39[79C3]	R7902	RES_1206	m39[79C4]
	R7903	RES_402	m39[79C3]	R7904	RES_402	m39[79C5]	R7905	RES_402	m39[79D7]	R7906	RES_402	m39[79A4]	R7910	RES_402	m39[79B2]	R7911	RES_402	m39[79B3]	R7912	RES_402	m39[79B3]	R7913	RES_402	m39[79A2]	R7914	RES_402	m39[79A3]	R7915	RES_402	m39[79A3]	R7940	RES_402	m39[79D5]	R7991	RES_402	m39[79C7]	R7992	RES_402	m39[79C7]	R7999	RES_402	m39[79C3]	R8000	RES_402	m39[80C3]	R8001	RES_402	m39[80C7]	R8002	RES_1206	m39[80C4]	R8003	RES_402	m39[80C3]	R8004	RES_402	m39[80C5]	R8005	RES_402	m39[80D7]	R8007	RES_402	m39[80A4]	R8010	RES_402	m39[80B2]	R8011	RES_402	m39[80B3]	R8012	RES_402	m39[80B3]	R8040	RES_402	m39[80C5]	R8092	RES_402	m39[80C7]	R8099	RES_402	m39[80C3]	R8101	RES_402	m39[81C3]	R8102	RES_1206	m39[81C4]	R8103	RES_402	m39[81C3]	R8104	RES_402	m39[81C5]	R8105	RES_402	m39[81D7]	R8107	RES_402	m39[81A4]	R8110	RES_402	m39[81B3]	R8140	RES_402	m39[81C5]	R8190	RES_402	m39[81C3]	R8191	RES_402	m39[81C7]	R8192	RES_402	m39[81C7]	R8198	RES_402	m39[81A5]	R8199	RES_402	m39[81A5]	R8300	RES_402	m39[83B4]	R8301	RES_402	m39[83C5]	R8302	RES_402	m39[83B5]	R8303	RES_402	m39[83C4]	R8495	RES_402	m39[84A2]	R8496	RES_402	m39[84A2]	R8497	RES_402	m39[84A2]	R8502	RES_402	m39[85D6]	R8503	RES_402	m39[85D7]	R8504	RES_402	m39[85D7]	R8505	RES_402	m39[85C7]	R8506	RES_402	m39[85C8]	R8507	RES_402	m39[85D7]	R8508	RES_402	m39[85C7]	R8510	RES_402	m39[85C5]	R8521	RES_402	m39[85C3]	R8522	RES_402	m39[85C3]	R8523	RES_402	m39[85C3]	R8524	RES_402	m39[85B3]	R8525	RES_402	m39[85B3]	R8526	RES_402	m39[85B3]	R8540	RES_402	m39[85C2]	R8554	RES_402	m39[85B7]	R8555	RES_402	m39[85B6]	R8556	RES_402	m39[85B6]	R8557	RES_402	m39[85A6]	R8561	RES_402	m39[85A7]	R8570	RES_402	m39[85A5]	R8585	RES_603	m39[85A3]	R8586	RES_402	m39[85A3]	R8588	RES_402	m39[85C5]	R8590	RES_402	m39[85C3]	R8591	RES_402	m39[85D3]	R8592	RES_402	m39[85D2]	R8593	RES_402	m39[85D3]	R8594	RES_402	m39[85D3]	R8596	RES_402	m39[85D3]	R8597	THERMISTE_0603-LF	m39[85D3]	R8598	RES_402	m39[85D2]	R8599	RES_1206	m39[85C4]	R8630	RES_603	m39[86C7]	R8710	RES_402	m39[87B8]	R8711	RES_402	m39[87A8]	R8712	RES_402	m39[87B7]	R8713	RES_402	m39[87A7]	R8720	RES_402	m39[87B4]	R8721	RES_402	m39[87A4]	R8722	RES_402	m39[87B4]	R8723	RES_402	m39[87A4]	R8730	RES_402	m39[87A3]	R8731	RES_402	m39[87A3]	R8732	RES_402	m39[87A3]	R8733	RES_402	m39[87A1]	R8800	RES_603	m39[88D7]	R8801	RES_402	m39[88A7]	R8802	RES_402	m39[88D4]	R8803	RES_402	m39[88D4]	R8804	RES_402	m39[88D4]	R8805	RES_402	m39[88C4]	R8806	RES_402	m39[88C4]	R8807	RES_402	m39[88C4]	R8808	RES_402	m39[88C4]	R8809	RES_402	m39[88C4]	R8810	RES_402	m39[88C4]	R8811	RES_402	m39[88B4]	R8812	RES_402	m39[88C4]	R8813	RES_402	m39[88D4]	R8830	RES_402	m39[88B4]	R8831	RES_402	m39[88B4]	R8832	RES_402	m39[88B4]	R8833	RES_402	m39[88B4]	R8850	RES_402	m39[88B4]	R8930	RES_402	m39[89C7]	R8931	RES_402	m39[89C7]	R8932	RES_402	m39[89C7]	R8933	RES_402	m39[89C7]	R8940	RES_402	m39[89B8]	R8941	RES_402	m39[89B8]	R8942	RES_402	m39[89B7]	R8943	RES_402	m39[89B7]	R8944	RES_402	m39[89B7]	R8945	RES_402	m39[89B7]	R8946	RES_402	m39[89B7]	R8947	RES_402	m39[89B7]
	R8948	RES_402	m39[89A7]	R8949	RES_402	m39[89A7]	R8980	RES_402	m39[89C4]	R8981	RES_402	m39[89C4]	R8982	RES_402	m39[89C4]	R8983	RES_402	m39[89C4]	R8990	RES_402	m39[89B5]	R8991	RES_402	m39[89B4]	R8992	RES_402	m39[89B4]	R8993	RES_402	m39[89B4]	R8994	RES_402	m39[89B4]	R8995	RES_402	m39[89B4]	R8996	RES_402	m39[89B4]	R8997	RES_402	m39[89B4]	R8998	RES_402	m39[89A4]	R8999	RES_402	m39[89A4]	R9030	RES_402	m39[90C7]	R9031	RES_402	m39[90C7]	R9032	RES_402	m39[90C7]	R9033	RES_402	m39[90C7]	R9040	RES_402	m39[90B8]	R9041	RES_402	m39[90A8]	R9042	RES_402	m39[90B7]	R9043	RES_402	m39[90B7]	R9044	RES_402	m39[90B7]	R9045	RES_402	m39[90B7]	R9046	RES_402	m39[90B7]	R9047	RES_402	m39[90B7]	R9048	RES_402	m39[90A7]	R9049	RES_402	m39[90A7]	R9080	RES_402	m39[90C4]	R9081	RES_402	m39[90C4]	R9082	RES_402	m39[90C4]	R9083	RES_402	m39[90B5]	R9090	RES_402	m39[90A4]	R9091	RES_402	m39[90A4]	R9092	RES_402	m39[90B4]	R9093	RES_402	m39[90B4]	R9094	RES_402	m39[90B4]	R9095	RES_402	m39[90B4]	R9096	RES_402	m39[90B4]	R9097	RES_402	m39[90B4]	R9098	RES_402	m39[90A4]	R9099	RES_402	m39[90A4]	R9190	RES_402	m39[91D2]	R9191	RES_402	m39[91D2]	R9195	RES_402	m39[91A3]	R9202	RES_402	m39[92C6]	R9250	RES_402	m39[92C6]	R9350	RES_402	m39[93A8]	R9351	RES_402	m39[93A8]	R9370	RES_402	m39[93D1]	R9371	RES_402	m39[93D1]	R9372	RES_402	m39[93C1]	R9373	RES_402	m39[93C1]	R9390	RES_402	m39[93A1]	R9391	RES_402	m39[93A1]	R9400	RES_402	m39[94C8]	R9401	RES_402	m39[94C7]	R9410	RES_402	m39[94C6]	R9411	RES_402	m39[94C6]	R9450	RES_402	m39[94C2]	R9470	RES_402	m39[94C7]	R9472	RES_402	m39[94B3]	R9473	RES_402	m39[94B2]	R9474	RES_402	m39[94B2]	R9475	RES_402	m39[94B1]	R9490	RES_805	m39[94C6]	R9491	RES_805	m39[94D6]	R9700	RES_402	m39[97D7]	R9701	RES_402	m39[97D8]	R9702	RES_402	m39[97D7]	R9703	RES_402	m39[97D7]	R9704	RES_402	m39[97C7]	R9705	RES_402	m39[97C7]	R9706	RES_402	m39[97D8]	R9707	RES_402	m39[97C8]	R9708	RES_402	m39[97C7]	R9709	RES_402	m39[97C7]	R9710	RES_402	m39[97D2]	R9711	RES_402	m39[97D2]	R9712	RES_402	m39[97D2]	R9713	RES_402	m39[97C2]	R9714	RES_402	m39[97C2]	R9715	RES_402	m39[97C7]	R9716	RES_402	m39[97C8]	R9717	RES_402	m39[97C8]	R9720	RES_402	m39[97D1]	R9721	RES_402	m39[97D1]	R9722	RES_402	m39[97C2]	R9740	RES_402	m39[97A8]	R9741	RES_402	m39[97A8]	R9742	RES_402	m39[97A7]	R9750	RES_402	m39[97A3]	R9751	RES_402	m39[97A3]	RP2300	RP4K4P_SM-LF	m39[23D5]	RP3000	RP4K4P_SM-LF	m39[30B4 30C4 30D4 30D4]	RP3001	RP4K4P_SM-LF	m39[30C4 30A4 30A4 30D4]	RP3002	RP4K4P_SM-LF	m39[30A4 30A4 30A4 30D4]	RP3003	RP4K4P_SM-LF	m39[30C4 30C4 30C4 30D4]	RP3004	RP4K4P_SM-LF	m39[30C4 30C4 30D4]	RP3005	RP4K4P_SM-LF	m39[30B4 30A4 30A4 30D4]	RP3006	RP4K4P_SM-LF	m39[30B4 30B4 30A4 30D4]	RP3007	RP4K4P_SM-LF	m39[30C4 30C4 30C4 30C4]	RP3008	RP4K4P_SM-LF	m39[30C4 30C4 30C4 30C4]	RP3009	RP4K4P_SM-LF	m39[30B4 30B4 30C4 30C4]	RP3010	RP4K4P_SM-LF	m39[30B4 30B4 30B4 30B4]	RP3011	RP4K4P_SM-LF	m39[30B4 30A4 30B4 30B4]	RP7200	RP4K4P_SM-LF	m39[72A4]	SDF3800	PCB_STANDOFF	m39[38B2]	SDF3801	PCB_STANDOFF	m39[38A2]	SDF4700	PCB_STANDOFF	m39[47A2]	SDF4701	PCB_STANDOFF	m39[47A2]	SDF5300	PCB_STANDOFF	m39[53A5]	SDF5301	PCB_STANDOFF	m39[53A5]	SDF9400	PCB_STANDOFF	m39[94B6]	SDF9401	PCB_STANDOFF	m39[94A6]	SW2600	SWI_TACT_4SM_EVQPH_S	m39[26C6]	M-LF	SW5900	SWI_TACT_4SM_EVQPH_S	m39[59D8]	M-LF	SW5901	SWI_TACT_4SM_EVQPH_S	m39[59B8]	M-LF	U600	74L1C125_TSSOP	m39[6A7 6B7 6B7 6C7]						
	U601	SN74LVC1G04_SOT23-5	m39[6C7]	U1000	ADT7461_MSOP	m39[10D5]	U1200	NB_945GM_BGA	m39[12D5]	U1200	NB_945GM_BGA	m39[13D4]	U1200	NB_945GM_BGA	m39[14D5]	U1200	NB_945GM_BGA	m39[15D3 15D7]	U1200	NB_945GM_BGA	m39[16D2 16C8]	U1200	NB_945GM_BGA	m39[17D5]	U1200	NB_945GM_BGA	m39[18D4 18D7]	U2100	SB_1CH7M_BGA	m39[21D6]	U2100	SB_1CH7M_BGA	m39[22B7 22D3]	U2100	SB_1CH7M_BGA	m39[23D4]	U2100	SB_1CH7M_BGA	m39[24D4 24D7]	U2601	MC74VHC1G08_SOT23-5	m3																																																																																																																																																																																																																																																																																																																																										

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D	XW9330	SHORT_SM	m39[93B7]												
	XW9345	SHORT_SM	m39[93A7]												
	Y2600	CRYSTAL_4PIN_SM-LF	m39[26D8]												
	Y3301	CRYSTAL_5X3.2-SM	m39[33C7]												
	Y4101	CRYSTAL_4PIN_SM-3.2X 2.5MM	m39[41B5]												
	Y4400	CRYSTAL_HC49-USMD	m39[44D2]												
	Y5800	CRYSTAL_SM-3	m39[59B8]												
	Y6700	CRYSTAL_4PIN_SM-LF	m39[59B7]												
	ZH500	HOLE_VIA	m39[5C1]												
	ZH501	HOLE_VIA	m39[5C1]												
C	ZH502	HOLE_VIA	m39[5C1]												
	ZH503	HOLE_VIA	m39[5C1]												
	ZH504	HOLE_VIA	m39[5B1]												
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	ZH608	MTGHOLE	m39[9D3]												
	ZH609	MTGHOLE	m39[9D2]												
	ZH610	MTGHOLE	m39[9D2]												
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