

MODEL NAME :DCL56 VGA/B

COMPAL P/N : LS-2231

PCB NO : DA8CL56L100

Revision :2.0

DATE :2004/03/02

History :

- (1) 12/05 : Modify ATI trapping value page 03
- (2) 12/06 : VRAM Channel A&B swap page 08 & 09
- (3) 12/08 : ADD R82 for SPWG function verify page11
- (4) 12/10 : (a) ADD R83 R84 to detect Graphic chip type page 10  
(b) Change JP4 connecter to Match LS-2201 T/P Board page 10
- (5) 12/11 : ADD R85 R86 pull down for ATI suggest page 4,5
- (6) 12/12 : ADD power play function page 12
- (7) 01/30 : ADD L13 L14 L15 & L5 from 0603 to 0805 page 10
- (8) 01/30 : ADD C128 page 11
- (9) 03/02 : Change C21,C72,C96 to 100U page 06,08,09

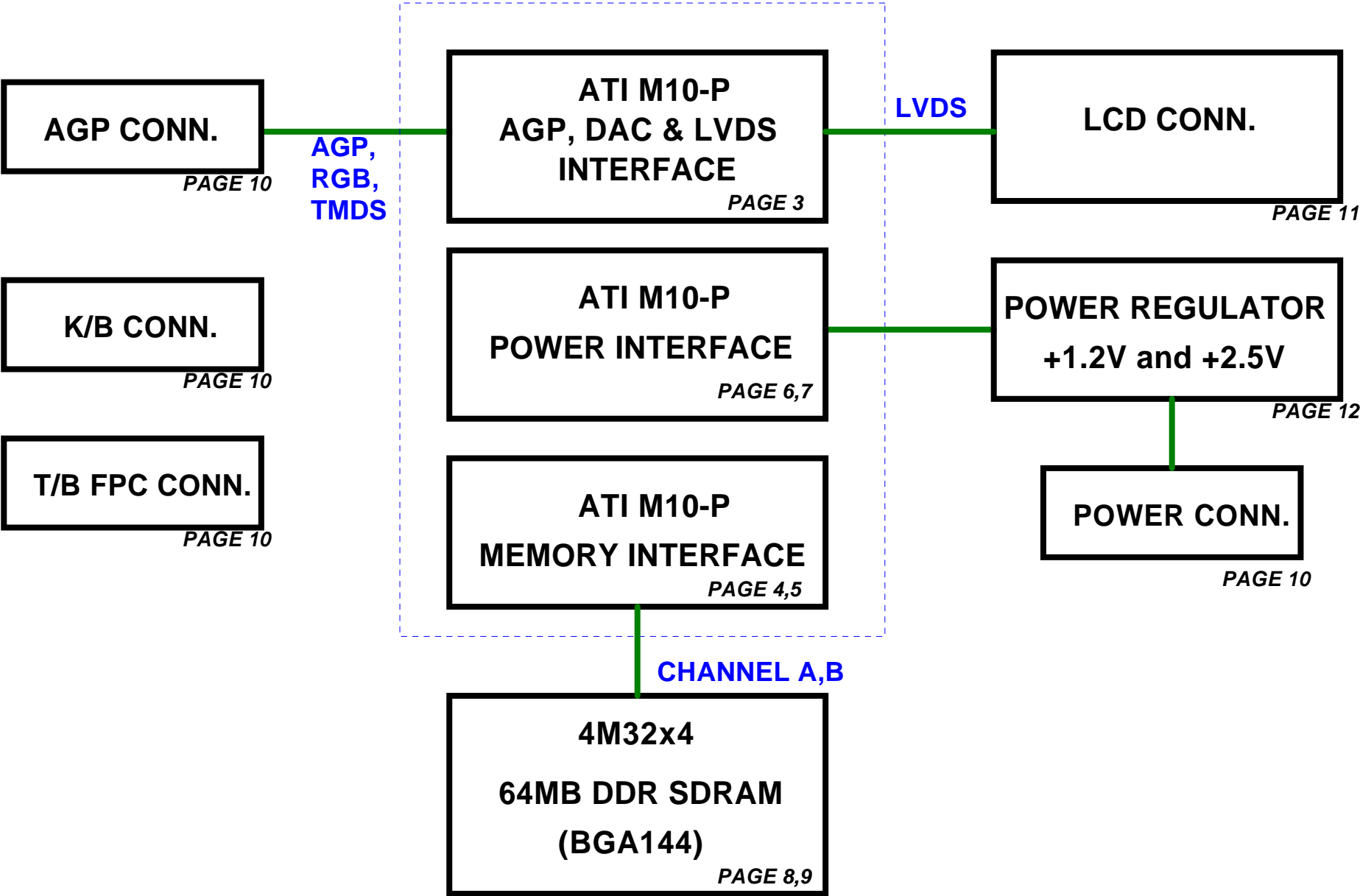
DCL56 Schematics Document

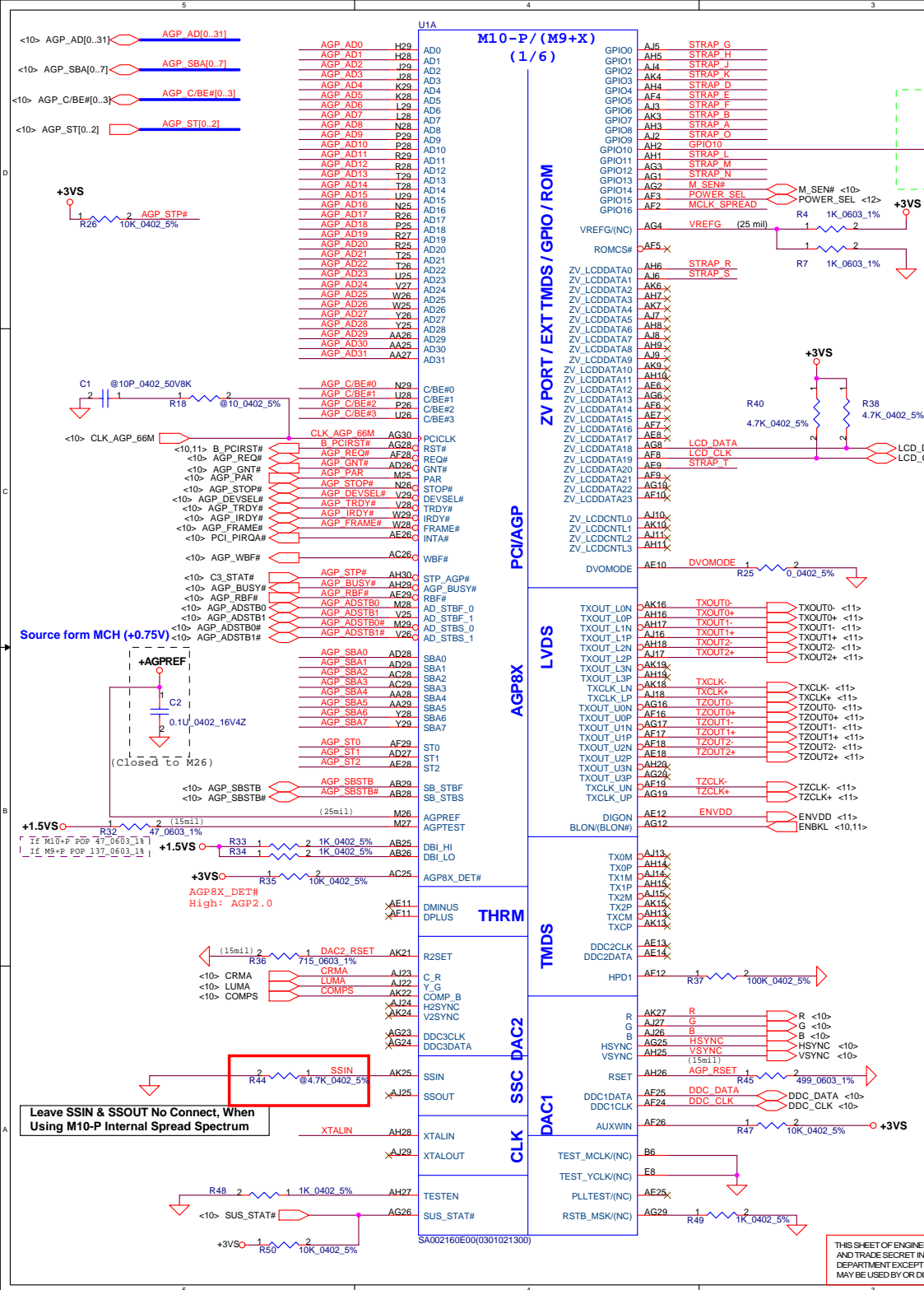
ATI Mobility Radeon M10/M11P Graphics Controller

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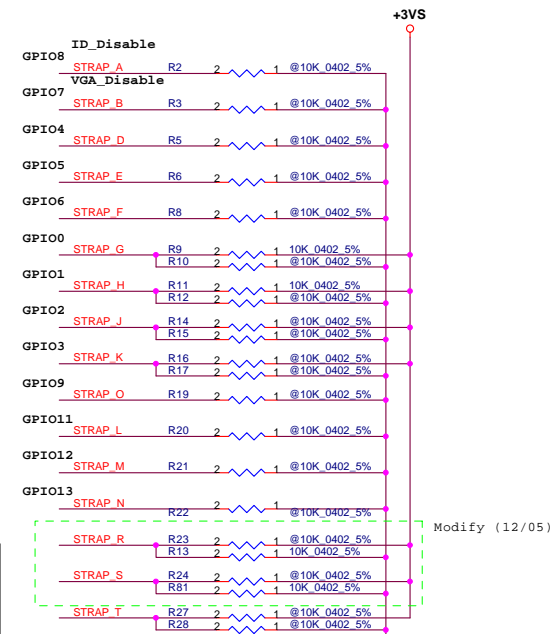
Compal Electronics, Inc.			
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Size	Document Number		Rev
A3	DCL56 VGA/B LS-2231		2.0
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ATI MOBILITY RADEON M10-P



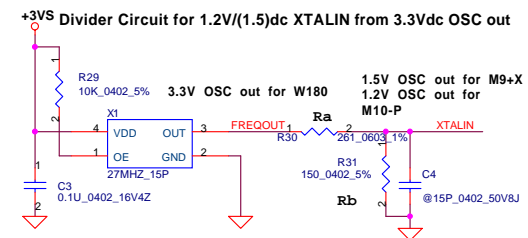


## AGP, DAC & LVDS INTERFACE

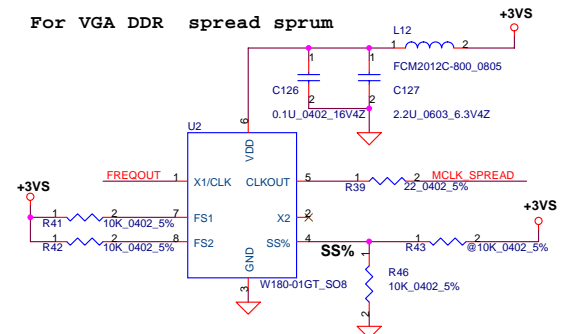


### Vedio Memory Config.

R	S	Size	Vendor
0	0	4Mx32	Samsung
0	1	4Mx32	Hynix
1	0	8Mx32	Samsung
1	1	8Mx32	Hynix



For VGA DDR spread sprum







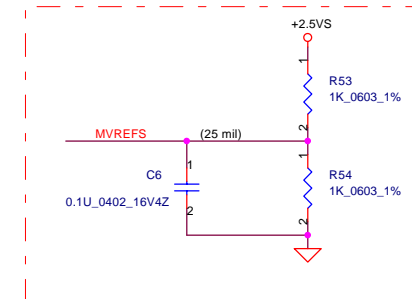
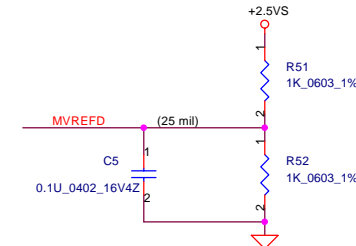
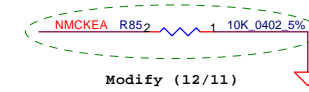
 Note: PLACE CLOSE TO U6 (VGA M9+X/M10-P)

<b>Compal Electronics, Inc.</b>			
Title			
<b>AT1M10-P &amp; M9+X (AGP BUS)</b>			
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# MEMORY INTERFACE A

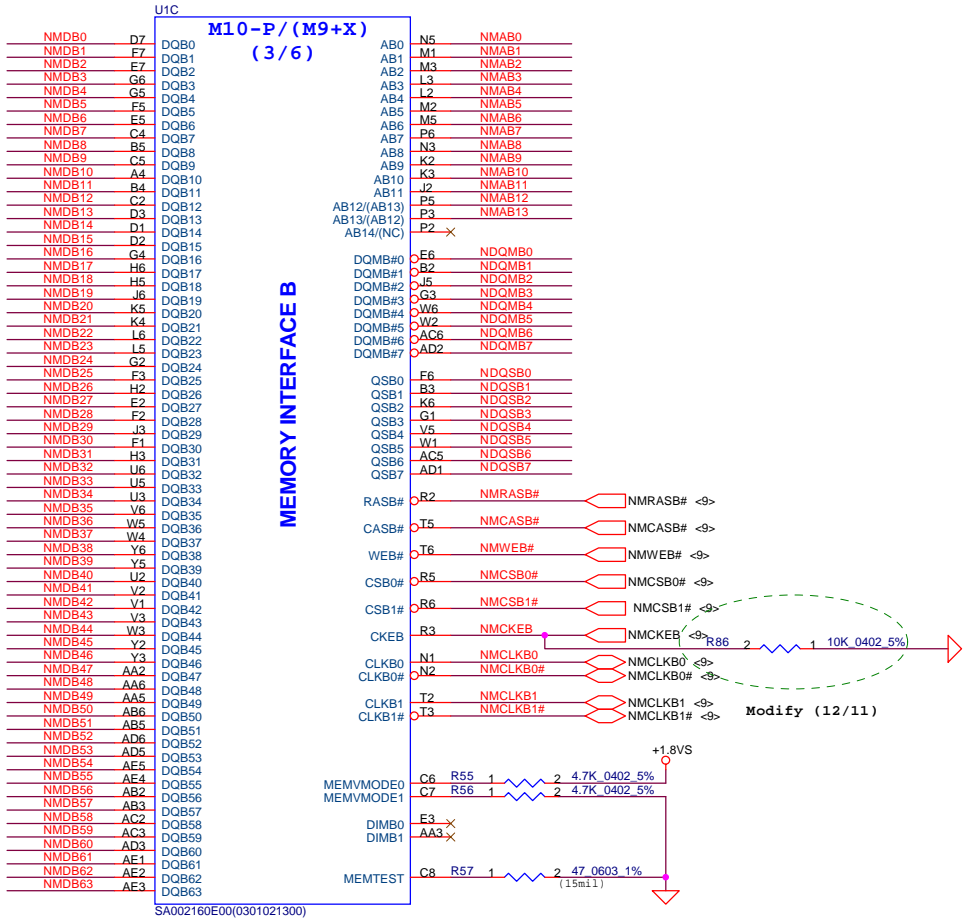
<8> NMDA[0..63]  NMDA[0..63]  
<8> NMAA[0..13]  NMAA[0..13]  
<8> NDQMA[0..7]  NDQMA[0..7]  
<8> NDQSA[0..7]  NDQSA[0..7]



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ATIM10-P/M9+X DDR-A			
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MEMORY INTERFACE B



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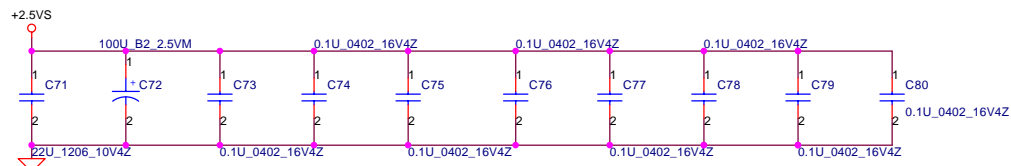
Compal Electronics, Inc.			
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ATI M10-P/M9+X DDR-B			
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As close as ppossible to related pin

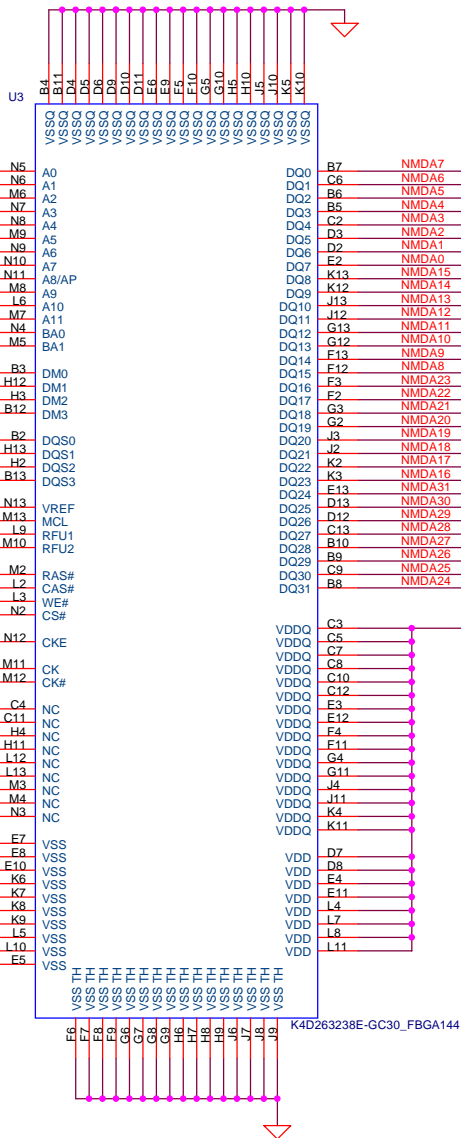
# POWER INTERFACE

As close as ppossible to related pin

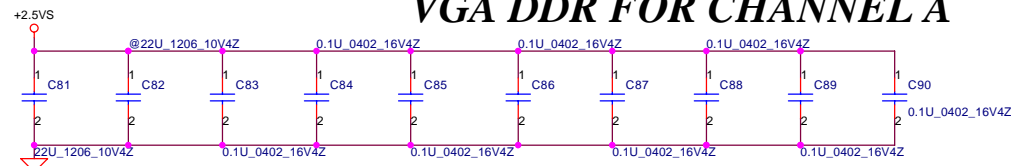


As close as possible to related pin

<4> NMAA[0..13] NMAA[0..13]  
 <4> NMDA[0..63] NMDA[0..63]  
 <4> NDQMA[0..7] NDQMA[0..7]  
 <4> NDQSA[0..7] NDQSA[0..7]

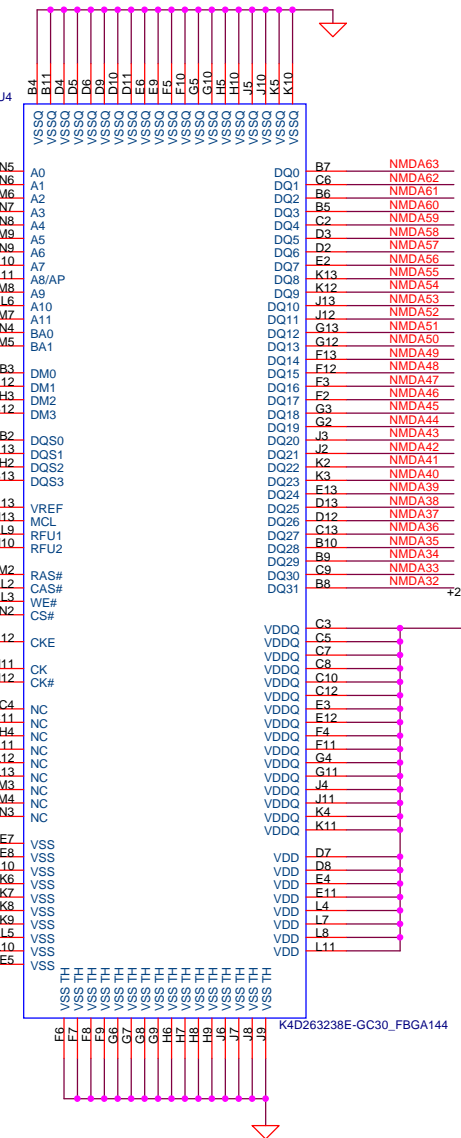


K4D263238E-GC30\_FBG144



As close as possible to related pin

<4> NMAA[0..13] NMAA[0..13]  
 <4> NMDA[0..63] NMDA[0..63]  
 <4> NDQMA[0..7] NDQMA[0..7]  
 <4> NDQSA[0..7] NDQSA[0..7]



K4D263238E-GC30\_FBG144

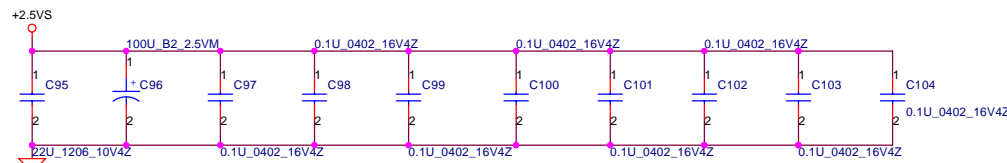
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# VGA DDR FOR CHANNEL A

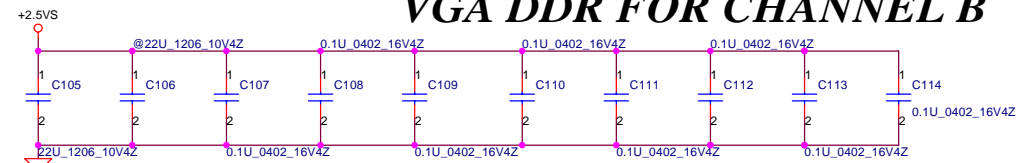
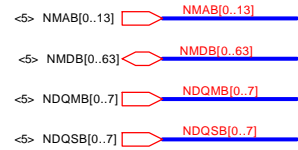
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VGA DDR FOR CHANNEL A			
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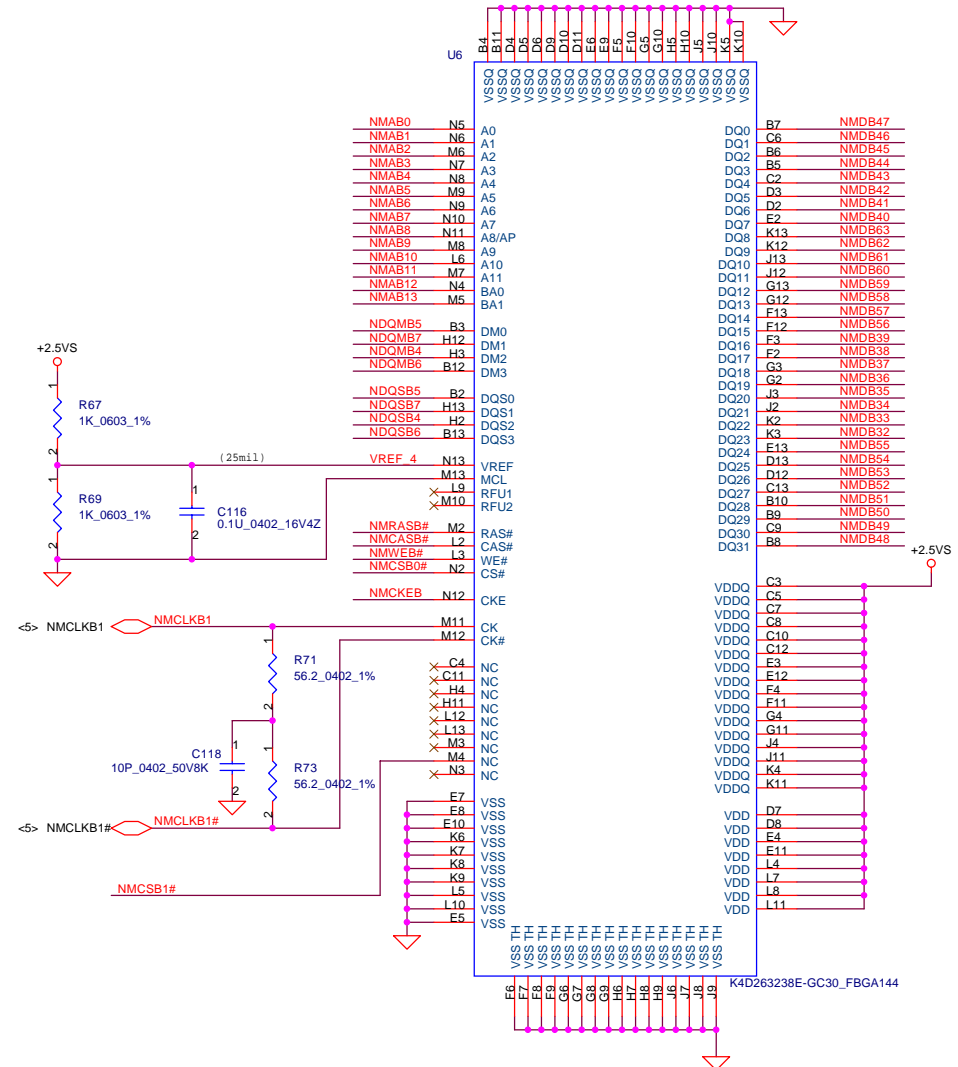
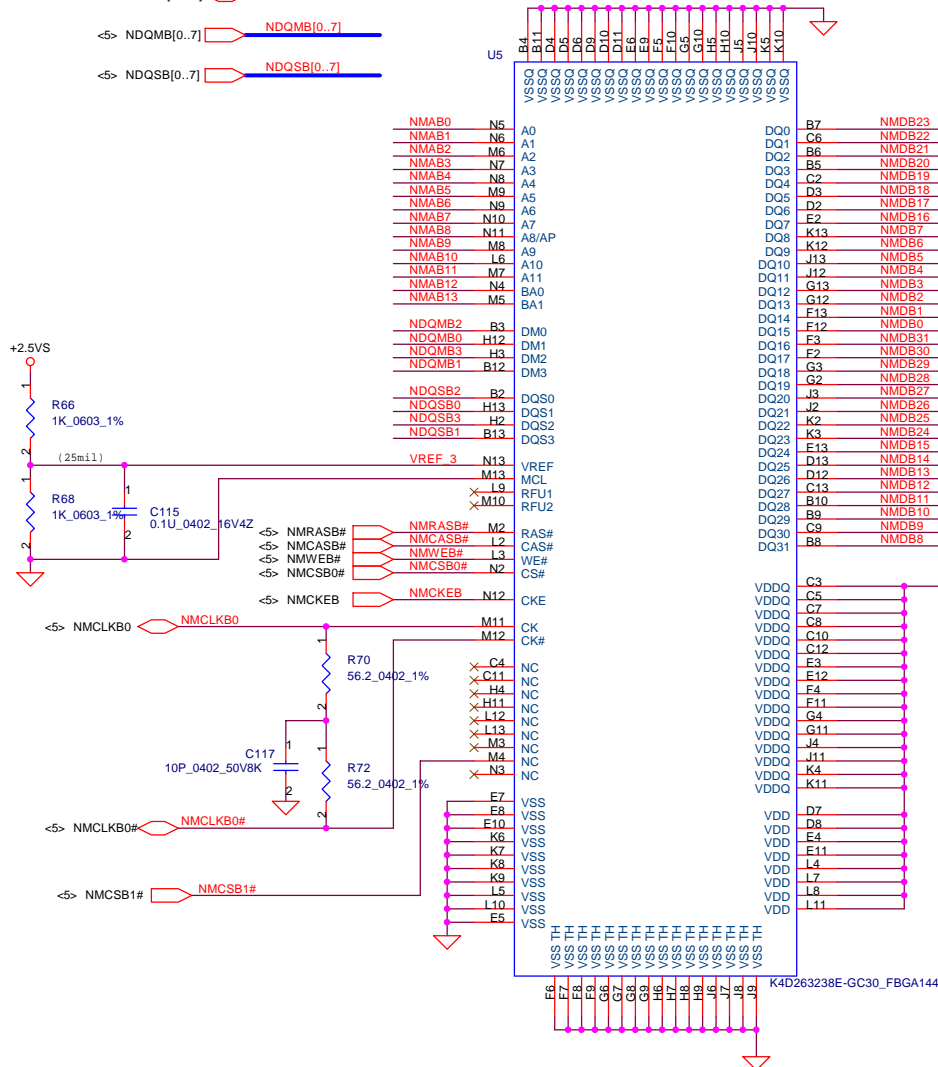
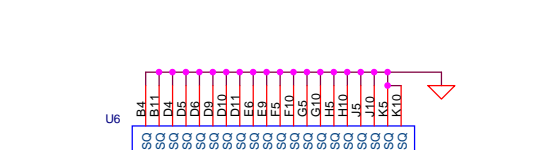
### VGA DDR FOR CHANNEL B



As close as ppossible to related pin



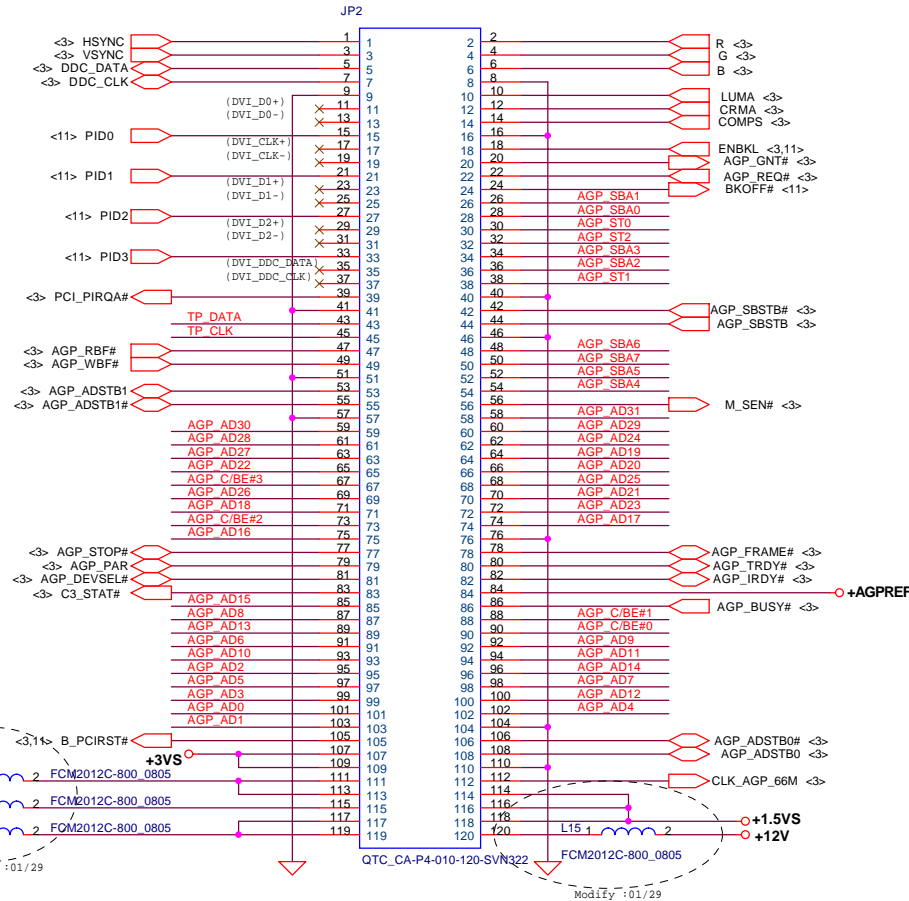
As close as ppossible to related pin



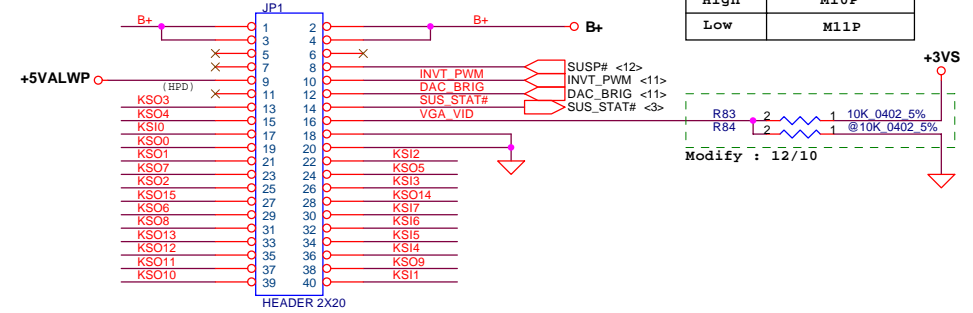
# AGP, POWER & K/B CONNECTOR

## AGP 120 Pin connector

<3> AGP\_ST[0..2] AGP ST0..2  
 <3> AGP\_AD[0..31] AGP AD0..31  
 <3> AGP\_C/BE#[0..3] AGP C/BE0..3  
 <3> AGP\_SBA[0..7] AGP SBA0..7



## POWER CONN.



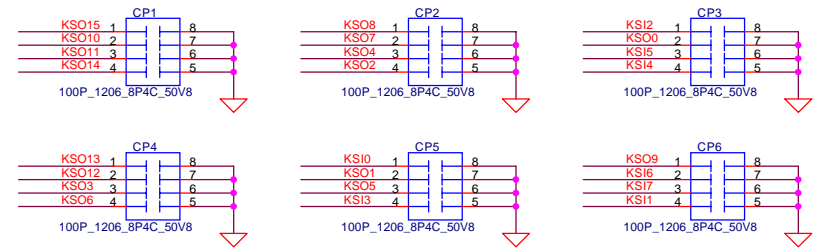
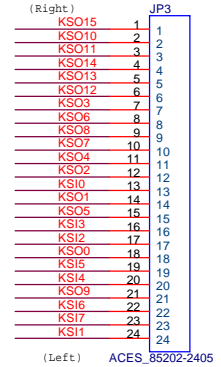
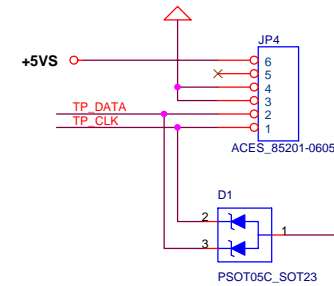
VGA Detect :

VGA_VID	Graphic Chip
High	M10P
Low	M11P

Modify : 12/10

## INT\_KBD CONN.

## T/P FPC CONN.



As close as possible to K/B connector

## Compal Electronics, Inc.

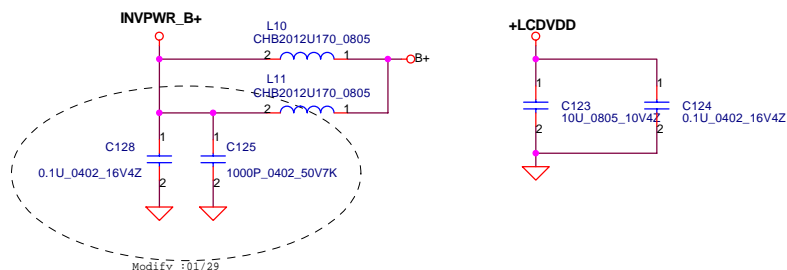
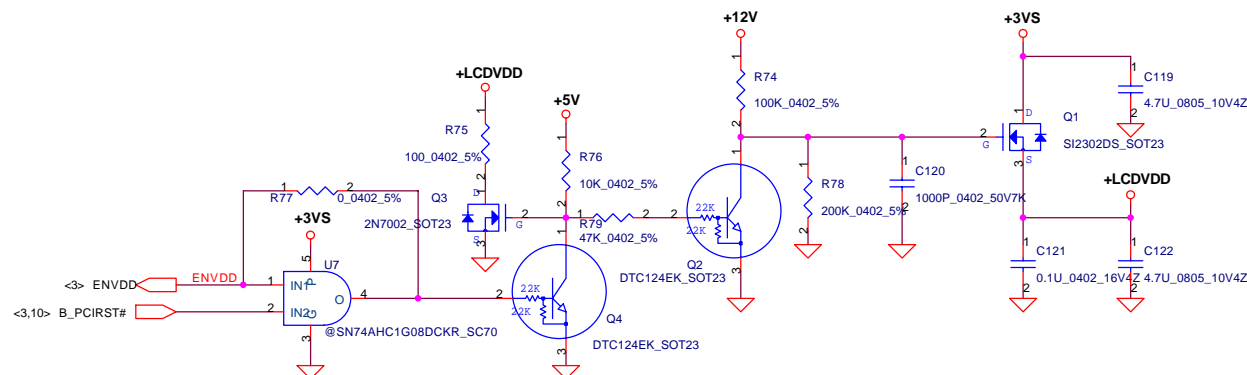
Title AGP, POWER, K/B & T/P CONNECTOR		
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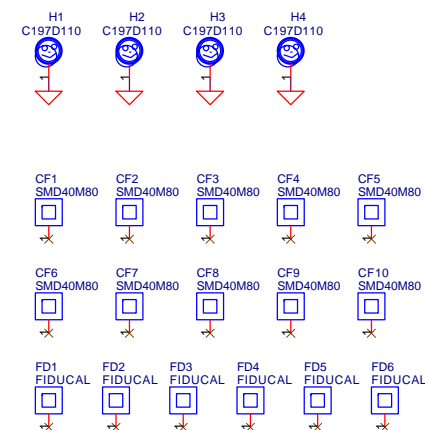
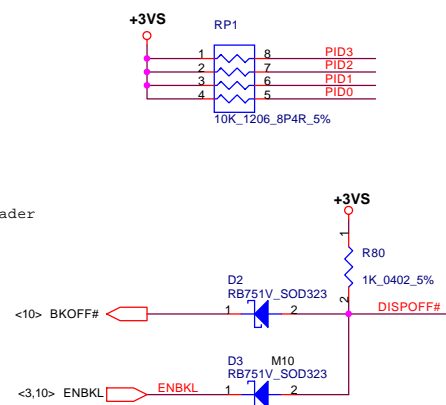
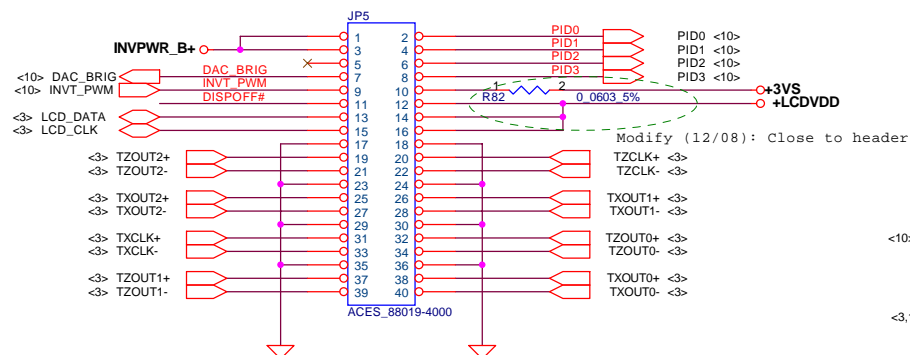
## LCD CONNECTOR

v:要上零件 , x:不上零件

SKU\component	R83	R84	R13	R23	R24	R81
M10P+64M(Hyn)	V	X	V	X	V	X
M11P+64M(Sam)	X	V	V	X	X	V
M11P+64M(Hyn)	X	V	V	X	V	X
M11P+128M(Sam)	X	V	X	V	X	V
M11P+128M(Hyn)	X	V	X	V	V	X



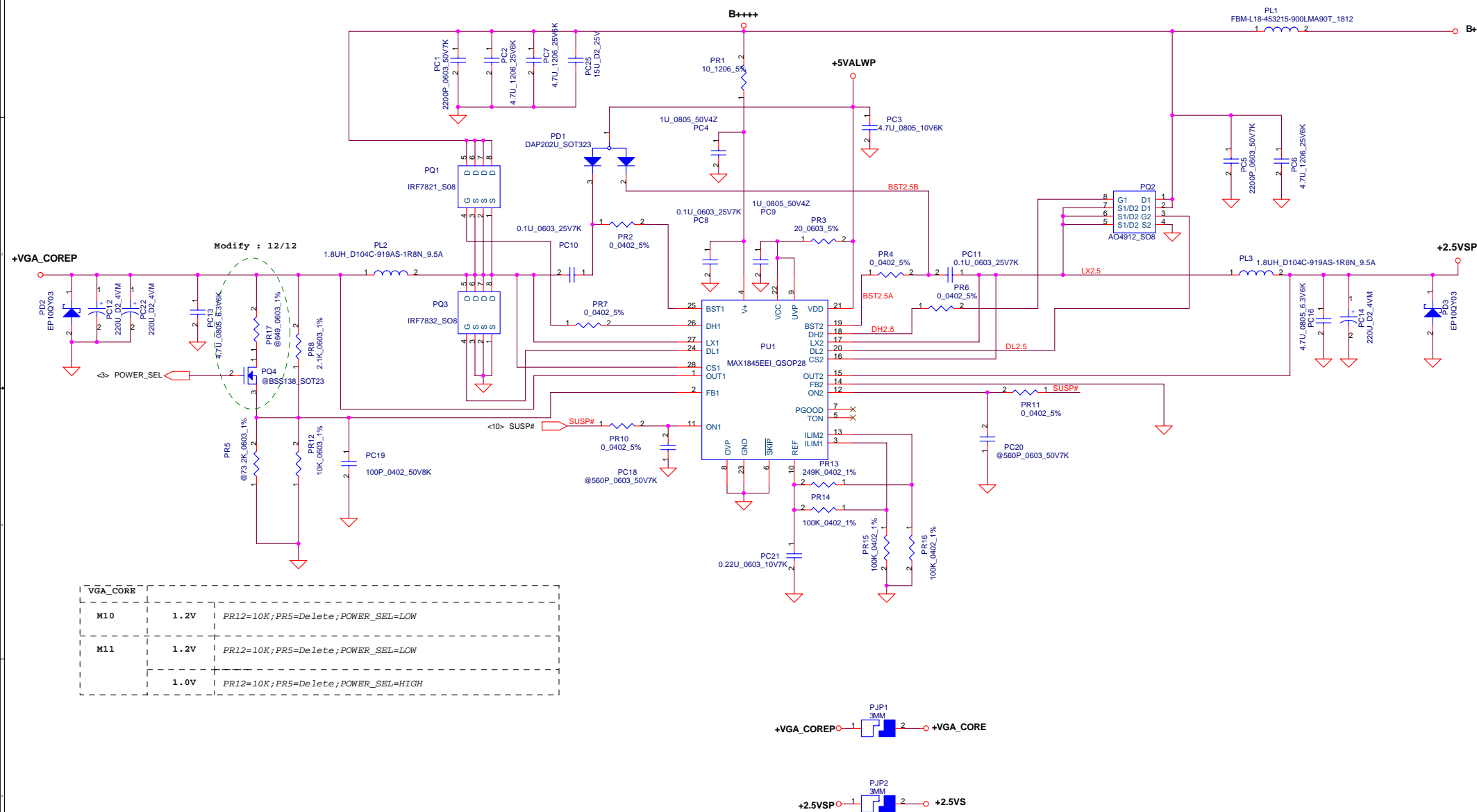
## LVDS Connector



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Title			
LCD CONN. & SPREAD SPECTRUM			
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