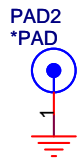
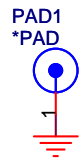
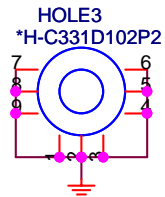
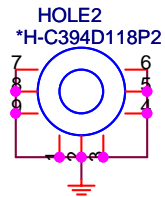
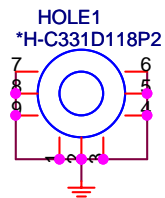
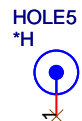
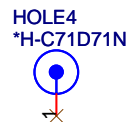


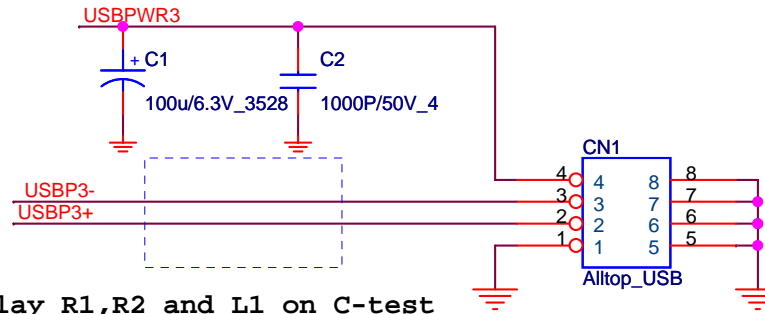
## PTH



## NPTH

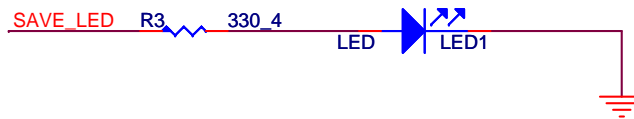


## USB

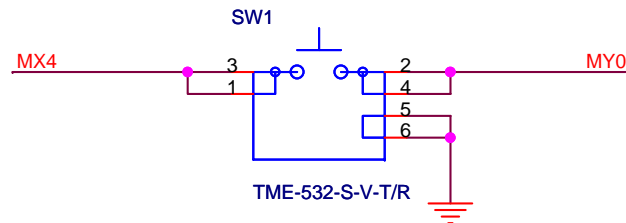


Delete to co-lay R1,R2 and L1 on C-test

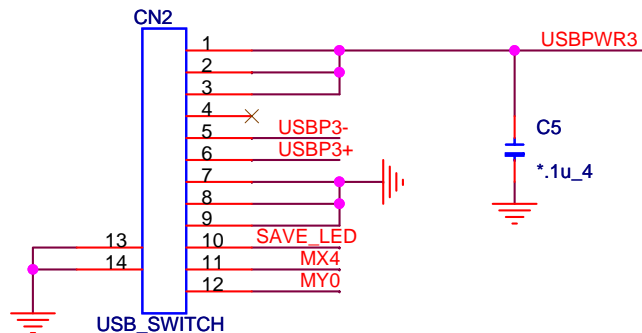
## Green LED (HKC)



## SWITCH

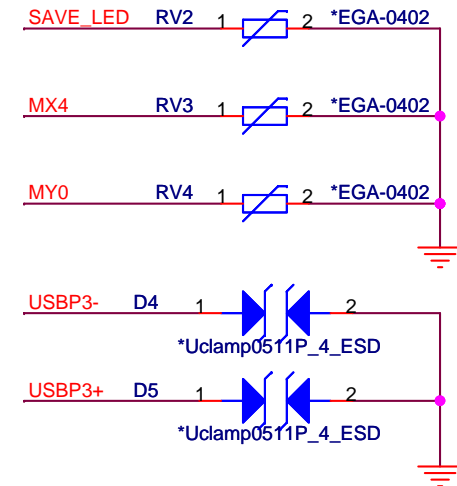


## Connect to MB



## ESD

Close to Connector Pin



Quanta Computer Inc.

PROJECT : ZY9

Size Document Number

SW Board

Rev  
1A

Date: Thursday, May 21, 2009

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