

HM42-DN Block Diagram

Project code: 91.4HD01.001
PCB P/N : 48.4GX01.0SA
PCB 版號 : 09919 SA
REVISION : PCB STACKUP

TOP
VCC
S
S
GND
BOTTOM

SYSTEM DC/DC		46
RT8223	INPUTS	46
DCBATOUT	5V_S5 (5.5A)	
	3D3V_S5 (5A)	
SYSTEM DC/DC		47
RT8209E	5V_S5	1D5V_S3 (1.4A)
RT9026	5V_S5	OD75_S3 (1.2A)
SYSTEM DC/DC		48
RT8209E	INPUTS	48
DCBATOUT	1D1V_S0 (11A)	
RT9025	3D3V_S5	1D1V_S5 (1.4A)
RT9025	5V_S5	1D1V_VGA
RT9161	3D3V_S0	2D5V_S0 (200mA)
RT9025	1D5V_S3	1D05V_S0 (1.4A)
CHARGER		50
BQ24745	INPUTS	50
DCBATOUT	CHG_PWR	18V 6.0A
	UP+5V	5V 100mA
CPU DC/DC		45
ISL6265AHR	INPUTS	45
DCBATOUT	VCC_CORE_S0	0~1.55V 18A
	VDDNB	0~1.55V 4A

Discrete Madison Hynix

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
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File

BLOCK DIAGRAM

Size

A3

Document Number

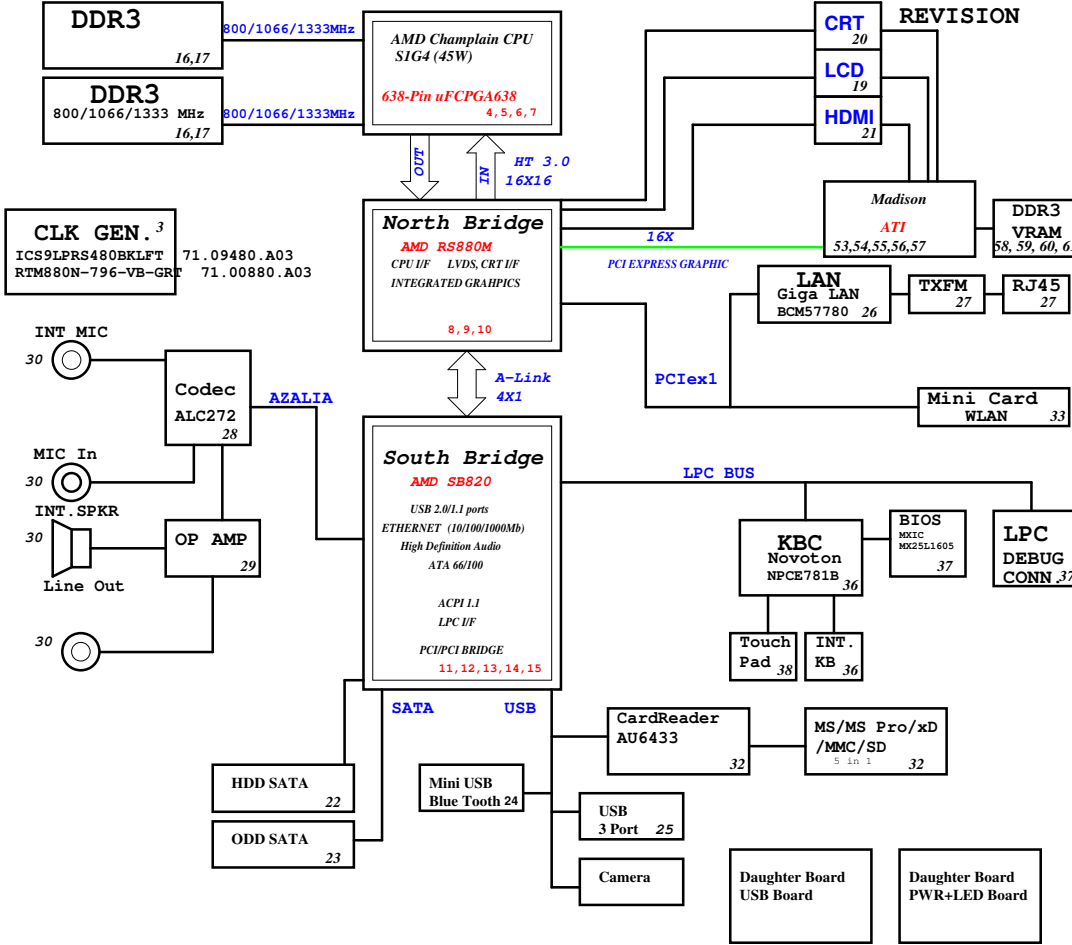
JE40-DN

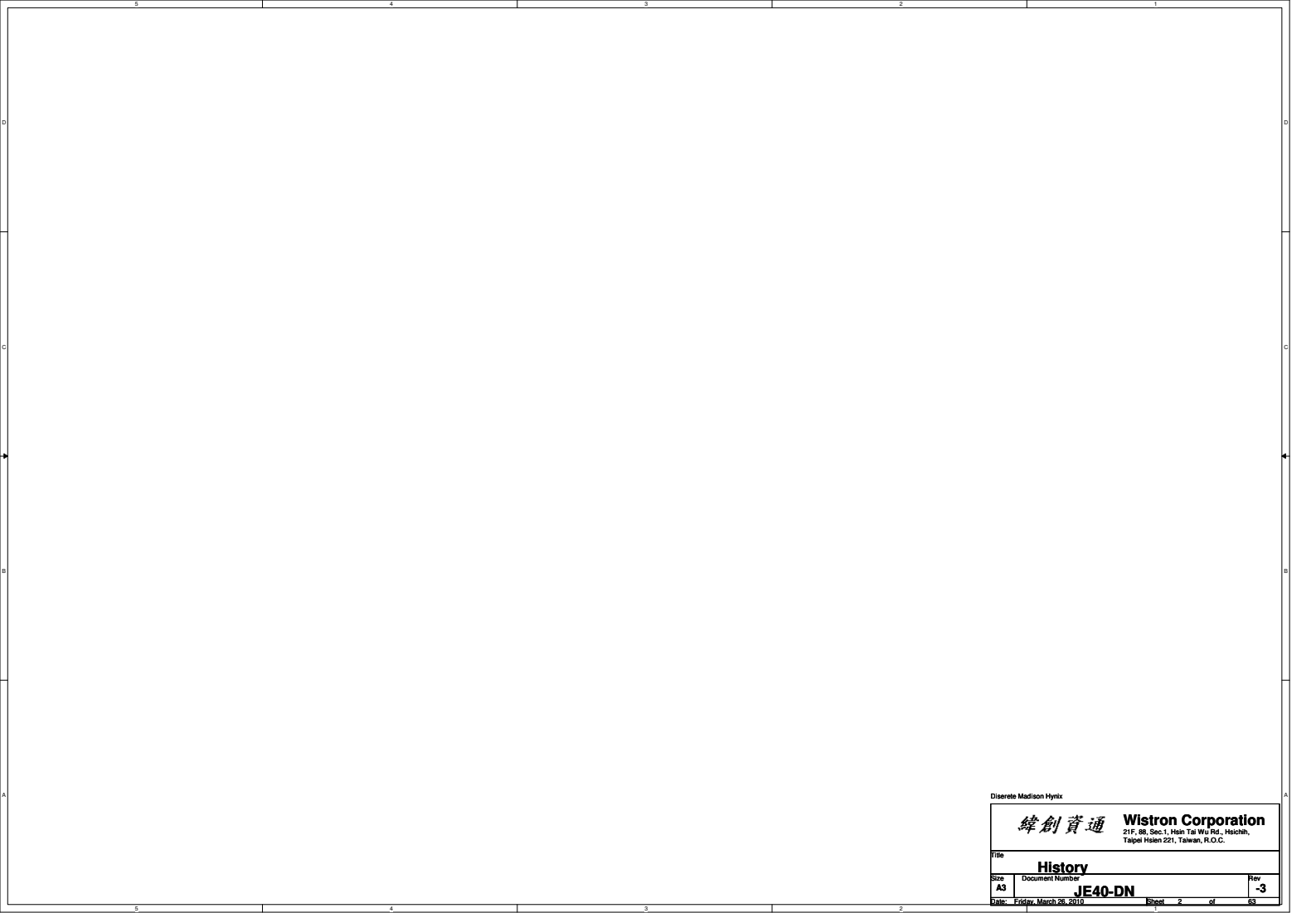
Rev

-3

Date: Friday, March 28, 2019

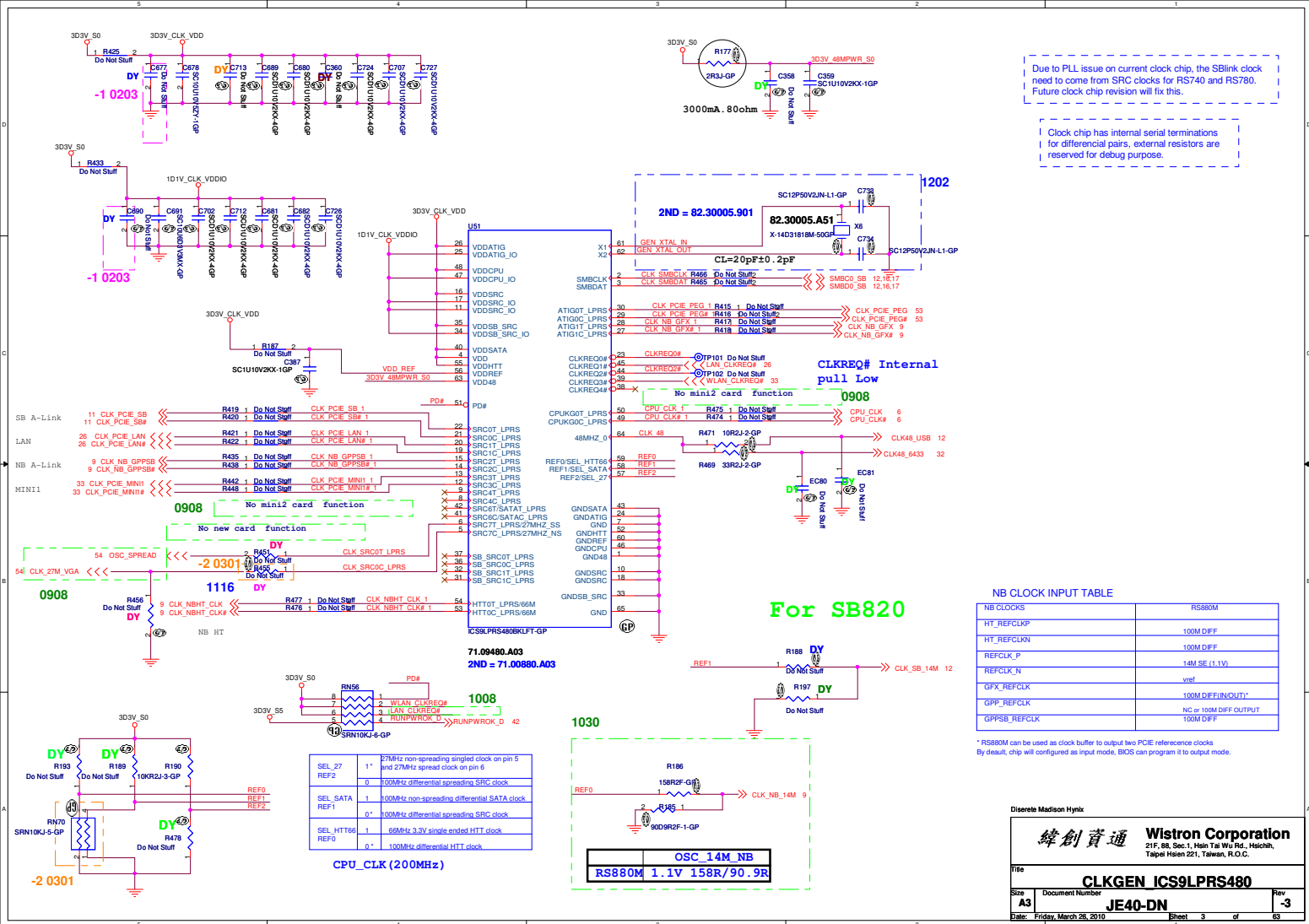
Sheet 1 of 80

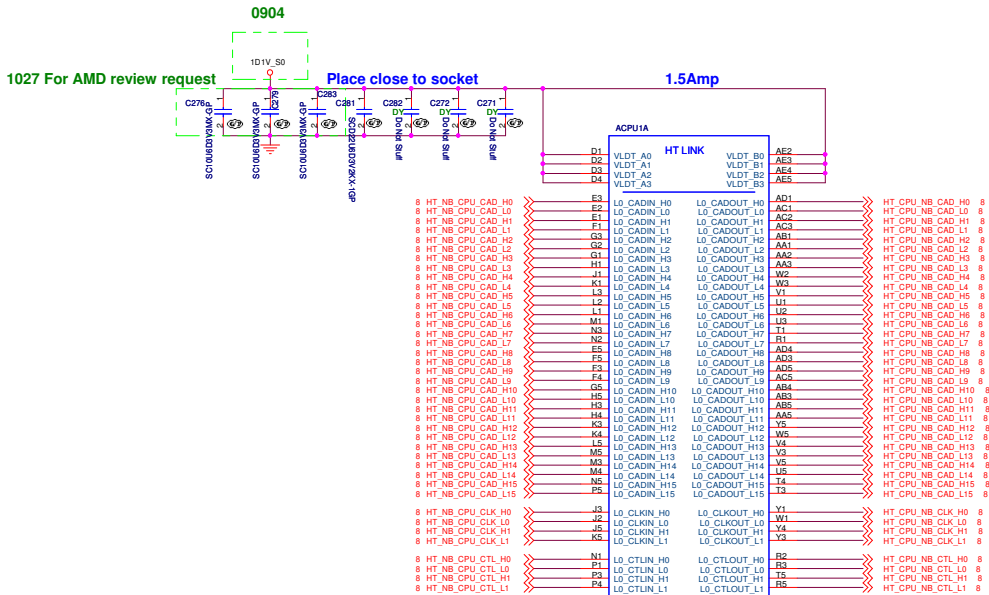




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History			
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SKT-CPU63AP-GP-U2
62.10055.111
2ND = 62.10055.181(ENG 未上)

SKT-BGA638H176

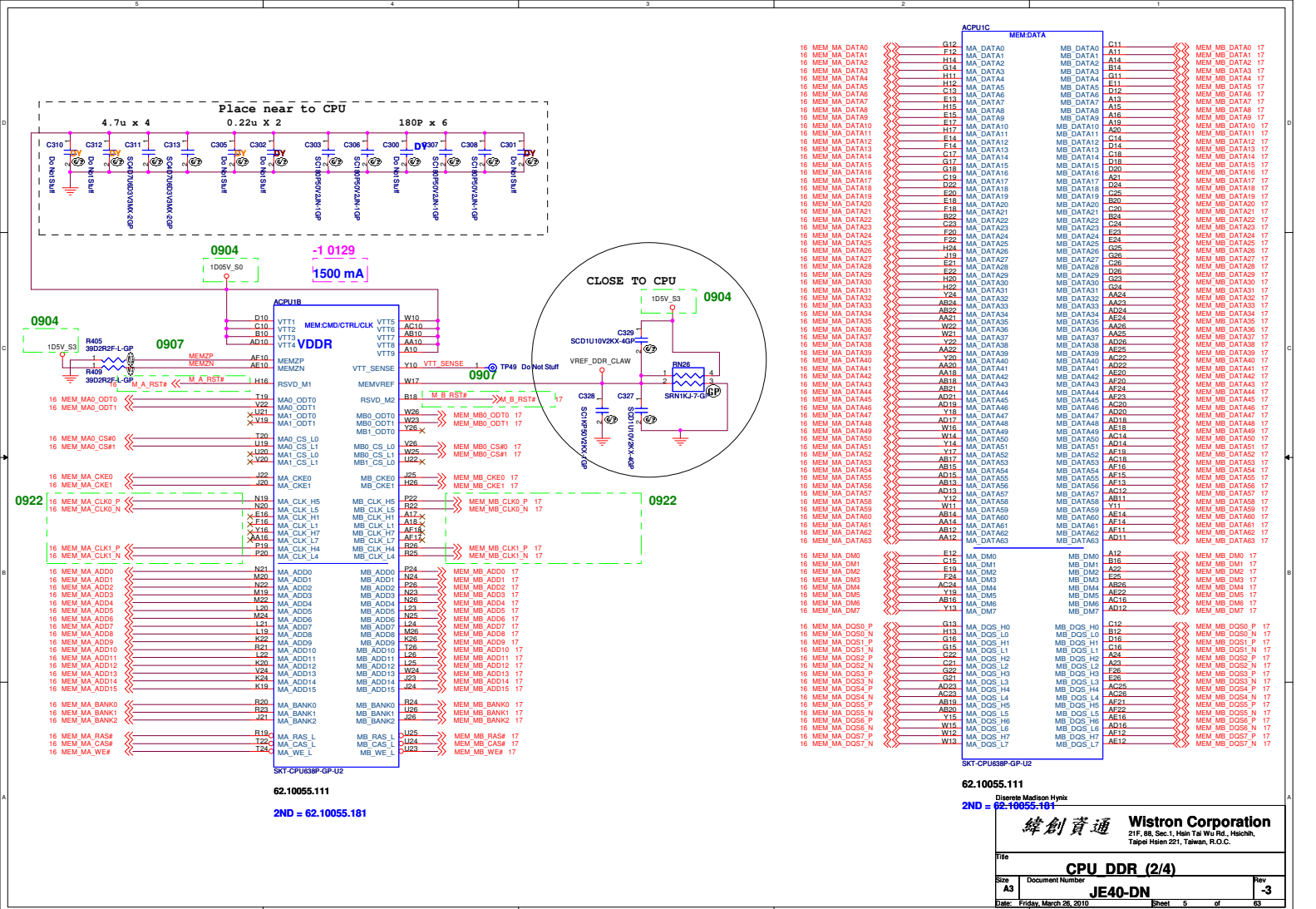
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File CPU HT LINK I/F (1/4)

Rev A3 Document Number JE40-DN Rev -3

Date: Friday, March 26, 2010 Sheet 4 of 60

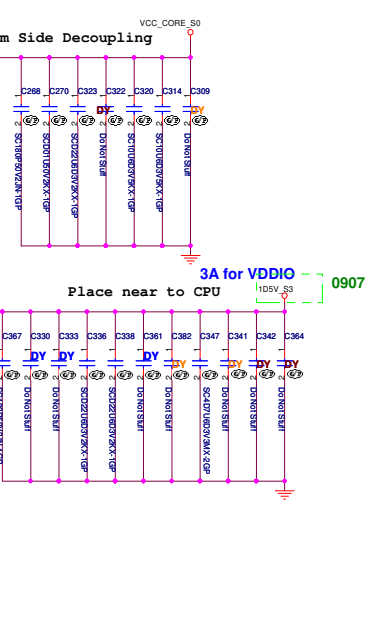
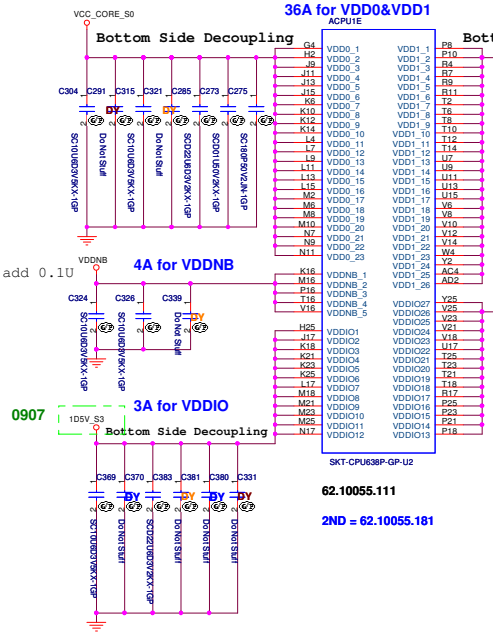


ACPU1F		
AA4	VSS1	VSS66
AA11	VSS2	VSS67
AA13	VSS3	VSS68
AA15	VSS4	VSS69
AA17	VSS5	VSS70
AA19	VSS6	VSS71
AB2	VSS7	VSS72
AB7	VSS8	VSS73
AB9	VSS9	VSS74
AB23	VSS10	VSS75
AB25	VSS11	VSS76
AC11	VSS12	VSS77
AC13	VSS13	VSS78
AC17	VSS14	VSS79
AC19	VSS15	VSS80
AC21	VSS16	VSS81
AC23	VSS17	VSS82
AD6	VSS18	VSS83
AD8	VSS19	VSS84
AD25	VSS20	VSS85
AE11	VSS21	VSS86
AE13	VSS22	VSS87
AE15	VSS23	VSS88
AE17	VSS24	VSS89
AE19	VSS25	VSS90
AE21	VSS26	VSS91
AE23	VSS27	VSS92
B4	VSS28	VSS93
B6	VSS29	VSS94
B8	VSS30	VSS95
B9	VSS31	VSS96
B11	VSS32	VSS97
B13	VSS33	VSS98
B15	VSS34	VSS99
B17	VSS35	VSS100
B19	VSS36	VSS101
B21	VSS37	VSS102
B23	VSS38	VSS103
B25	VSS39	VSS104
D6	VSS40	VSS105
D8	VSS41	VSS106
D9	VSS42	VSS107
D11	VSS43	VSS108
D13	VSS44	VSS109
D15	VSS45	VSS110
D17	VSS46	VSS111
D19	VSS47	VSS112
D21	VSS48	VSS113
D23	VSS49	VSS114
D25	VSS50	VSS115
E4	VSS51	VSS116
F2	VSS52	VSS117
F11	VSS53	VSS118
F13	VSS54	VSS119
F15	VSS55	VSS120
F17	VSS56	VSS121
F19	VSS57	VSS122
F21	VSS58	VSS123
F23	VSS59	VSS124
F25	VSS60	VSS125
H7	VSS61	VSS126
H9	VSS62	VSS127
H21	VSS63	VSS128
H23	VSS64	VSS129
JA	VSS65	

SKT-CPU638P-GP-U2

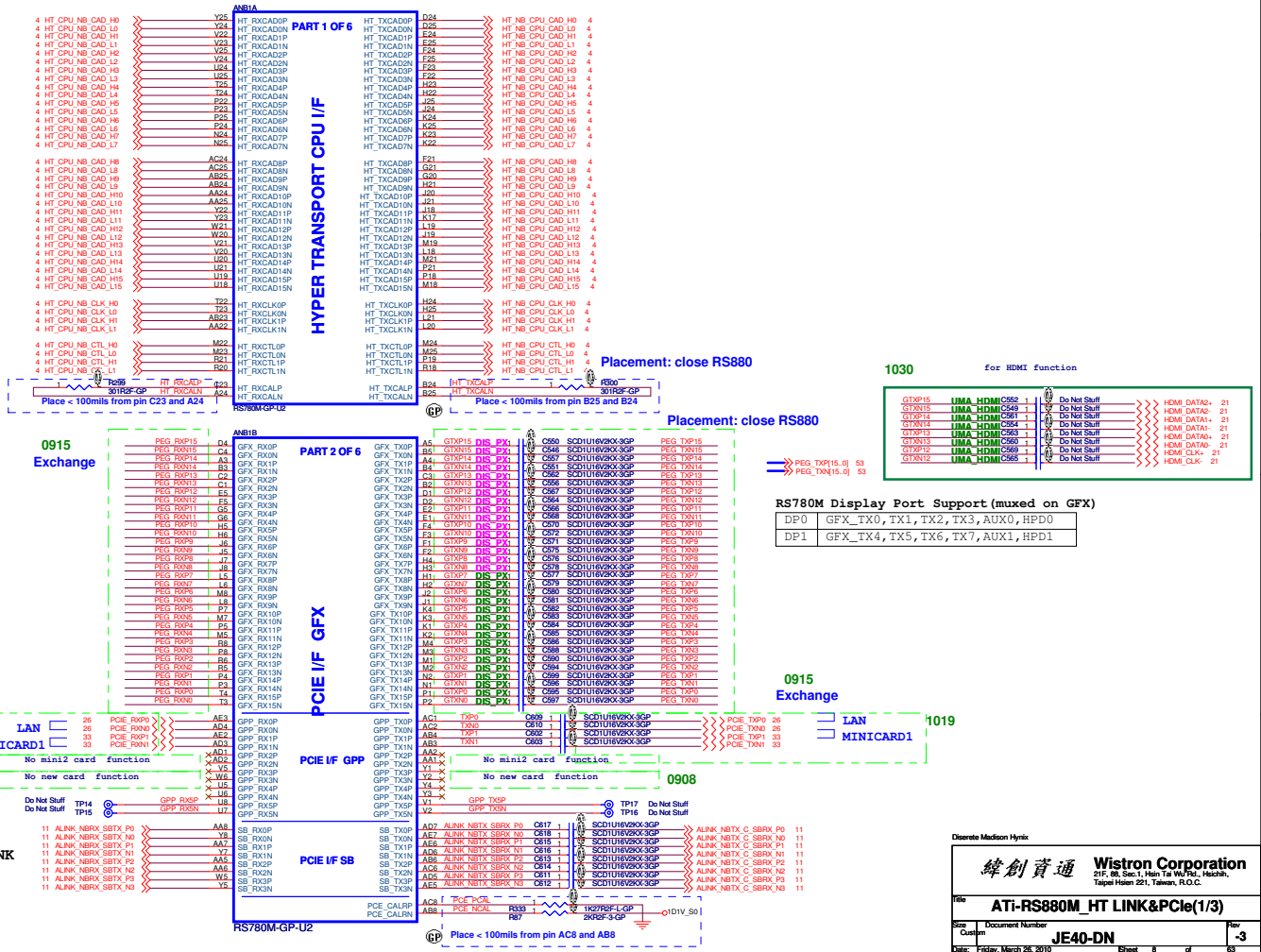
62.10055.111

2ND = 62.10055.181



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緯創資通		Wistron Corporation	
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File			
CPU_Power_(4/4)			
Size A3	Document Number JE40-DN	Rev -3	
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Discrete Media System

緯創資通 Wistron Corporation
Taipei Hsien 221, Taiwan, R.O.C.

ATI-RS880M HT LINK & PCIe (1/3)

Doc Number JE40-DN

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1027 For AMD review request

0.6A per ANT Rev1.1, Page3

0.45A per ANT Rev1.1, Page3

1027 For AMD review request

1027 For AMD review request

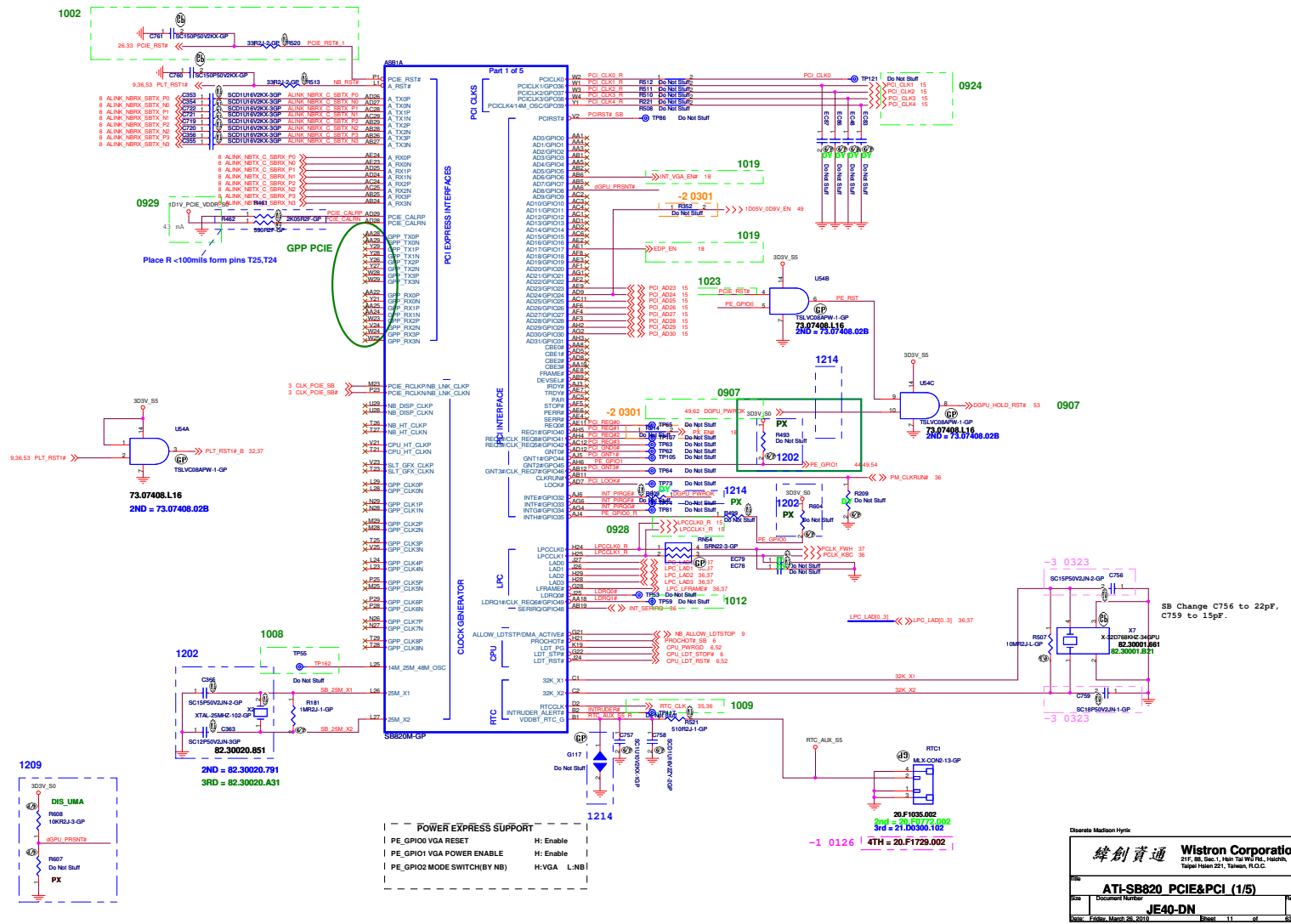
MEM_COMP_P and MEM_COMP_N trace width >=10mils and 10mils spacing from other Signals in X,Y,Z directions

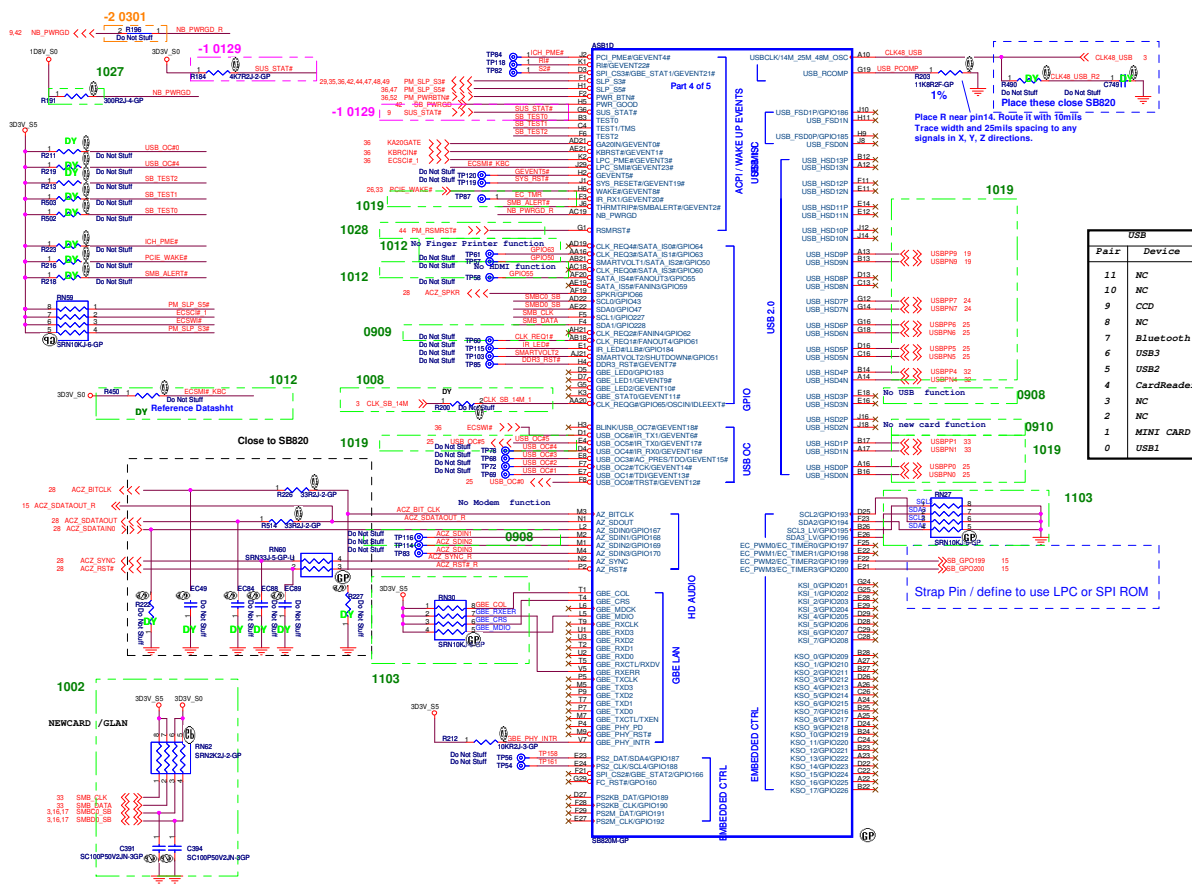
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File
ATL-RS880M_Side Port&PWR&GND(3/3)
Size A3 Document Number
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Discrete Madson Hymn

File			
ATI-SB820 USB&GPIO (2/5)			
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Title			
ATI-SB820 SATA-IDE (3/5)			
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File		AT-SB820 POWER&GND (4/5)	
Size	Document Number	Rev	
Custom	JE40-DN		
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1115

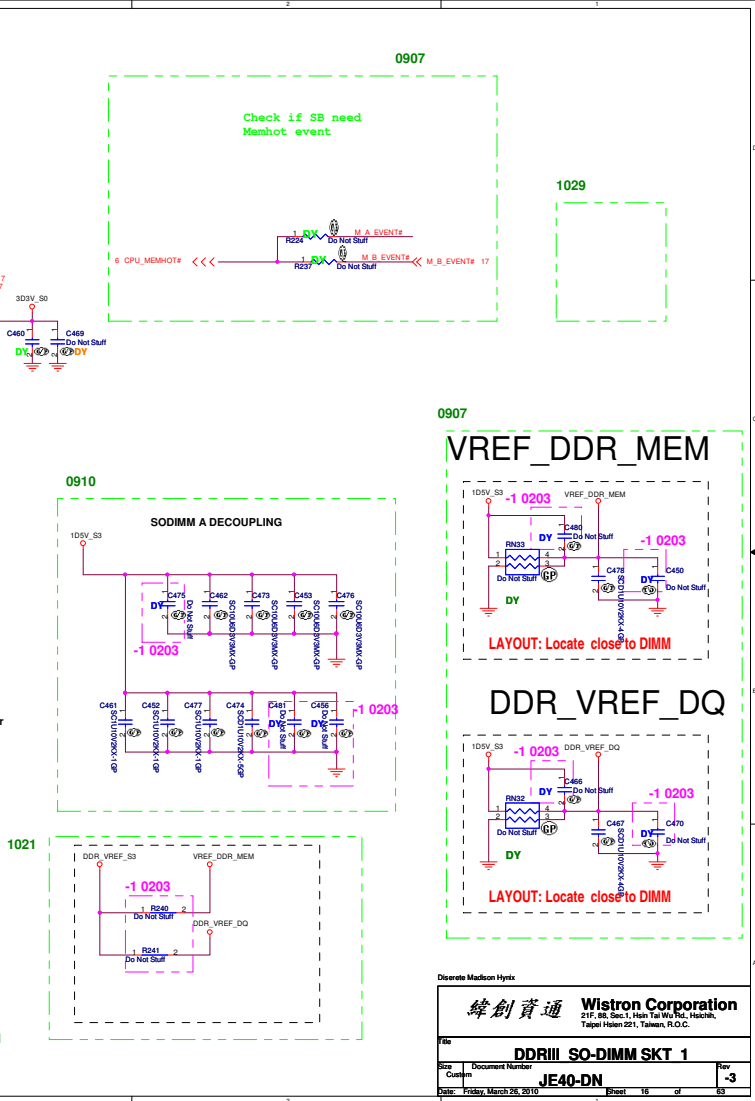


Do Not Stuff	TP67		PCI_AD23	11
Do Not Stuff	TP70		PCI_AD24	11
Do Not Stuff	TP66		PCI_AD25	11
Do Not Stuff	TP77		PCI_AD26	11
Do Not Stuff	TP80		PCI_AD27	11
Do Not Stuff	TP110		PCI_AD28	11
Do Not Stuff	TP108		PCI_AD29	11
Do Not Stuff	TP111		PCI_AD30	11

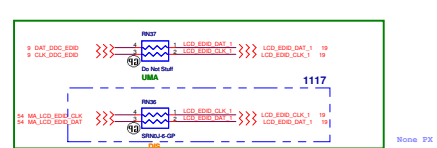
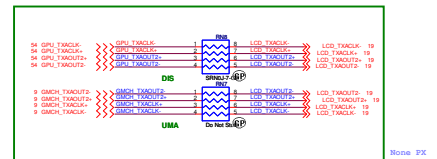
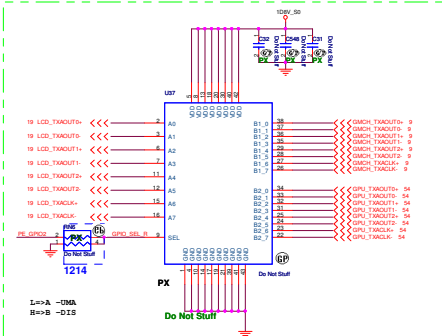
	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

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Title			
ATI-SB820 STRAPPING (5/5)			
Size	Document Number	Rev	
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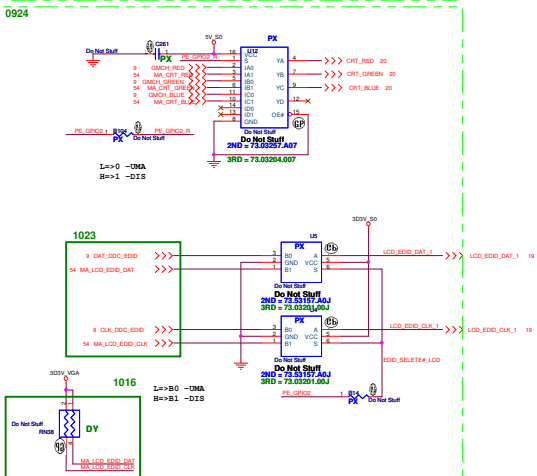






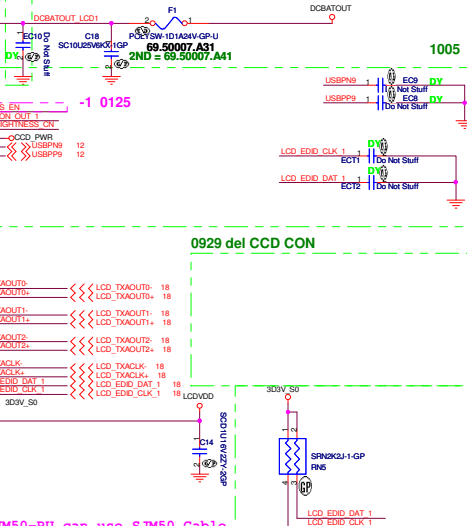
Input (S)	Function
L	B ₀ Connected to A
H	B ₁ Connected to A

H = HIGH Logic Level L = LOW Logic Level



LCD/INVERTER/CCD CONN

1028 for EMC request

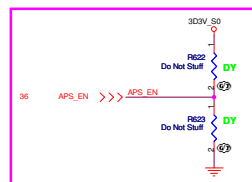


Change PX

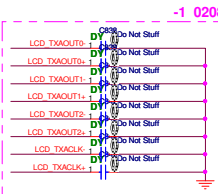
0922

Inverter Pin		
Pin	Symbol	
1	Vin	
2	Vin	
3	Brightness	
4	BLON	
5	GND	
6	GND	

CCD Pin		
Pin	Symbol	
1	CCD_PWR	
2	USB-	
3	USB+	
4	GND	
5	GND	

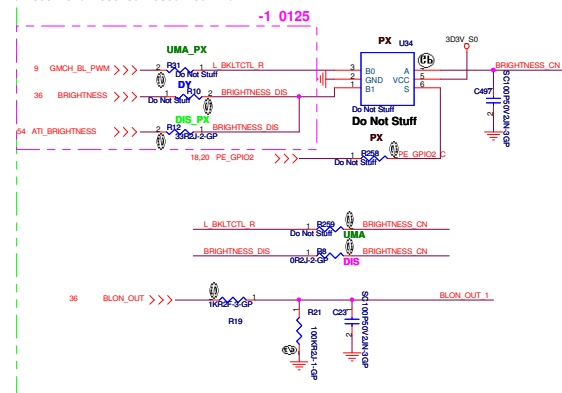


-1 0125



1026

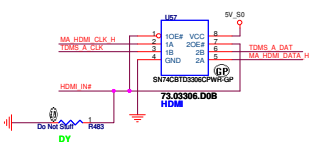
Reserve direct connector to KBC



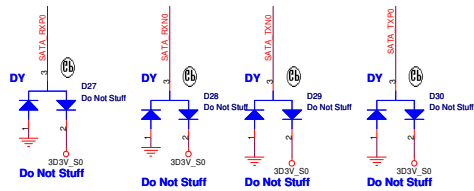
Discrete Medicon Hyink

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LCD CONN			
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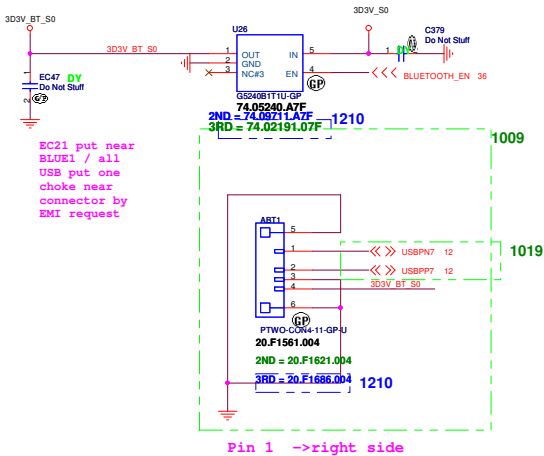


0923



BLUETOOTH MODULE

1.5A / High Active Voltage 2V

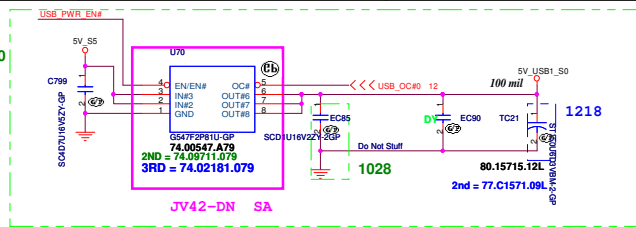


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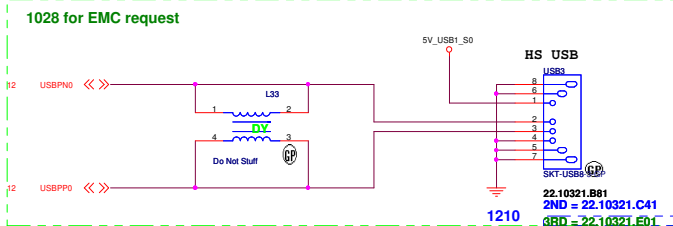
緯創資通		Wistron Corporation	
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Title			
BLUETOOTH			
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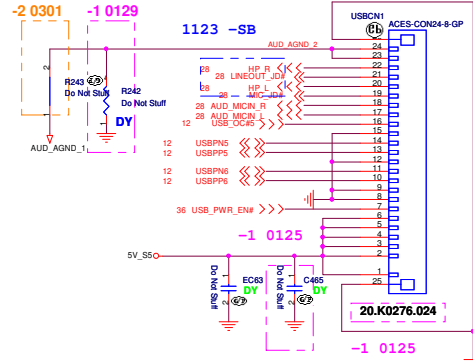
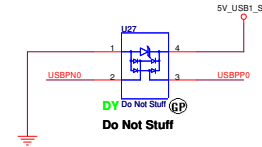
0910



0914



Pin1 -> right side
22.10218.T51 Pin1 -> left side (JV42-DN)
change Net sequence



Eng stuff 20.K0216.024 Pin1 -> left side
PD change to 20.K0276.024 Pin1 -> left side
so do not swap net



0910

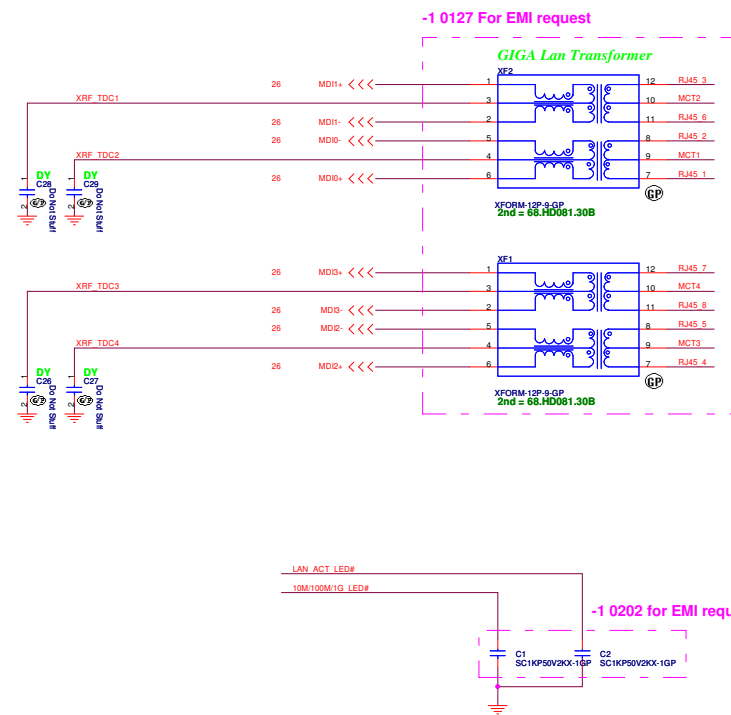
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USB	
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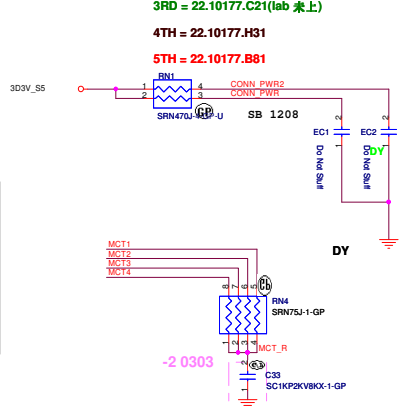
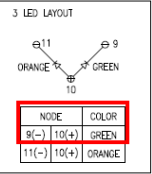
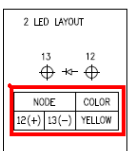
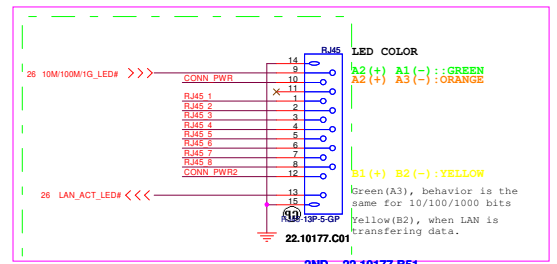
- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat,except RJ-45 moat.

LAN Connector

LAN Connector



0923



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File

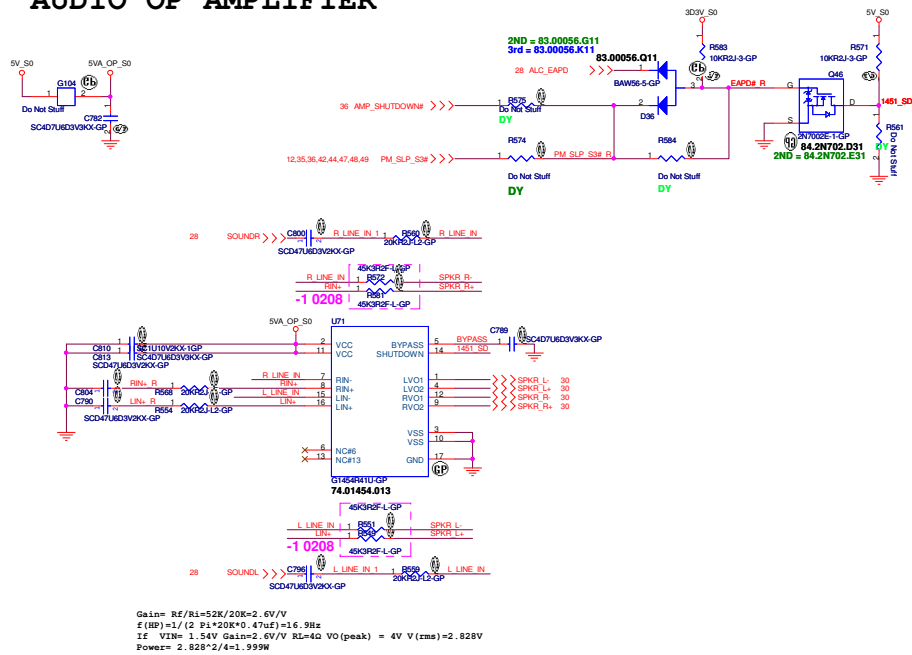
LAN CONN

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AUDIO OP AMPLIFIER



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Rev

AUDIO AMP

JE40-DN

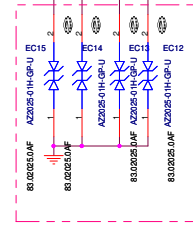
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Internal Speaker

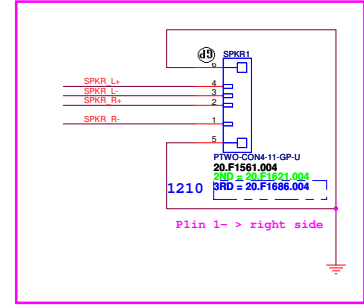
0914

0910

29 SPKR_L-
29 SPKR_L+
29 SPKR_R-
29 SPKR_R+
29 SPKR_L-
29 SPKR_L+
29 SPKR_R-
29 SPKR_R+



-1 0125 for EMI request



Pin 1- > right side

JV42-DN SA

0910

LINE OUT

0911

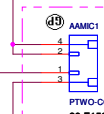
1016

Internal Mic

28 INT_MIC1 <<<

MLV50803K20-GP

L59



20.F1561.002
2ND = 20.F1621.002
3RD = 20.F1686.002

1210

-1 0209 For MIC issue

JV42-DN PX
Pin 1- >right side
20.F1240.002 Pin1->left side
same as JV70-CP

Discrete Madison Hynd

緯創資通

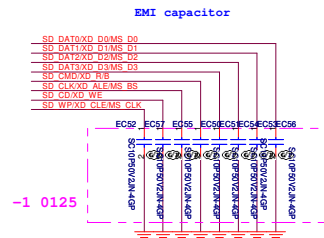
Wistron Corporation
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File		
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AUDIO JACK		-3
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No Modem Function

Discrete Madison Hyrix

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Title		MDC	
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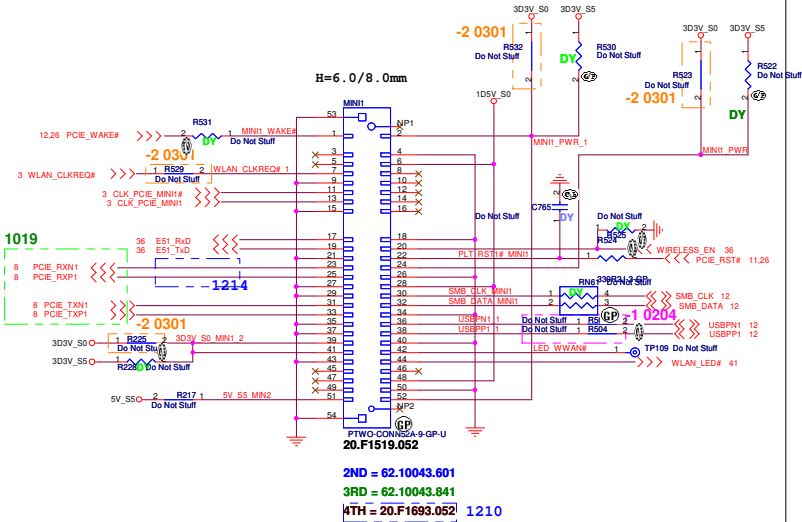
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 緯創資通		Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichin, Taipei Hsein 221, Taiwan, R.O.C.	
Title			
CARDREADER			
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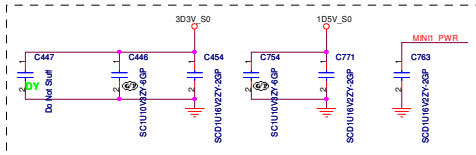
Mini Card Connector(WLAN)

No Mini Card Function (Robson2 and 3G)

0923



Place near MINI1



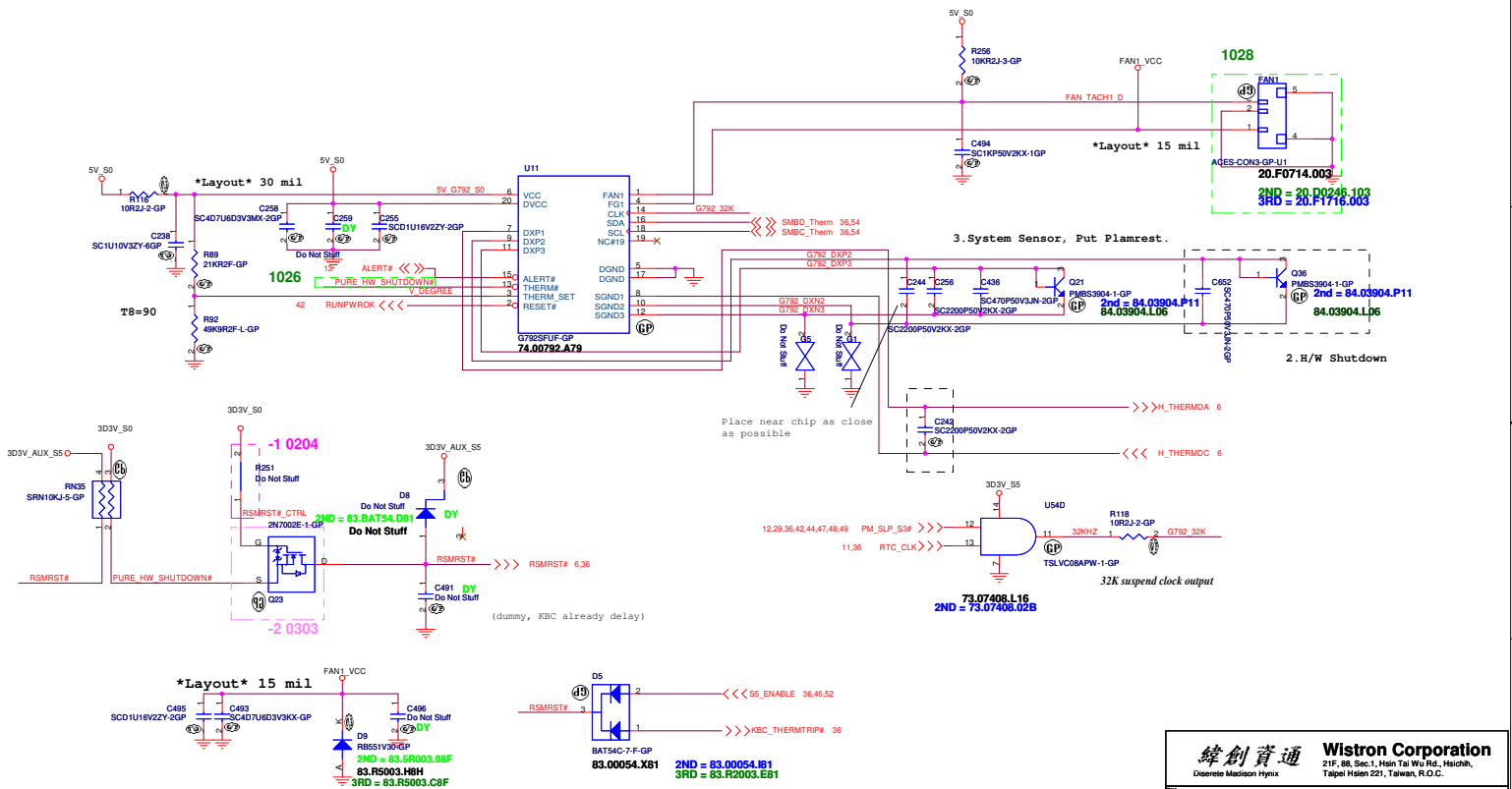
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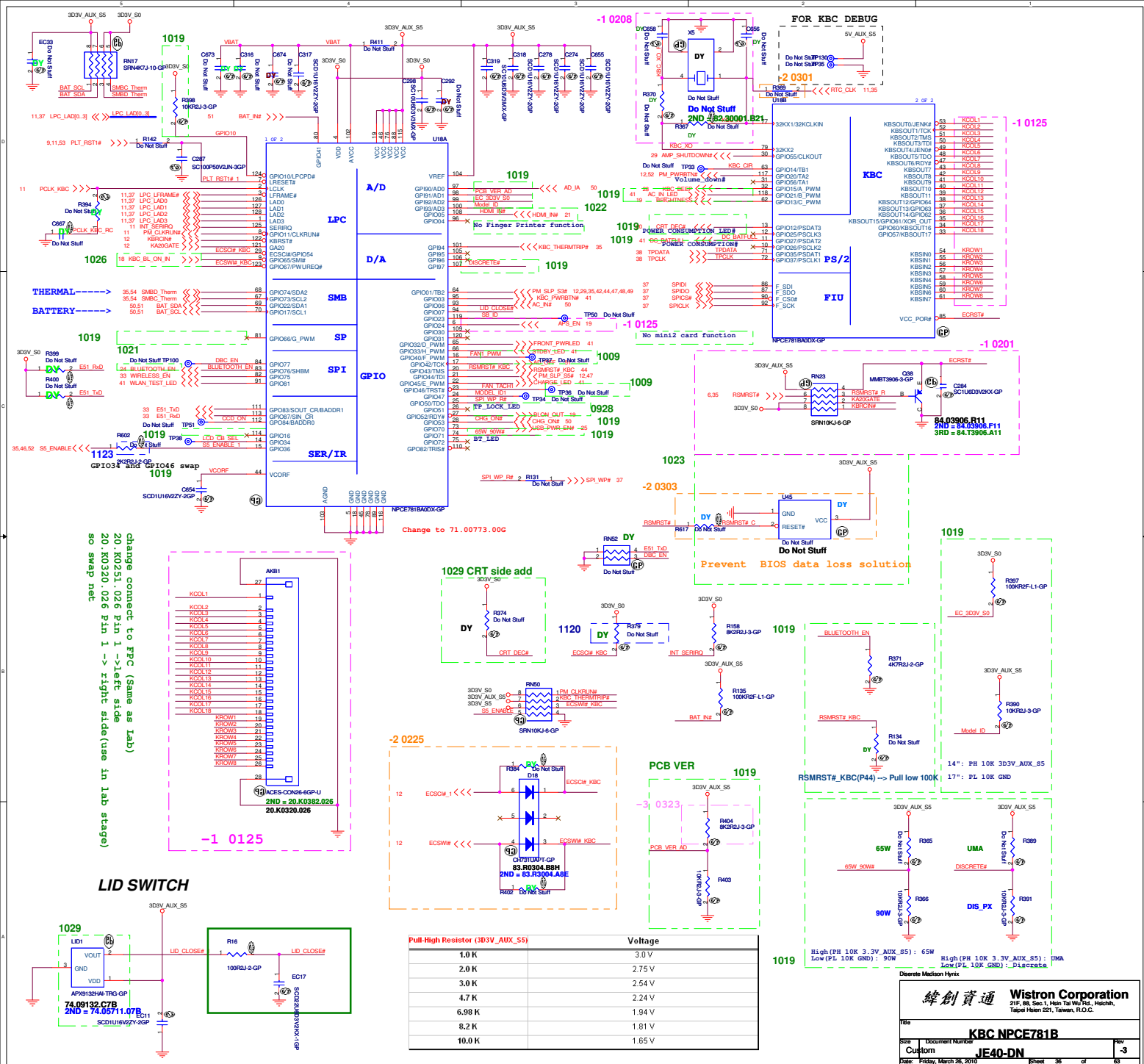
緯創資通		Wistron Corporation	
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File			
MINI CARD			
JE40-DN			
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No NEWCARD Function

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NEW CARD			
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for ENE FAE suggest, SPICS# is push-pull pin,
don't need to pull high

base on FAE Kevin discuss with KBC

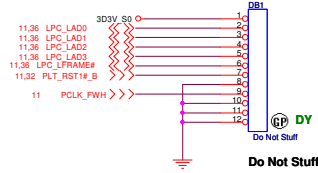
-1 0205
-2 0303

3rd 4th source LAB ENG未上

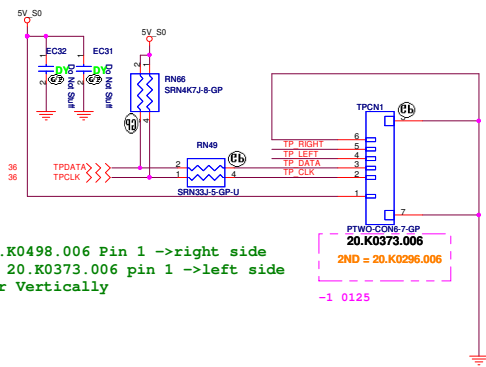
16M Bits
SPI FLASH ROM

GOLDEN FINGER FOR DEBUG BOARD

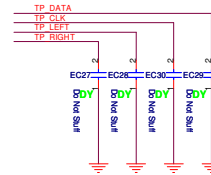
11.36 LPC_LAD[0..3] <<> LPC_LAD[0..3]



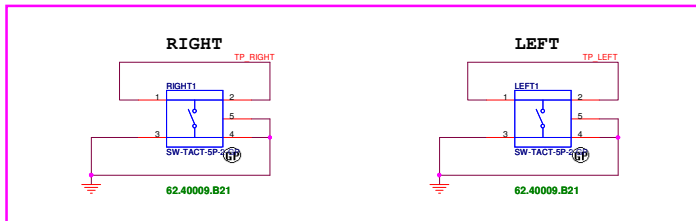
Discrete Madison Hynix		
緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsien 221, Taiwan, R.O.C.		
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BIOS		
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Eng stuff 20.K0498.006 Pin 1 -->right side
 PD change to 20.K0373.006 pin 1 -->left side
 so net mirror Vertically



-1 0205



No Finger printer Function

Discrete Madison Hynix

緯創資通

Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichang,
 Taipei Hsien 221, Taiwan, R.O.C.

Title

Touch PAD

Size

Document Number

JE40-DN

Rev

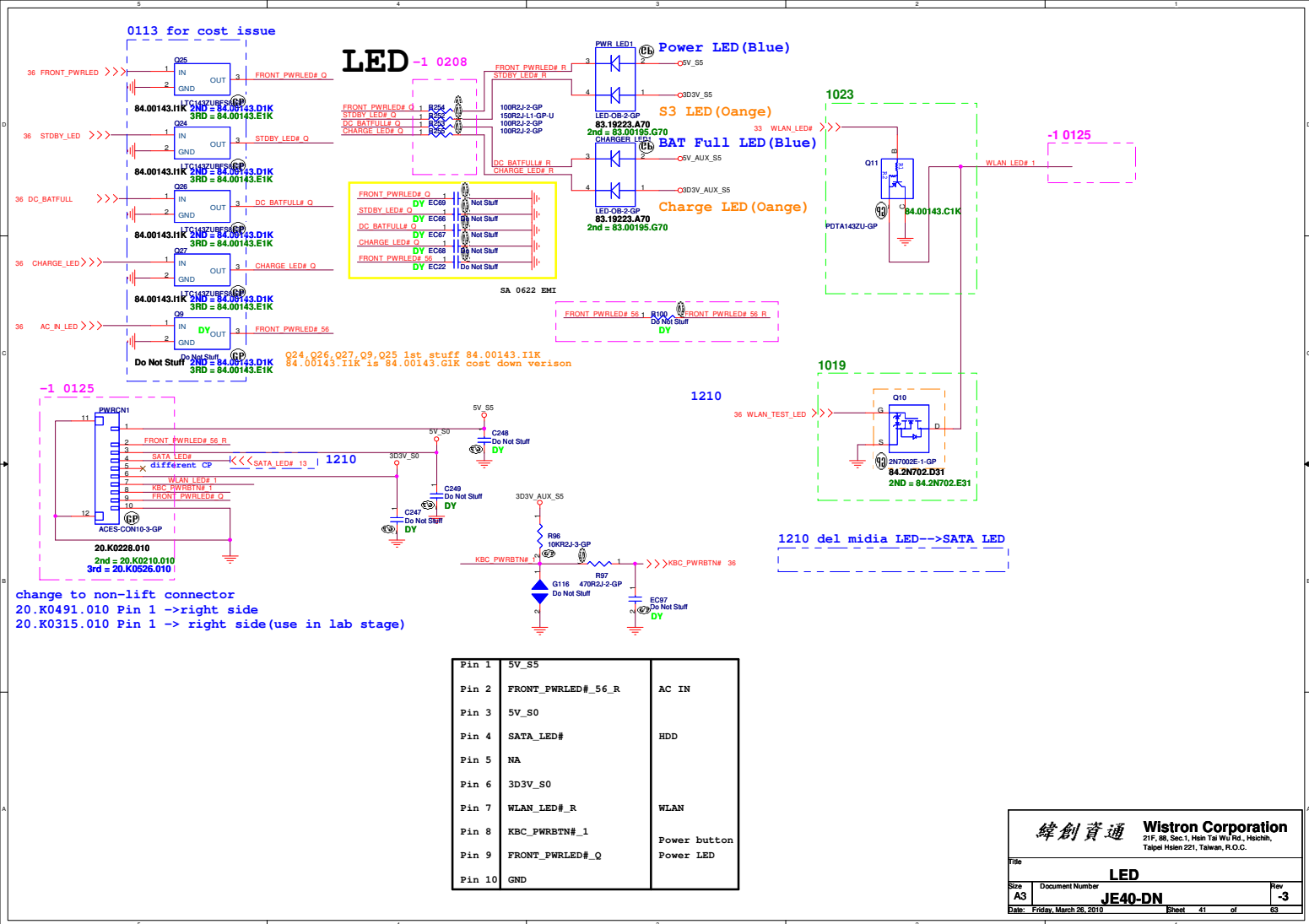
-3

Date: Friday, March 28, 2019

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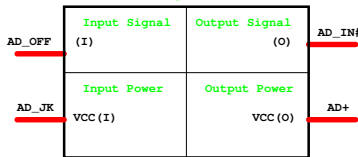
NONE BOARD

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		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
NONE BOARD			
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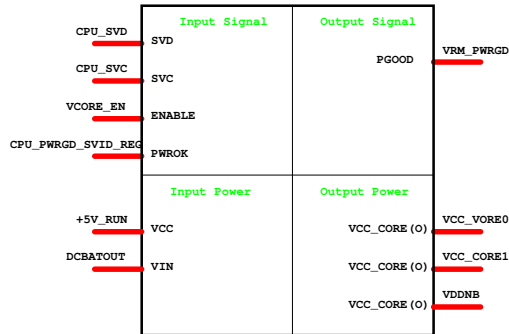


Pin 1	5V_S5	
Pin 2	FRONT_PWRLED#_56_R	AC IN
Pin 3	5V_S0	
Pin 4	SATA_LED#	HDD
Pin 5	NA	
Pin 6	3D3V_S0	
Pin 7	WLAN_LED#_R	WLAN
Pin 8	KBC_PWRBTN#_1	Power button
Pin 9	FRONT_PWRLED#_Q	Power LED
Pin 10	GND	

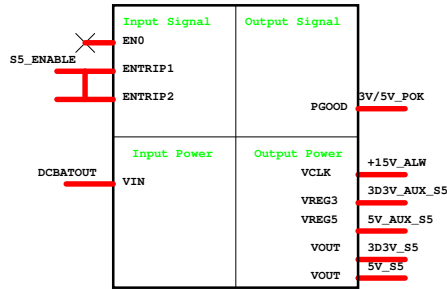
Adapter



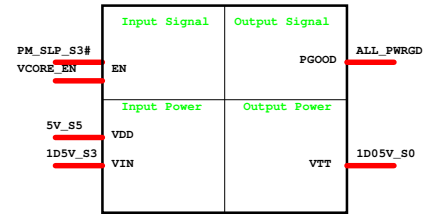
CPU_CORE ISL6265HRTZ



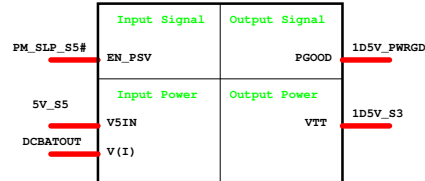
DCDC 5V/3D3V(RT8223)



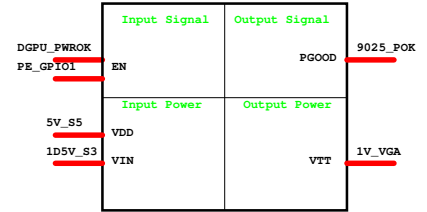
DCDC 1D05V_S0(RT9025)



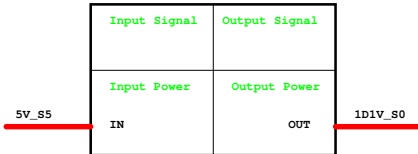
DCDC 1D5V(RT8209E)



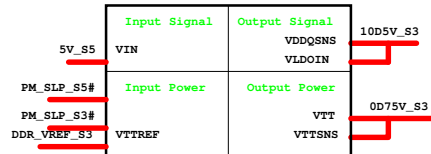
DCDC 1V_VGA(RT9025)



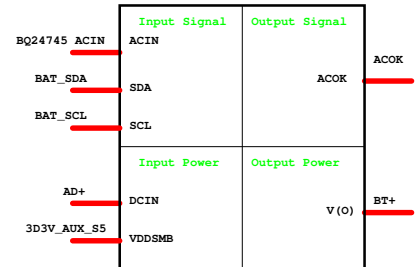
1D1V LDO RT8209E



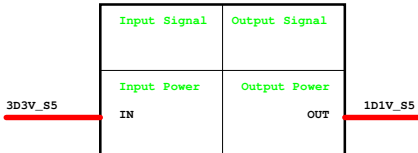
0D75V LDO RT9026



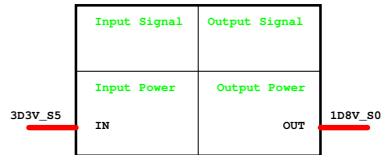
CHARGER BQ24745



1D1V LDO RT9025



1D8V LDO RT8015A



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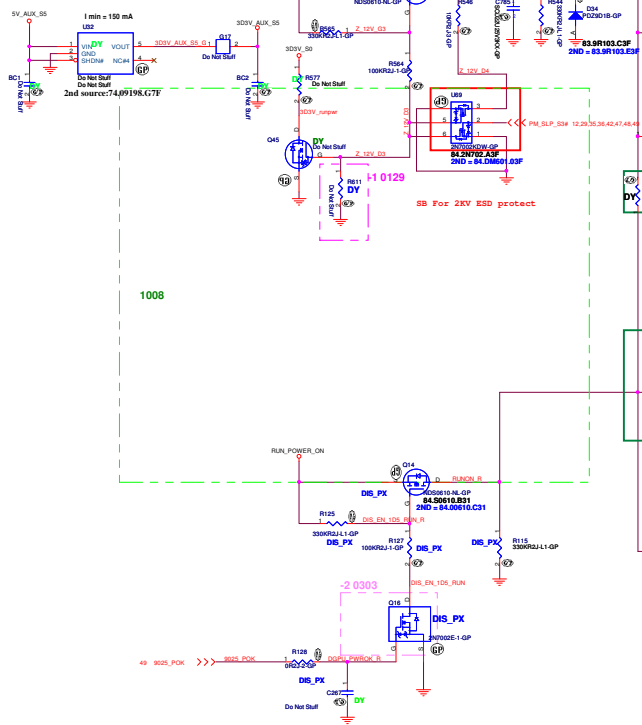
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taippei Hsien 221, Taiwan, R.O.C.

Power Block Diagram		
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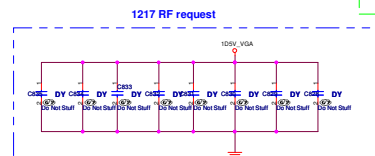
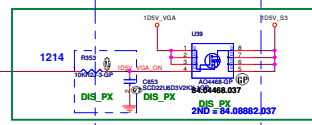
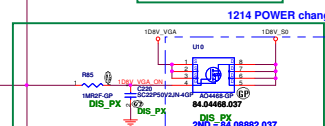
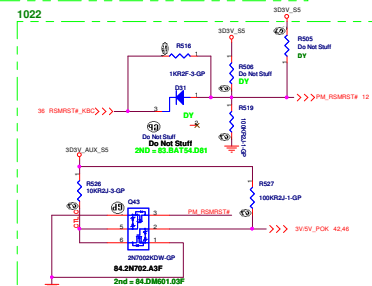
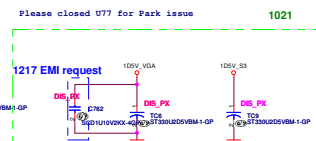
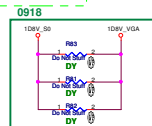
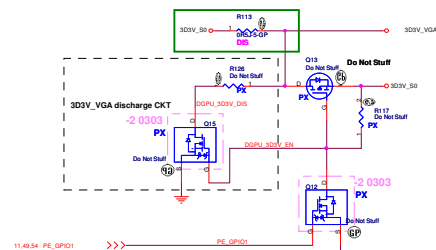
Run Power

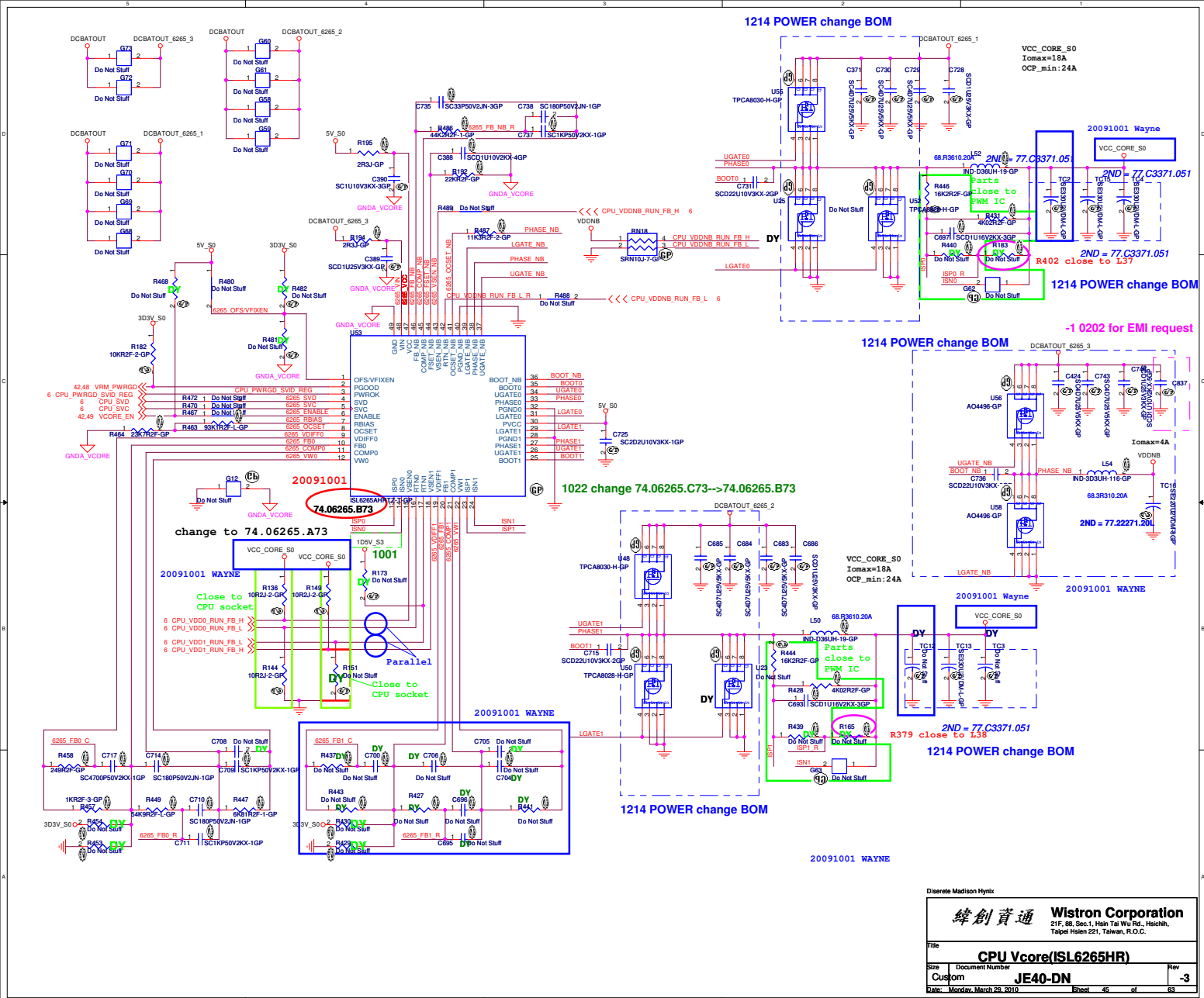
Aux Power

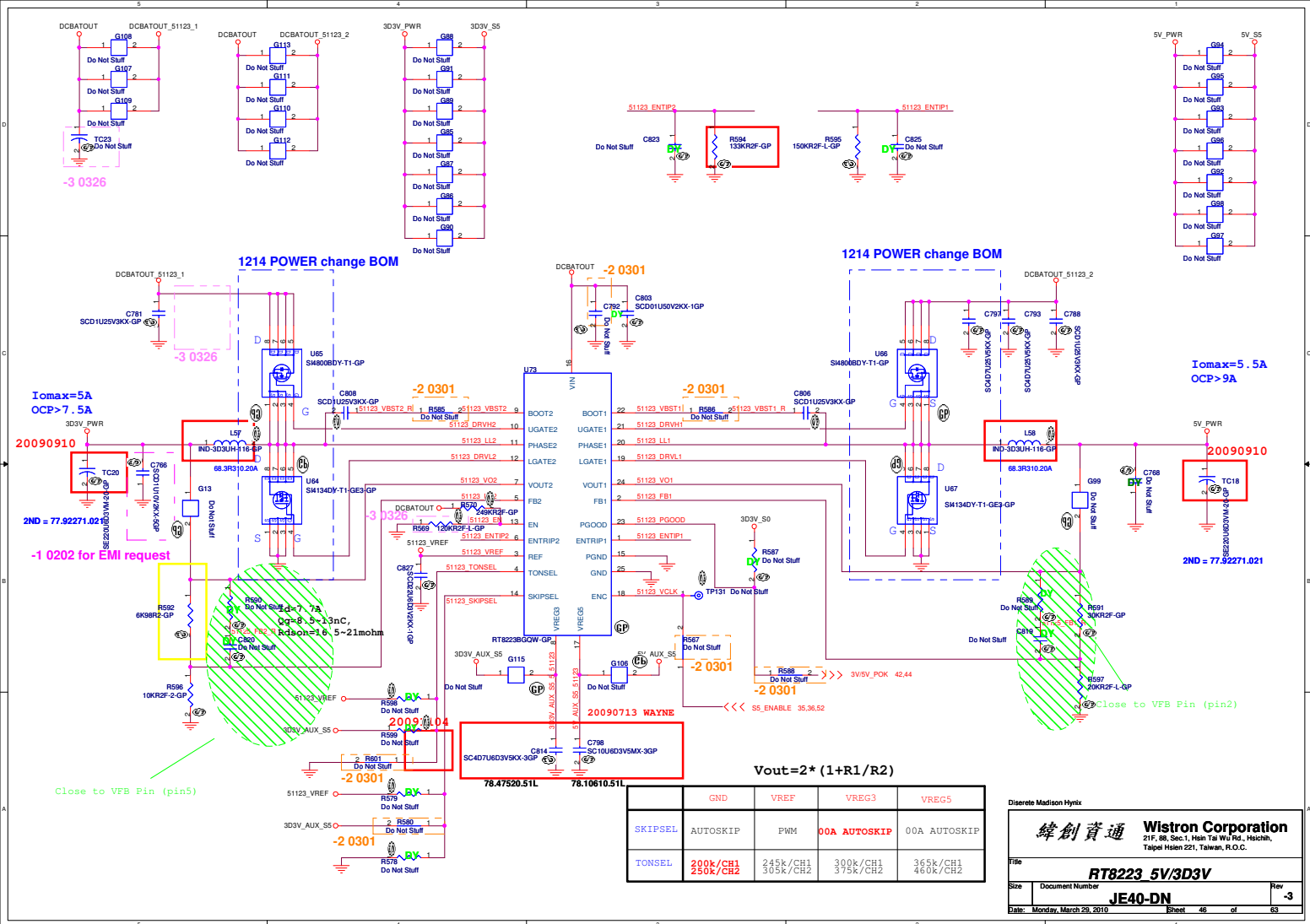
3D3V AUX S5



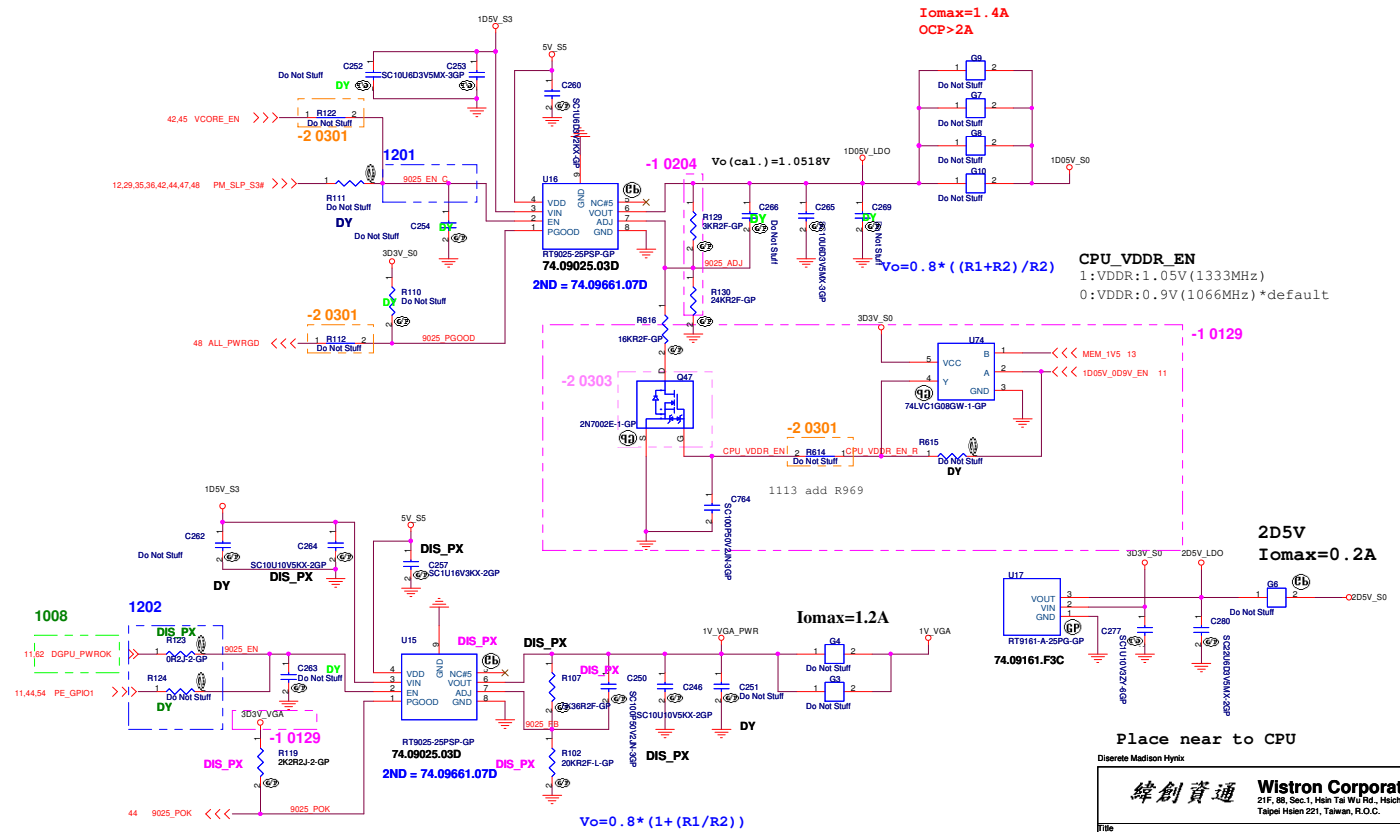
+3VS to 3.3V_DELAY Transfer

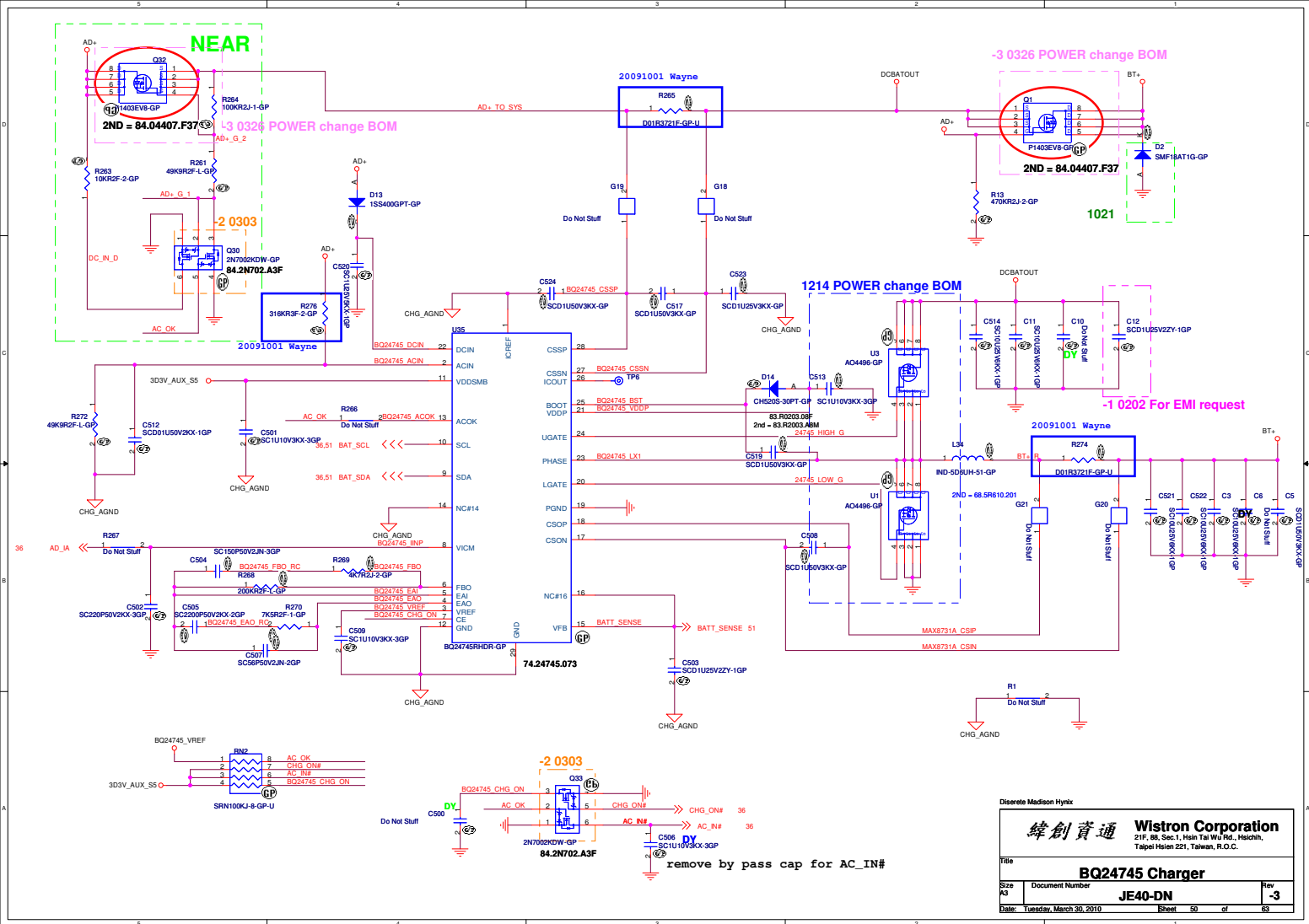


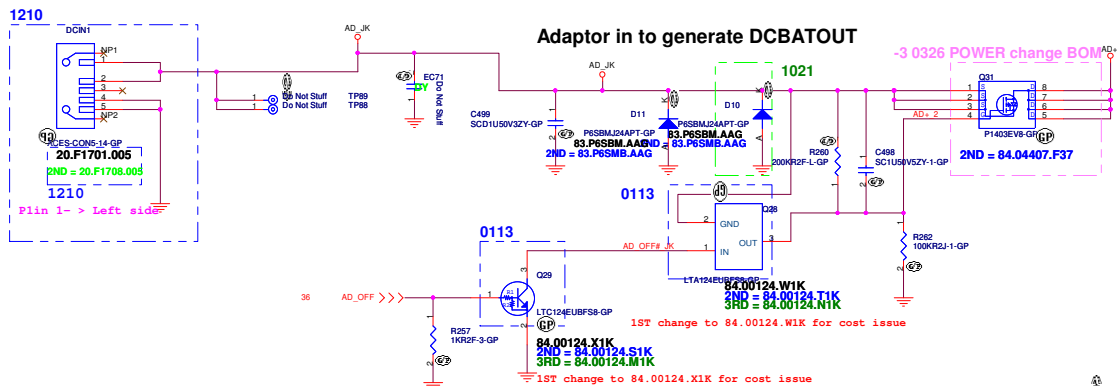


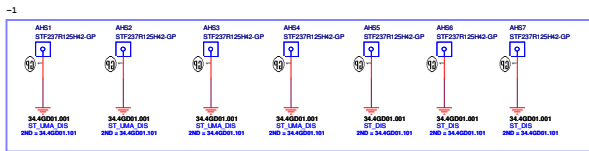
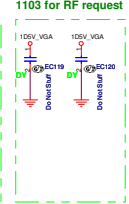
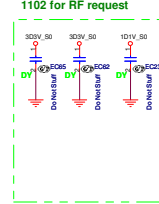
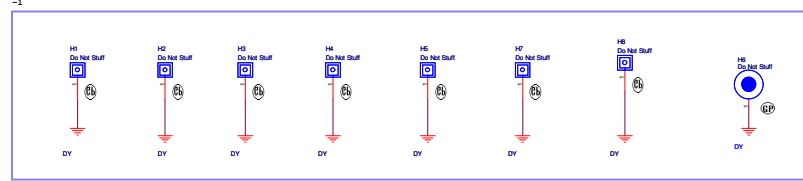
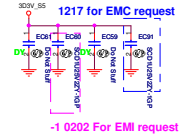
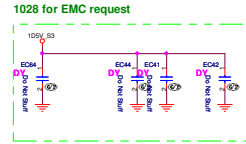
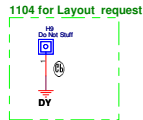
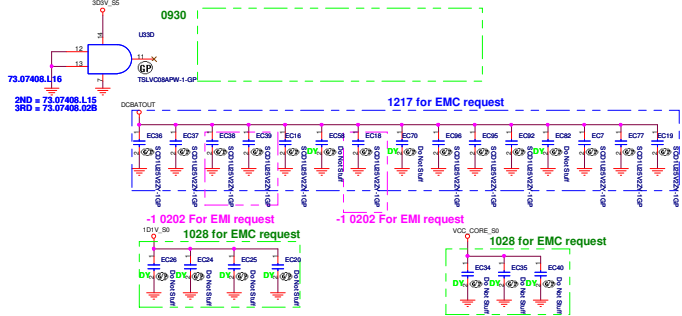


RT9025 for 1D05V_S0





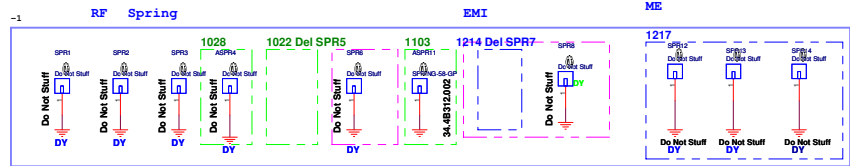




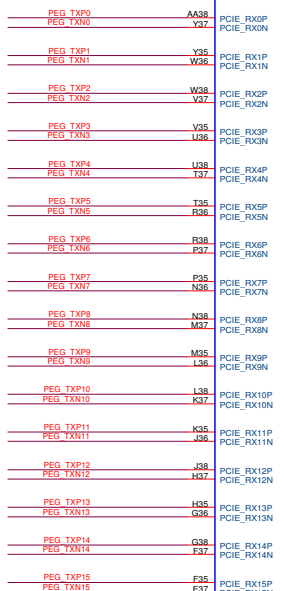
Check test point

- 303V_S0
- 303V_AUX_S5
- 303V_S5
- 5V_S5
- 12.36 PU_POWER1W
- 4.11 CPU_POWER3
- 35.34-46 SS_ENABLE
- 6.11 CPU_LED_RSTA
- TP120
- TP125
- TP123
- TP122
- TP124
- TP127
- TP126
- TP128

Test Point放在Dimm Door打開可量測處



8 PEG_TXP[15:0] << PEG_TXP[15:0]
8 PEG_TXN[15:0] << PEG_TXN[15:0]



PCI EXPRESS INTERFACE

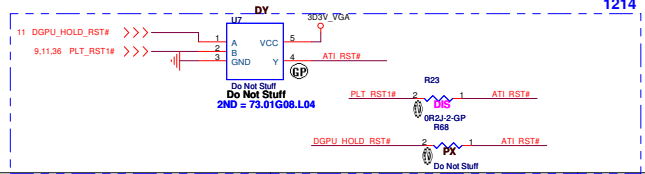


CLK
PCI_E, REFCLKP
PCI_E, REFCLKN
MADSON-PRO-GP

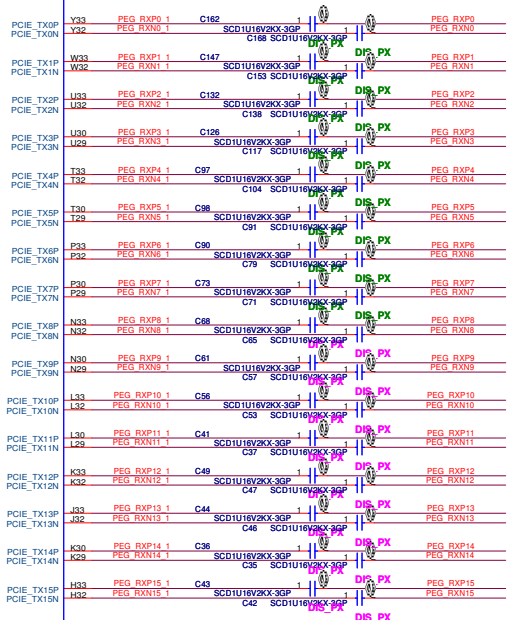
71.MDSON.M01

DIS_PX

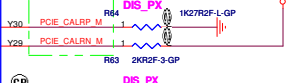
1214



8 PEG_RXP[15:0] << PEG_RXP[15:0]
8 PEG_RXN[15:0] << PEG_RXN[15:0]



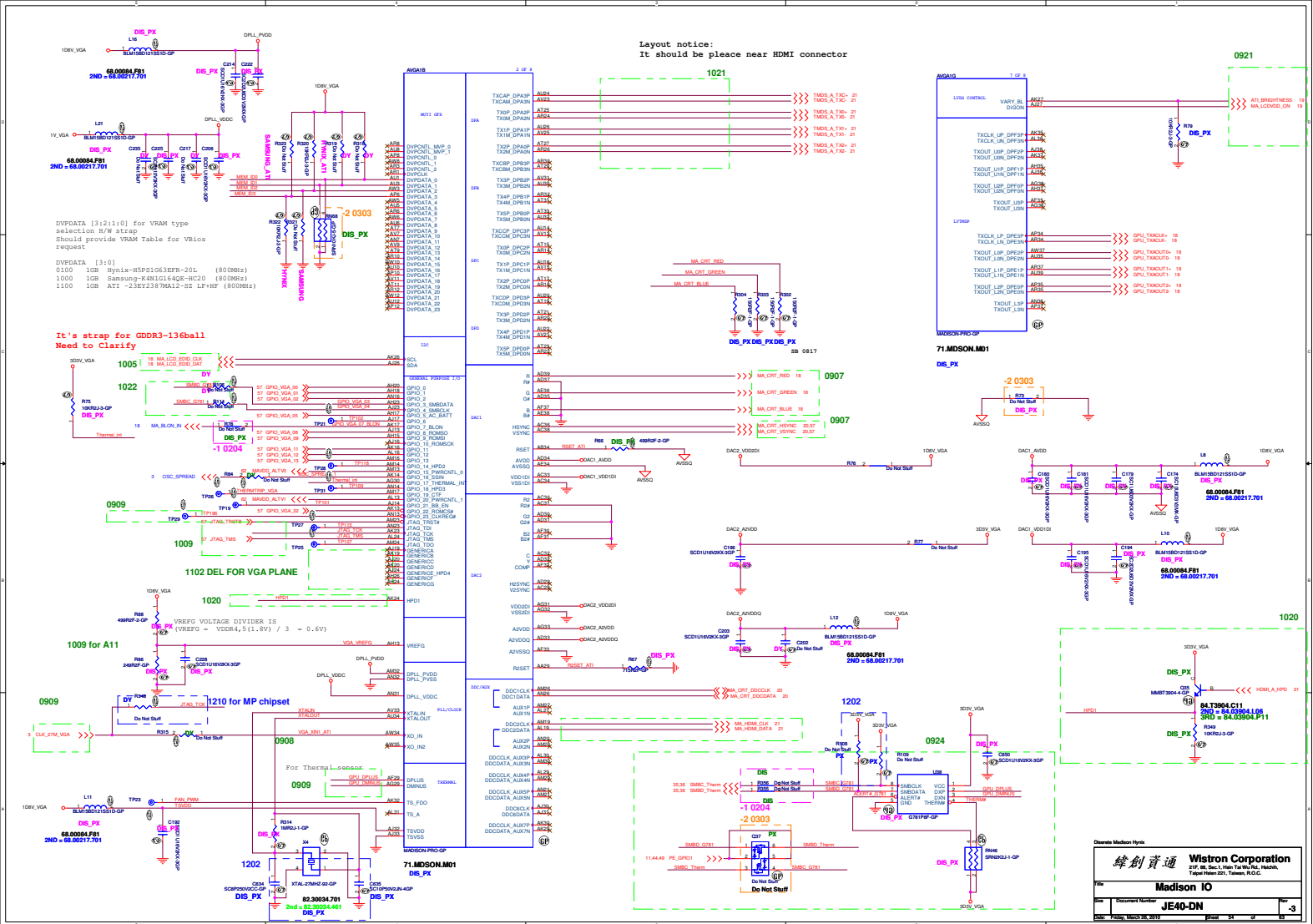
0908



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緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

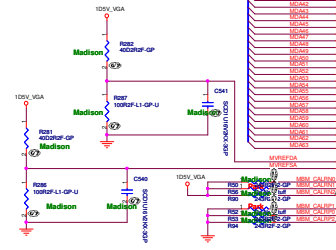
Title		Madison PCIE	
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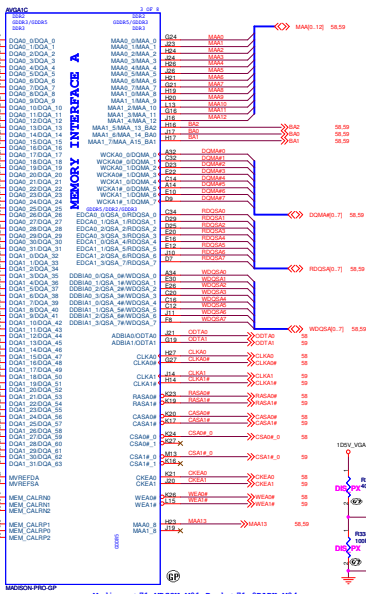


For SSTL-1.8/SSTL-2/DRI/GDR1: 0.5 * VDDR1.
For DOR3/GDR3/GDR4/GDR5: 0.7 * VDDR1.

DIVIDER RESISTORS	GDR5	DOR3	DOR3
MYREF	1.5V	1.8/1.5V	1.5V
MYREF TO PWR	40.2K	50.2K	40.2K
MYREF TO GND	100K	100K	100K



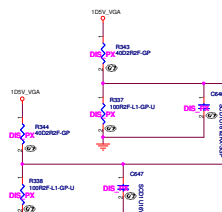
Madison: MEM_CALRP[0,2] signals are used
Park: MEM_CALRP1 and MEM_CALRP1 are used



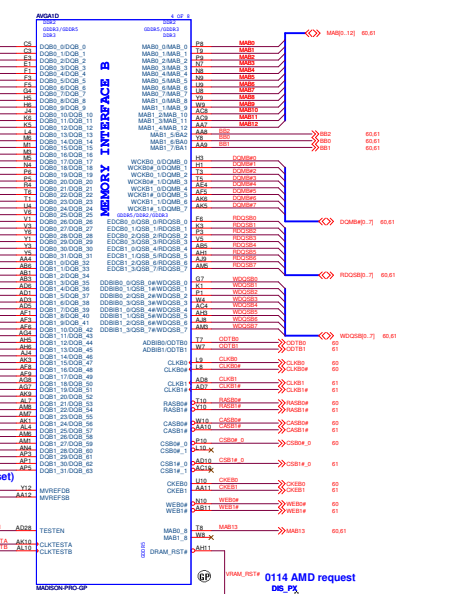
Madison->71.MDS0N.MD1, Park->71.GPARK.MD4

For SSTL-1.8/SSTL-2/DRI/GDR1: 0.5 * VDDR1.
For GDR3/GDR3/GDR4/GDR5: 0.7 * VDDR1.

DIVIDER RESISTORS	GDR5	DOR3	DOR3
MYREF	1.5V	1.8/1.5V	1.5V
MYREF TO PWR	40.2K	50.2K	40.2K
MYREF TO GND	100K	100K	100K



0114 AMD request (for MP chipset)

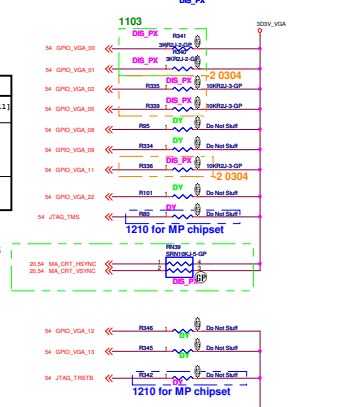


Madison->71.MDS0N.MD1

0114 AMD request

STRAPS	PIN	DESCRIPTION	RECOMMENDED SETTINGS S: DO NOT RETAIL RESISTOR R: RETAIL RESISTOR X: DESIRED DEFERENTIAL N/A: NOT APPLICABLE
TX_PWRN_ENB (Internal PD)	GP100	FOR FULL TX OUTPUT SWING Transmitter Power Savings Enable = 50k Tx output swing = Full Tx output swing	X
TX_DEMPR_EN (Internal PD)	GP101	Transmitter De-emphasis Enable = 0: 2x de-emphasis disabled = 1: 2x de-emphasis enabled	X
RESERVED	GP108	RESERVED	0
BIF_VGA_DIS	GP109	VGA ENABLED	0
RESERVED	GP1021	RESERVED	0
BIOS_ROM_EN	GP1022_ROMCS	ENABLE EXTERNAL BIOS ROM	0
VIP_DEVICE_STRAP_ENA (Internal PD)	GP10[13,12,11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT If BIOS_ROM_EN=1, then Config[3:0] Defines the ROM type If BIOS_ROM_EN=0, then Config[3:0] Defines the primary memory aperture size	X X X
RSVD	V2SYNC		0
RSVD	R2SYNC		0
AUD[1] AUD[0] (Internal PD)	VGA_HSYNC VGA_VSYNC	AUD[1:0] 0:No audio function 1:Audio for DisplayPort and HDMI (1 if adapter is detected) 10:Audio for DisplayPort only 11:Audio for both DisplayPort and HDMI	X X

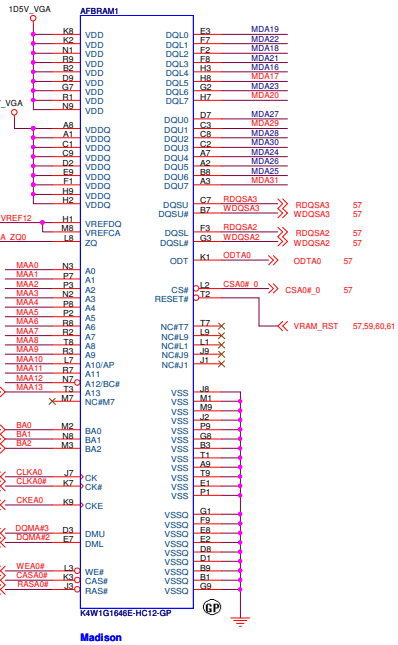
AMD RESERVED CONFIGURATION STRAPS ALLOW FOR PULLED PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET			
R2SYNC, GENERICC, GPIO2, GPIO21			
If BIOS_ROM_EN (GPIO22) = 0		If BIOS_ROM_EN (GPIO22) = 1	
Size of the primary memory aperture	GPIO[13,12,11]	Manufacturer	Part Number
128MB	x000	ST	M25P05A
256MB	x001	Microelectronics	M25P10A
512MB	x010		M25P20
1GB	x011		M25P40
2GB	x012		M25P80
4GB	x013	Chingis (Formerly PMC)	PH25LV012A PH25LV010A



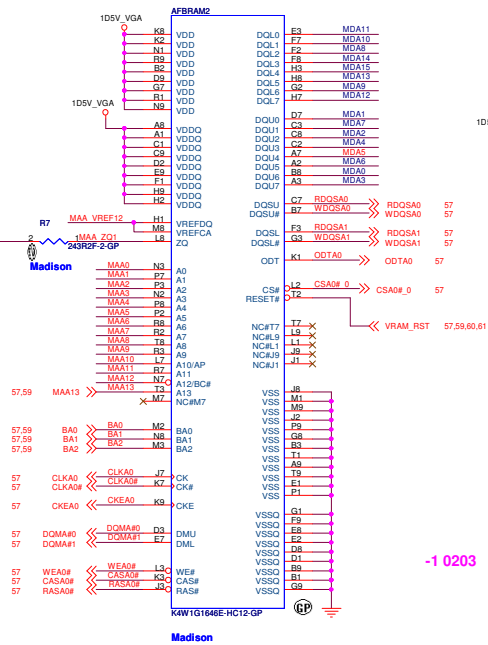
Designator	For M97-M2	For Mannheim
R_MEM_1	10K	10K
R_MEM_2	40R/Short	51R
R_MEM_3	DY	DY
C_MEM	2.2nF	68pF

Wistron Corporation
217, 2nd, 3rd, 4th, 5th, 6th, 7th, 8th, 9th, 10th, 11th, 12th, 13th, 14th, 15th, 16th, 17th, 18th, 19th, 20th, 21st, 22nd, 23rd, 24th, 25th, 26th, 27th, 28th, 29th, 30th, 31st, 32nd, 33rd, 34th, 35th, 36th, 37th, 38th, 39th, 40th, 41st, 42nd, 43rd, 44th, 45th, 46th, 47th, 48th, 49th, 50th, 51st, 52nd, 53rd, 54th, 55th, 56th, 57th, 58th, 59th, 60th, 61st, 62nd, 63rd, 64th, 65th, 66th, 67th, 68th, 69th, 70th, 71st, 72nd, 73rd, 74th, 75th, 76th, 77th, 78th, 79th, 80th, 81st, 82nd, 83rd, 84th, 85th, 86th, 87th, 88th, 89th, 90th, 91st, 92nd, 93rd, 94th, 95th, 96th, 97th, 98th, 99th, 100th, 101st, 102nd, 103rd, 104th, 105th, 106th, 107th, 108th, 109th, 110th, 111th, 112th, 113th, 114th, 115th, 116th, 117th, 118th, 119th, 120th, 121st, 122nd, 123rd, 124th, 125th, 126th, 127th, 128th, 129th, 130th, 131st, 132nd, 133rd, 134th, 135th, 136th, 137th, 138th, 139th, 140th, 141st, 142nd, 143rd, 144th, 145th, 146th, 147th, 148th, 149th, 150th, 151st, 152nd, 153rd, 154th, 155th, 156th, 157th, 158th, 159th, 160th, 161st, 162nd, 163rd, 164th, 165th, 166th, 167th, 168th, 169th, 170th, 171st, 172nd, 173rd, 174th, 175th, 176th, 177th, 178th, 179th, 180th, 181st, 182nd, 183rd, 184th, 185th, 186th, 187th, 188th, 189th, 190th, 191st, 192nd, 193rd, 194th, 195th, 196th, 197th, 198th, 199th, 200th, 201st, 202nd, 203rd, 204th, 205th, 206th, 207th, 208th, 209th, 210th, 211st, 212nd, 213th, 214th, 215th, 216th, 217th, 218th, 219th, 220th, 221st, 222nd, 223rd, 224th, 225th, 226th, 227th, 228th, 229th, 230th, 231st, 232nd, 233rd, 234th, 235th, 236th, 237th, 238th, 239th, 240th, 241st, 242nd, 243rd, 244th, 245th, 246th, 247th, 248th, 249th, 250th, 251st, 252nd, 253rd, 254th, 255th, 256th, 257th, 258th, 259th, 260th, 261st, 262nd, 263rd, 264th, 265th, 266th, 267th, 268th, 269th, 270th, 271st, 272nd, 273rd, 274th, 275th, 276th, 277th, 278th, 279th, 280th, 281st, 282nd, 283rd, 284th, 285th, 286th, 287th, 288th, 289th, 290th, 291st, 292nd, 293rd, 294th, 295th, 296th, 297th, 298th, 299th, 300th, 301st, 302nd, 303rd, 304th, 305th, 306th, 307th, 308th, 309th, 310th, 311st, 312nd, 313th, 314th, 315th, 316th, 317th, 318th, 319th, 320th, 321st, 322nd, 323rd, 324th, 325th, 326th, 327th, 328th, 329th, 330th, 331st, 332nd, 333rd, 334th, 335th, 336th, 337th, 338th, 339th, 340th, 341st, 342nd, 343rd, 344th, 345th, 346th, 347th, 348th, 349th, 350th, 351st, 352nd, 353rd, 354th, 355th, 356th, 357th, 358th, 359th, 360th, 361st, 362nd, 363rd, 364th, 365th, 366th, 367th, 368th, 369th, 370th, 371st, 372nd, 373rd, 374th, 375th, 376th, 377th, 378th, 379th, 380th, 381st, 382nd, 383rd, 384th, 385th, 386th, 387th, 388th, 389th, 390th, 391st, 392nd, 393rd, 394th, 395th, 396th, 397th, 398th, 399th, 400th, 401st, 402nd, 403rd, 404th, 405th, 406th, 407th, 408th, 409th, 410th, 411st, 412nd, 413th, 414th, 415th, 416th, 417th, 418th, 419th, 420th, 421st, 422nd, 423rd, 424th, 425th, 426th, 427th, 428th, 429th, 430th, 431st, 432nd, 433rd, 434th, 435th, 436th, 437th, 438th, 439th, 440th, 441st, 442nd, 443rd, 444th, 445th, 446th, 447th, 448th, 449th, 450th, 451st, 452nd, 453rd, 454th, 455th, 456th, 457th, 458th, 459th, 460th, 461st, 462nd, 463rd, 464th, 465th, 466th, 467th, 468th, 469th, 470th, 471st, 472nd, 473rd, 474th, 475th, 476th, 477th, 478th, 479th, 480th, 481st, 482nd, 483rd, 484th, 485th, 486th, 487th, 488th, 489th, 490th, 491st, 492nd, 493rd, 494th, 495th, 496th, 497th, 498th, 499th, 500th, 501st, 502nd, 503rd, 504th, 505th, 506th, 507th, 508th, 509th, 510th, 511st, 512nd, 513th, 514th, 515th, 516th, 517th, 518th, 519th, 520th, 521st, 522nd, 523rd, 524th, 525th, 526th, 527th, 528th, 529th, 530th, 531st, 532nd, 533rd, 534th, 535th, 536th, 537th, 538th, 539th, 540th, 541st, 542nd, 543rd, 544th, 545th, 546th, 547th, 548th, 549th, 550th, 551st, 552nd, 553rd, 554th, 555th, 556th, 557th, 558th, 559th, 560th, 561st, 562nd, 563rd, 564th, 565th, 566th, 567th, 568th, 569th, 570th, 571st, 572nd, 573rd, 574th, 575th, 576th, 577th, 578th, 579th, 580th, 581st, 582nd, 583rd, 584th, 585th, 586th, 587th, 588th, 589th, 590th, 591st, 592nd, 593rd, 594th, 595th, 596th, 597th, 598th, 599th, 600th, 601st, 602nd, 603rd, 604th, 605th, 606th, 607th, 608th, 609th, 610th, 611st, 612nd, 613th, 614th, 615th, 616th, 617th, 618th, 619th, 620th, 621st, 622nd, 623rd, 624th, 625th, 626th, 627th, 628th, 629th, 630th, 631st, 632nd, 633rd, 634th, 635th, 636th, 637th, 638th, 639th, 640th, 641st, 642nd, 643rd, 644th, 645th, 646th, 647th, 648th, 649th, 650th, 651st, 652nd, 653rd, 654th, 655th, 656th, 657th, 658th, 659th, 660th, 661st, 662nd, 663rd, 664th, 665th, 666th, 667th, 668th, 669th, 670th, 671st, 672nd, 673rd, 674th, 675th, 676th, 677th, 678th, 679th, 680th, 681st, 682nd, 683rd, 684th, 685th, 686th, 687th, 688th, 689th, 690th, 691st, 692nd, 693rd, 694th, 695th, 696th, 697th, 698th, 699th, 700th, 701st, 702nd, 703rd, 704th, 705th, 706th, 707th, 708th, 709th, 710th, 711st, 712nd, 713th, 714th, 715th, 716th, 717th, 718th, 719th, 720th, 721st, 722nd, 723rd, 724th, 725th, 726th, 727th, 728th, 729th, 730th, 731st, 732nd, 733rd, 734th, 735th, 736th, 737th, 738th, 739th, 740th, 741st, 742nd, 743rd, 744th, 745th, 746th, 747th, 748th, 749th, 750th, 751st, 752nd, 753rd, 754th, 755th, 756th, 757th, 758th, 759th, 760th, 761st, 762nd, 763rd, 764th, 765th, 766th, 767th, 768th, 769th, 770th, 771st, 772nd, 773rd, 774th, 775th, 776th, 777th, 778th, 779th, 780th, 781st, 782nd, 783rd, 784th, 785th, 786th, 787th, 788th, 789th, 790th, 791st, 792nd, 793rd, 794th, 795th, 796th, 797th, 798th, 799th, 800th, 801st, 802nd, 803rd, 804th, 805th, 806th, 807th, 808th, 809th, 810th, 811st, 812nd, 813th, 814th, 815th, 816th, 817th, 818th, 819th, 820th, 821st, 822nd, 823rd, 824th, 825th, 826th, 827th, 828th, 829th, 830th, 831st, 832nd, 833rd, 834th, 835th, 836th, 837th, 838th, 839th, 840th, 841st, 842nd, 843rd, 844th, 845th, 846th, 847th, 848th, 849th, 850th, 851st, 852nd, 853rd, 854th, 855th, 856th, 857th, 858th, 859th, 860th, 861st, 862nd, 863rd, 864th, 865th, 866th, 867th, 868th, 869th, 870th, 871st, 872nd, 873rd, 874th, 875th, 876th, 877th, 878th, 879th, 880th, 881st, 882nd, 883rd, 884th, 885th, 886th, 887th, 888th, 889th, 890th, 891st, 892nd, 893rd, 894th, 895th, 896th, 897th, 898th, 899th, 900th, 901st, 902nd, 903rd, 904th, 905th, 906th, 907th, 908th, 909th, 910th, 911st, 912nd, 913th, 914th, 915th, 916th, 917th, 918th, 919th, 920th, 921st, 922nd, 923rd, 924th, 925th, 926th, 927th, 928th, 929th, 930th, 931st, 932nd, 933rd, 934th, 935th, 936th, 937th, 938th, 939th, 940th, 941st, 942nd, 943rd, 944th, 945th, 946th, 947th, 948th, 949th, 950th, 951st, 952nd, 953rd, 954th, 955th, 956th, 957th, 958th, 959th, 960th, 961st, 962nd, 963rd, 964th, 965th, 966th, 967th, 968th, 969th, 970th, 971st, 972nd, 973rd, 974th, 975th, 976th, 977th, 978th, 979th, 980th, 981st, 982nd, 983rd, 984th, 985th, 986th, 987th, 988th, 989th, 990th, 991st, 992nd, 993rd, 994th, 995th, 996th, 997th, 998th, 999th, 1000th, 1001st, 1002nd, 1003rd, 1004th, 1005th, 1006th, 1007th, 1008th, 1009th, 1010th, 1011st, 1012nd, 1013th, 1014th, 1015th, 1016th, 1017th, 1018th, 1019th, 1020th, 1021st, 1022nd, 1023rd, 1024th, 1025th, 1026th, 1027th, 1028th, 1029th, 1030th, 1031st, 1032nd, 1033rd, 1034th, 1035th, 1036th, 1037th, 1038th, 1039th, 1040th, 1041st, 1042nd, 1043rd, 1044th, 1045th, 1046th, 1047th, 1048th, 1049th, 1050th, 1051st, 1052nd, 1053rd, 1054th, 1055th, 1056th, 1057th, 1058th, 1059th, 1060th, 1061st, 1062nd, 1063rd, 1064th, 1065th, 1066th, 1067th, 1068th, 1069th, 1070th, 1071st, 1072nd, 1073rd, 1074th, 1075th, 1076th, 1077th, 1078th, 1079th, 1080th, 1081st, 1082nd, 1083rd, 1084th, 1085th, 1086th, 1087th, 1088th, 1089th, 1090th, 1091st, 1092nd, 1093rd, 1094th, 1095th, 1096th, 1097th, 1098th, 1099th, 1100th, 1101st, 1102nd, 1103rd, 1104th, 1105th, 1106th, 1107th, 1108th, 1109th, 1110th, 1111st, 1112nd, 1113th, 1114th, 1115th, 1116th, 1117th, 1118th, 1119th, 1120th, 1121st, 1122nd, 1123rd, 1124th, 1125th, 1126th, 1127th, 1128th, 1129th, 1130th, 1131st, 1132nd, 1133rd, 1134th, 1135th, 1136th, 1137th, 1138th, 1139th, 1140th, 1141st, 1142nd, 1143rd, 1144th, 1145th, 1146th, 1147th, 1148th, 1149th, 1150th, 1151st, 1152nd, 1153rd, 1154th, 1155th, 1156th, 1157th, 1158th, 1159th, 1160th, 1161st, 1162nd, 1163rd, 1164th, 1165th, 1166th, 1167th, 1168th, 1169th, 1170th, 1171st, 1172nd, 1173rd, 1174th, 1175th, 1176th, 1177th, 1178th, 1179th, 1180th, 1181st, 1182nd, 1183rd, 1184th, 1185th, 1186th, 1187th, 1188th, 1189th, 1190th, 1191st, 1192nd, 1193rd, 1194th, 1195th, 1196th, 1197th, 1198th, 1199th, 1200th, 1201st, 1202nd, 1203rd, 1204th, 1205th, 1206th, 1207th, 1208th, 1209th, 1210th, 1211st, 1212nd, 1213th, 1214th, 1215th, 1216th, 1217th, 1218th, 1219th, 1220th, 1221st, 1222nd, 1223rd, 1224th, 1225th, 1226th, 1227th, 1228th, 1229th, 1230th, 1231st, 1232nd, 1233rd, 1234th, 1235th, 1236th, 1237th, 1238th, 1239th, 1240th, 1241st, 1242nd, 1243rd, 1244th, 1245th, 1246th, 1247th, 1248th, 1249th, 1250th, 1251st, 1252nd, 1253rd, 1254th, 1255th, 1256th, 1257th, 1258th, 1259th, 1260th, 1261st, 1262nd, 1263rd, 1264th, 1265th, 1266th, 1267th, 1268th, 1269th, 1270th, 1271st, 1272nd, 1273rd, 1274th, 1275th, 1276th, 1277th, 1278th, 1279th, 1280th, 1281st, 1282nd, 1283rd, 1284th, 1285th, 1286th, 1287th, 1288th, 1289th, 1290th, 1291st, 1292nd, 1293rd, 1294th, 1295th, 1296th, 1297th, 1298th, 1299th, 1300th, 1301st, 1302nd, 1303rd, 1304th, 1305th, 1306th, 1307th, 1308th, 1309th, 1310th, 1311st, 1312nd, 1313th, 1314th, 1315th, 1316th, 1317th, 1318th, 1319th, 1320th, 1321st, 1322nd, 1323rd, 1324th, 1325th, 1326th, 1327th, 1328th, 1329th, 1330th, 1331st, 1332nd, 1333rd, 1334th, 1335th, 1336th, 1337th, 1338th, 1339th, 1340th, 1341st, 1342nd, 1343rd, 1344th, 1345th, 1346th, 1347th, 1348th, 1349th, 1350th, 1351st, 1352nd, 1353rd, 1354th, 1355th, 1356th, 1357th, 1358th, 1359th, 1360th, 1361st, 1362nd, 1363rd, 1364th, 1365th, 1366th, 1367th, 1368th, 1369th, 1370th, 1371st, 1372nd, 1373rd, 1374th, 1375th, 1376th, 1377th, 1378th, 1379th, 1380th, 1381st, 1382nd, 1383rd, 1384th, 1385th, 1386th, 1387th, 1388th, 1389th, 1390th, 1391st, 1392nd, 1393rd, 1394th, 1395th, 1396th, 1397th, 1398th, 1399th, 1400th, 1401st, 1402nd, 1403rd, 1404th, 1405th, 1406th, 1407th, 1408th, 1409th, 1410th, 1411st, 1412nd, 1413th, 1414th, 1415th, 1416th, 1417th, 1418th, 1419th, 1420th, 1421st, 1422nd, 1423rd, 1424th, 1425th, 1426th, 1427th, 1428th, 1429th, 1430th, 1431st, 1432nd, 1433rd, 1434th, 1435th, 1436th, 1437th, 1438th, 1439th, 1440th, 1441st, 1442nd, 1443rd, 1444th, 1445th, 1446th, 1447th, 1448th, 1449th, 1450th, 1451st, 1452nd, 1453rd, 1454th, 1455th, 1456th, 1457th, 1458th, 1459th, 1460th, 1461st, 1462nd, 1463rd, 1464th, 1465th, 1466th, 1467th, 1468th, 1469th, 1470th, 1471st, 1472nd, 1473rd, 1474th, 1475th, 1476th, 1477th, 1478th, 1479th, 1480th, 1481st, 1482nd, 1483rd, 1484th, 1485th, 1486th, 1487th, 1488th, 1489th, 1490th, 1491st, 1492nd, 1493rd, 1494th, 1495th, 1496th, 1497th, 1498th, 1499th, 1500th, 1501st, 1502nd, 1503rd, 1504th, 1505th, 1506th, 1507th, 1508th, 1509th, 1510th, 1511st, 151

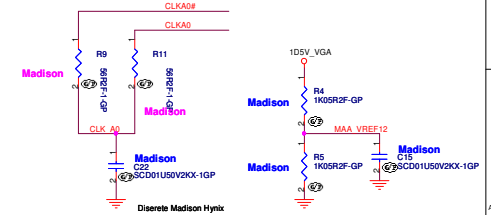
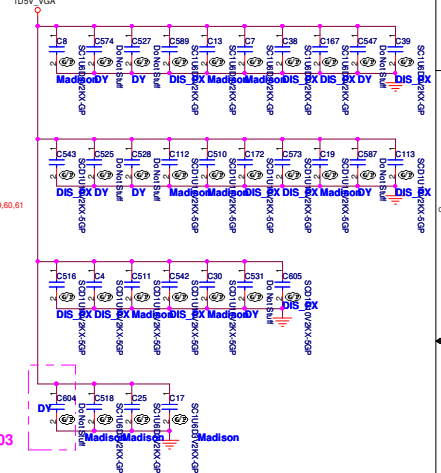
DDR3



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SAMSUNG: 72.41164.H0U
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Taipei Hsien 221, Taiwan, R.O.C.

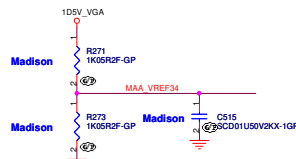
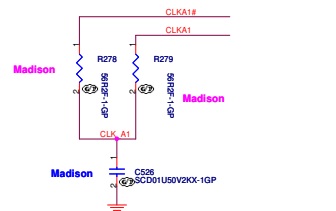
Title			
VRAM(1/4)			
Size	Document Number	Rev	
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SAMSUNG: 72.41164.H0U
HYNIX: 72.51G63.C0U

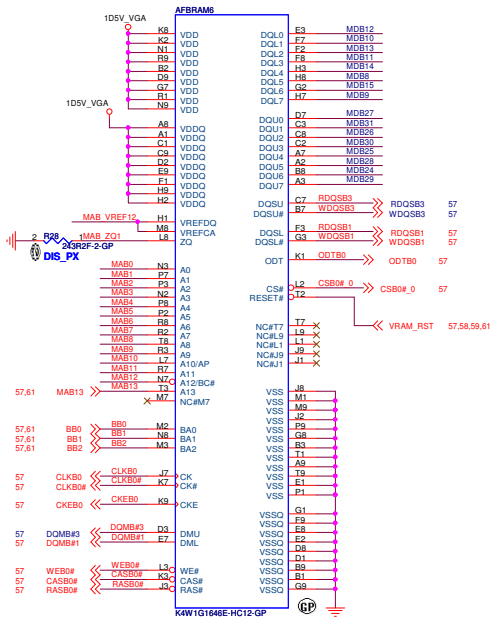
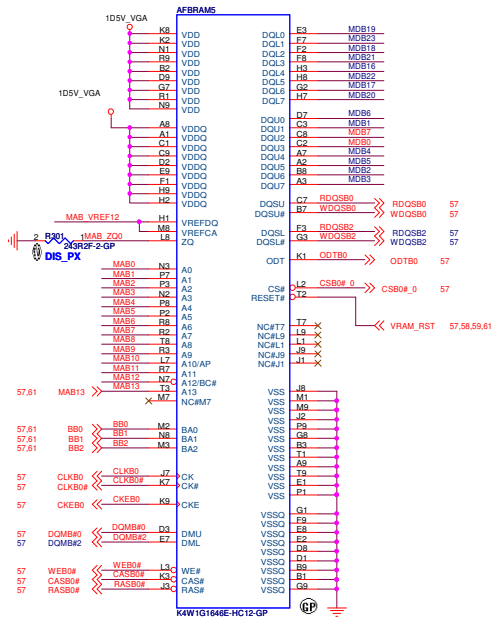
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57.61 DOMB0[0..7] <<>

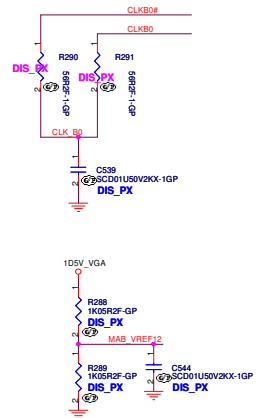
57.61 RDQSB[0..7] <<>

57.61 WDQSB[0..7] <<>

57.61 MAB[0..12] <<>

57.61 MAB[0..12] <<>

57.61 MAB[0..63] <<>



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NB
RS780-->RS880M
71.RS780.M02-->71.RS880.M02

BOM
1st -> All SKU

SB
SB820M-1-GP
71.SB820.00U-->71.SB820.M03

SATA_CALRP
A11: 80歐姆 1% resistor to GND
A12: TBD歐姆 1% resistor to GND

SATA_CALRN
A11:A11: 931歐姆 1% resistor to VDDAN_11_SATA
A12: TBD歐姆 1% resistor to VDDAN_11_SATA.

VGA
Madison--> PN:71.MDSON.M01
Park--> PN:71.0PARK.M04

VRAM
Samsung-->VRAM FBRAM1~8 PN:VR.1GB0B.006
Hynix--> VRAM FBRAM1~8 PN:VR.1GB0G.004
ATI--> VRAM FBRAM1~8 PN: VR.1GB0T.002

CRT
UMA-->L1-->2R 0603
C62-->47U/6.3V
DIS-->L1-->Bead(L1 68.00217.711 L1608-UH38 SBK160808T-221Y-N-GP)
C62-->DY
PX--> 133 歐姆 (R58 63.10334.1DL R402H16 133R2F-GP)

R605(MAD/PARK/PX)
Madison-->R605 64.15005.6DL R402H16150R2F-L-GP
Park -->R605 64.15005.6DL R402H16150R2F-L-GP
PX -->R605 64.15005.6DL R402H16150R2F-L-GP

HDMI
UMA -->R612 R613 R618 R619 R620 R621 R624 R625 64.71505.6DL R402H16715R2F-2-GP

R432(MAD/PARK)
Madison-->15KR2F 64.15025.6DL
Park-->R432 64.68115.6DL R402H16 6.81KR2F

R250
Madison-->R250 64.32425.6DL R402H16 32K4R2F
Park -->R250 64.32425.6DL R402H16 32K4R2F
PX -->R250 64.32425.6DL R402H16 32K4R2F

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Title			
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