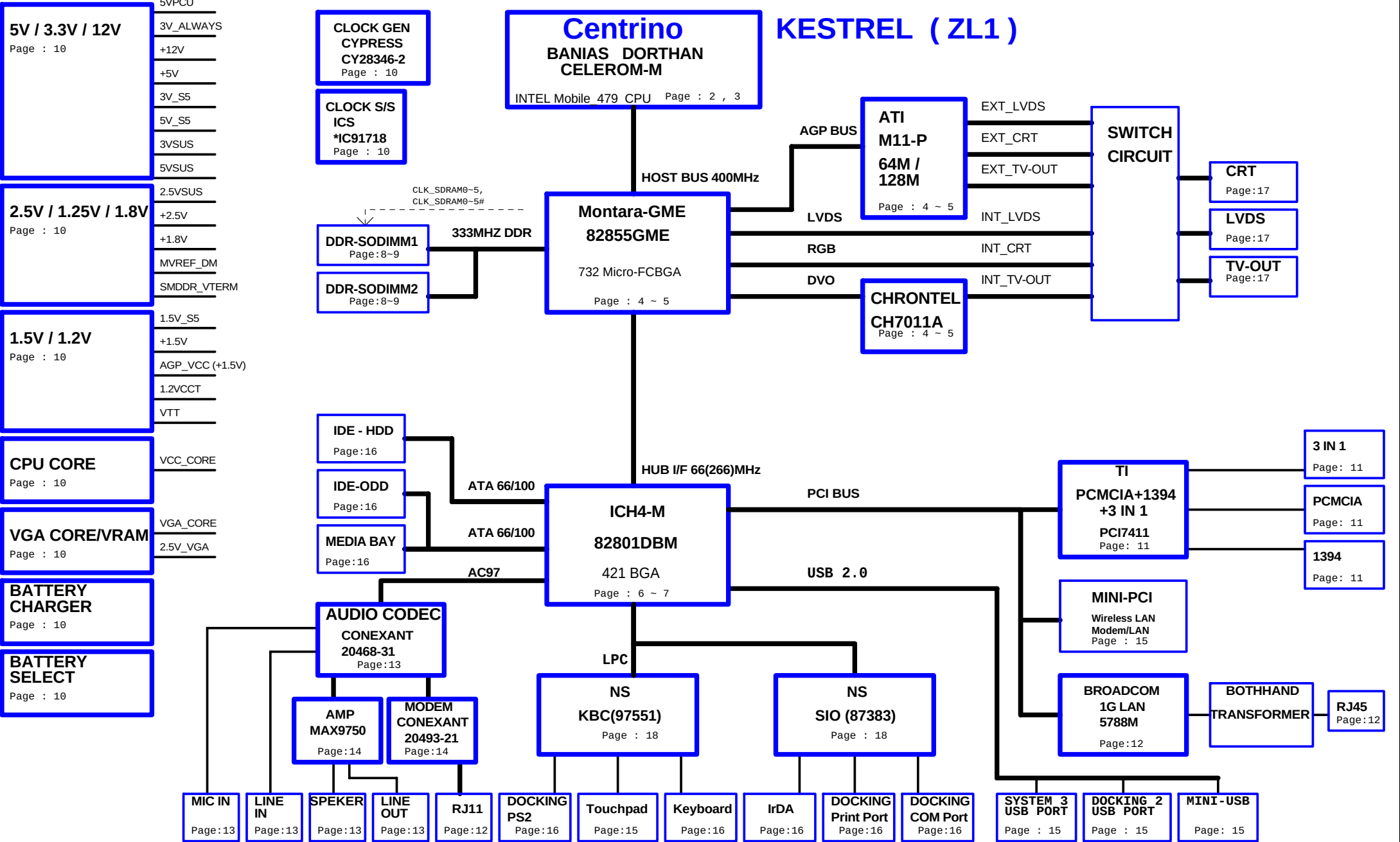
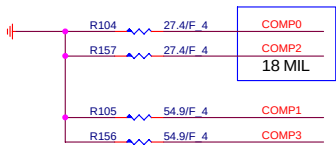
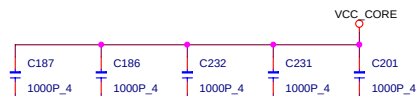




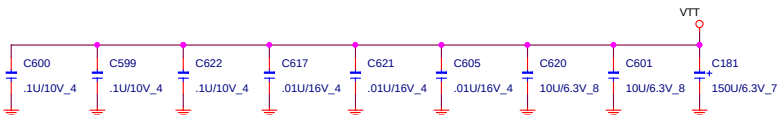
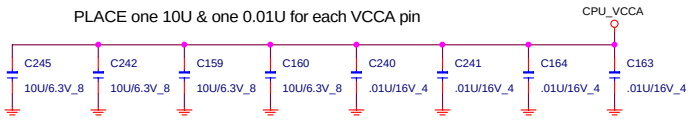
PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
REQ0# / GNT0#	AD18	INTA#	M11
REQ1# / GNT1#	AD20	INTD#	BROADCOM LAN
REQ2# / GNT2#	AD23	INTB# , INTC#	MINI-PCI
REQ3# / GNT3#	AD22	INTE#, INTF#, INTG#	TI 7411



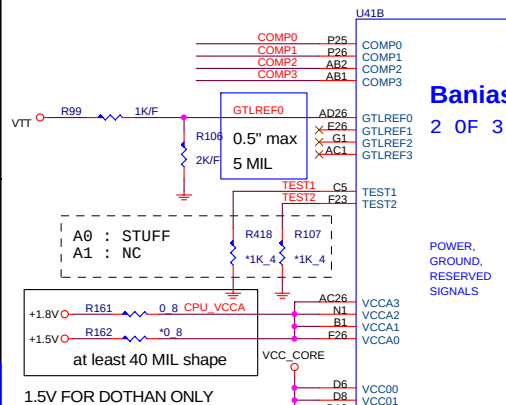
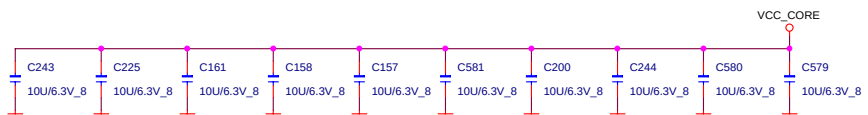
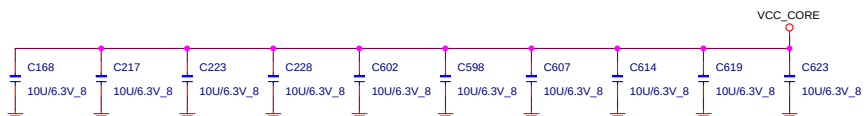
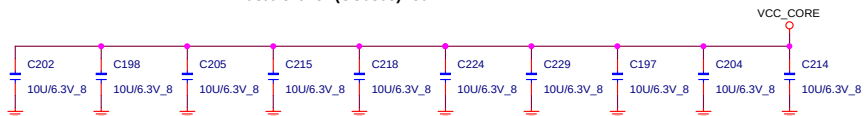
COMPO ~ 4
max length
500 MIL



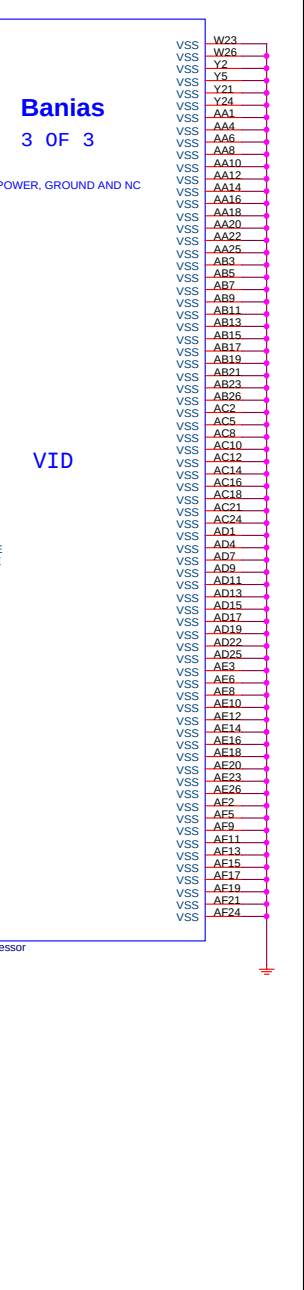
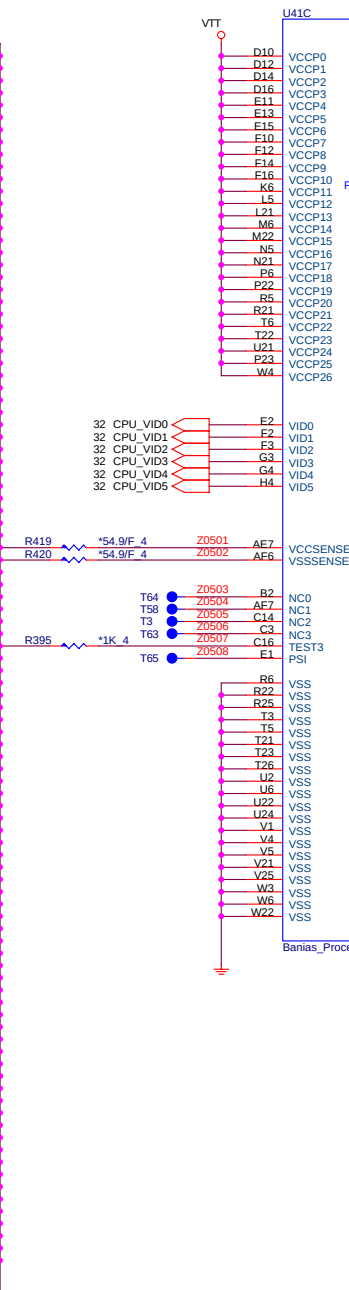
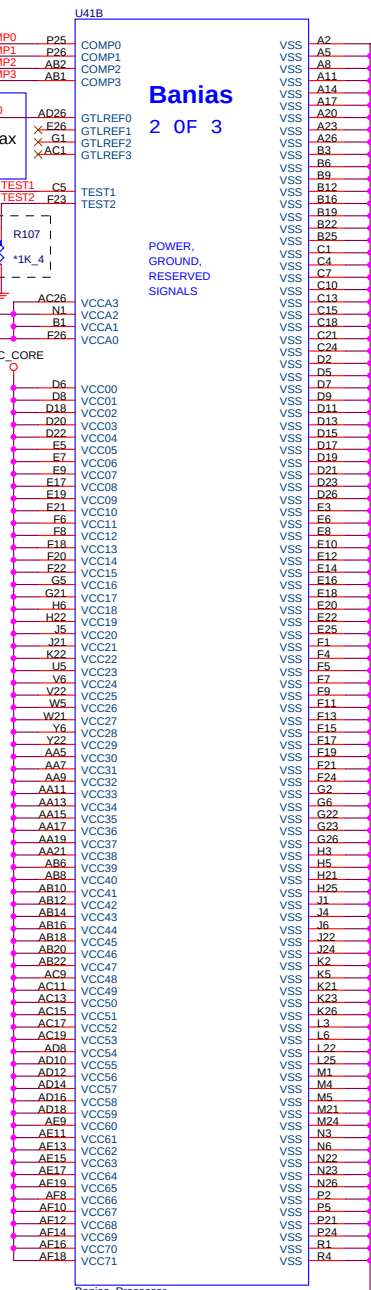
PLACE one 10U & one 0.01U for each VCCA pin

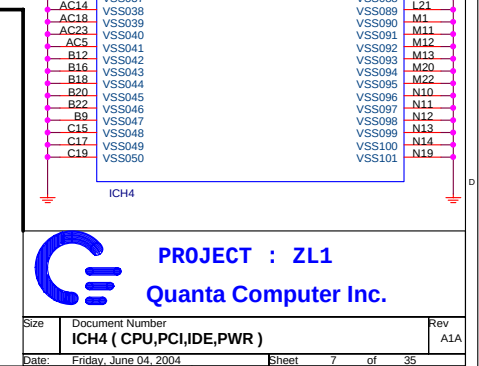
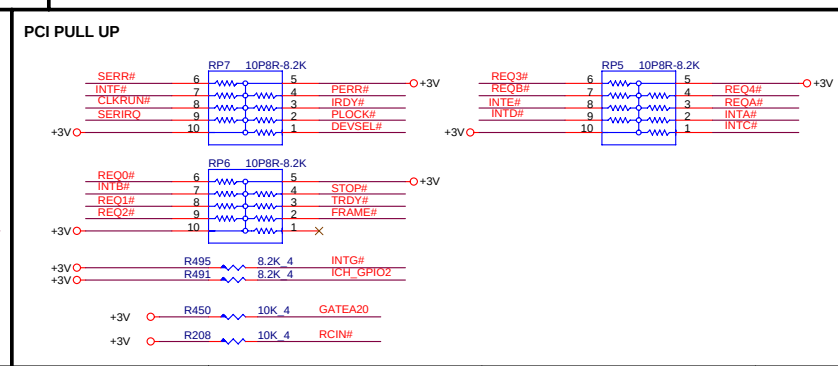
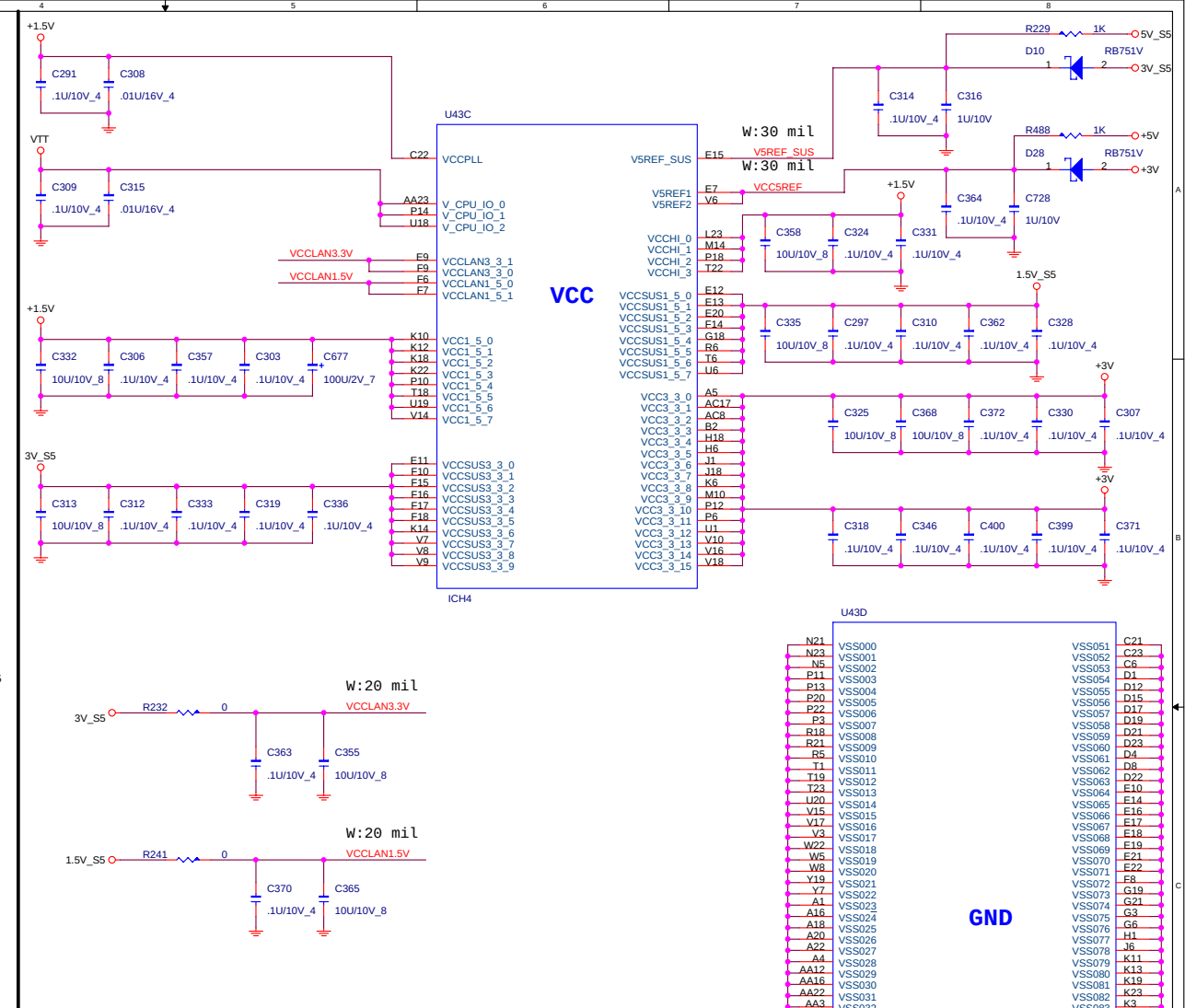


10U/6.3V/X5R(CC0805) *30

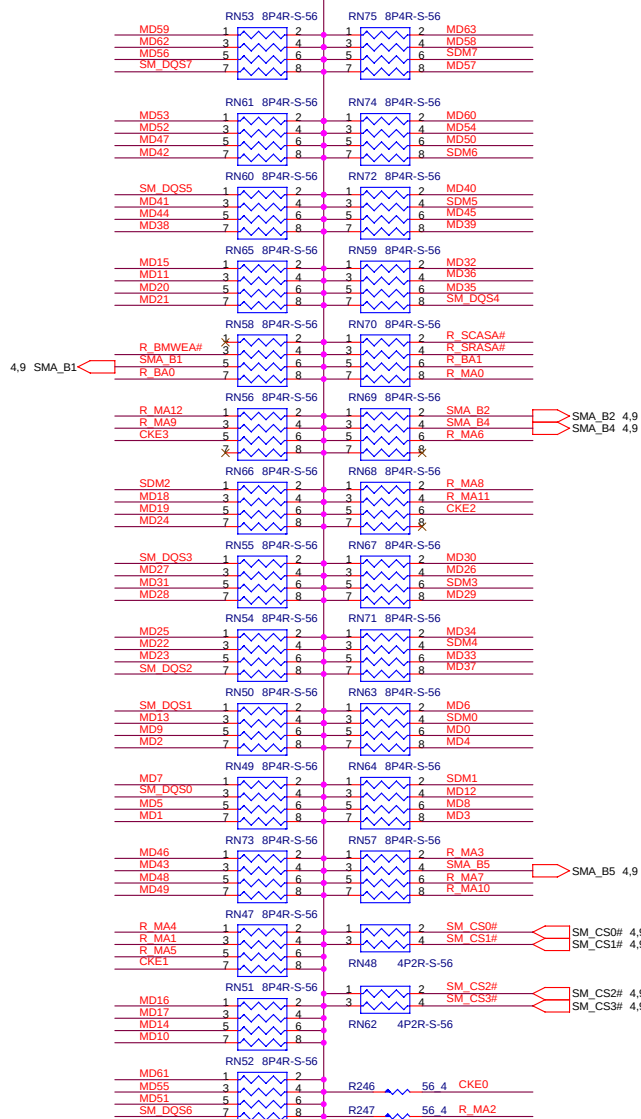


1.5V FOR DOTHAN ONLY

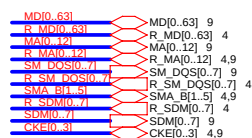




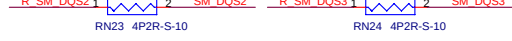
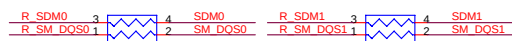
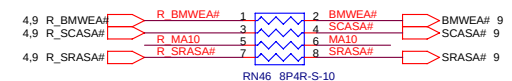
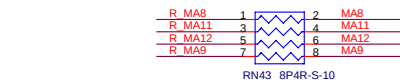
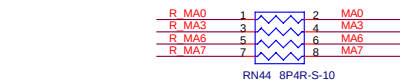
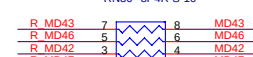
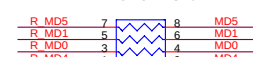
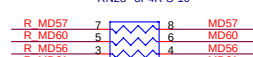
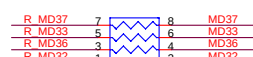
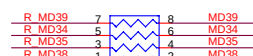
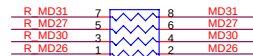
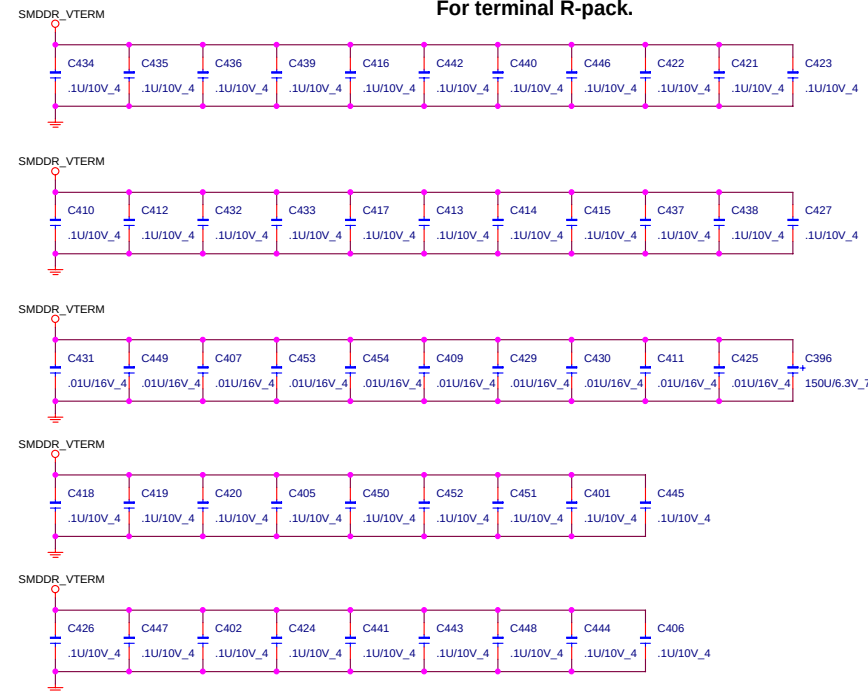
SMDDR_VTERM



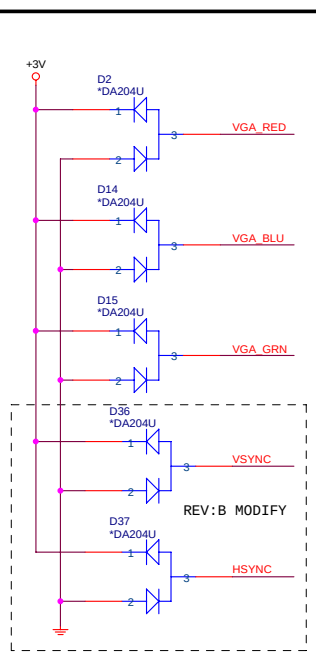
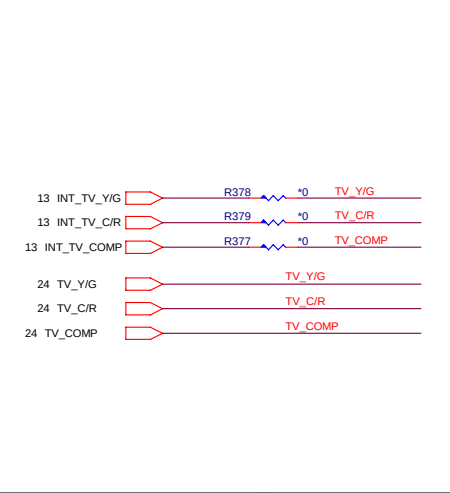
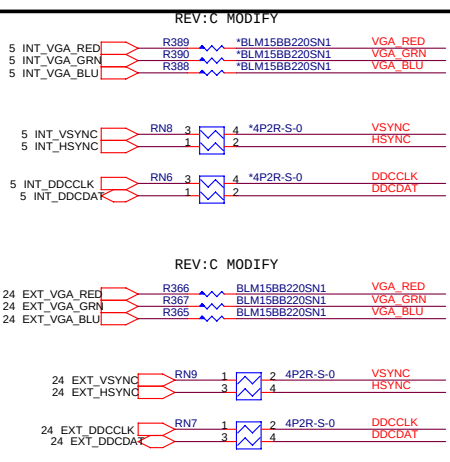
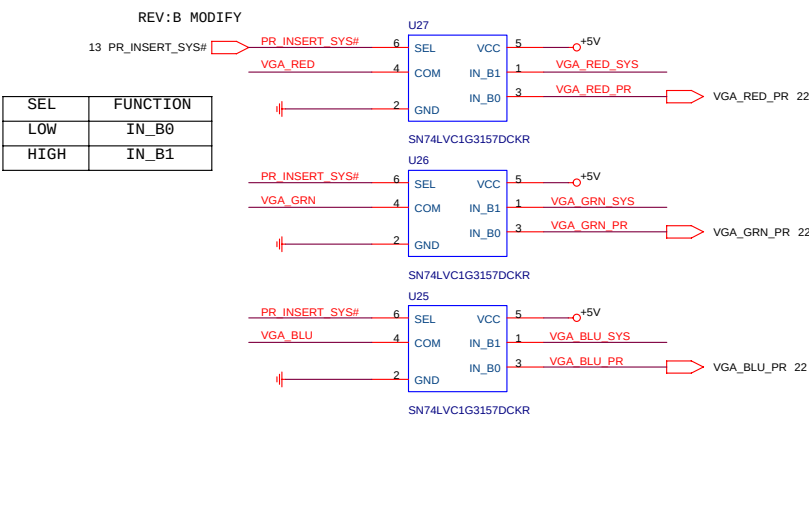
Damping put B0T, termination put TOP



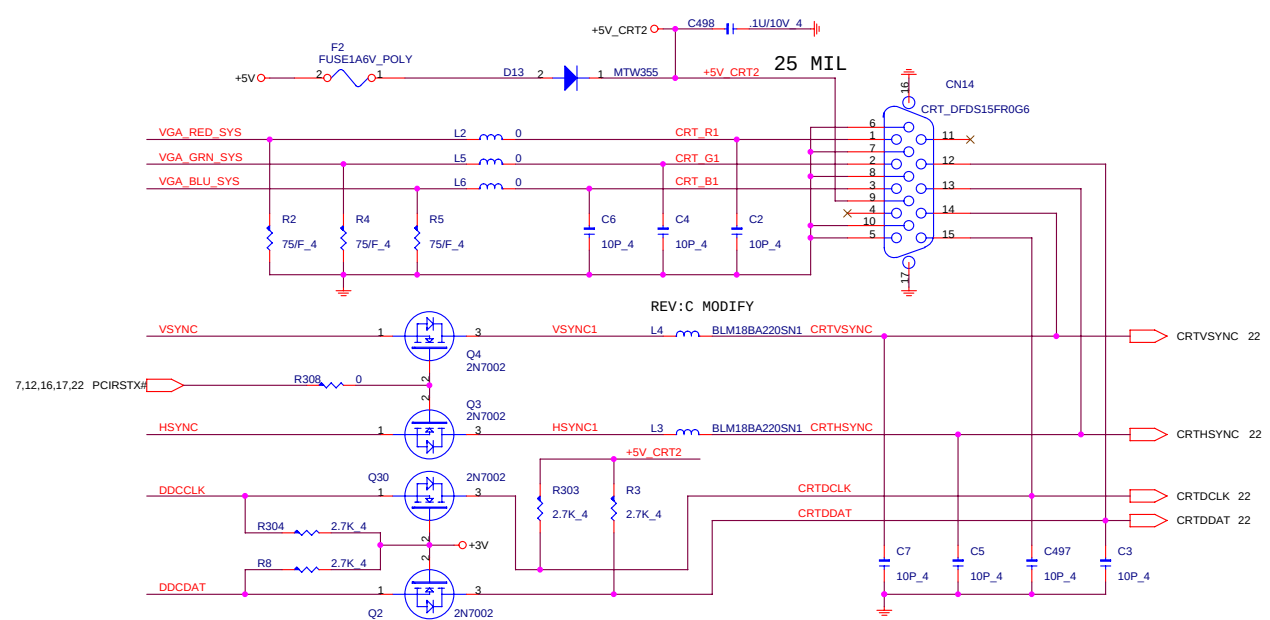
For terminal R-pack.



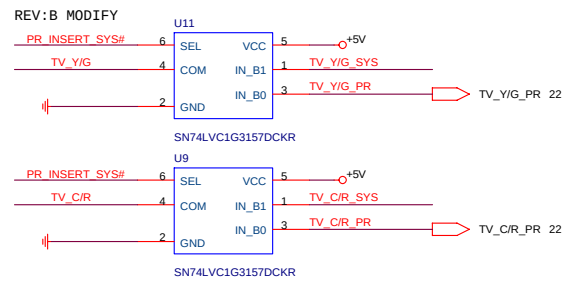
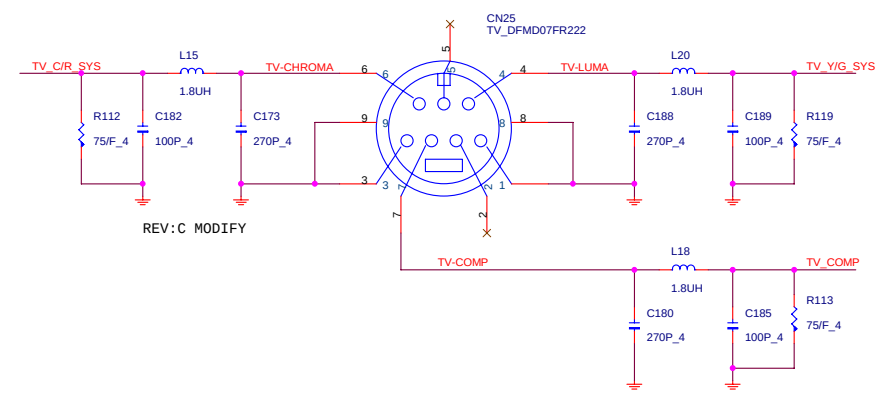
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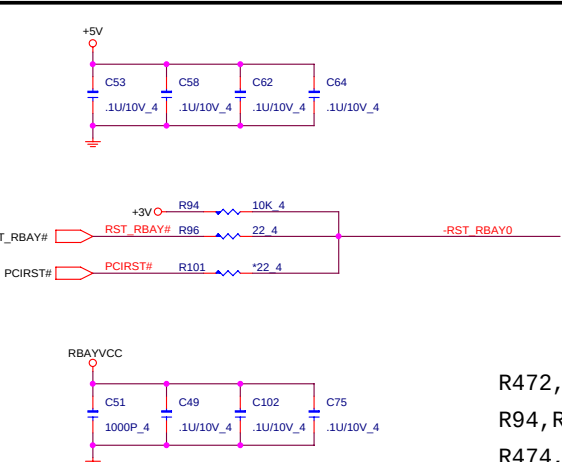
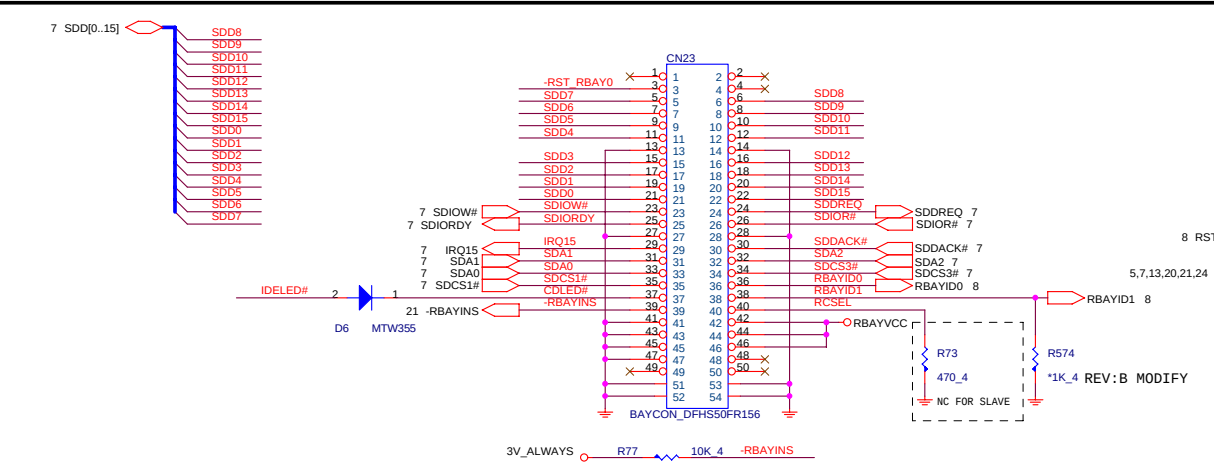
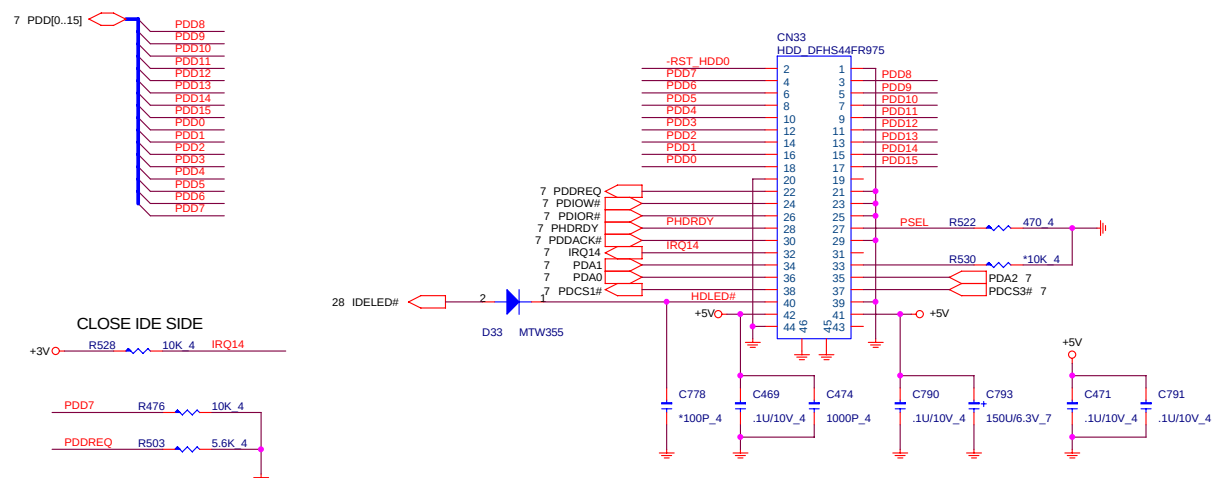


CRT PORT



S-VIDEO

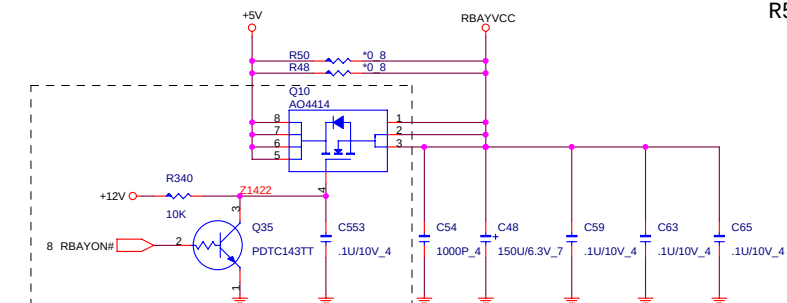
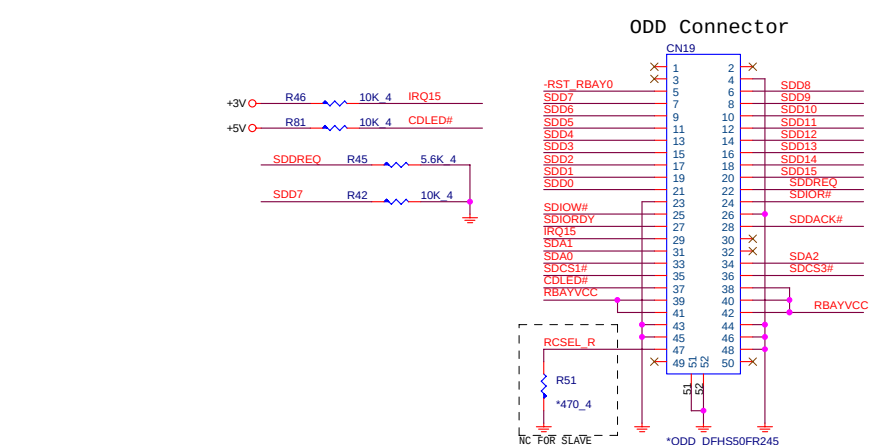


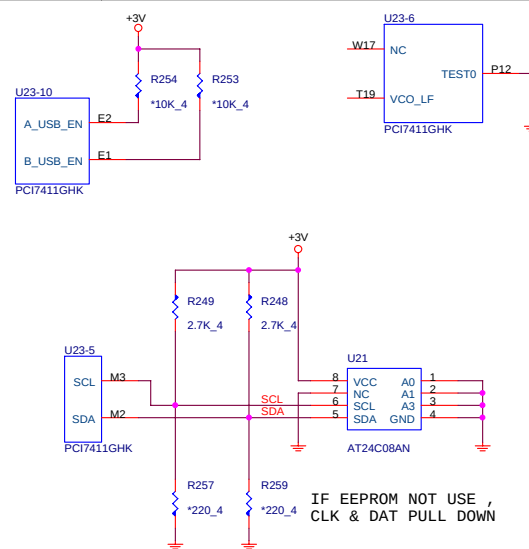
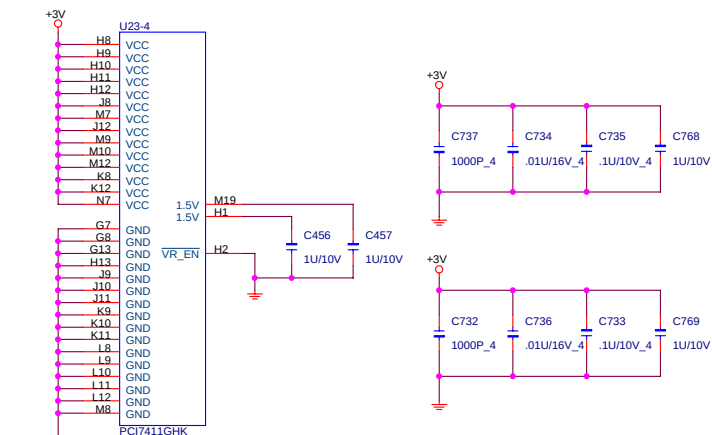
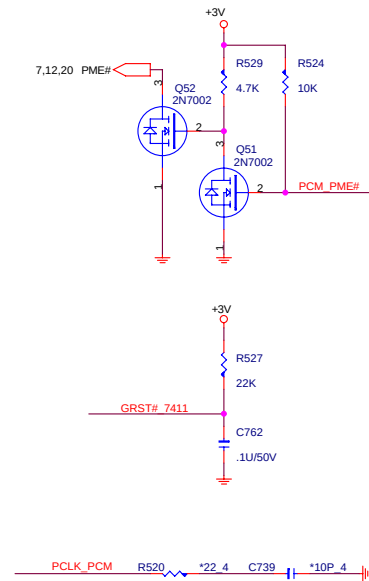


BAY ID STATUS

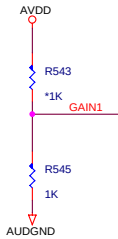
RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD

	W/O Media Bay	W Media Bay
R472, R473	DNS	STUFF
R94, R96	DNS	STUFF
R474, R101	STUFF	DNS
R48, R50	STUFF	DNS
Q10, Q35	DNS	STUFF
R340, C553	DNS	STUFF
R73, CN23	DNS	STUFF
R51, CN19	STUFF	DNS
R574	STUFF	DNS

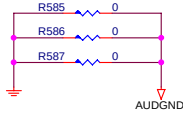
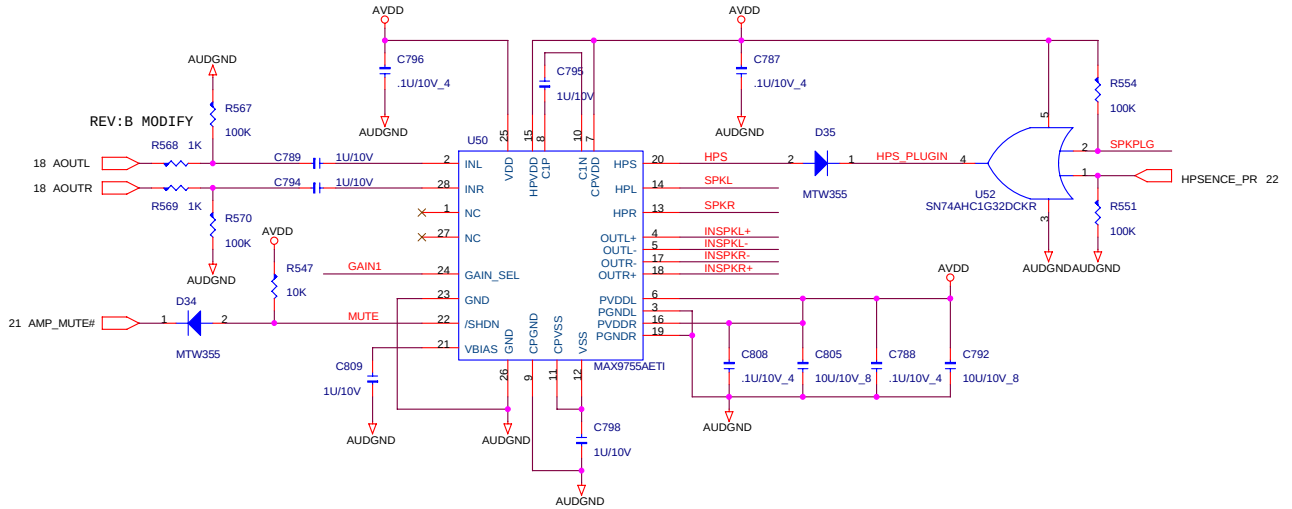
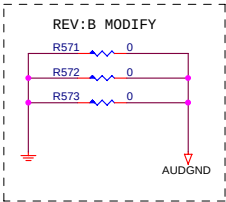
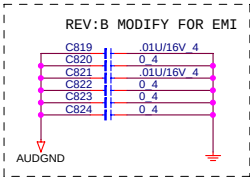
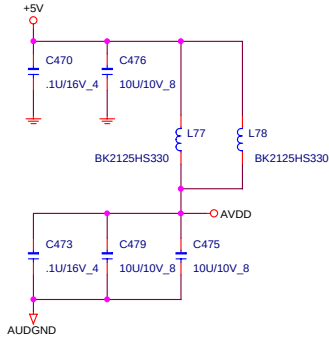




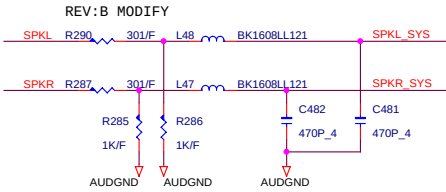
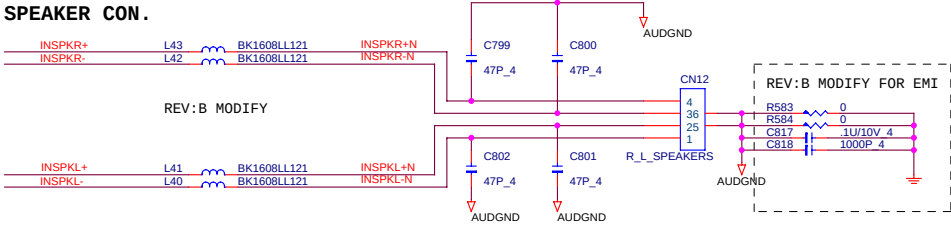
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



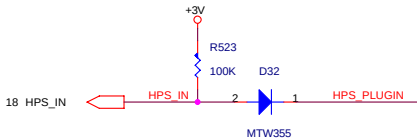
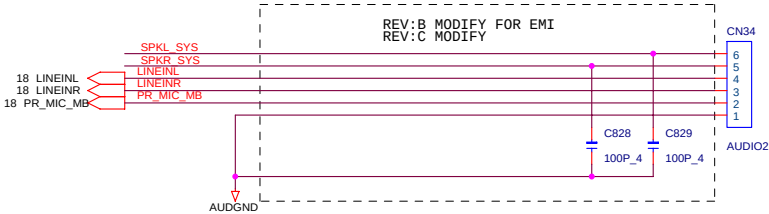
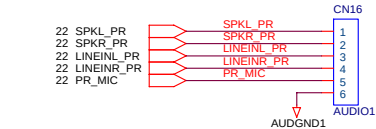
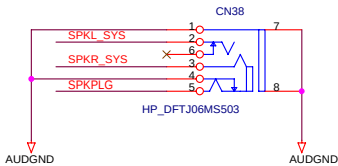
AMP POWER



SPEAKER CON.



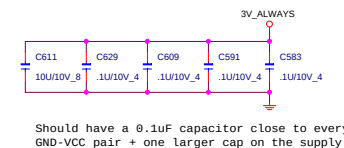
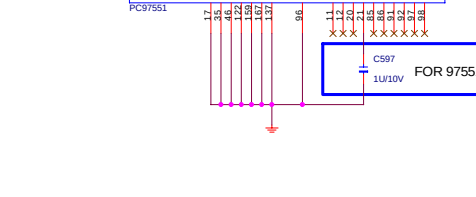
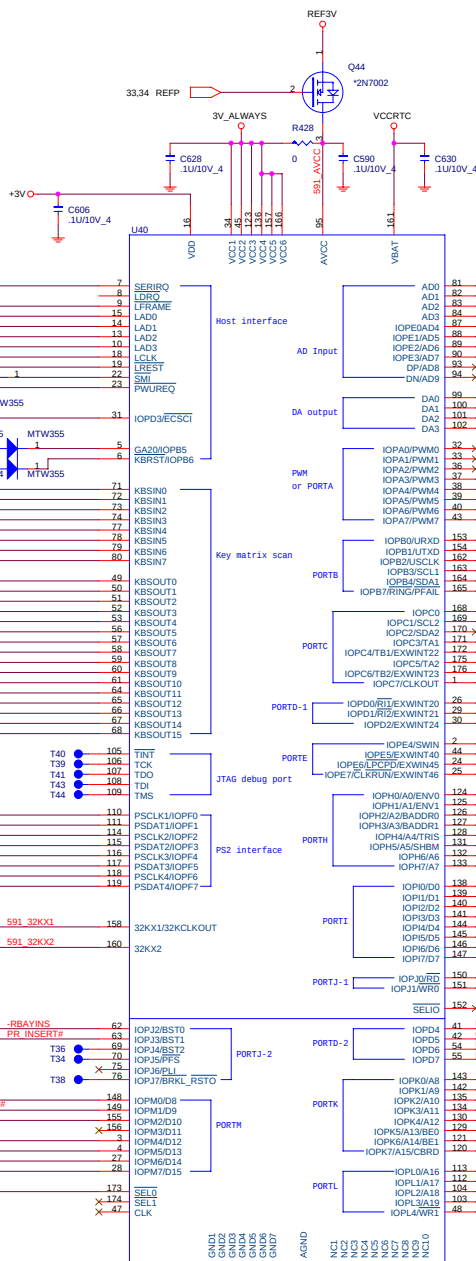
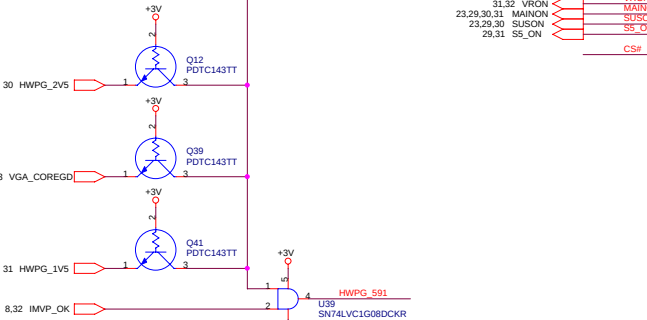
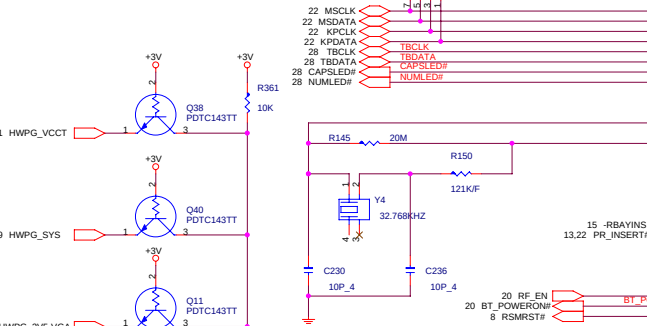
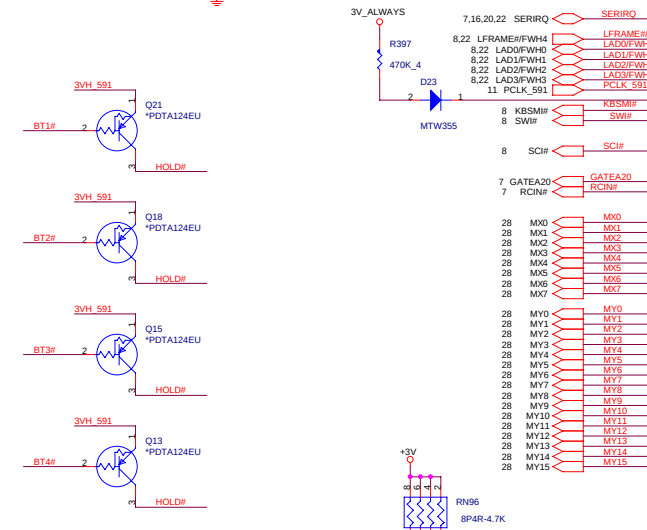
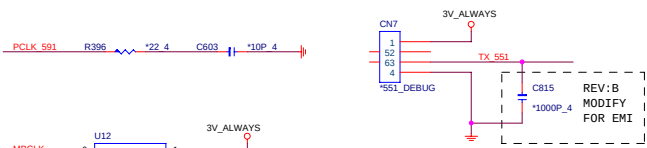
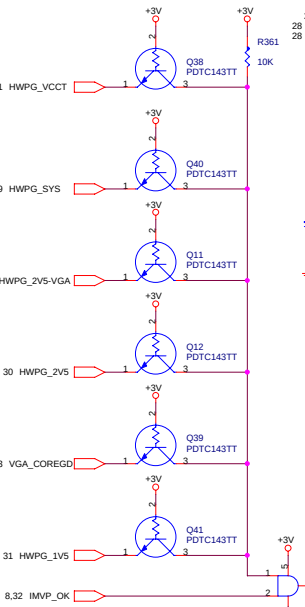
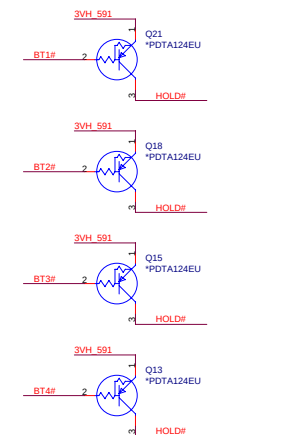
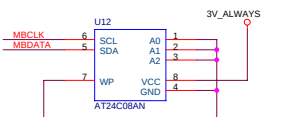
LINE OUT



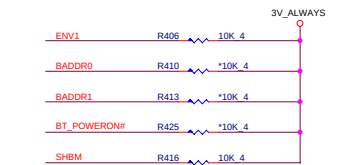

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PCLK 591 R396 *22 4 C603 *10P 4

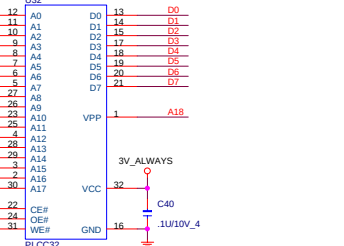
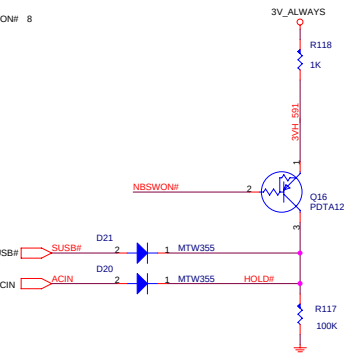
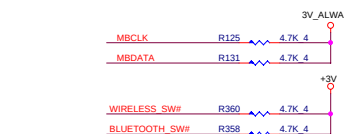


Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.

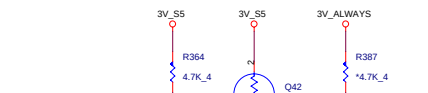


SHBM=1: Enable shared memory with host BIOS

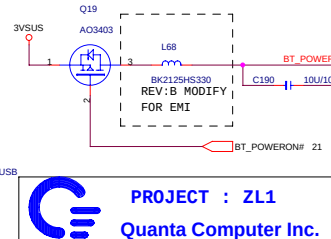
I/O Address		
BADDR1-6	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)	HCFGBAH, HCFGBAL
1 1	Reserved	



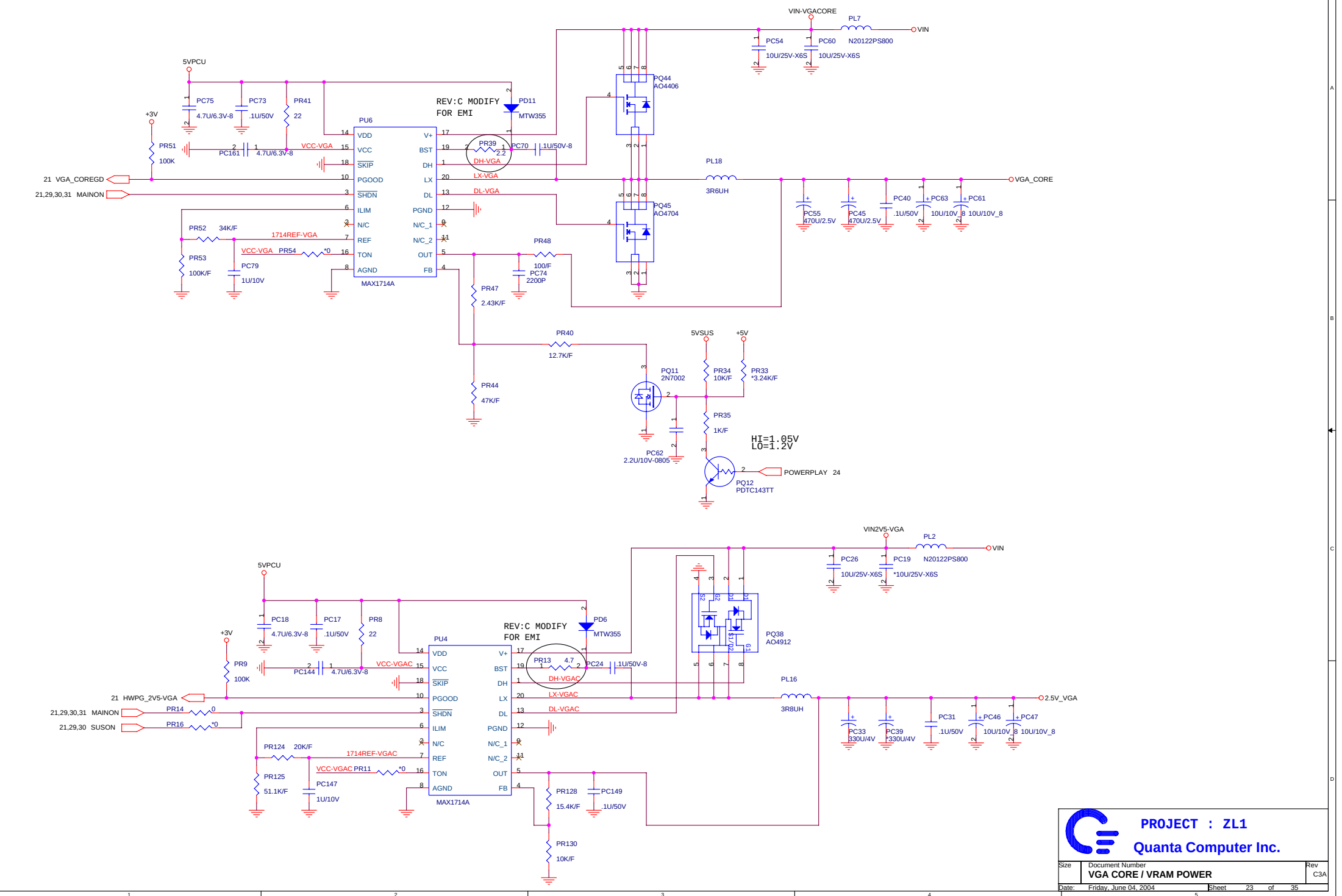
BIU configuration should match flash speed used



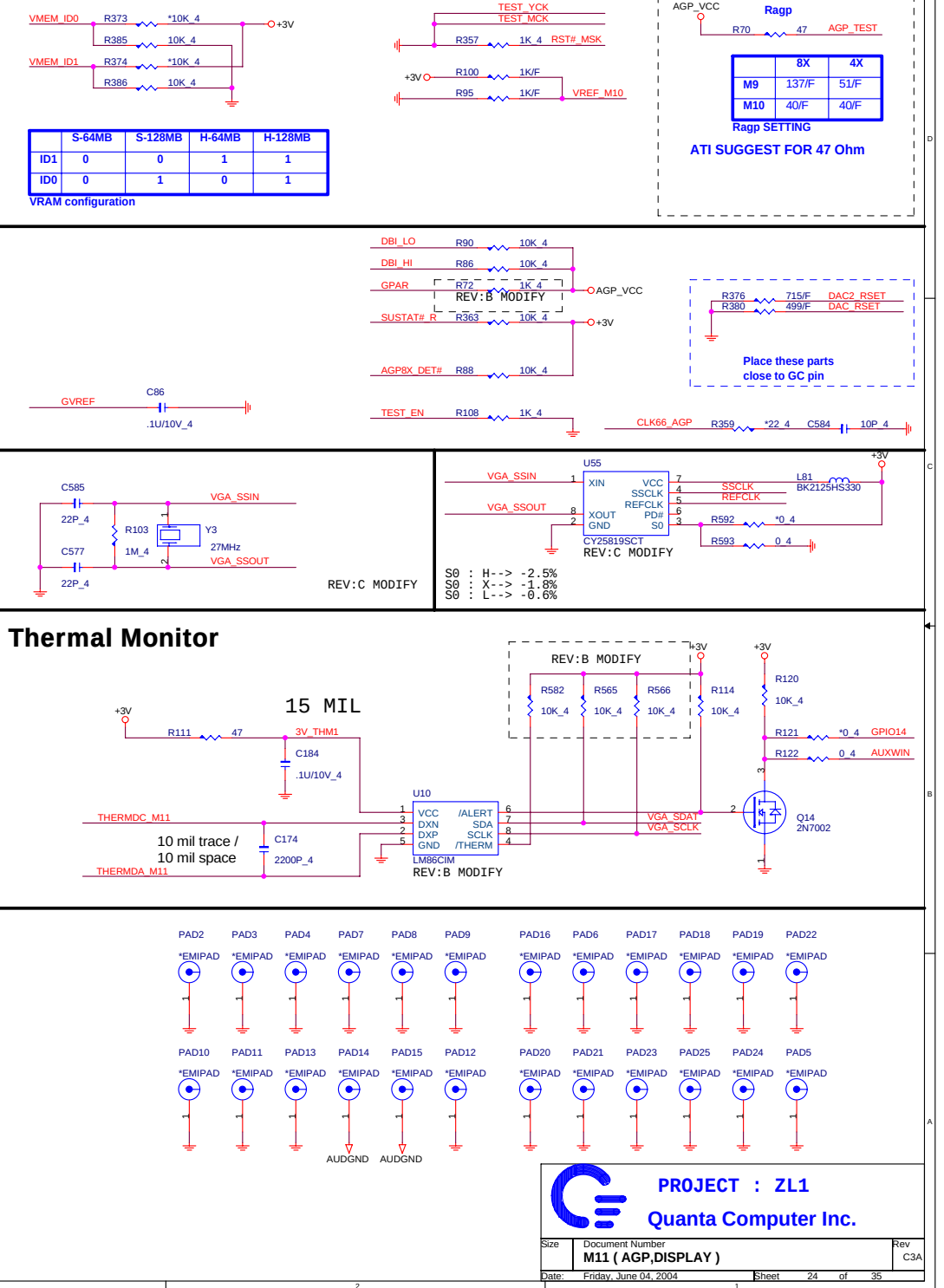
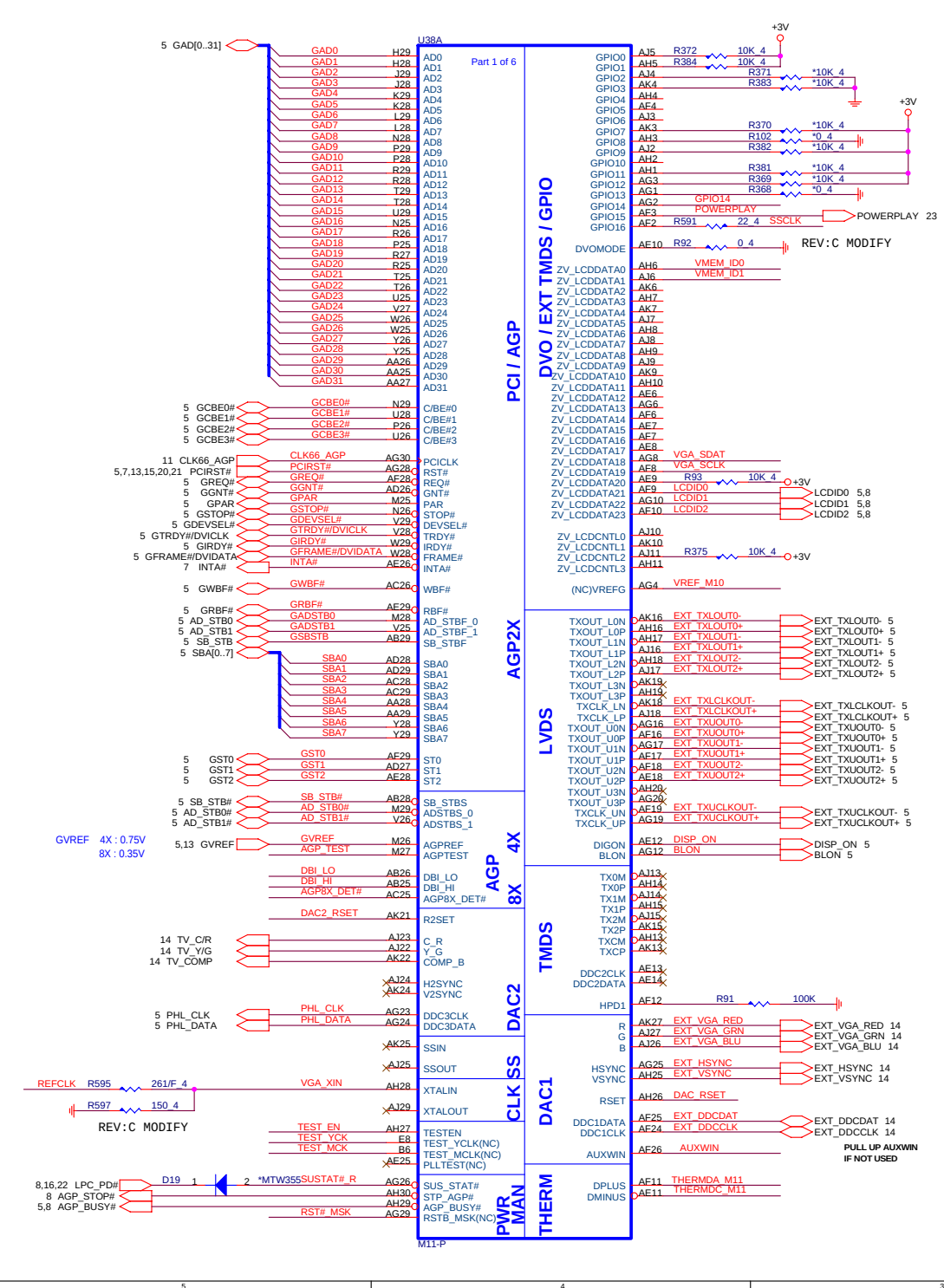
MINI-PCI



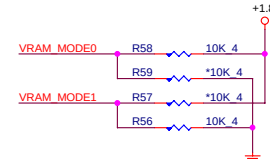
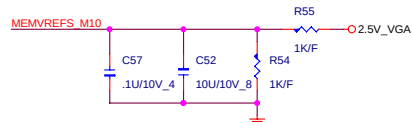
M11 Core & BATTERY



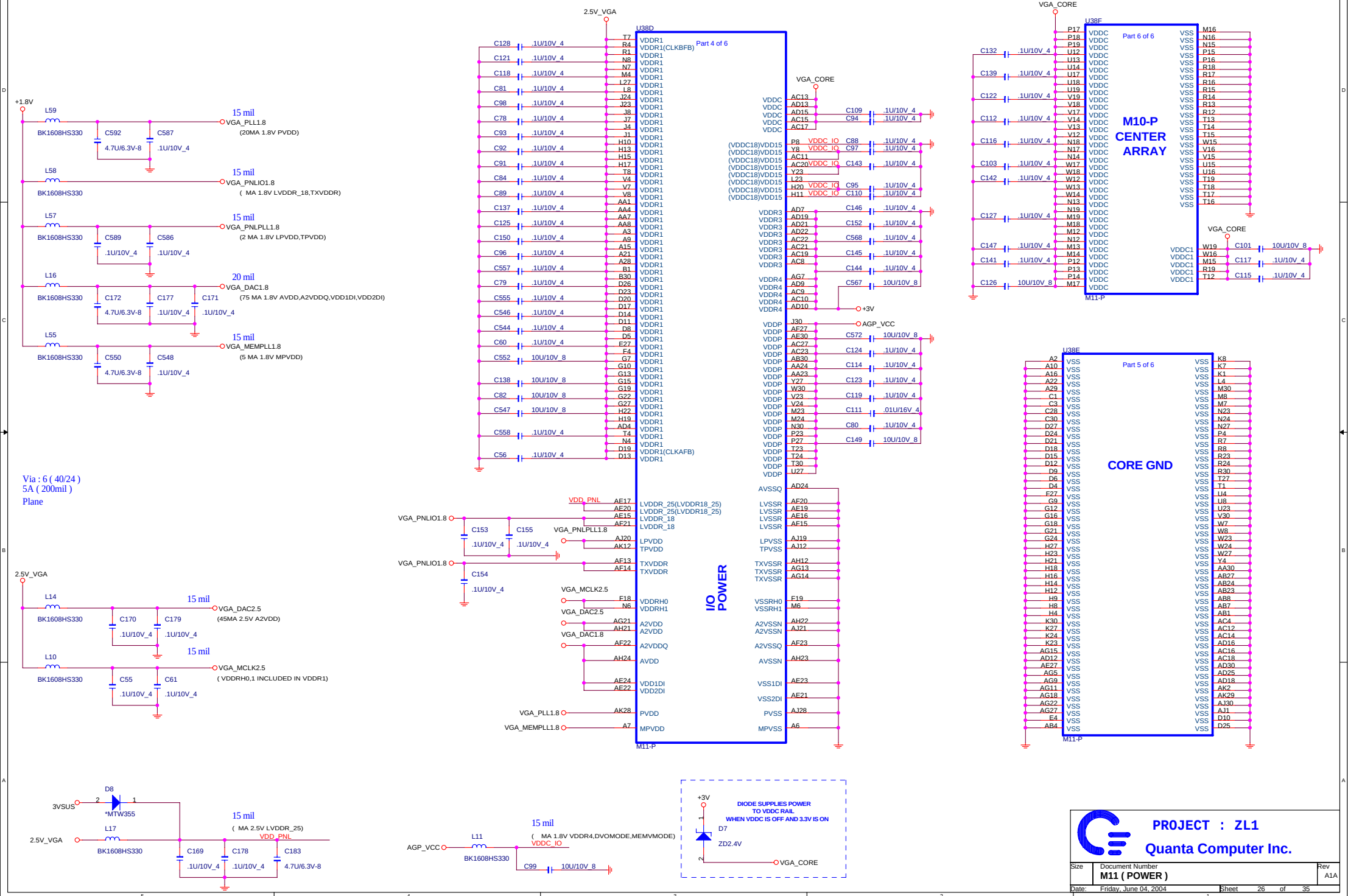
VGA(AGP,DISPLAY)



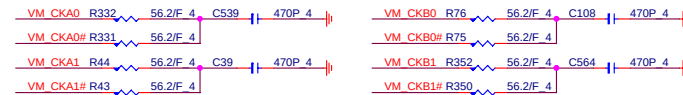
<u>VM_DA[63..0]</u>		VM_DA[63..0]	27
<u>VM_AA[13..0]</u>		VM_AA[13..0]	27
<u>VM_DOA#[7..0]</u>		VM_DQA#[7..0]	27
<u>VM_QSA[7..0]</u>		VM_QSA[7..0]	27

VRAM type setting

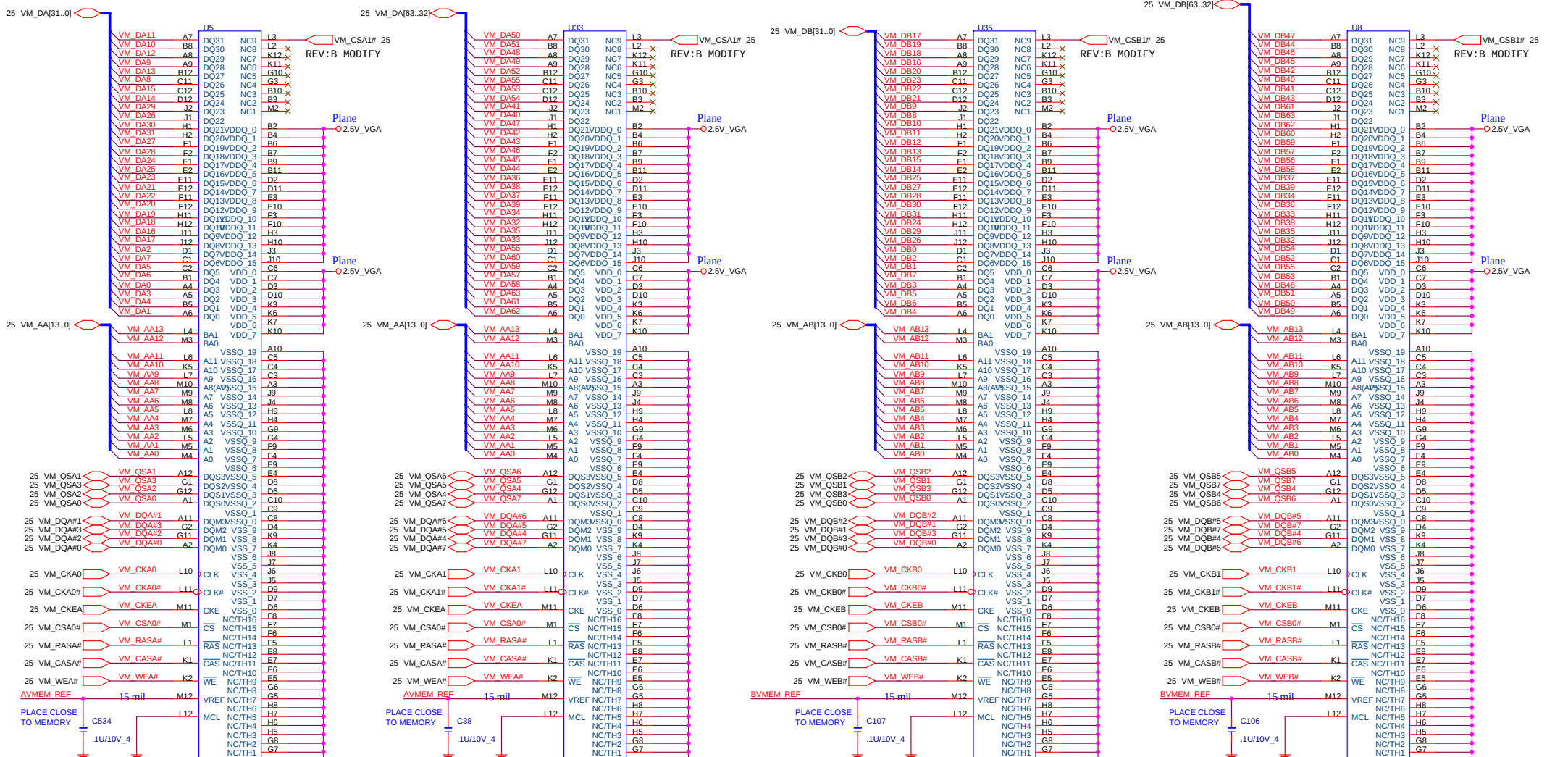
VGA(POWER)



VRAM Channel A



VRAM Channel B

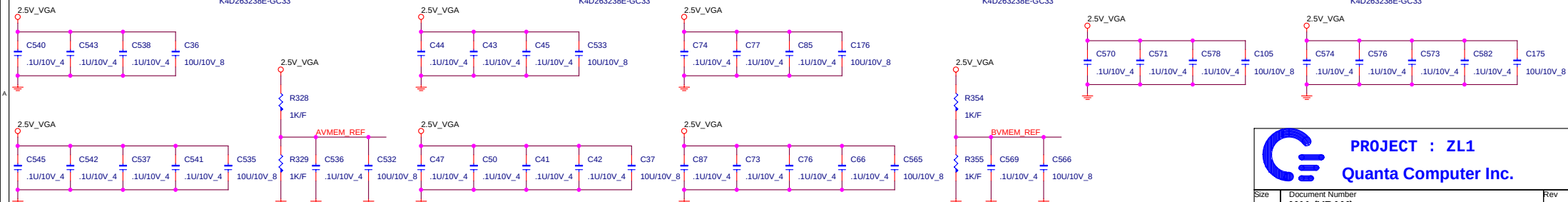


4Mx32 or 8Mx32
K4D263238E-GC33

4Mx32 or 8Mx32
K4D263238E-GC33

4Mx32 or 8Mx32
K4D263238E-GC33

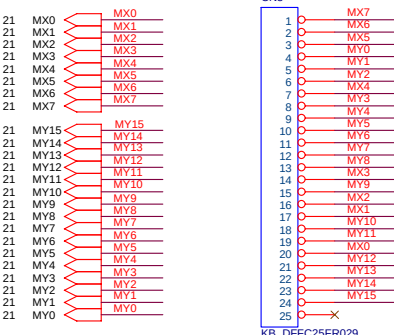
4Mx32 or 8Mx32
K4D263238E-GC33



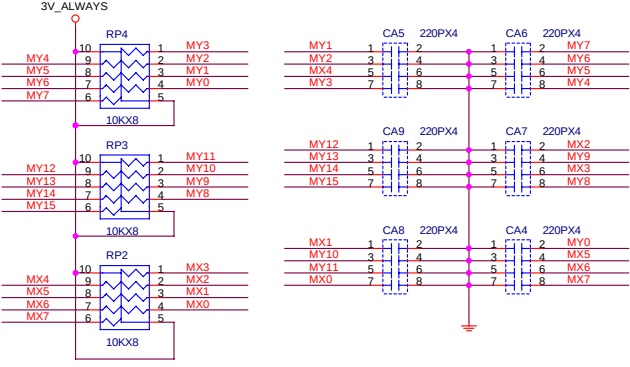
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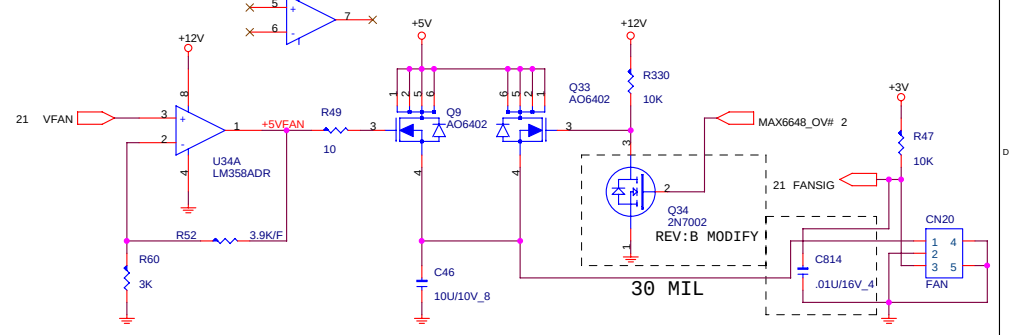
INT K/B



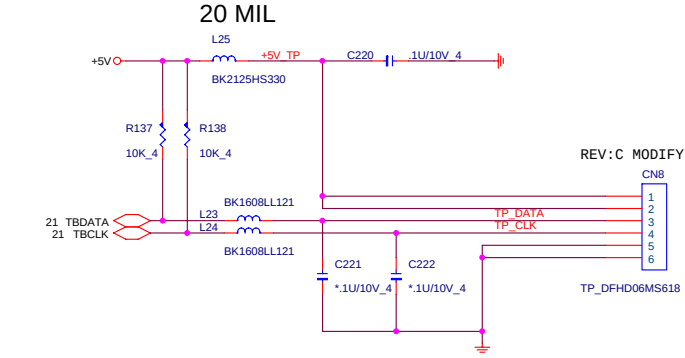
KB_DFFC25FR029



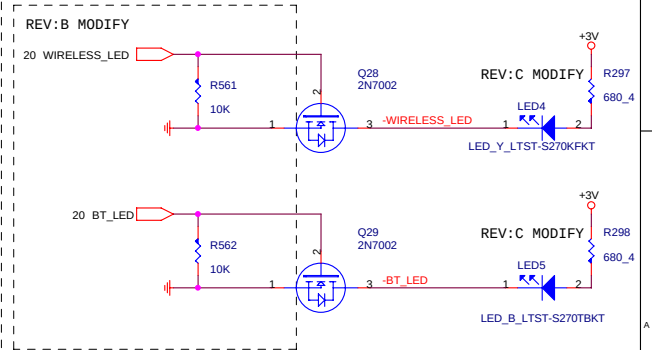
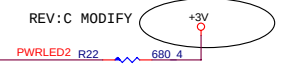
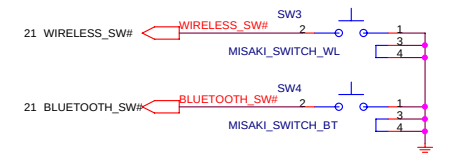
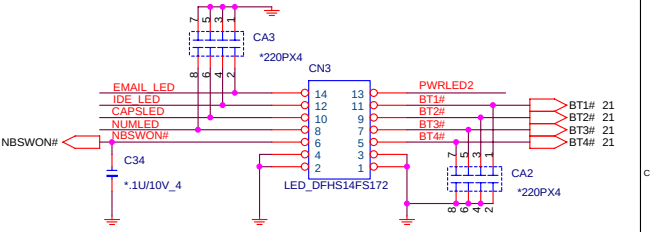
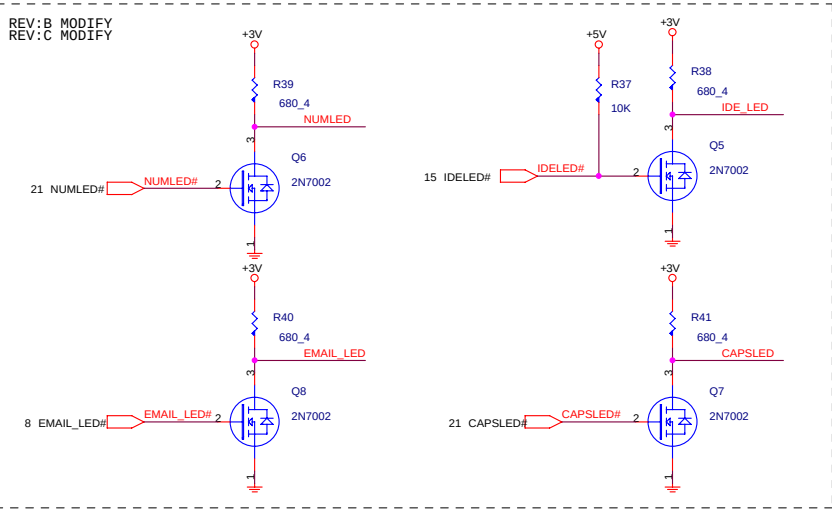
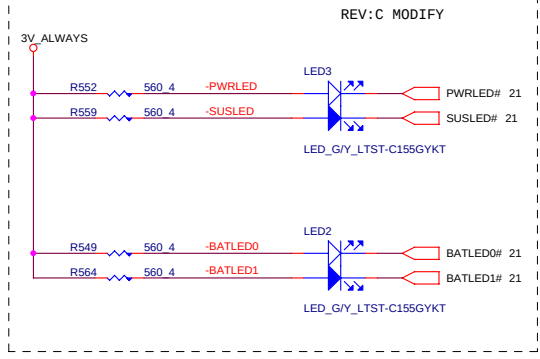
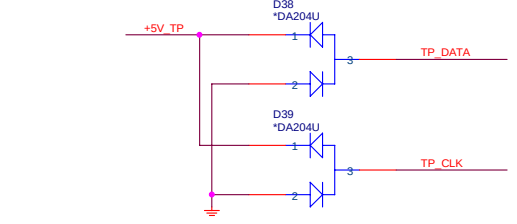
FAN CONTROL

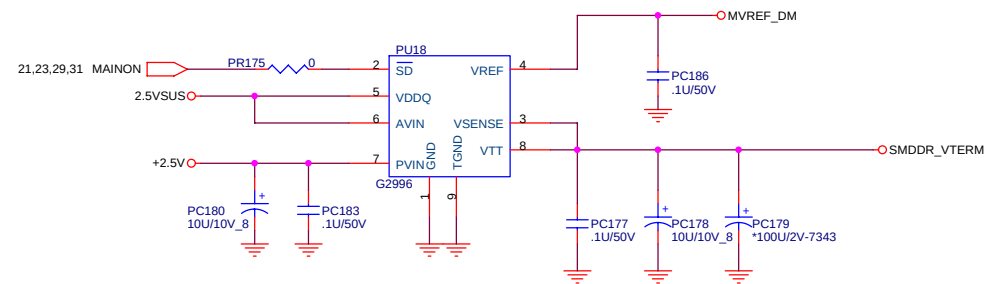
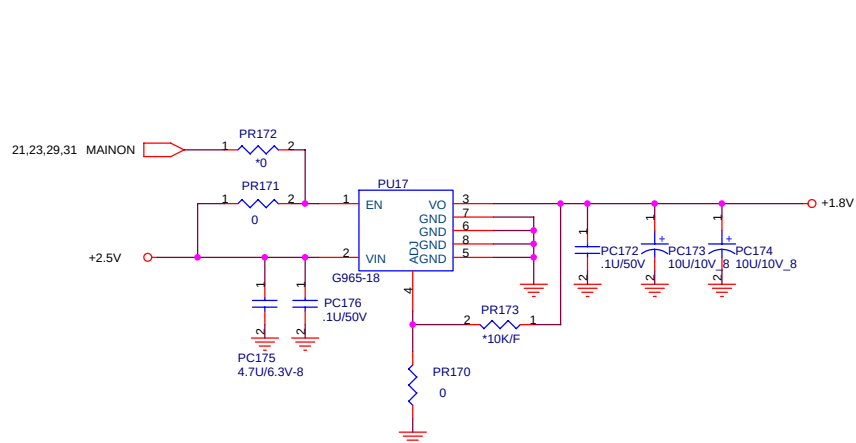
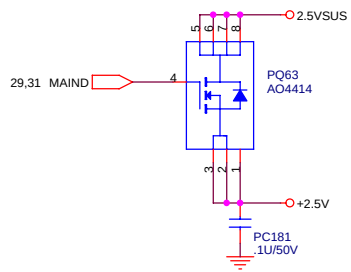
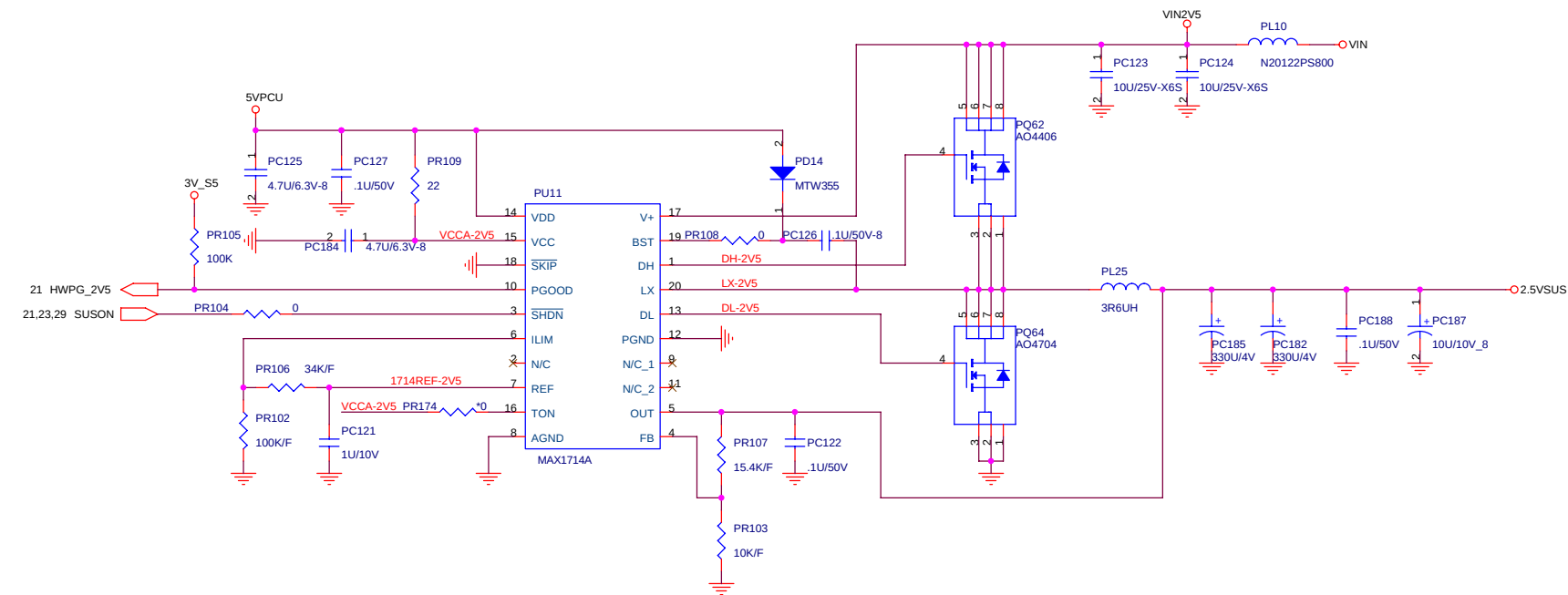


TOUCH PAD



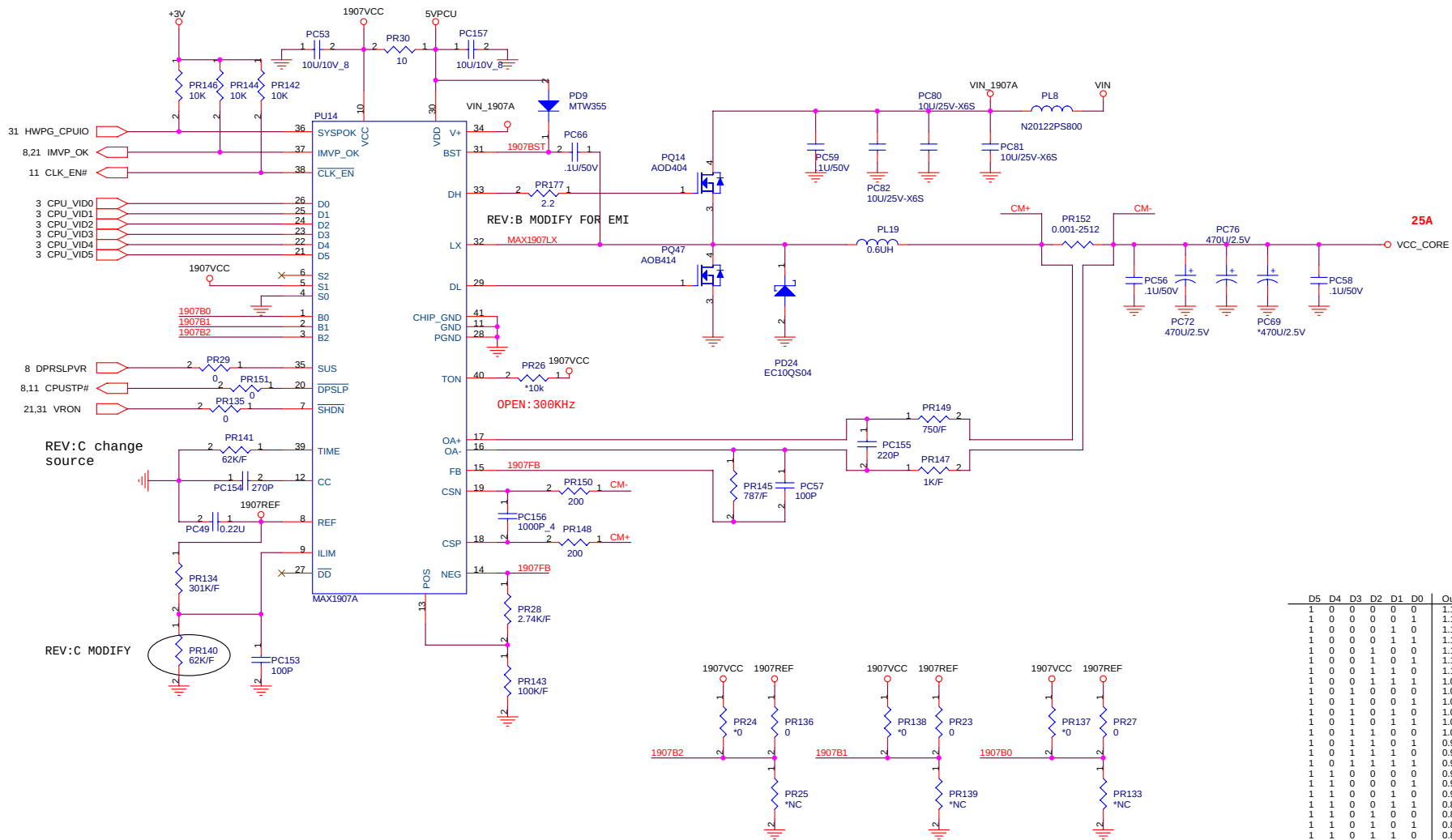
REV:C MODIFY





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	2.5VSUS / +1.25TERM	A1A
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D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	0	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

SUSPEND MODE (SUS=HIGH)

S2	S1	S0	Output
✓ OPEN	VCC	GND	0.748V

VCC_BOOT

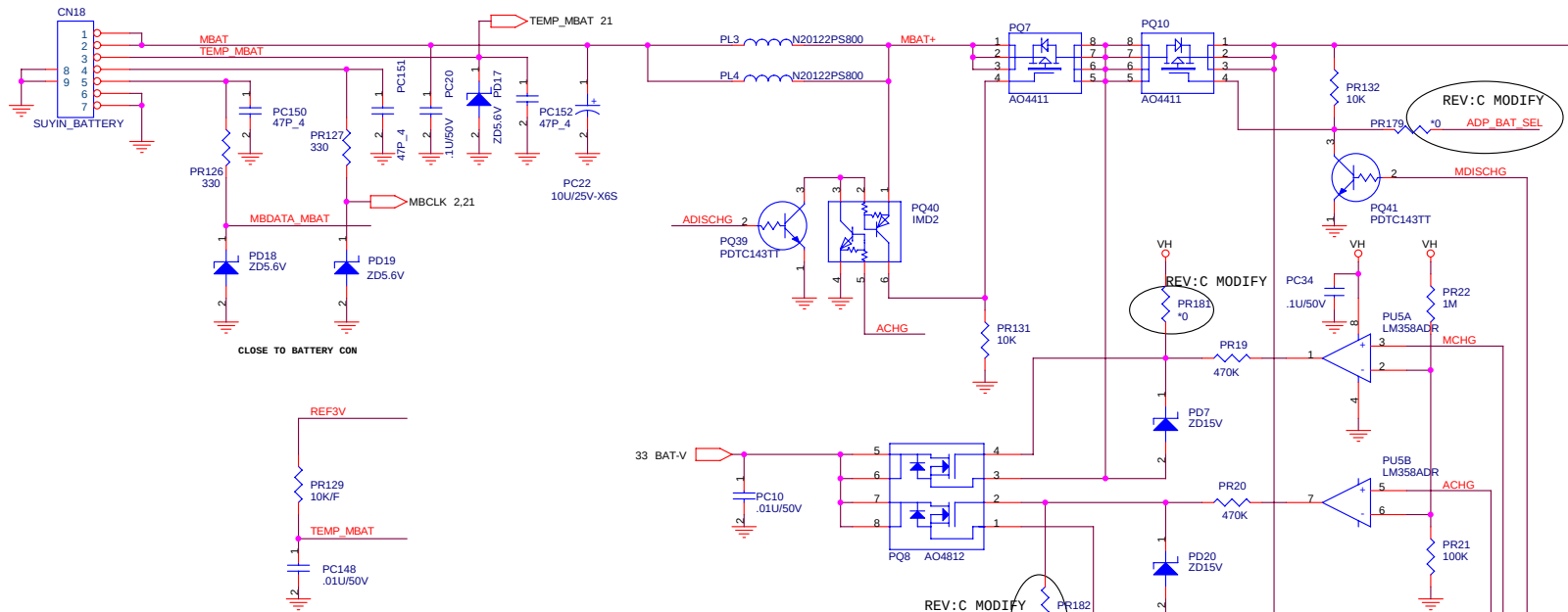
B2	B1	B0	Output
GND	GND	GND	1.708V
REF	REF	REF	1.372V
OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	0.700V
REF	VCC	VCC	1.212V



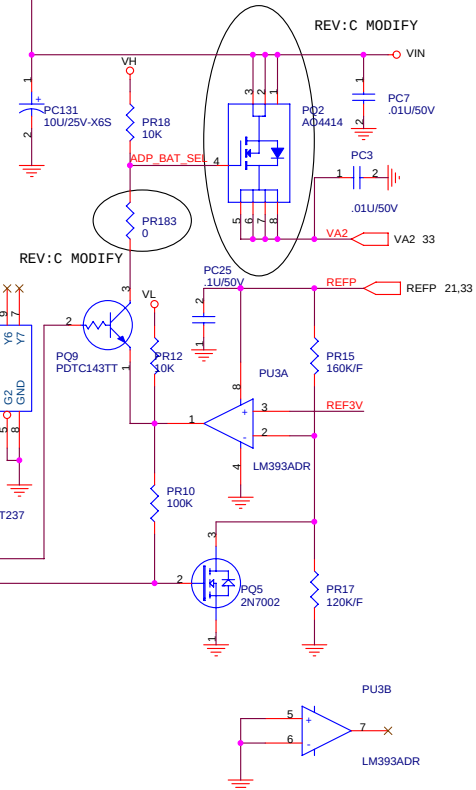
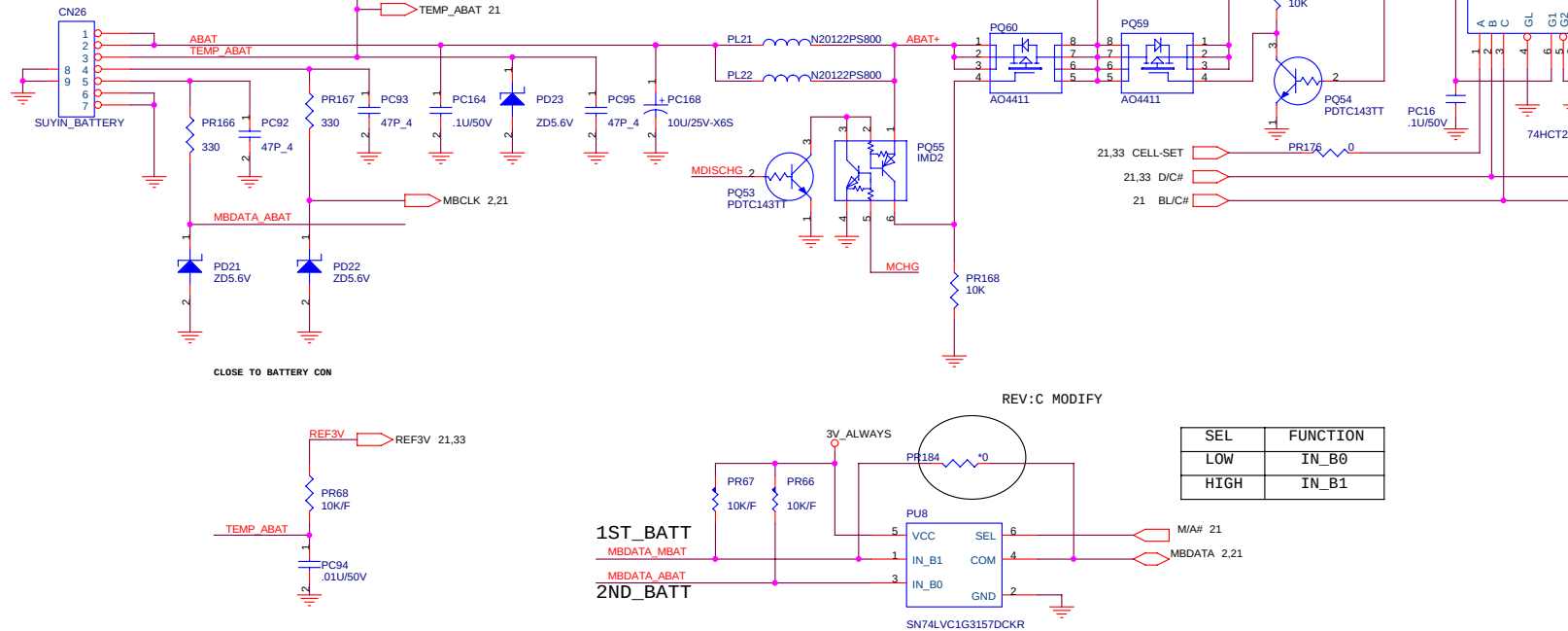
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Quanta Computer Inc.

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	CPU CORE (MAX1907)	C3A
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1ST_BATT_CONN



2ND_BATT_CONN



SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

MODEL :		REV :	CHANGE LIST :	PAGE	FROM	TO
ZL1 MotherBoard	B2B		PAGE2 . 1.UN-STUFF R392 FOR THERMTRIP 2. STUFF NUTS	1	A1A	
			PAGE5 . 1.ADD +3V FOR LCD EDID 2.MODIFY C528 VALUE	2	B2B	C3A
			PAGE8 . 1.STUFF R204 FOR THERMTRIPS	3	A1A	
			PAGE12. 1.ADD 10/100 CO LAYOUT CIRCUIT	4	A1A	
			PAGE13. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT 2.MODIFY LAN SWITCH POWER	5	B2B	
			PAGE14. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT	6	A1A	
			PAGE15. 1.ADD RBAYID1 PULL DOWN RESISTOR	7	A1A	
			PAGE18. 1.MODIFY FOR AUDIO QUALITY	8	B2B	
			PAGE19. 1.MODIFY FOR AUDIO QUALITY	9	A1A	
			PAGE20. 1.ADD EMI SOLUTION	10	A1A	
	C3A		PAGE22. 1.MODIFY PR INSERT# PULL HIGH VOLTAGE	11	A1A	C3A
			PAGE23. 1.STUFF PC45 FOR VGA CORE 2. MODIFY 2.5V VGA POWER	12	B2B	C3A
			PAGE24. 1.MODIFY GPAR PULL HIGH RESISTOR 2. ADD PULL HIGH FOR I2C BUS	13	B2B	C3A
			PAGE27. 1.MODIFY CS PIN FOR 128MB VRAM	14	B2B	C3A
			PAGE28. 1.MODIFY LED CIRCUIT 2. MODIFY Q34 TO 2N7002	15	B2B	
			PAGE29. 1.MODIFY MAX6648 OV# CIRCUIT	16	A1A	C3A
			PAGE32. 1.MODIFY CPU CORE VOLTAGE LEVEL	17	A1A	C3A
			PAGE33. 1.MODIFY CHARGER CIRCUIT	18	B2B	C3A
			PAGE34. 1.MODIFY CHARGER CIRCUIT	19	B2B	C3A
			PAGE02. 1.MODIFY MAX6648 AL# TO HIGH ACTIVE	20	B2B	C3A
			PAGE11. 1.MODIFY FOR SIGNAL QUALITY	21	A1A	C3A
			PAGE12. 1.MODIFY LAN LED 2. MODIFY LAN TRACE PULL HIGH	22	B2B	C3A
			PAGE13. 1.MODIFY LAN LED 2. MODIFY DVO_AVDD	23	B2B	C3A
			PAGE14. 1.MODIFY RGB BEAD VALUE	24	B2B	C3A
			PAGE16. 1.REMOVE COMMON MODE CHOKE	25	A1A	
			PAGE17. 1.MODIFY 3 IN 1 CARD READER FOOTPRINT	26	A1A	
			PAGE18. 1.MODIFY LINE IN SIGNAL	27	B2B	
			PAGE19. 1.MODIFY LINE IN SIGNAL	28	B2B	C3A
			PAGE20. 1.MODIFY USB COMMON MODE CHOKE 2. MODIFY MINI-PCI 3V_S5 TO 3VSUS	29	B2B	C3A
			PAGE21. 1.MODIFY 97551 PIN 21	30	A1A	
			PAGE22. 1.MODIFY U30 PIN23 TO +5V	31	A1A	C3A
			PAGE23. 1.MODIFY FOR EMI SOLUTION	32	B2B	C3A
			PAGE24. 1.ADD EMI SOLUTION	33	B2B	
			PAGE28. 1.MODIFY PWRLED2 TO +3V 2. ADD TP ESD PROTECT 3. MODIFY LED CIRCUIT	34	B2B	C3A
			PAGE29. 1.MODIFY FOR POWER SUHTDOWN ISSUE	35	B2B	C3A
PAGE31. 1.MODIFY FOR EMI SOLUTION						
PAGE32. 1.CHANGE SOURCE						
PAGE34. 1.MODIFY CHARGER CIRCUIT FOR COST DOWN						
 PROJECT : ZL1 Quanta Computer Inc.				COVER SHEET 1 OF 1		
Size	Document Number	Rev	PROJECT : ZL1	APPROVE BY: JIM HSU	DRAWING BY:JACKY CHENG	REV C3A
	CHANGE LIST	C3A	MB ASSY'S P/N : 31ZL1MB0004	PROJECT LEADER: JIM HSU	DOCUMENT NO: 204	DATE :2004/06/01
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