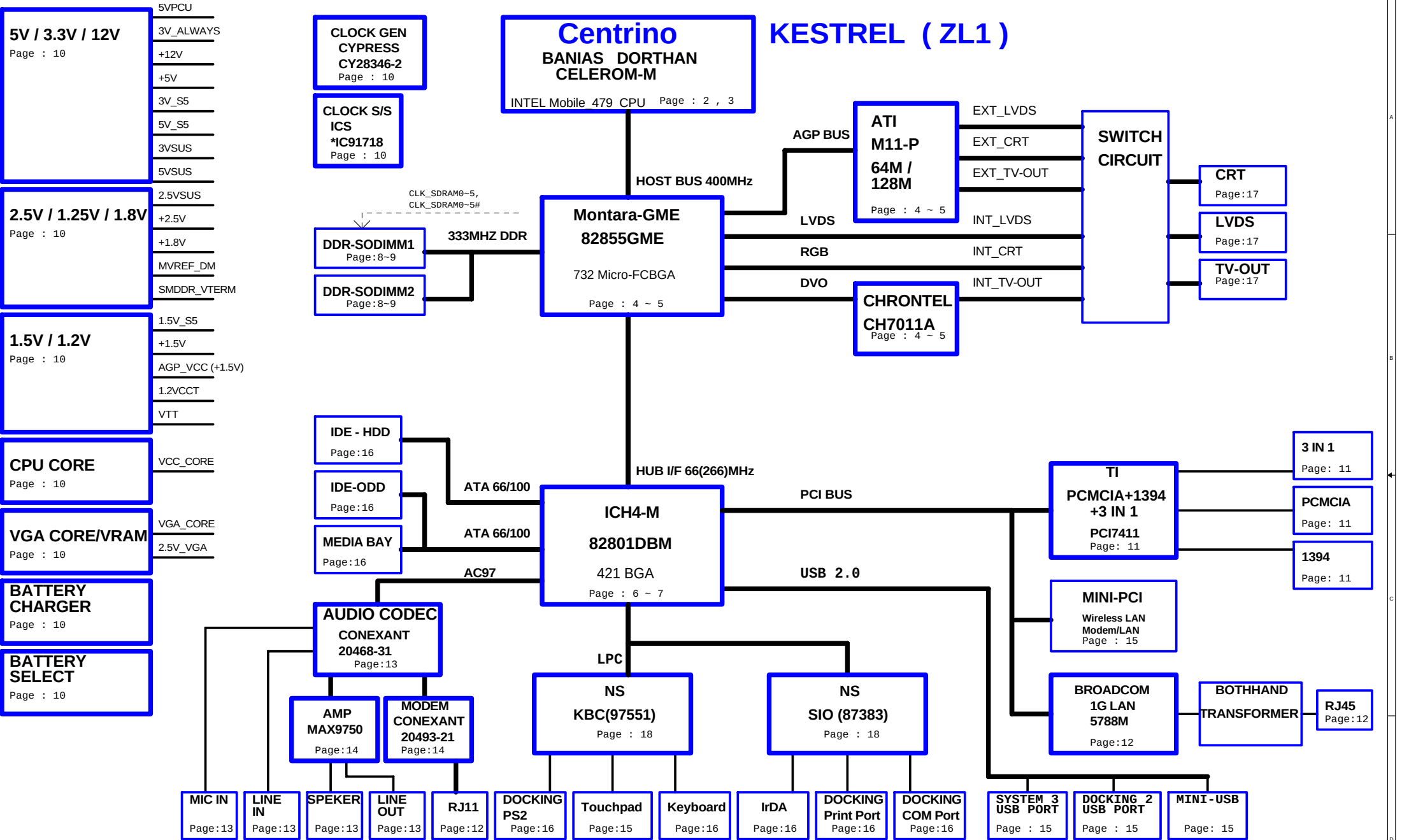
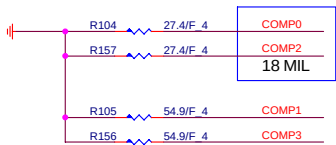


MODEL :			REV:	CHANGE LIST:	MODEL : ZL1 MB		
ZL1 MotherBoard	1A	BOM RELEASE			PAGE	FROM	TO
	1B	1. ADD SECOND SOURCE P/N IN BOM 2. UPDATE VRAM P/N 3. ADD U12 IN BOM			1	1A	
	2A	PAGE2 . 1.UN-STUFF R392 FOR THERMTRIP 2. STUFF NUTS PAGE5 . 1.ADD +3V FOR LCD EDID 2.MODIFY C528 VALUE PAGE8 . 1.STUFF R204 FOR THERMTRIPS PAGE12. 1.ADD 10/100 CO LAYOUT CIRCUIT PAGE13. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT 2.MODIFY LAN SWITCH POWER PAGE14. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT PAGE15. 1.ADD RBAYID1 PULL DOWN RESISTOR PAGE18. 1.MODIFY FOR AUDIO QUALITY PAGE19. 1.MODIFY FOR AUDIO QUALITY PAGE20. 1.ADD EMI SOLUTION PAGE22. 1.MODIFY PR INSERT# PULL HIGH VOLTAGE PAGE23. 1.STUFF PC45 FOR VGA CORE 2. MODIFY 2.5V VGA POWER PAGE24. 1.MODIFY GPAR PULL HIGH RESISTOR 2. ADD PULL HIGH FOR I2C BUS PAGE27. 1.MODIFY CS PIN FOR 128MB VRAM PAGE28. 1.MODIFY LED CIRCUIT 2. MODIFY Q34 TO 2N7002 PAGE29. 1.MODIFY MAX6648 OV# CIRCUIT PAGE32. 1.MODIFY CPU CORE VOLTAGE LEVEL PAGE33. 1.MODIFY CHARGER CIRCUIT PAGE34. 1.MODIFY CHARGER CIRCUIT			2	3C	
					3	1A	
					4	1A	
					5	3C	
					6	3B	
					7	1A	
					8	2A	
					9	1A	
					10	1A	
					11	2C	
					12	2C	
					13	3A	
					14	2C	
					15	2A	
					16	3A	
					17	3A	
18	3A						
19	3A						
20	3A						
21	2C						
22	3C						
23	2C	3D					
24	3C						
25	1A						
26	1A						
27	2A						
28	3A						
29	3C						
30	1A						
31	2C						
32	2C						
33	3A						
34	3A						
2B	PAGE18. 1.MODIFY FOR AUDIO QUALITY PAGE22. 1.MODIFY COM PORT QUALITY						
2C	PAGE02. 1.MODIFY MAX6648 AL# TO HIGH ACTIVE PAGE11. 1.MODIFY FOR SIGNAL QUALITY PAGE12. 1.MODIFY LAN LED 2. MODIFY LAN TRACE PULL HIGH PAGE13. 1.MODIFY LAN LED 2. MODIFY DVO_AVDD PAGE14. 1.MODIFY RGB BEAD VALUE PAGE16. REMOVE COMMON MODE CHOKE PAGE17. 1.MODIFY 3 IN 1 CARD READER FOOTPRINT PAGE18. 1.MODIFY LINE IN SIGNAL PAGE19. 1.MODIFY LINE IN SIGNAL PAGE20. 1.MODIFY USB COMMON MODE CHOKE 2. MODIFY MINI-PCI 3V_S5 TO 3VSUS PAGE21. 1.MODIFY 97551 PIN 21 PAGE22. 1.MODIFY U30 PIN23 TO +5V PAGE23. 1.MODIFY FOR EMI SOLUTION PAGE24. 1.ADD EMI SOLUTION PAGE28. 1.MODIFY PWRLED2 TO +3V 2. ADD TP ESD PROTECT 3. MODIFY LED CIRCUIT PAGE29. 1.MODIFY FOR POWER SUHTDOWN ISSUE PAGE31. 1.MODIFY FOR EMI SOLUTION PAGE32. 1.CHANGE SOURCE PAGE34. 1.MODIFY CHARGER CIRCUIT FOR COST DOWN						
3A	PAGE13. 1.MODIFY LAN CABLE CONNECTOR FOOTPRINT PAGE16. 1.ADD MULTI FUNCTION PIN FM LED PAGE17. 1.ADD TI SUGGESTION CIRCUIT PAGE18. 1.MODIFY BEEP VALUE PAGE19. 1.MODIFY L77 & L78 TO RESISTOR PAGE20. 1.MODIFY SPRING QUANTITY PAGE28. 1.MODIFY LED RESISTOR VALUE PAGE29. 1.MODIFY LM358ADR CIRCUIT PAGE33. 1.MODIFY LM393ADR CIRCUIT PAGE34. 1.MODIFY LM393ADR CIRCUIT						
3B	PAGE06. 1.MODIFY NB VCCASM BEAD (L30)						
3C	PAGE02. 1.MODIFY R598, R599 TO 0402 SIZE PAGE05. 1.DELETE C528 FOR PCB BURN ISSUE PAGE22. 1.DELETE D3 FOR COM PORT LOOKBACK TEST SOMETIMES FAIL ISSUE PAGE24. 1.UN-STUFF PAD5, PAD6 FOR SMT PRODUCTION SMOOTHLY PAGE29. 1.DELETE PC88 FOR ENHANCE 12V OVERSHUT						
3D	PAGE23. 1.MODIFY VGA_CORE POWER TO 1.3V						



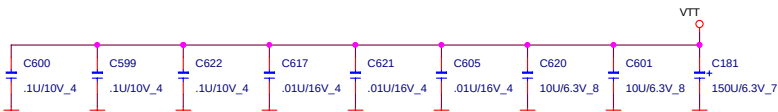
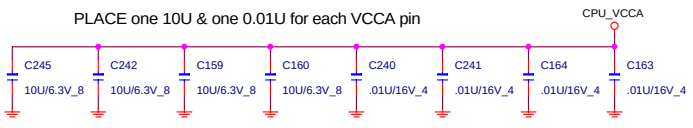
PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
		INTA#	M11
REQ0# / GNT0#	AD18	INTD#	BROADCOM LAN
REQ1# / GNT1#	AD20	INTB# , INTC#	MINI-PCI
REQ2# / GNT2#	AD23		
REQ3# / GNT3#	AD22	INTE#, INTF#, INTG#	TI 7411



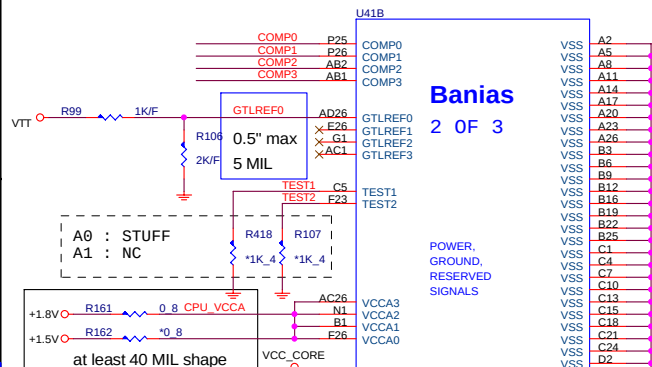
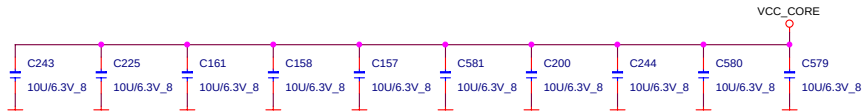
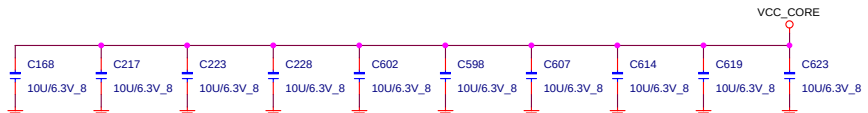
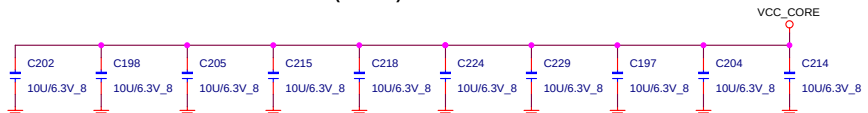
COMP0 ~ 4
max length
500 MIL



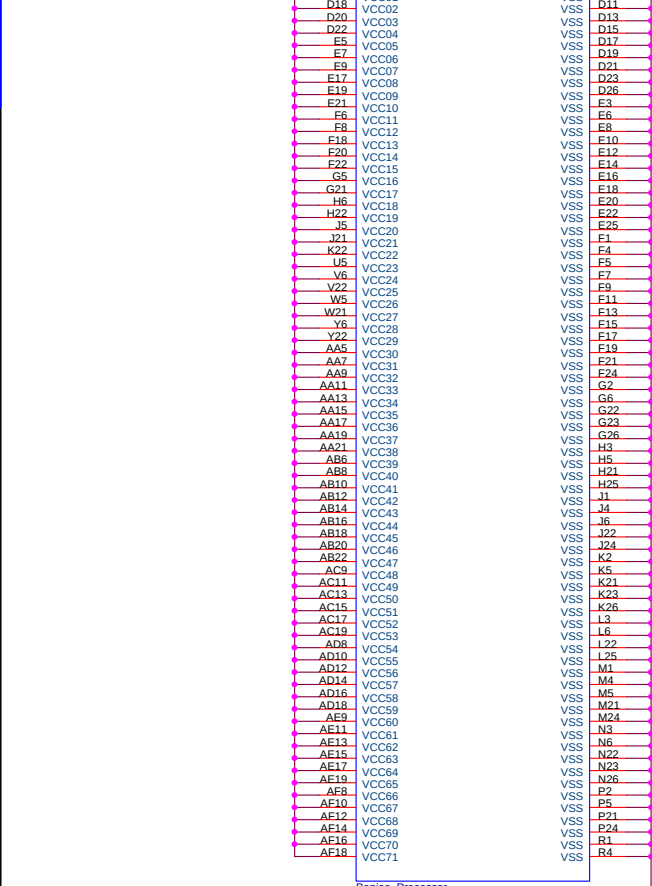
PLACE one 10U & one 0.01U for each VCCA pin



10U/6.3V/X5R(CC0805) *30

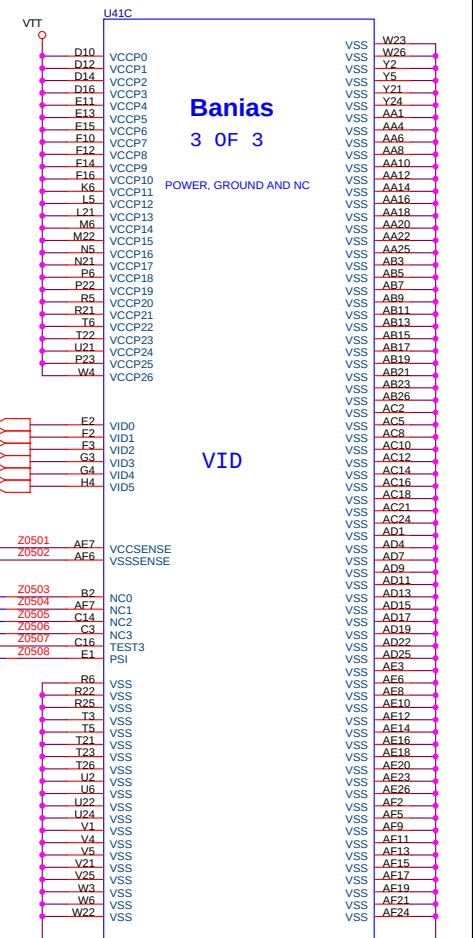


1.5V FOR DOTHAN ONLY



Banias
2 OF 3

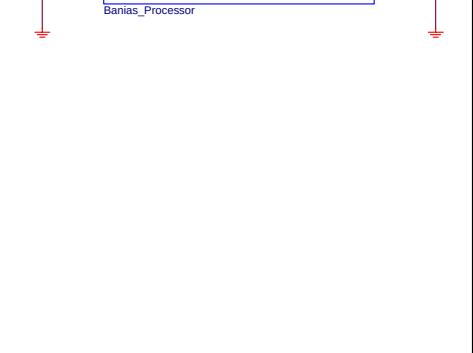
POWER,
GROUND,
RESERVED
SIGNALS



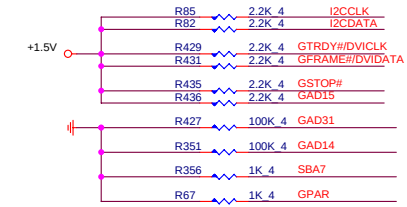
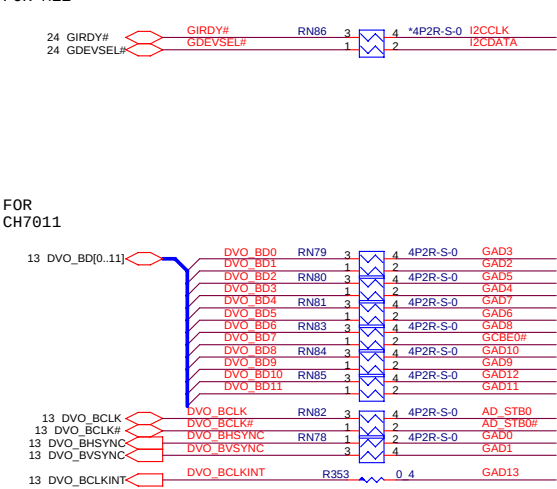
Banias
3 OF 3

POWER, GROUND AND NC

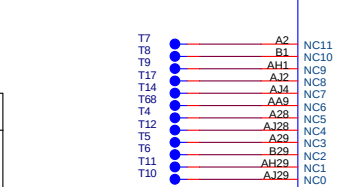
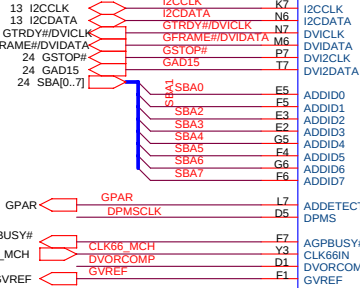
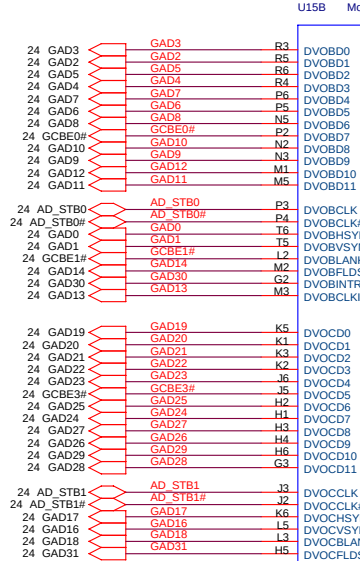
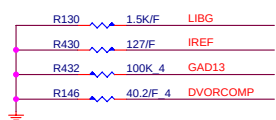
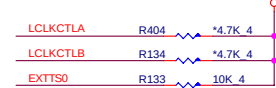
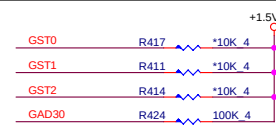
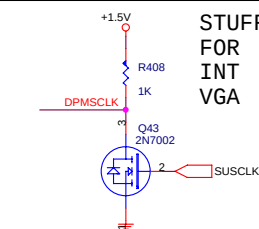
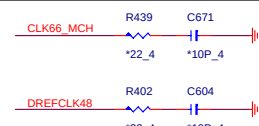
VID



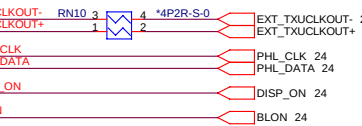
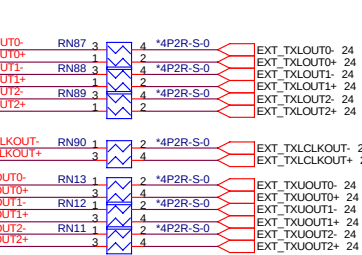
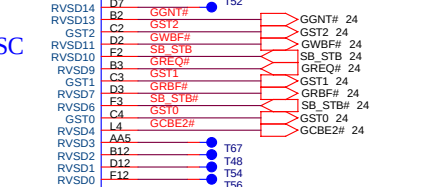
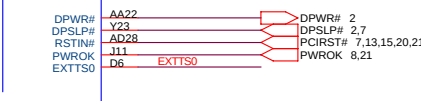
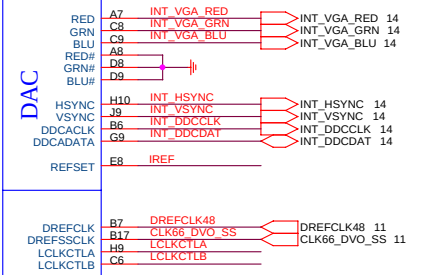
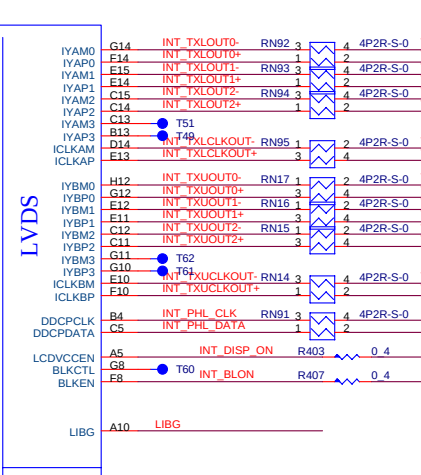
FOR
CH7011



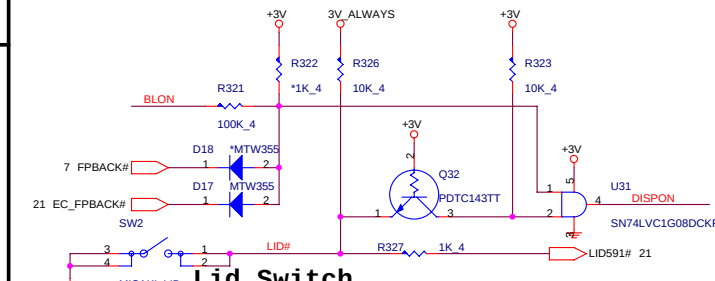
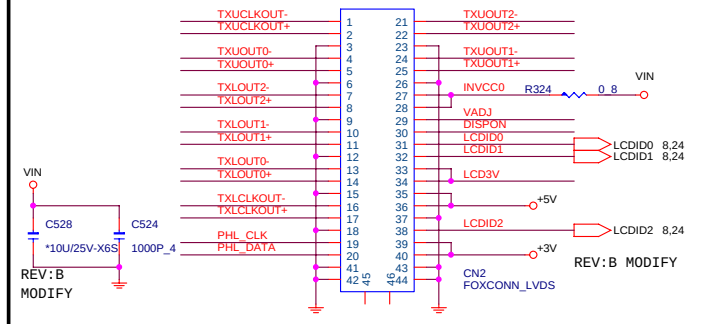
ST2 ST1 ST0			PSB Frequency	Memory Frequency	GFx Core Clock - Low	GFx Core Clock - High
★	0	0	400MHz	266MHz	133MHz	208MHz
0	0	1	400MHz	200MHz	100MHz	208MHz
0	1	0	400MHz	200MHz	100MHz	133MHz
0	1	1	400MHz	133MHz	133MHz	208MHz
1	0	0	533MHz	266MHz	133MHz	208MHz
1	0	1	533MHz	266MHz	133MHz	266MHz
1	1	0	533MHz	166MHz	166MHz	208MHz
1	1	1	400MHz	333MHz	166MHz	250MHz

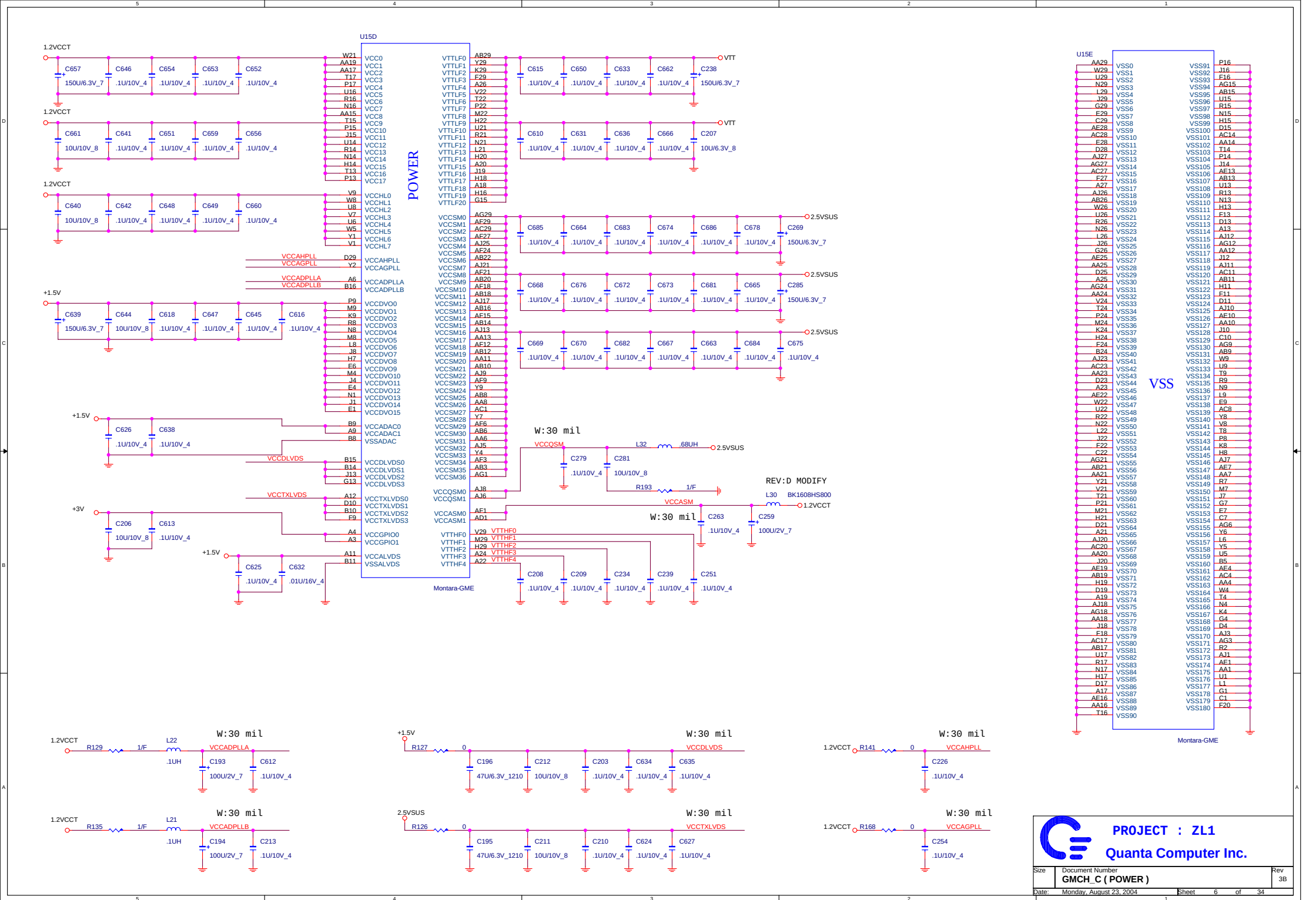


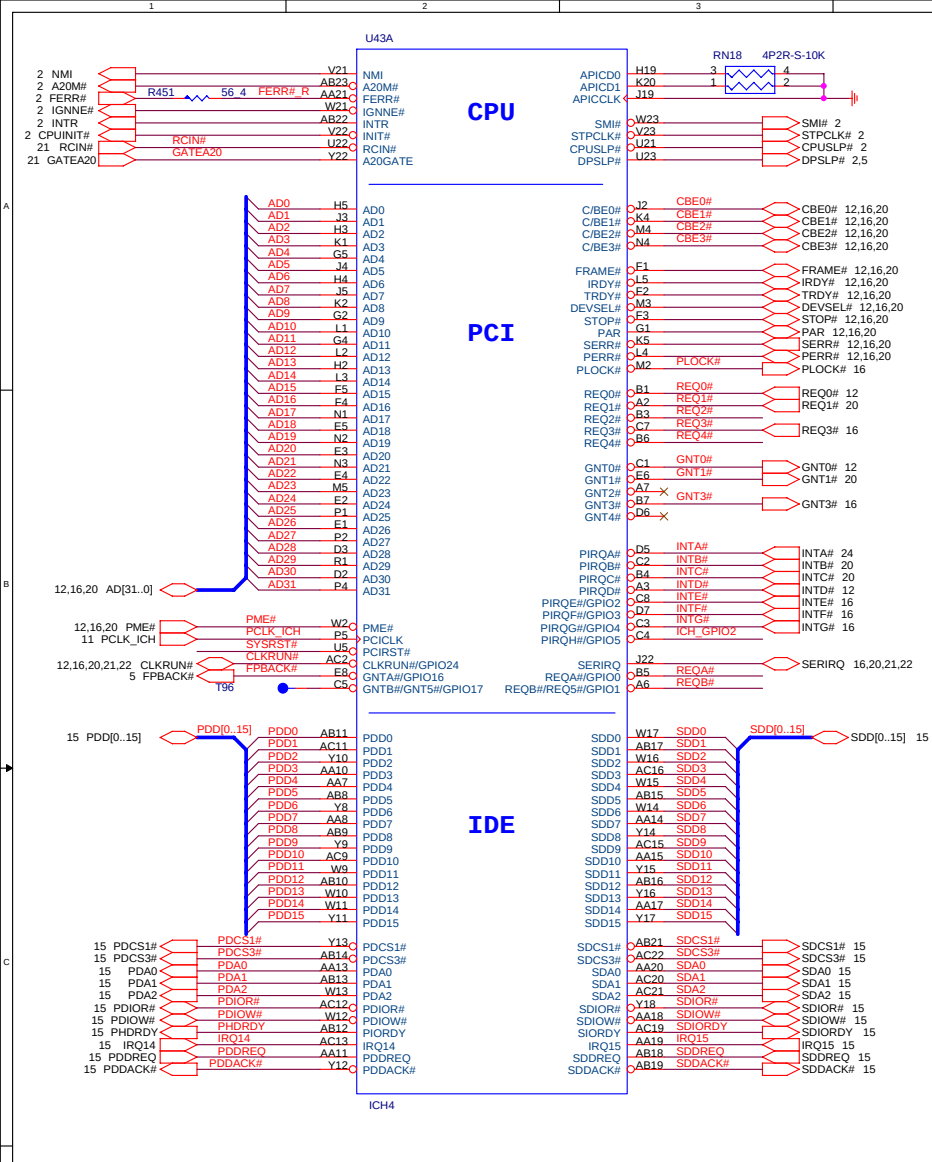
ontara-GME



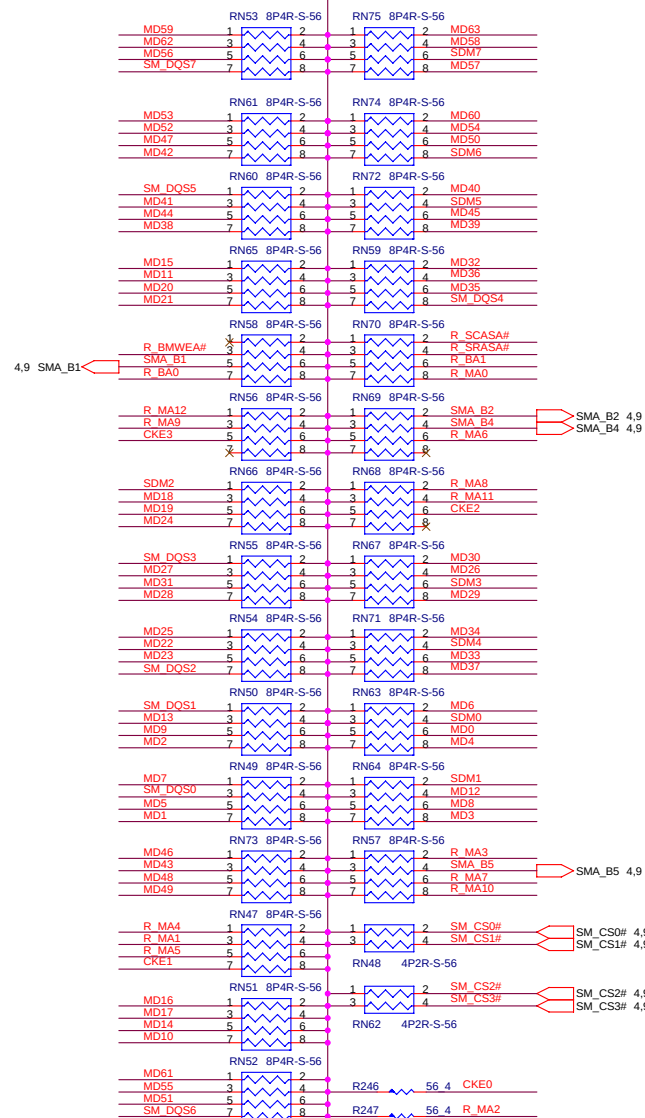
LCDID2	LCDID1	LCDID0	LCD TYPE	LCD CABLE P/N
0	0	0		
0	0	1		
0	1	0		
0	1	1	14" XGA	DD0ZL1LC000
1	0	0	15" XGA	DD0ZL1LC107
1	0	1	15" SXGA+	DD0ZL1LC204
1	1	0	15.4" WXGA	DD0ZL1LC301
1	1	1	15.4" WSXGA+	DD0ZL1LC409



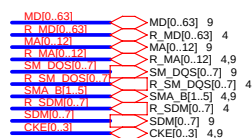




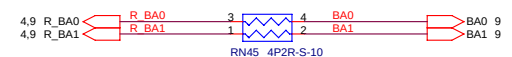
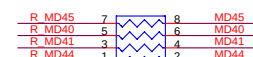
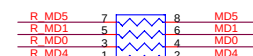
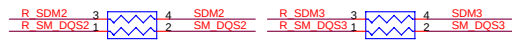
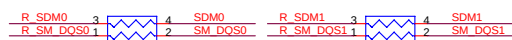
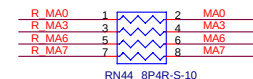
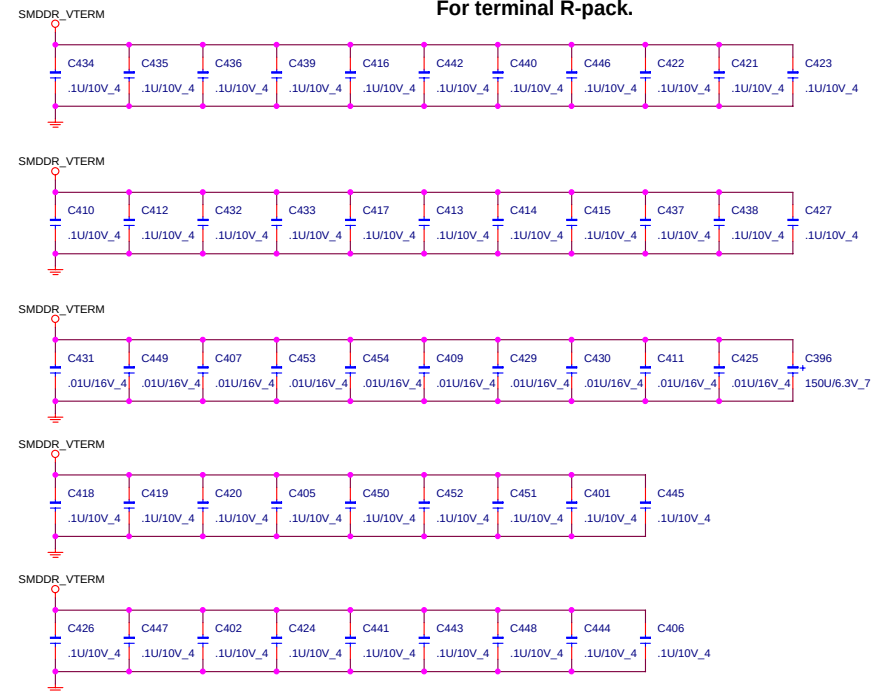
SMDDR_VTERM



Damping put BOT, termination put TOP

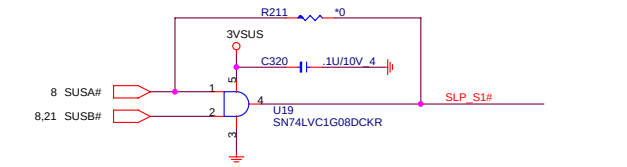
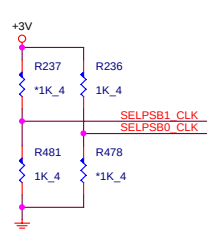


For terminal R-pack.



PROJECT : ZL1
Quanta Computer Inc.

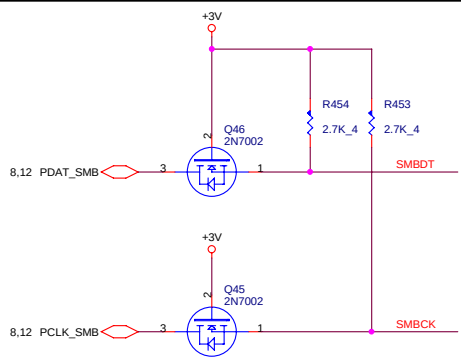
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66
0	0	1	100	66	66
0	1	0	200	66	66
0	1	1	133	66	66



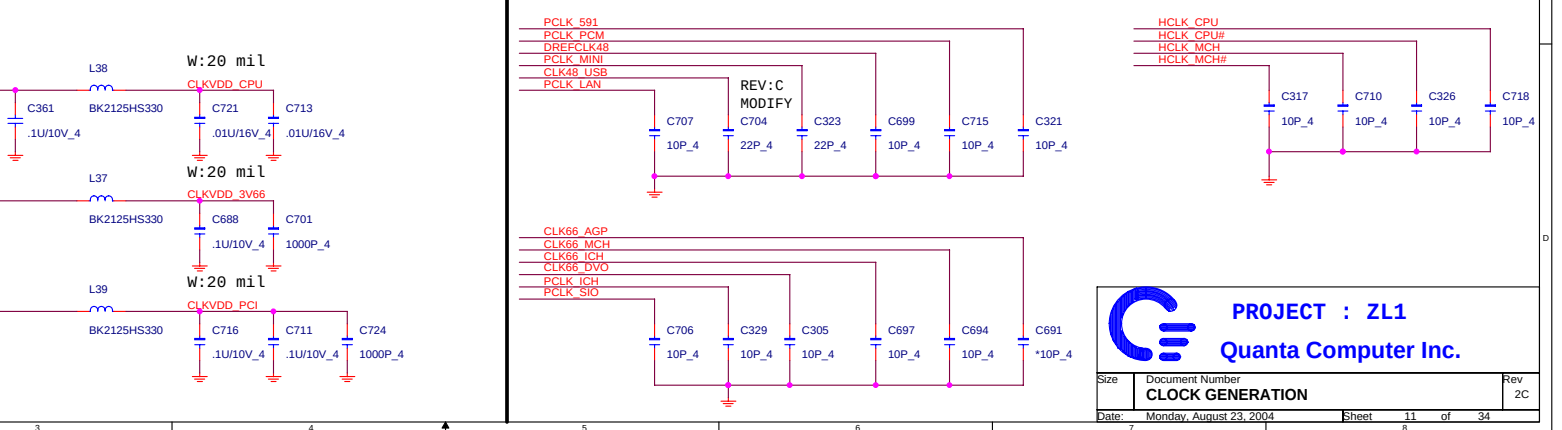
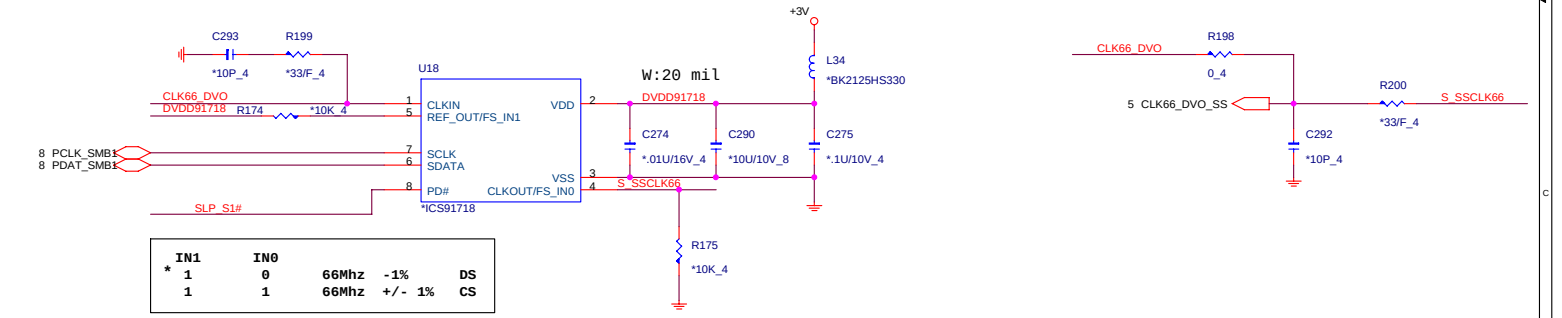
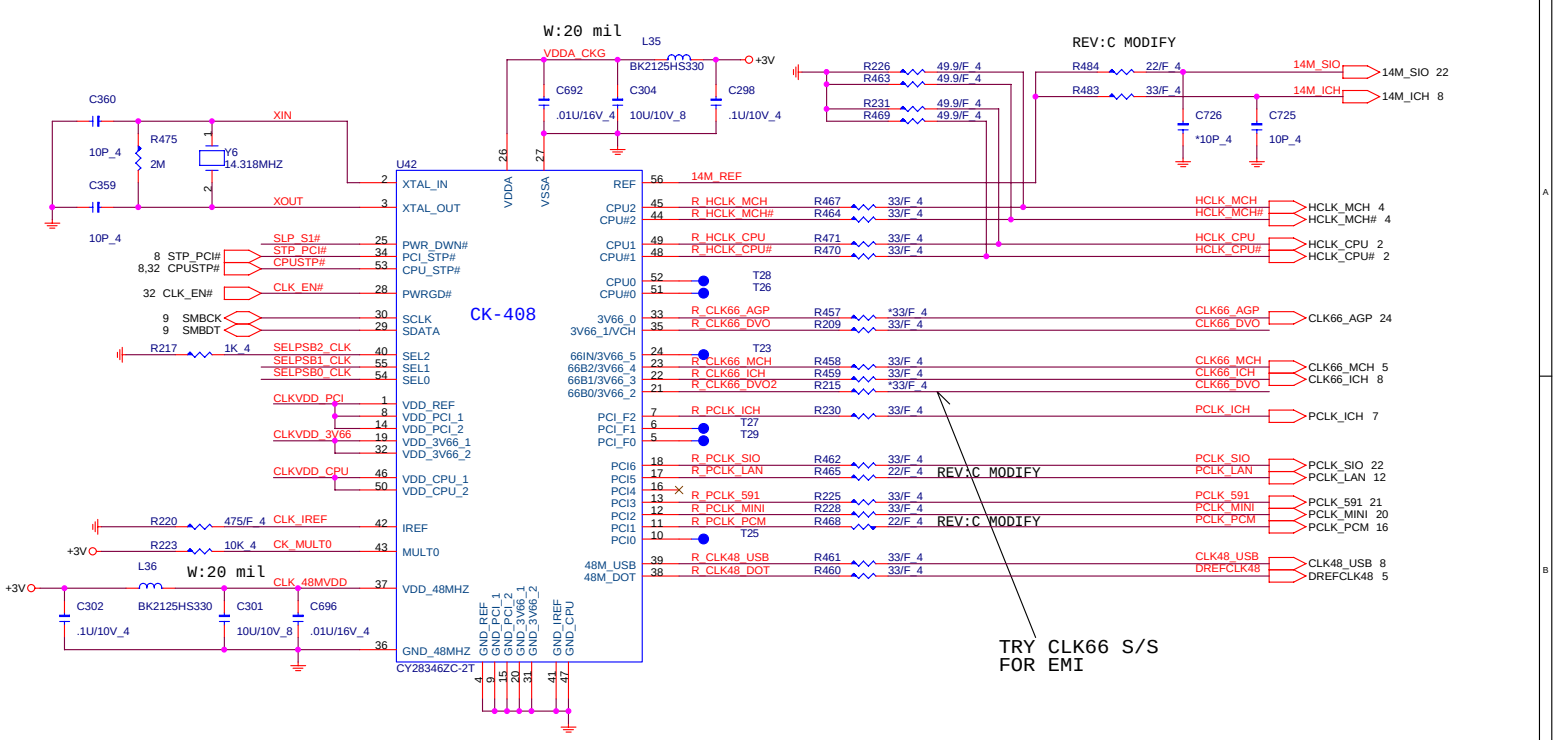
SMBus Byte 0 , Bit 5 = 0	3V66_1/VCH	66 MHz	W	S/S
SMBus Byte 0 , Bit 5 = 1	48 MHz		W/O	S/S

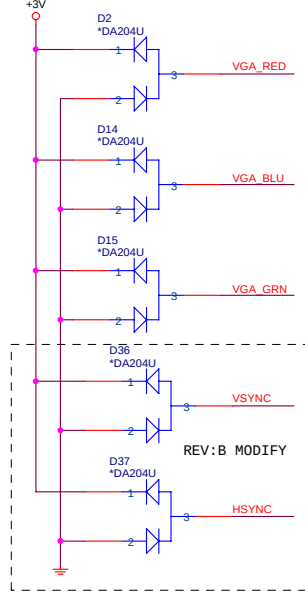
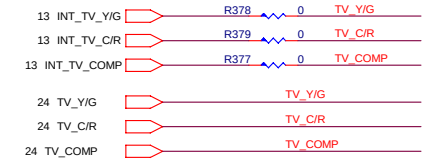
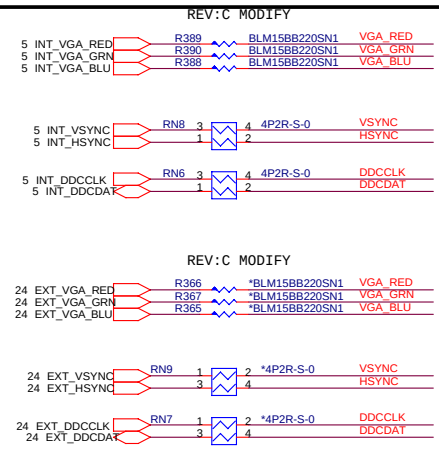
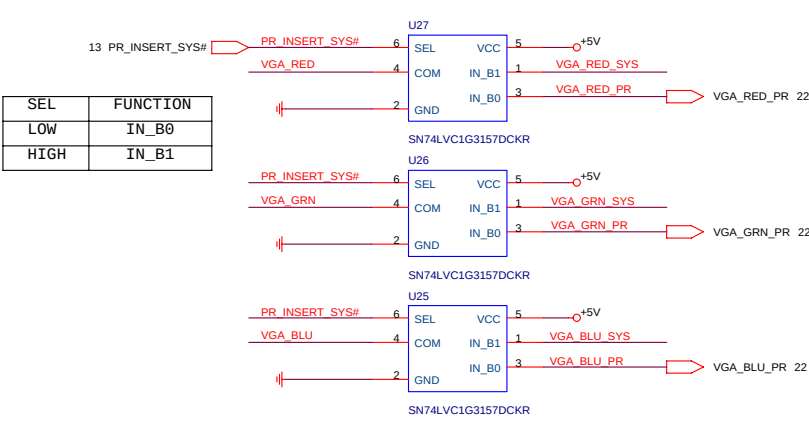
Byte 0 , Bit 7 = 0 Disable Spread Spectrum
Byte 0 , Bit 7 = 1 Enable Spread Spectrum

Byte 4 Bit 7	Byte 5 Bit 7	Byte 5 Bit 6	Spread Mode	Spread %
SS2	SS1	SS0		
0	0	0	DOWN	+0.00 , -0.25
0	0	1	DOWN	+0.00 , -0.50
0	1	0	DOWN	+0.00 , -0.75
0	1	1	DOWN	+0.00 , -1.00
1	0	0	CENTER	+0.13 , -0.13
1	0	1	CENTER	+0.25 , -0.25
1	1	0	CENTER	+0.37 , -0.37
1	1	1	CENTER	+0.50 , -1.50

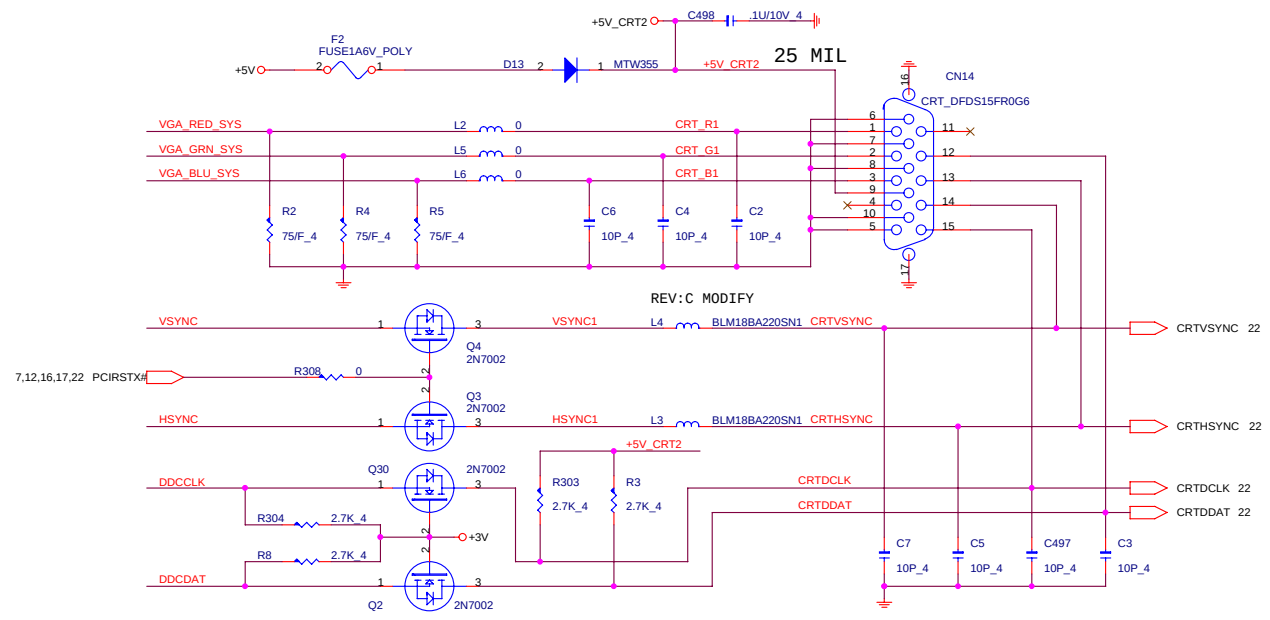


These are for backdrive issue

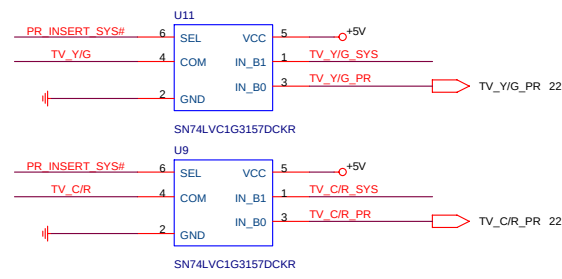
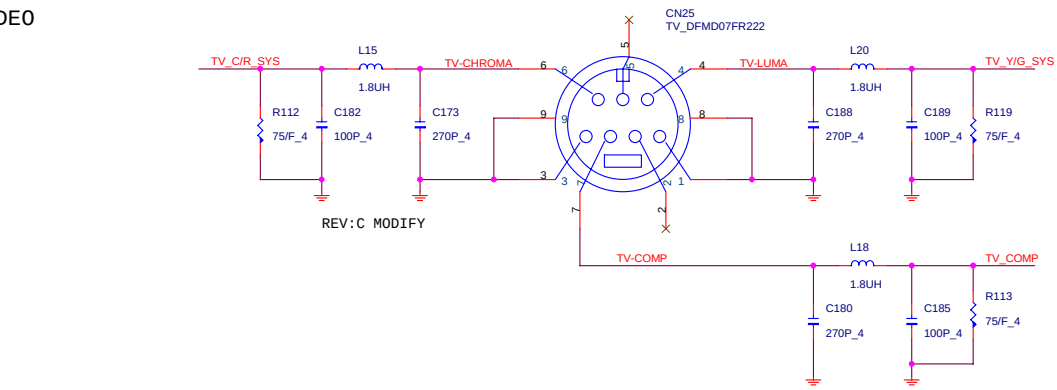


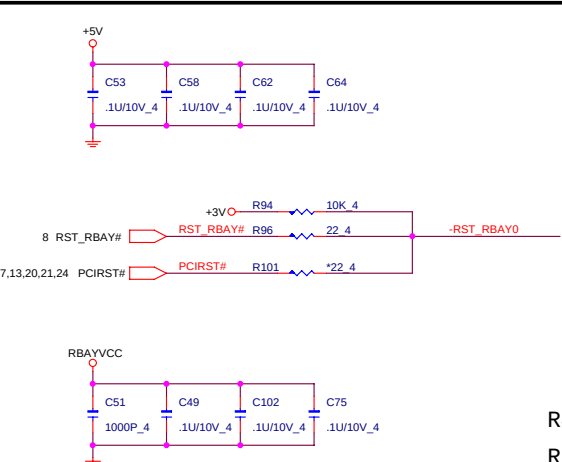
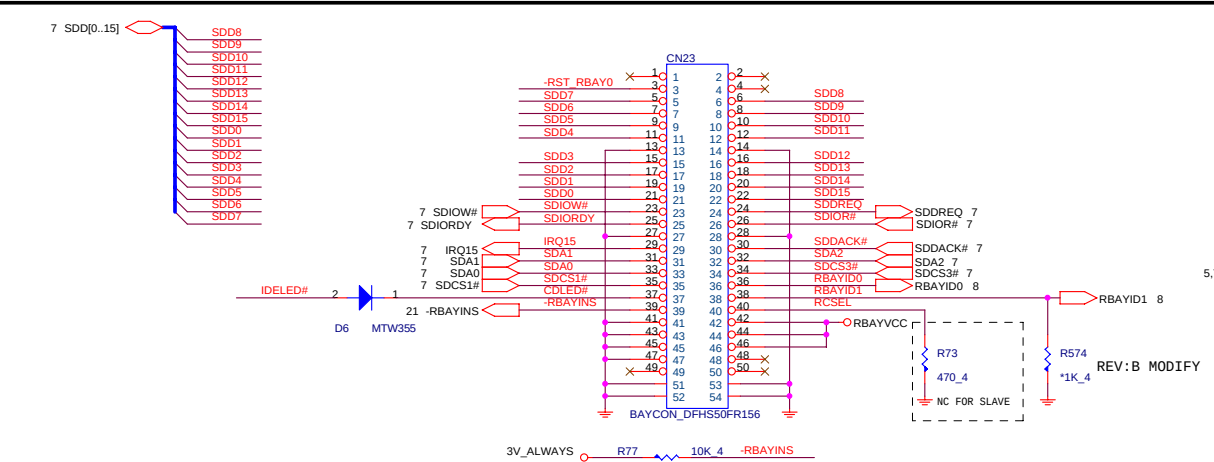
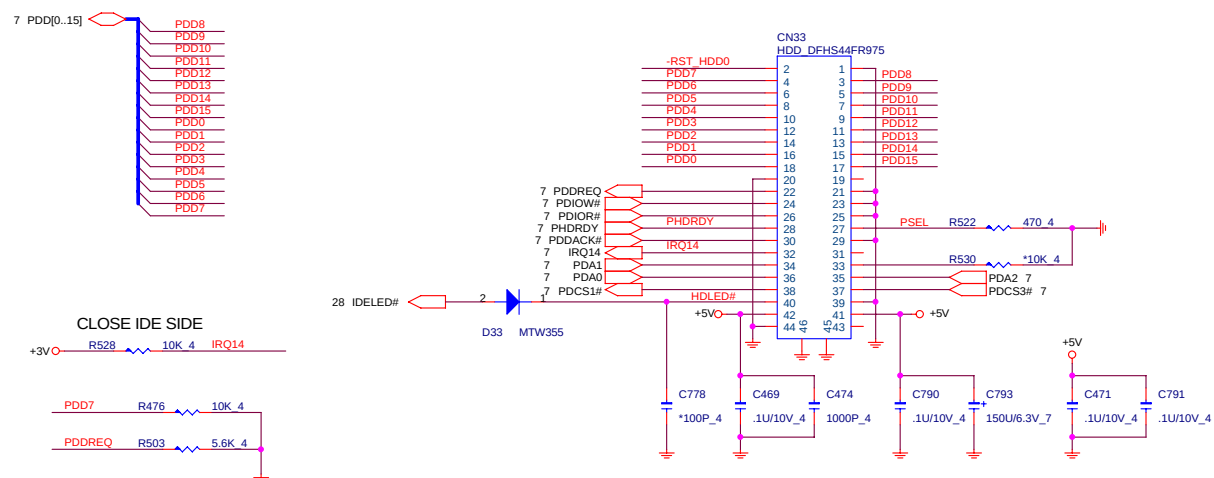


CRT PORT



S-VIDEO



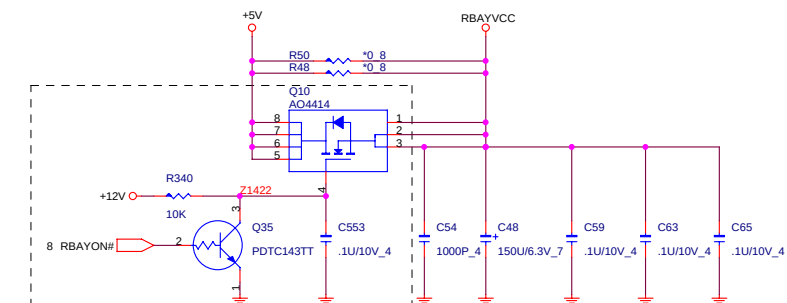
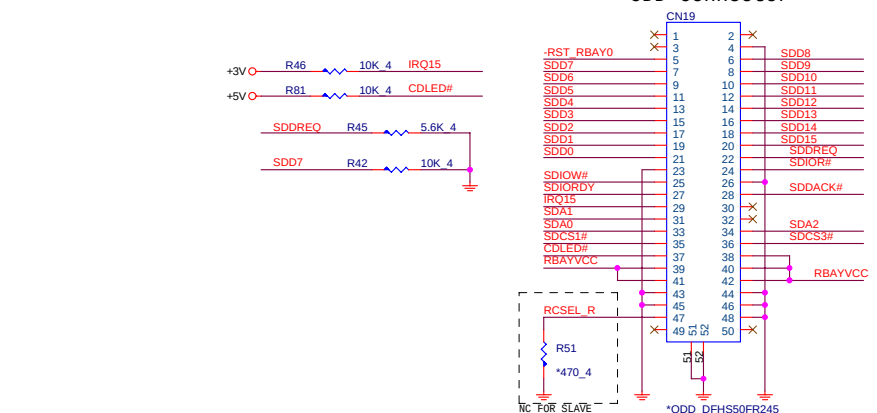


BAY ID STATUS

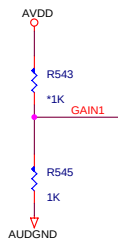
RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD

	W/O Media Bay	W Media Bay
R472, R473	DNS	STUFF
R94, R96	DNS	STUFF
R474, R101	STUFF	DNS
R48, R50	STUFF	DNS
Q10, Q35	DNS	STUFF
R340, C553	DNS	STUFF
R73, CN23	DNS	STUFF
R51, CN19	STUFF	DNS
R574	STUFF	DNS

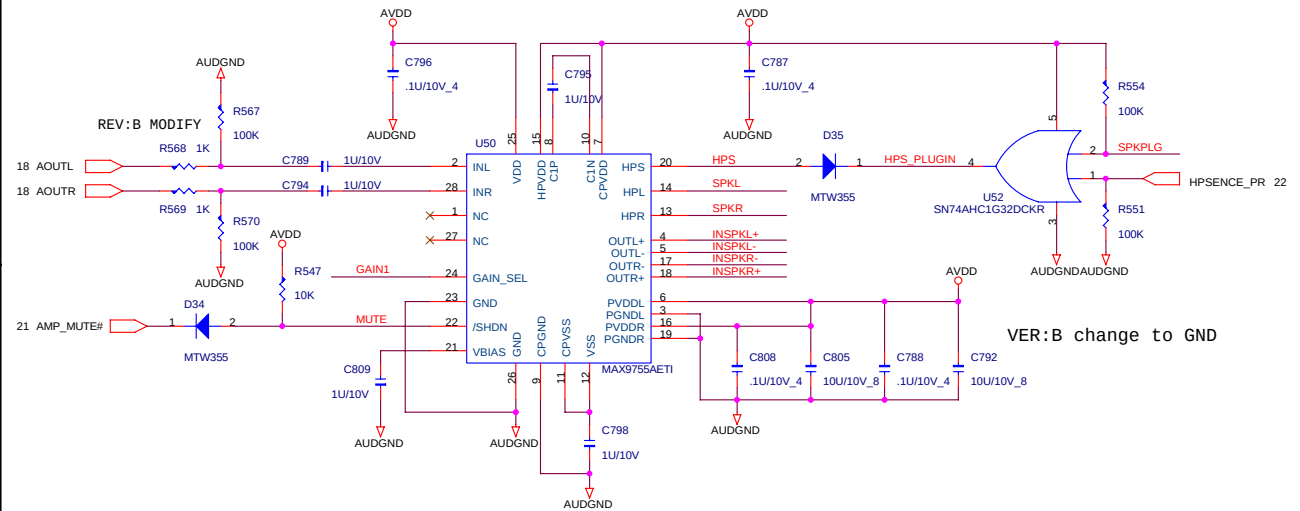
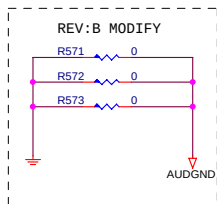
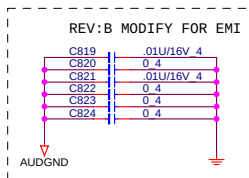
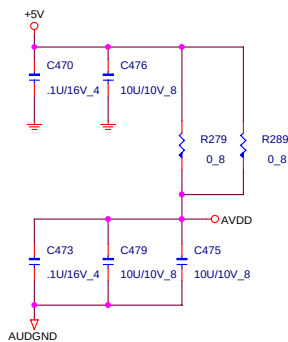
ODD Connector



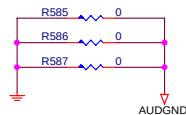
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



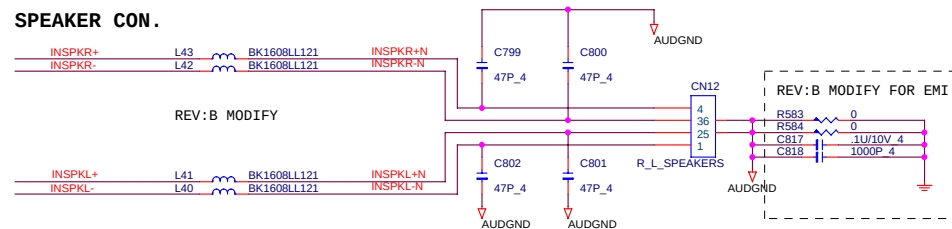
AMP POWER



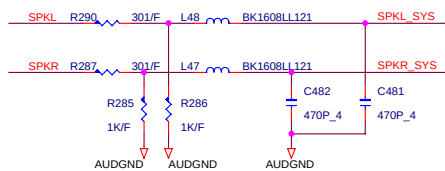
VER:B change to GND



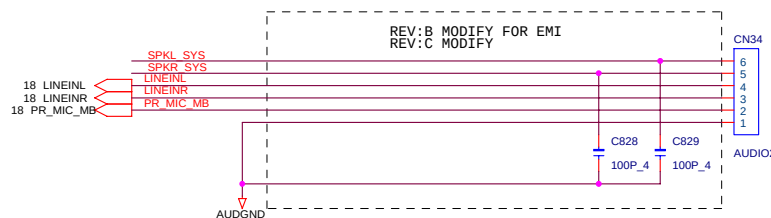
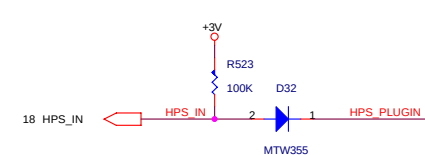
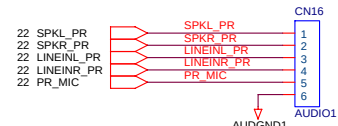
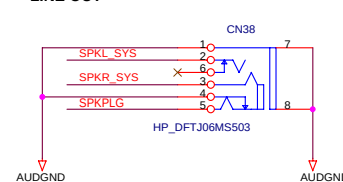
SPEAKER CON.



REV:B MODIFY



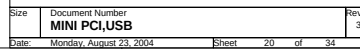
LINE OUT

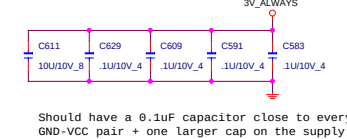
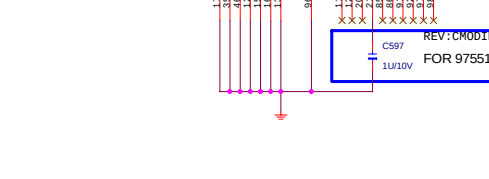
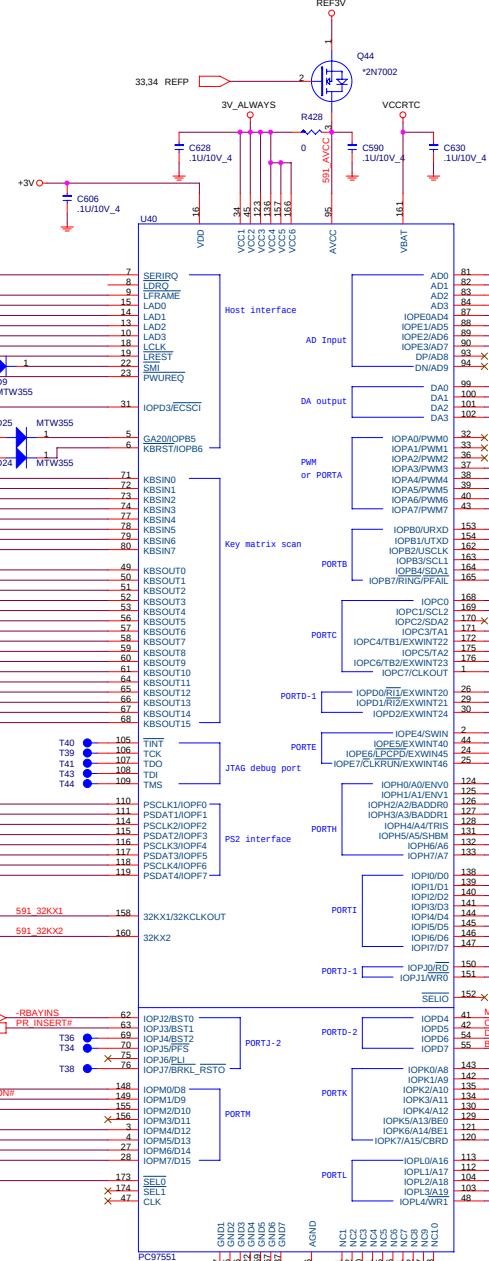
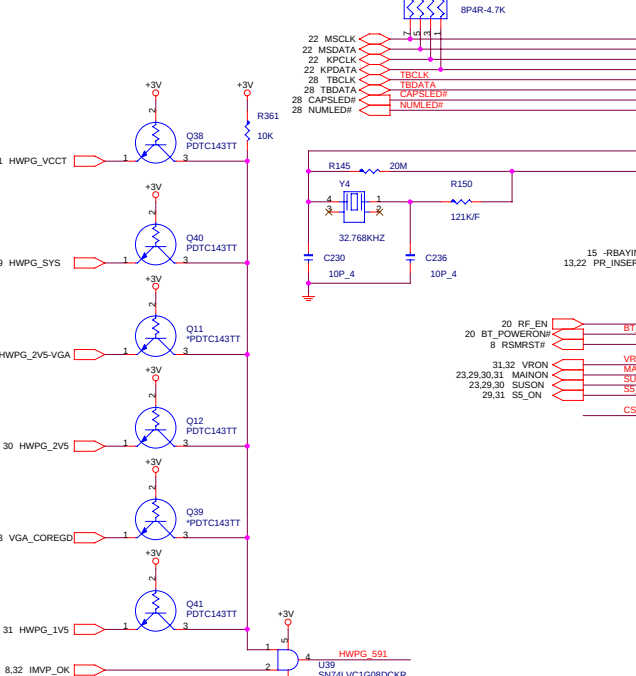
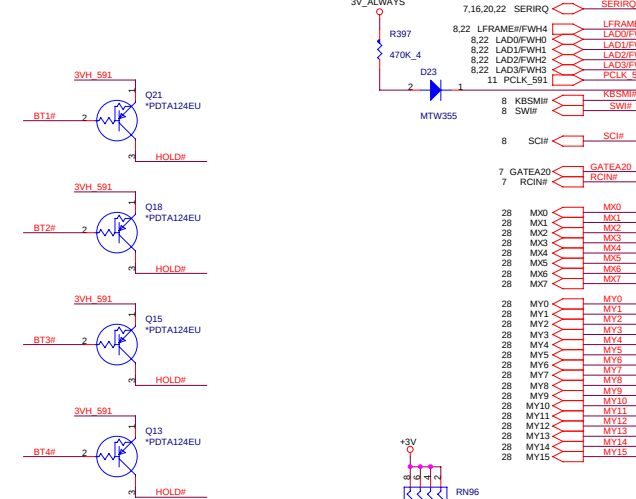


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MINI-PCI



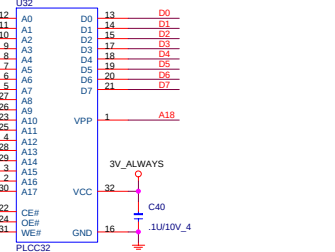
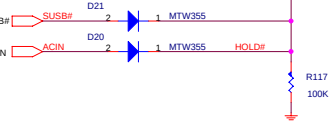
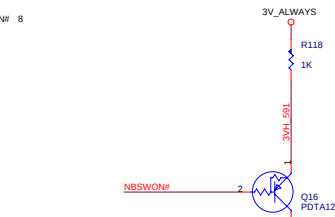
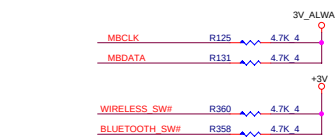
[illegible]

The schematic diagram shows the SHM module's electrical connections. It features a vertical bus labeled 3V_ALWAYS at the top right. Six horizontal signal lines are connected to this bus via resistors:

- ENV1** is connected to the bus through resistor **R406** (10K_4).
- BADDR0** is connected to the bus through resistor **R410** (*10K_4).
- BADDR1** is connected to the bus through resistor **R413** (*10K_4).
- BT_POWERON#** is connected to the bus through resistor **R425** (*10K_4).
- SHBM** is connected to the bus through resistor **R416** (10K_4).

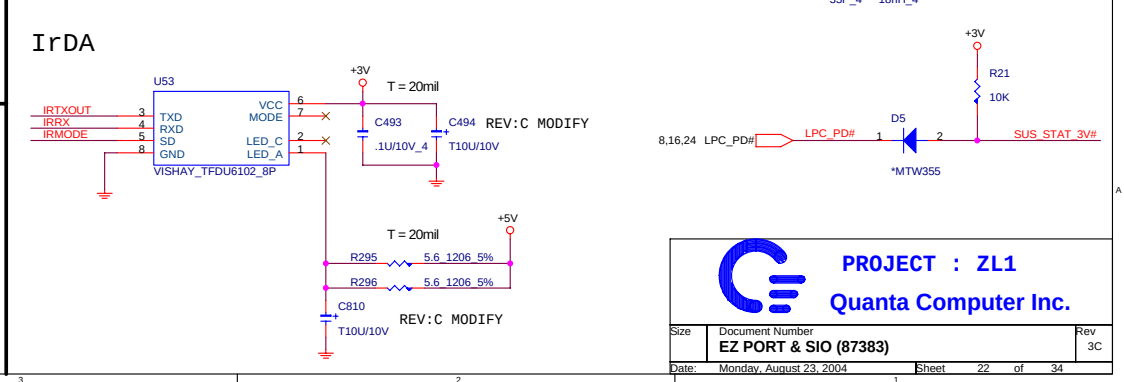
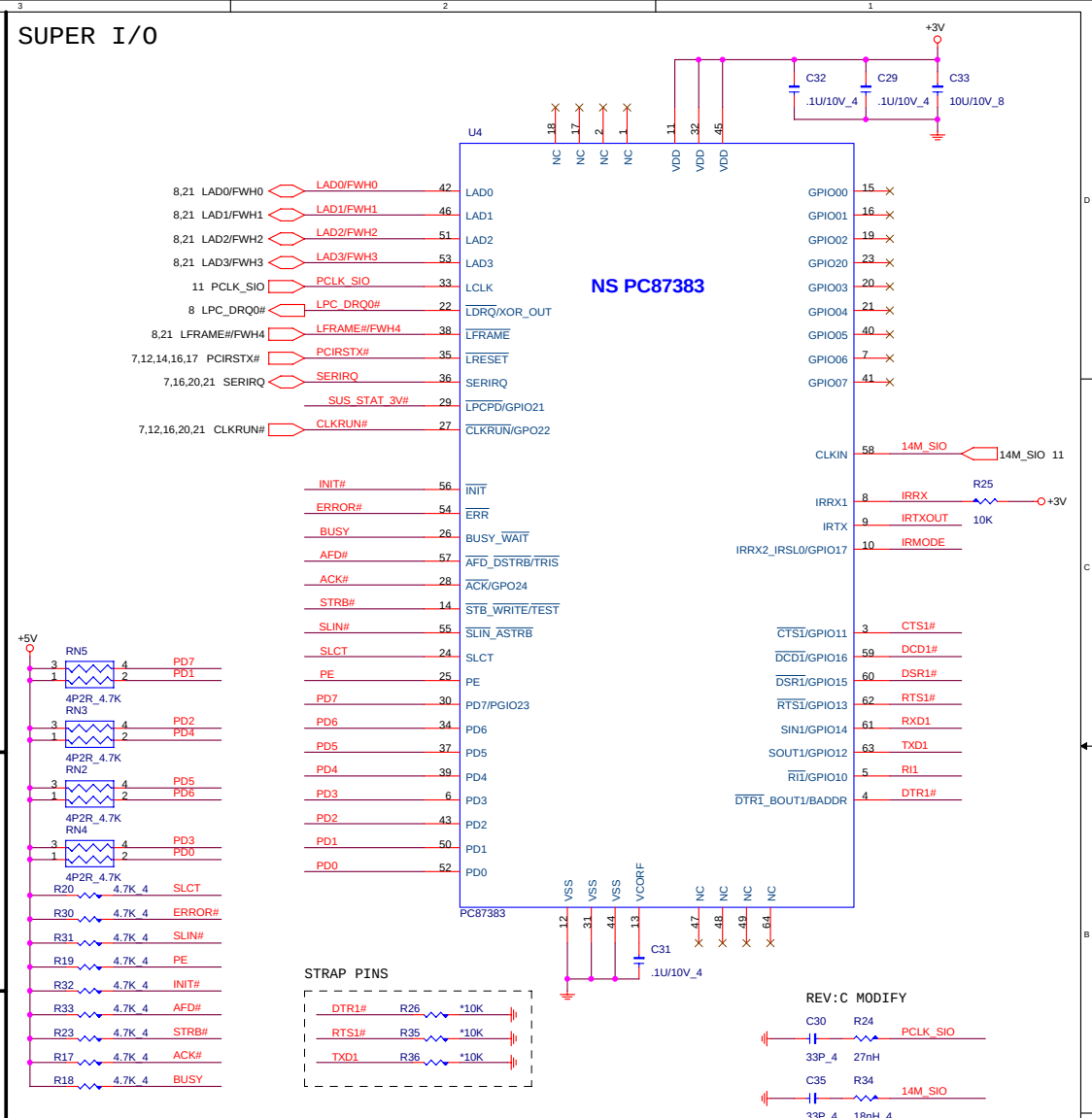
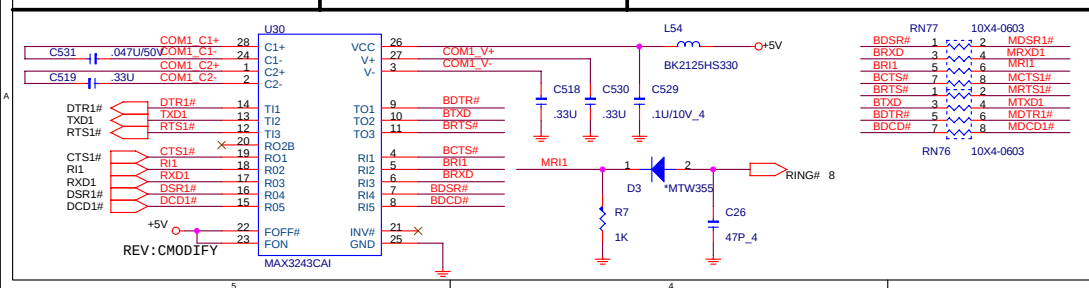
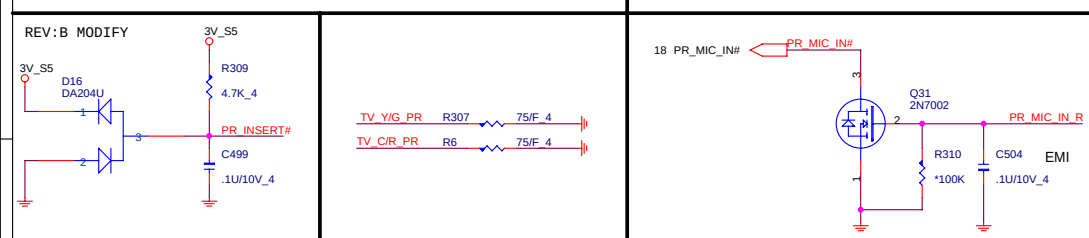
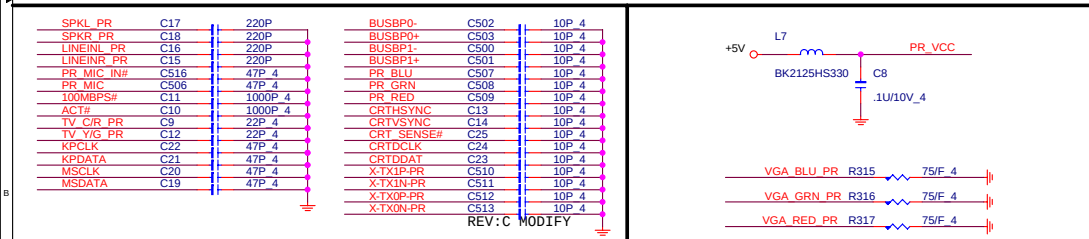
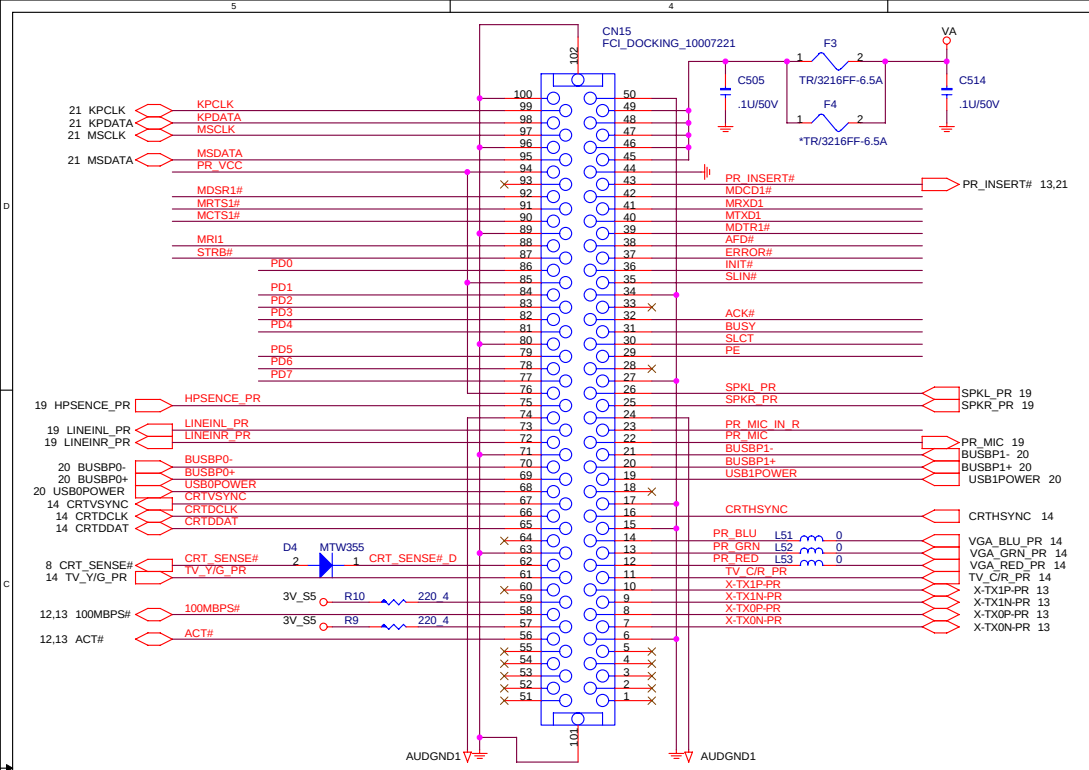
Below the schematic, the text reads: **SHM=1: Enable shared memory with host BIOS**.

I/O Address		
BADDR1-6	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL) (HCFGBAH, HCFGBAL)+1	
1 1	Reserved	

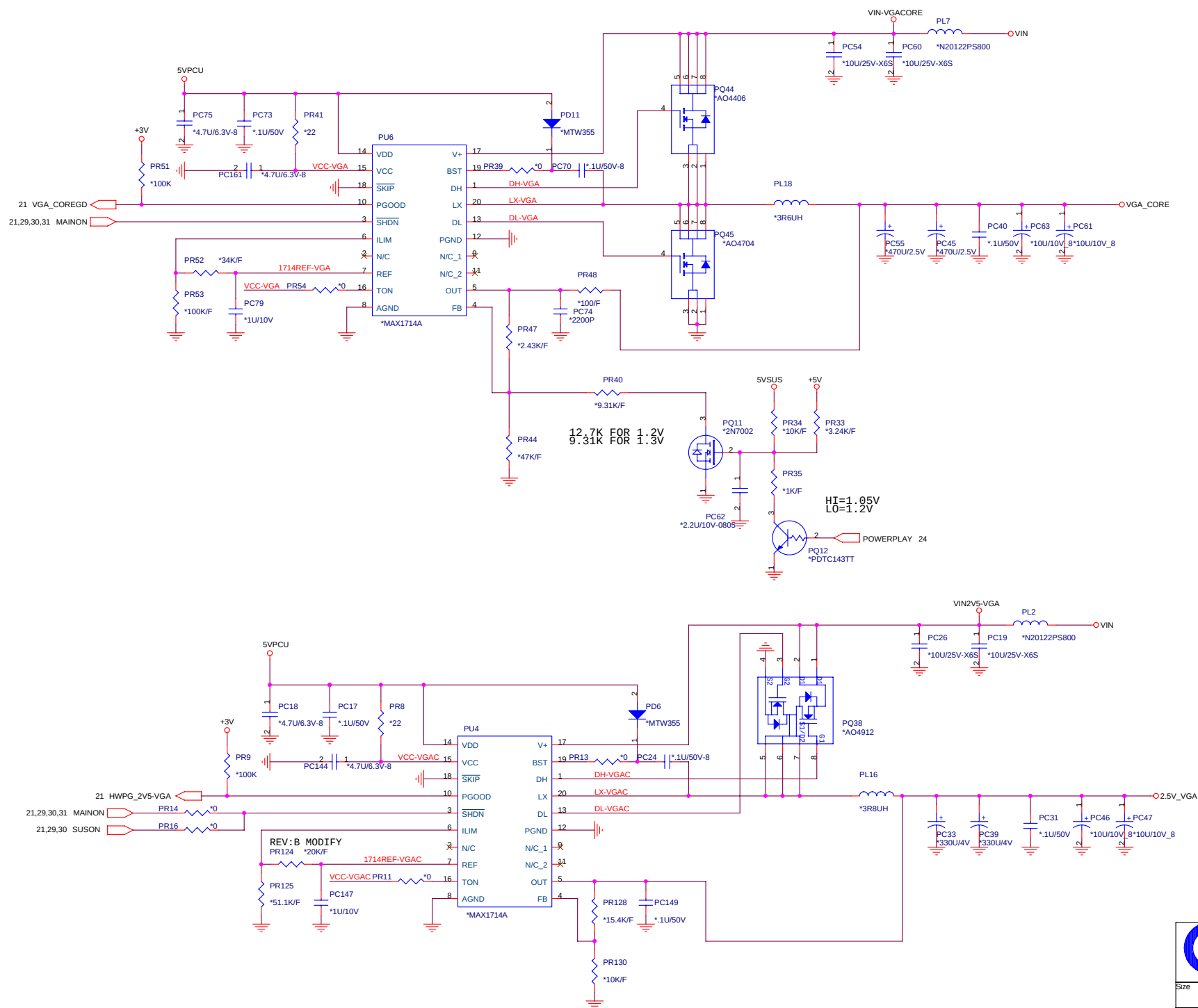


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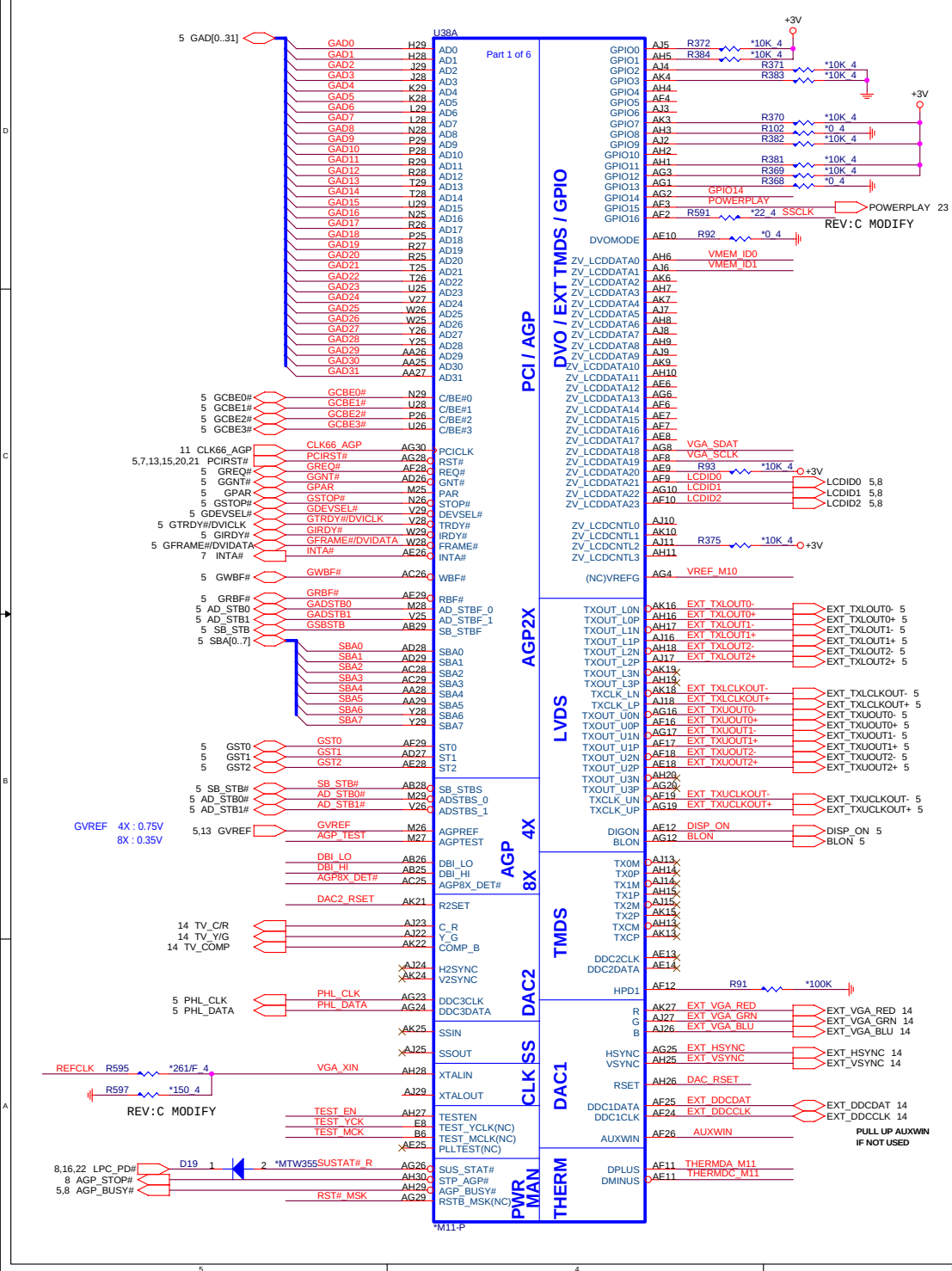
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M11 Core & BATTERY

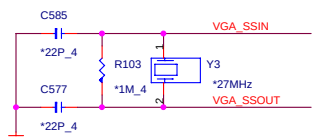
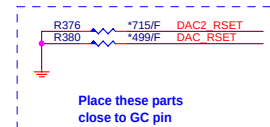
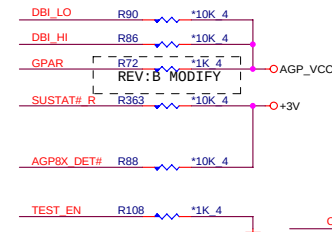
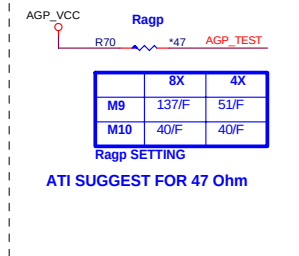
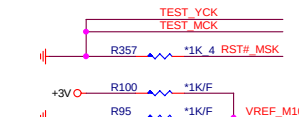


VGA(AGP,DISPLAY)

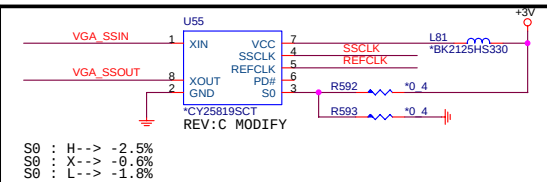


	32MB	64MB	128MB	128MB-Hyni
ID1	0	0	1	1
ID0	0	1	0	1

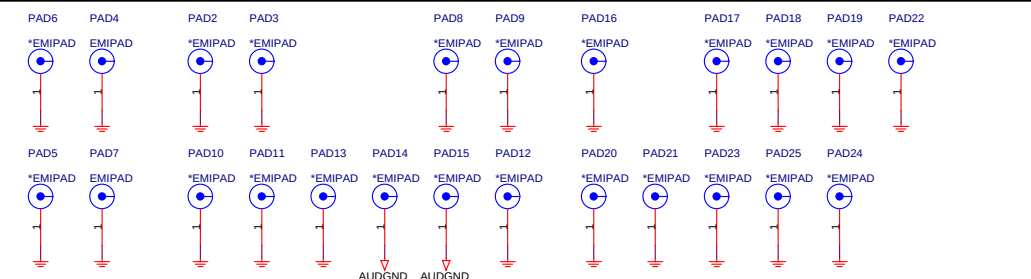
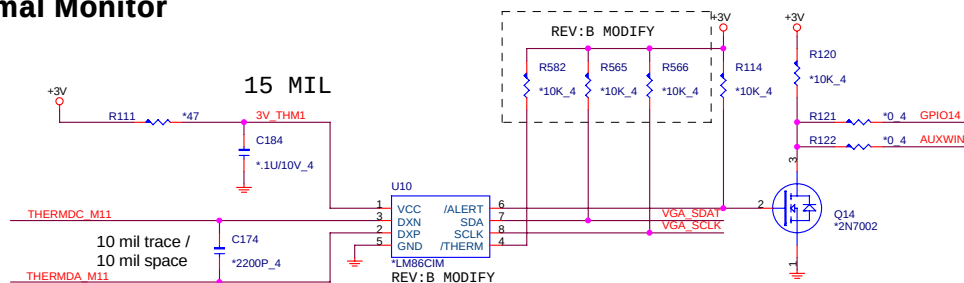
VRAM configuration



REV:C MODIFY



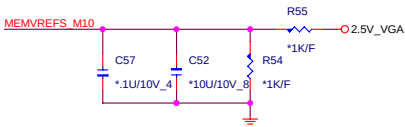
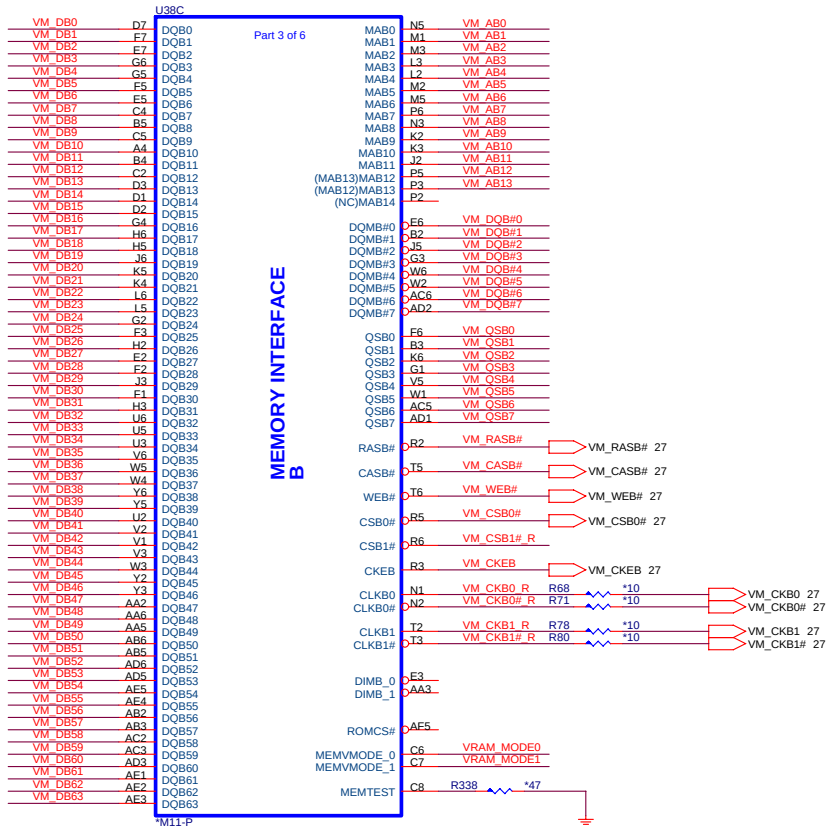
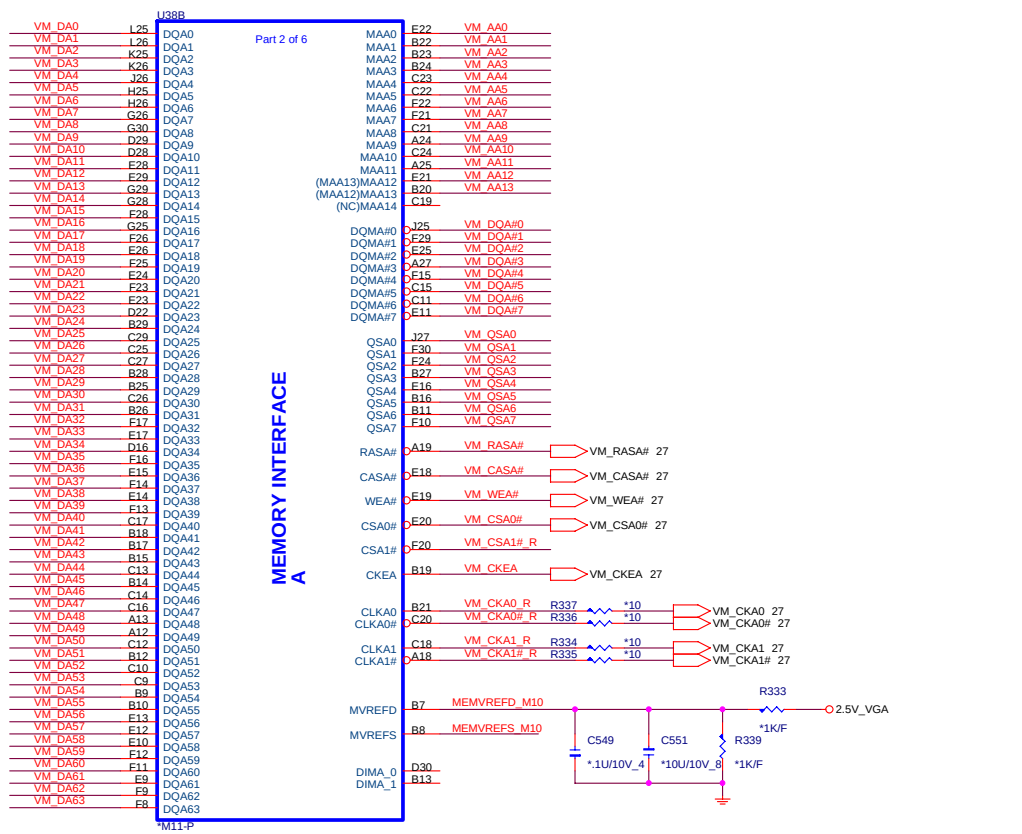
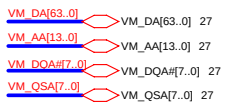
Thermal Monitor



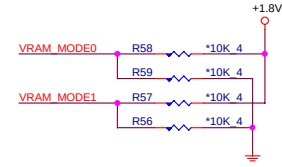
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VGA(MEMORY I/F)



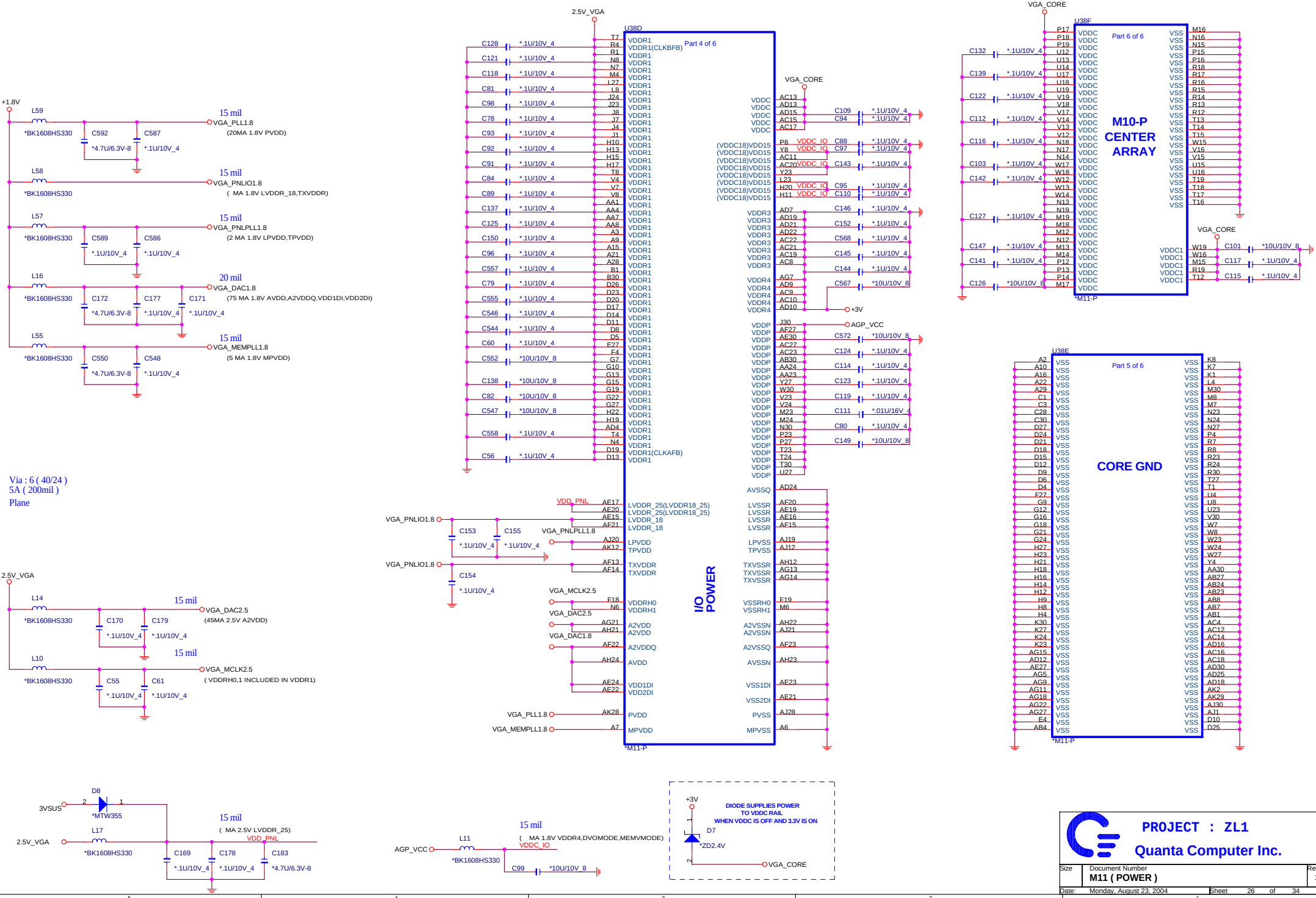
Mount these parts when use hynix 256Mb(8Mx32) VRAM chip



	VRAM_MODE1	VRAM_MODE0
2.5V	0	1
1.8V	1	0
2.8V	1	1

VRAM type setting

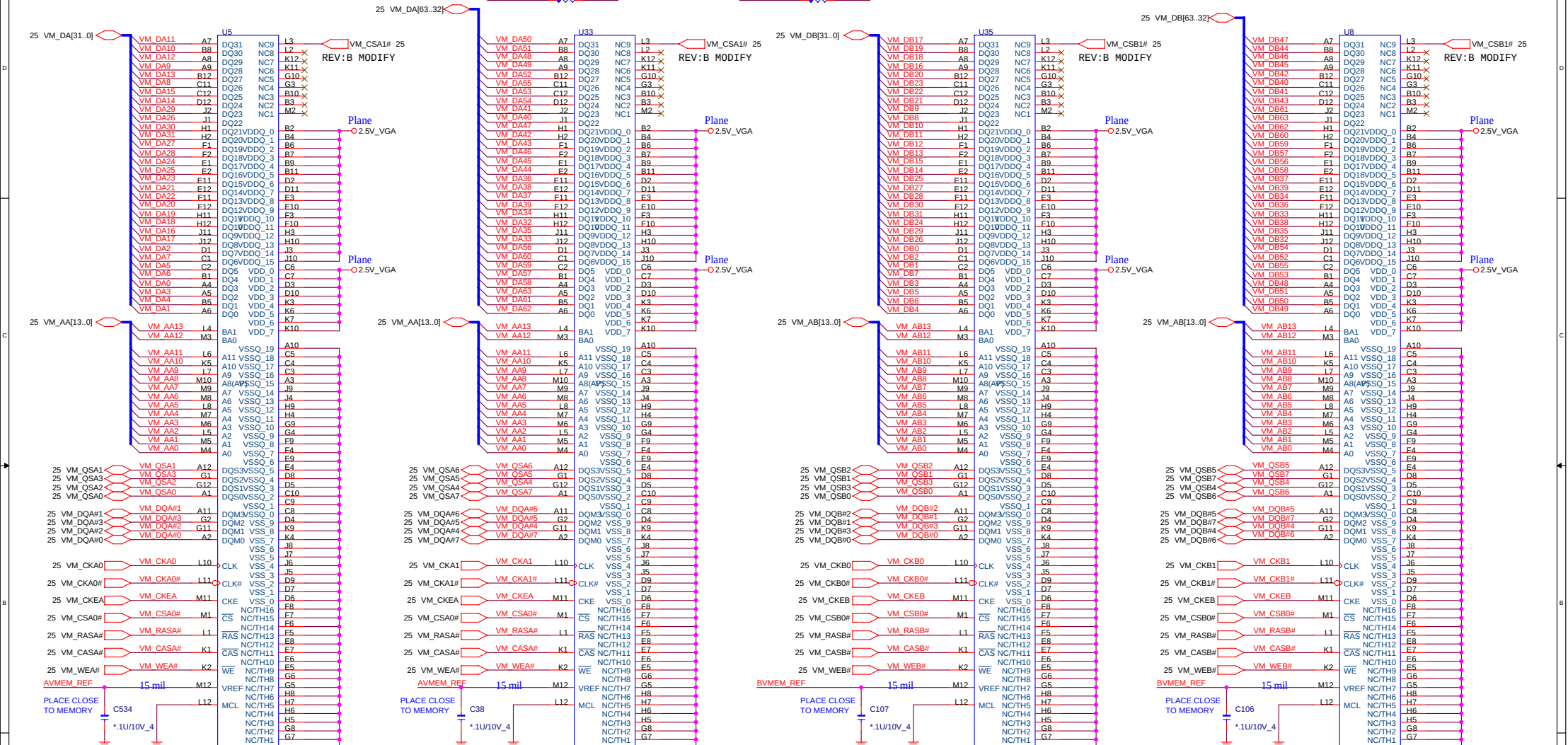
VGA(POWER)



VRAM Channel A



VRAM Channel B

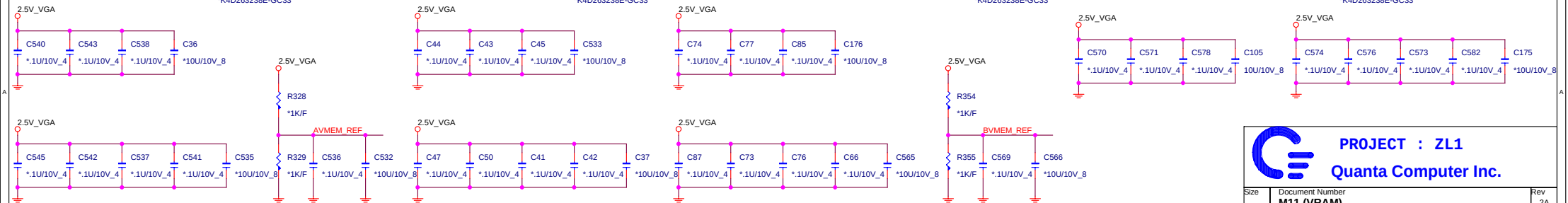


4Mx32 or 8Mx32
*K4D263238E-GC33

4Mx32 or 8Mx32
*K4D263238E-GC33

4Mx32 or 8Mx32
*K4D263238E-GC33

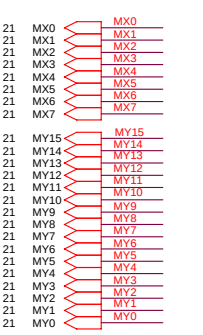
4Mx32 or 8Mx32
*K4D263238E-GC33



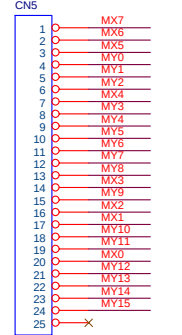
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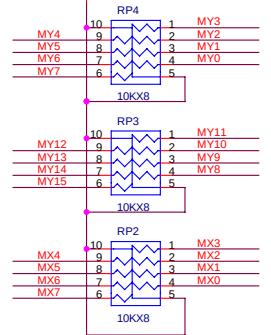
INT K/B



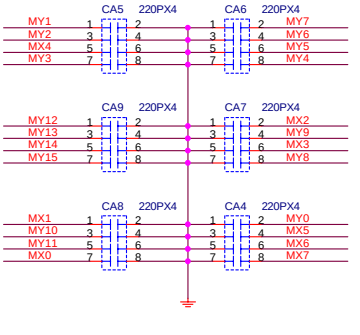
REV:C MODIFY



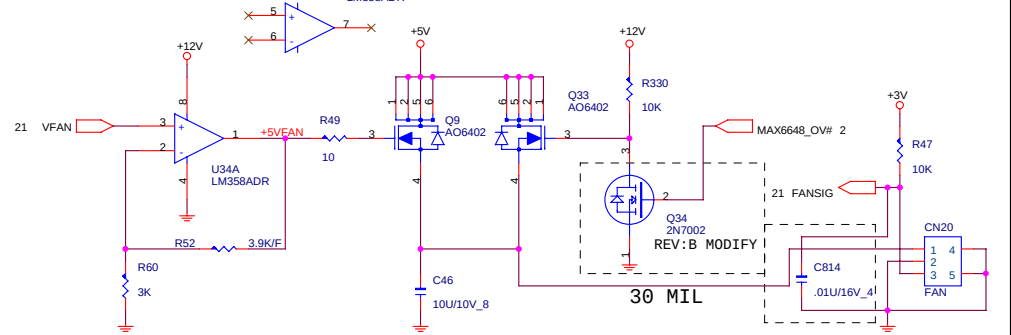
3V_ALWAYS



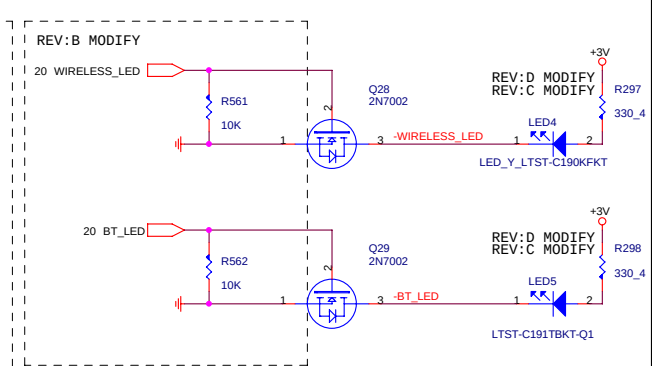
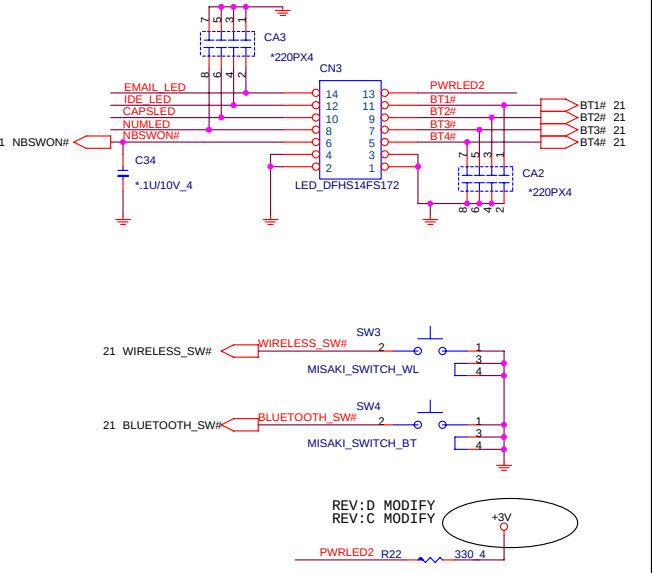
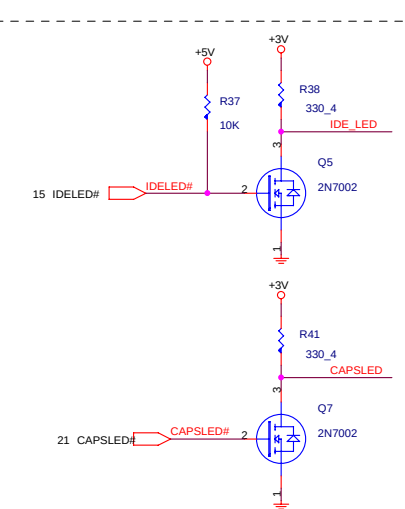
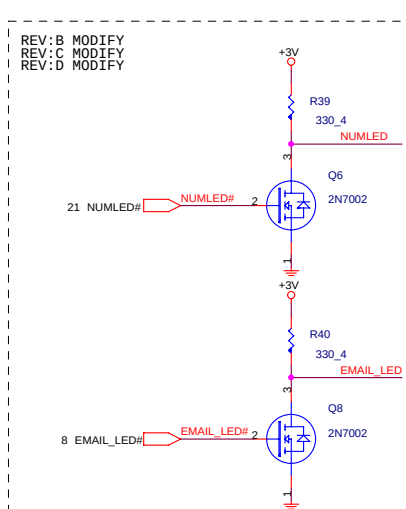
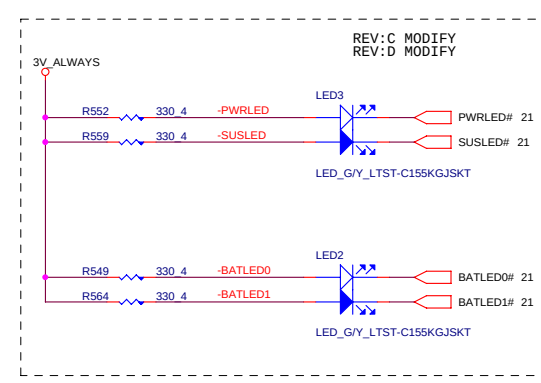
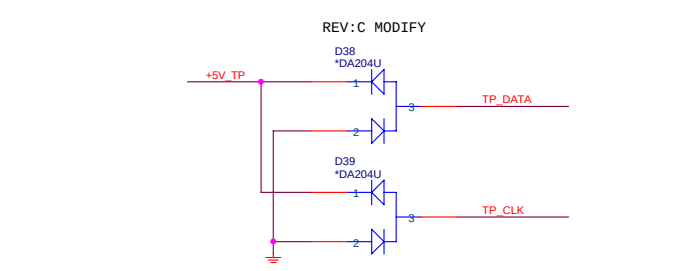
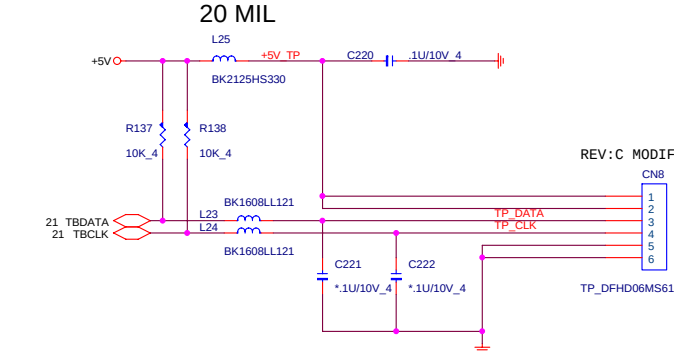
CA5 220PX4



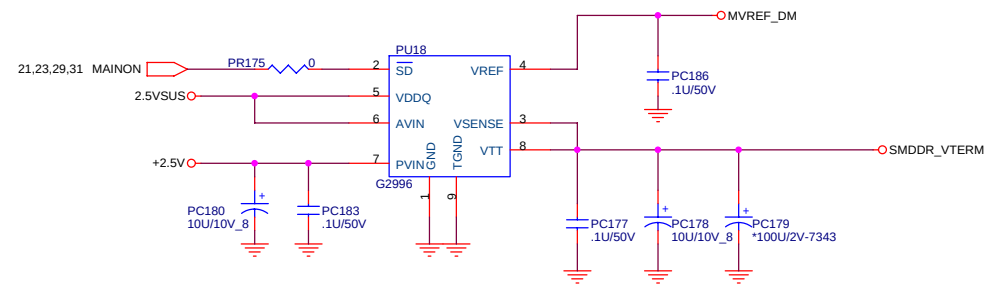
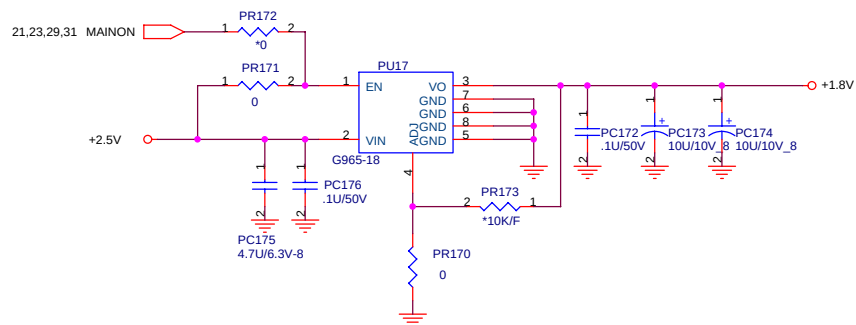
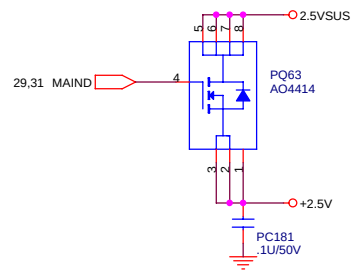
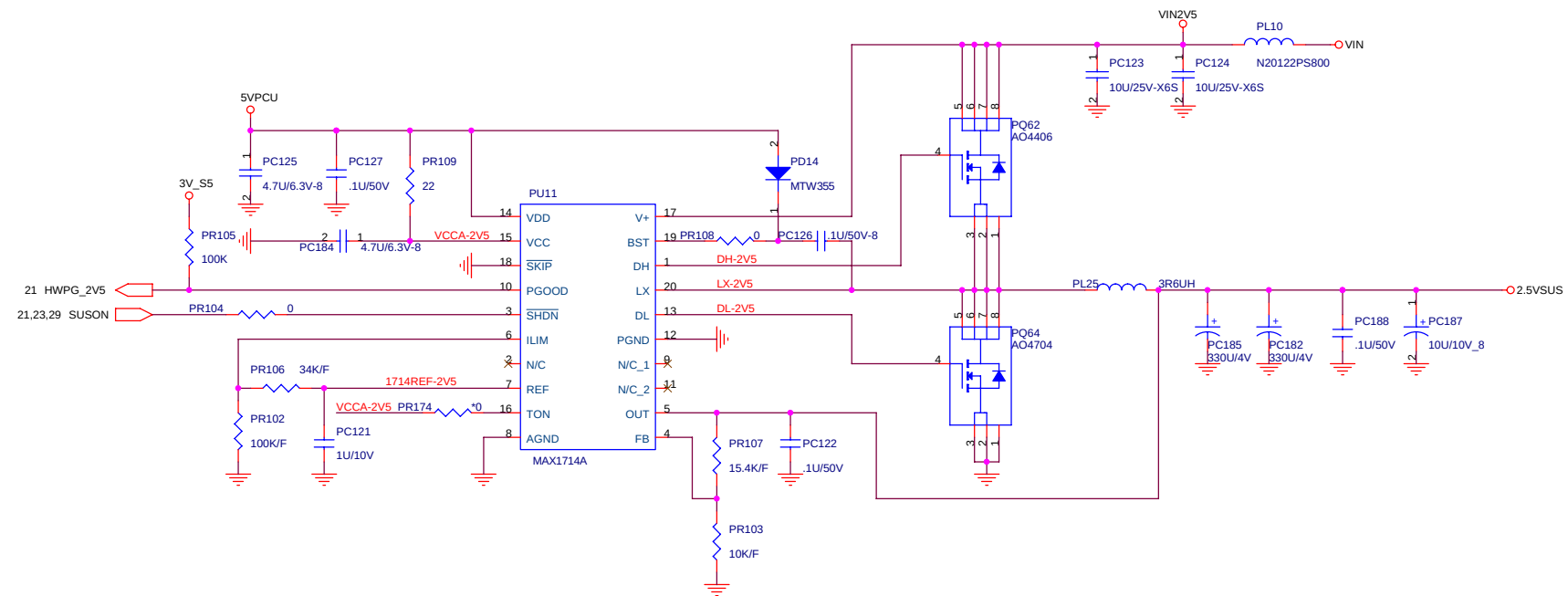
FAN CONTROL



TOUCH PAD

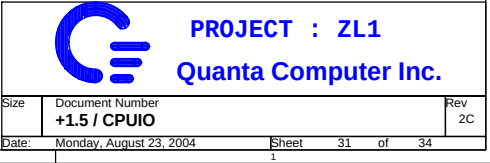


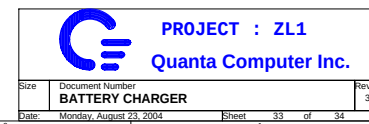
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2ND_BATT_CONN

